

XRT Timeline to be uploaded on 2024/06/08

Period: 2024/06/08 11:34:00 - 2024/06/13 11:07:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
06/08 11:47:00 - 06/08 17:22:30	Track (799.2, -362.9) ^{Ⓞ 06/08 11:44:00}	# OP start + 10min: AR13697
06/08 18:14:00 - 06/09 03:59:54	Track (820.8, -362.8) ^{Ⓞ 06/08 18:11:00}	# AR13697
06/09 05:58:00 - 06/09 17:41:24	Track (851.8, -362.4) ^{Ⓞ 06/09 05:55:00}	# AR13697
06/09 17:54:30 - 06/10 03:37:00	Track (872.2, -361.7) ^{Ⓞ 06/09 17:51:30}	# AR13697
06/10 06:13:00 - 06/10 18:00:54	Fixed (880.4, -361.0)	# fixed pointing for AR13697 (limb obs.)

PROG= 12 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 92	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec										
Seqn= 47	1-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96	4-time(s)	120.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)

Term	Pointing (x, y)	Comment
06/08 18:04:00 - 06/08 18:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min
06/09 05:48:00 - 06/09 05:54:54	Fixed (0.0, 0.0)	synoptic, shifted -15.0 min
06/09 17:44:30 - 06/09 17:51:24	Fixed (0.0, 0.0)	synoptic, shifted -18.5 min
06/10 06:03:00 - 06/10 06:09:54	Fixed (0.0, 0.0)	synoptic
06/10 18:04:00 - 06/10 18:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min
06/11 06:37:00 - 06/11 06:43:54	Fixed (0.0, 0.0)	synoptic, shifted 34.0 min

PROG= 19 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 41	1-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec										
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75	1-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wa

Term	Pointing (x, y)	Comment
06/09 04:03:00 - 06/09 05:44:54	Fixed (0.0, 0.0)	HOP349
06/10 04:17:00 - 06/10 05:59:54	Fixed (0.0, 0.0)	HOP349
06/11 04:03:00 - 06/11 06:33:54	Fixed (0.0, 0.0)	HOP349

PROG= 18 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec											
Seqn= 55 1-time(s) 2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 98 1-time(s) 2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 79 1-time(s) 2.0sec											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0 0 2.0sec
Subr= 2 7-time(s) 600.0sec											
Seqn= 8 1-time(s) 2.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
Seqn= 74 1-time(s) 2.0sec											
	med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec											
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2 0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval											

XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
06/10 18:14:00 - 06/11 03:59:54	Fixed (880.4, -361.0)	# fixed pointing for AR13697 (limb obs.)

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec											
Seqn= 92 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0 0 2.0sec
Subr= 2 5-time(s) 2.0sec											
Seqn= 47 1-time(s) 2.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2 0 2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3 0 2.0sec
Seqn= 96 4-time(s) 90.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 0 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 0 2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 1 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 1 2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 2 2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1 2 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval											

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
06/08 11:47:00 - 06/08 17:22:30	Track (799.2, -362.9) @ 06/08 11:44:00	# OP start + 10min: AR13697
06/08 18:14:00 - 06/09 03:59:54	Track (820.8, -362.8) @ 06/08 18:11:00	# AR13697
06/09 04:03:00 - 06/09 05:44:54	Fixed (0.0, 0.0)	HOP349
06/09 05:58:00 - 06/09 17:41:24	Track (851.8, -362.4) @ 06/09 05:55:00	# AR13697
06/09 17:54:30 - 06/10 03:37:00	Track (872.2, -361.7) @ 06/09 17:51:30	# AR13697
06/10 04:17:00 - 06/10 05:59:54	Fixed (0.0, 0.0)	HOP349
06/10 06:13:00 - 06/10 18:00:54	Fixed (880.4, -361.0)	# fixed pointing for AR13697 (limb obs.)
06/10 18:14:00 - 06/11 03:59:54	Fixed (880.4, -361.0)	# fixed pointing for AR13697 (limb obs.)
06/11 04:03:00 - 06/11 06:33:54	Fixed (0.0, 0.0)	HOP349

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec													
└─ Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 73 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 87 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

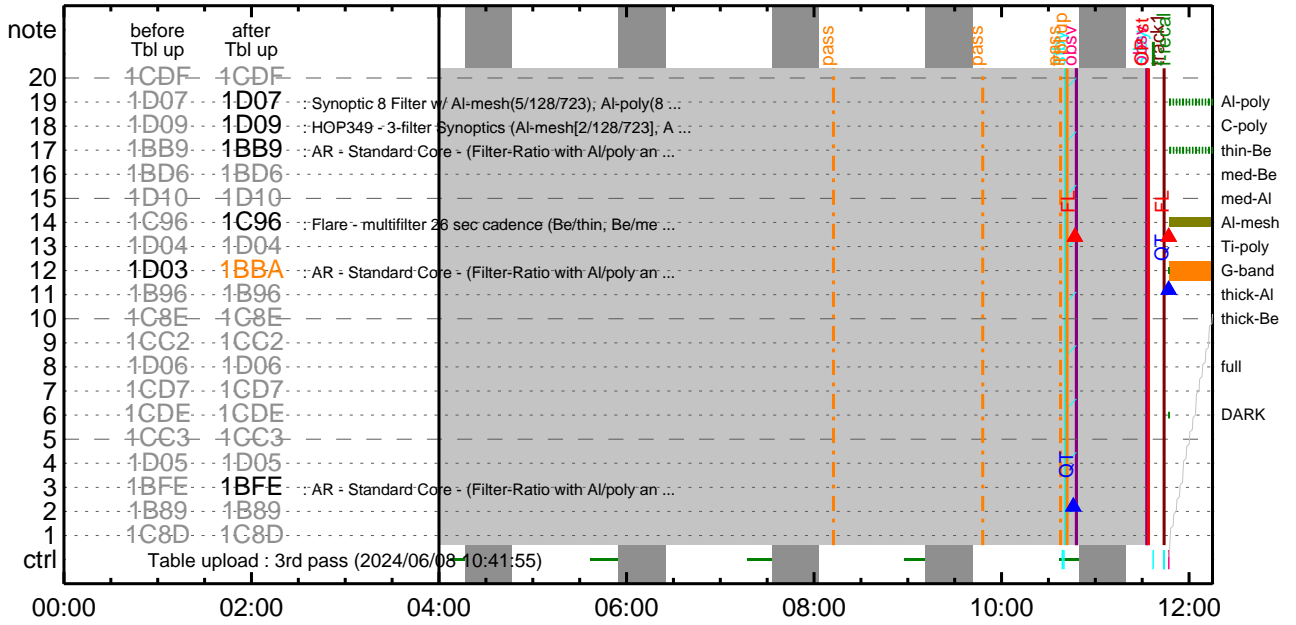
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Flare Detection

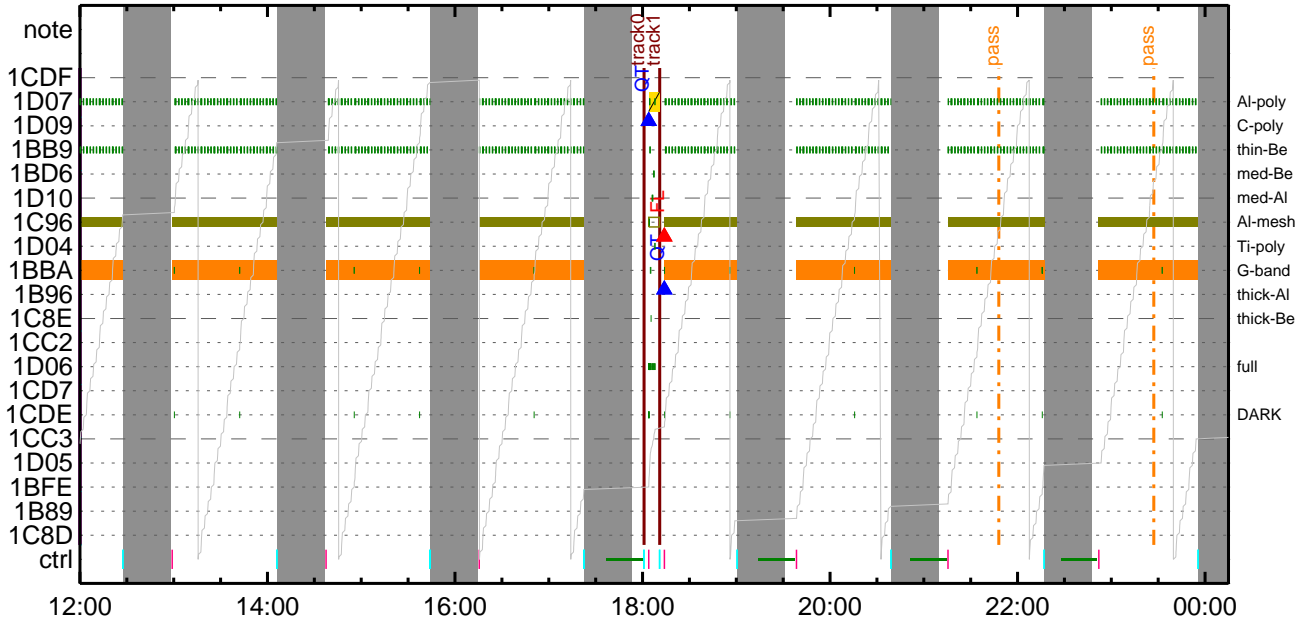
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FLD Patrol												
Term	Pointing (x, y)								Comment			
06/08 10:42:55 - 06/08 18:01:18	cannot be identified											
06/08 18:11:18 - 06/09 05:45:18	Track (820.8, -362.8)	@ 06/08 18:11:00						# AR13697				
06/09 05:55:18 - 06/09 17:41:48	Track (851.8, -362.4)	@ 06/09 05:55:00						# AR13697				
06/09 17:51:48 - 06/10 06:00:18	Track (872.2, -361.7)	@ 06/09 17:51:30						# AR13697				
06/10 06:10:18 - 06/10 18:01:18	Fixed (880.4, -361.0)							# fixed pointing for AR13697 (limb obs.)				
06/10 18:11:18 - 06/11 06:34:18	Fixed (880.4, -361.0)							# fixed pointing for AR13697 (limb obs.)				
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

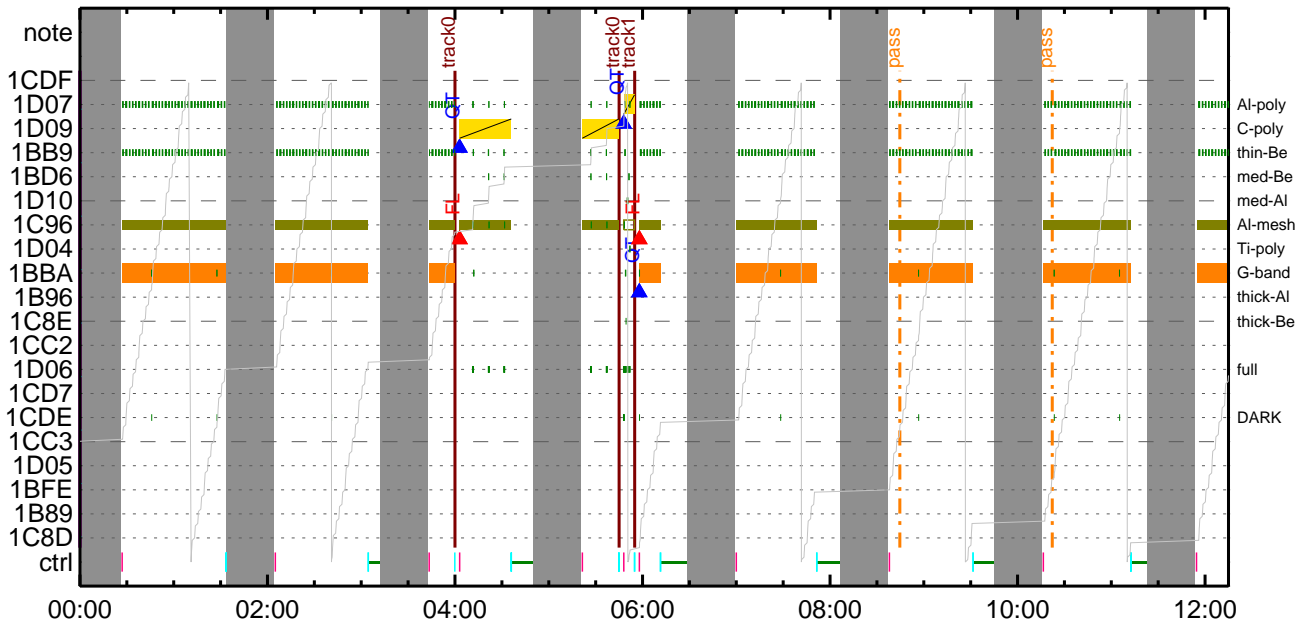
CMDI #0877 2024/06/08



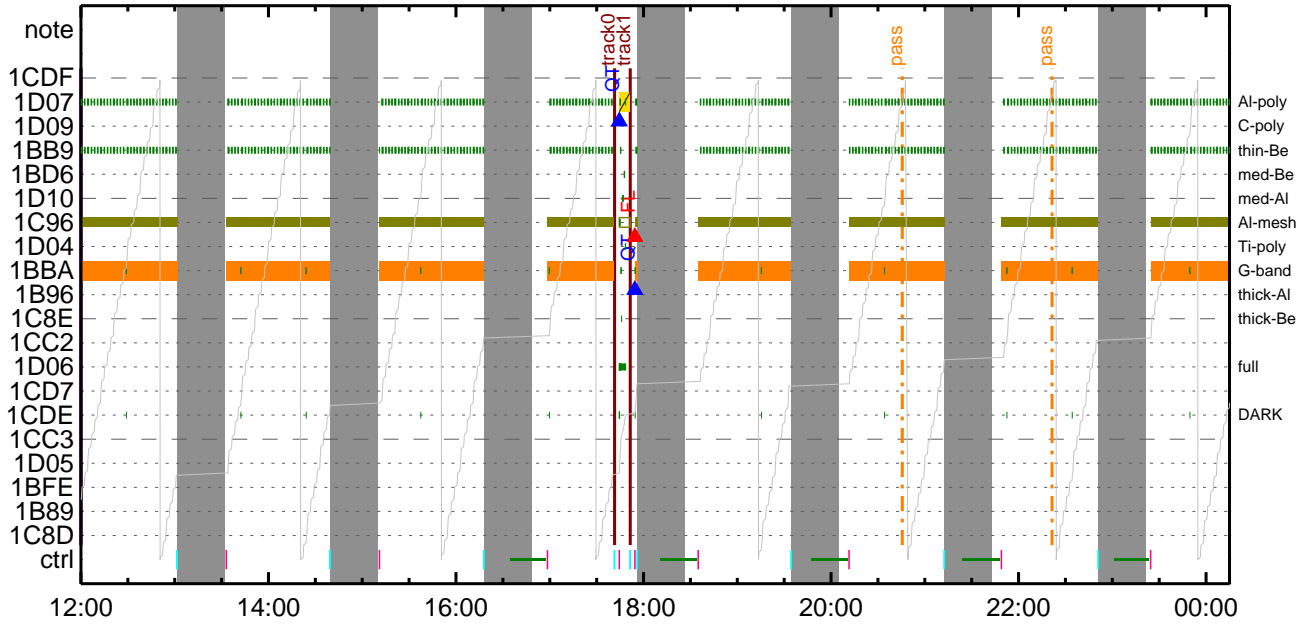
CMDI #0877 2024/06/08



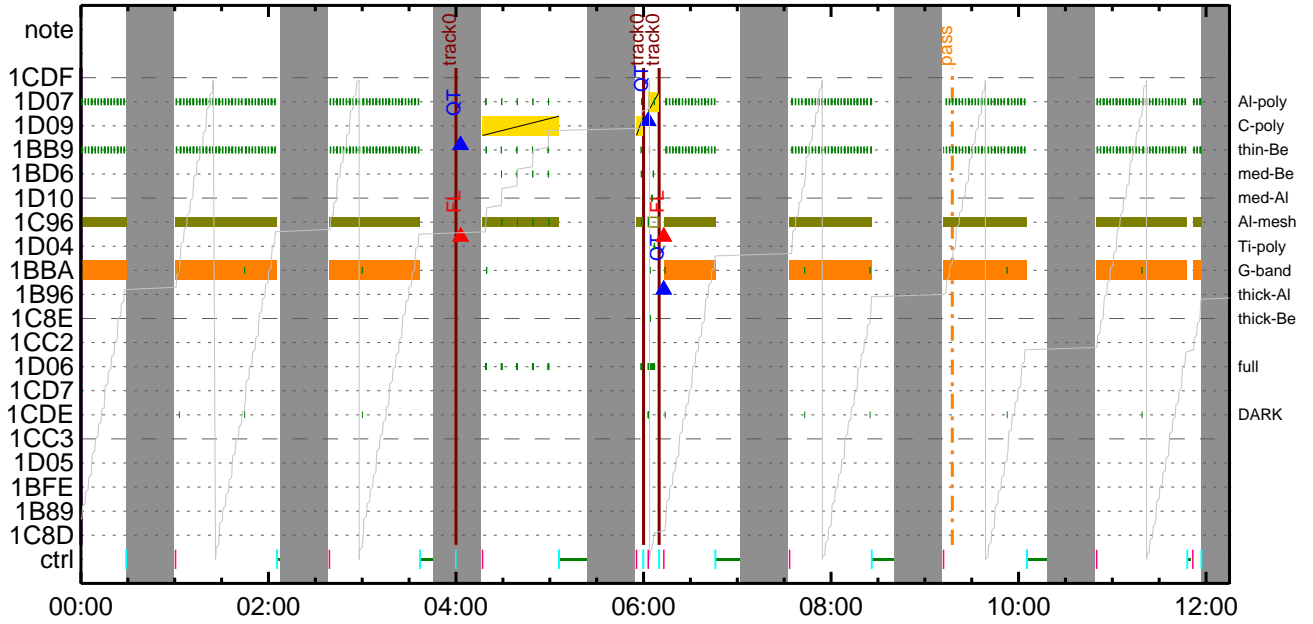
CMDI #0877 2024/06/09



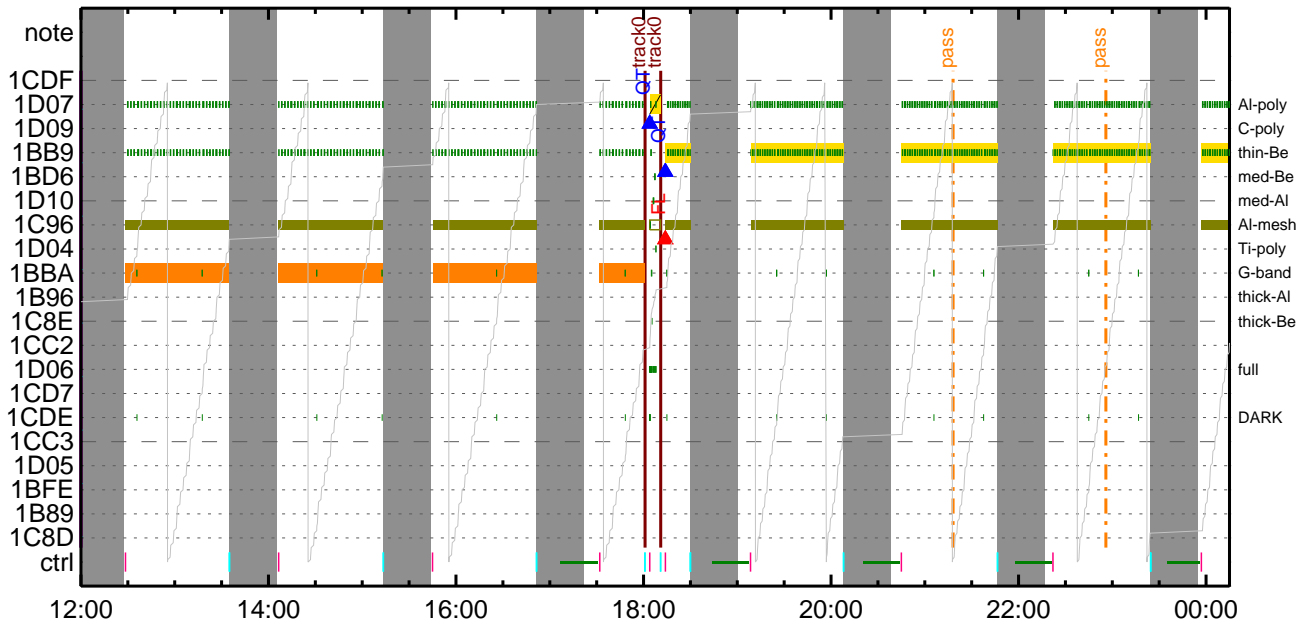
CMDI #0877 2024/06/09



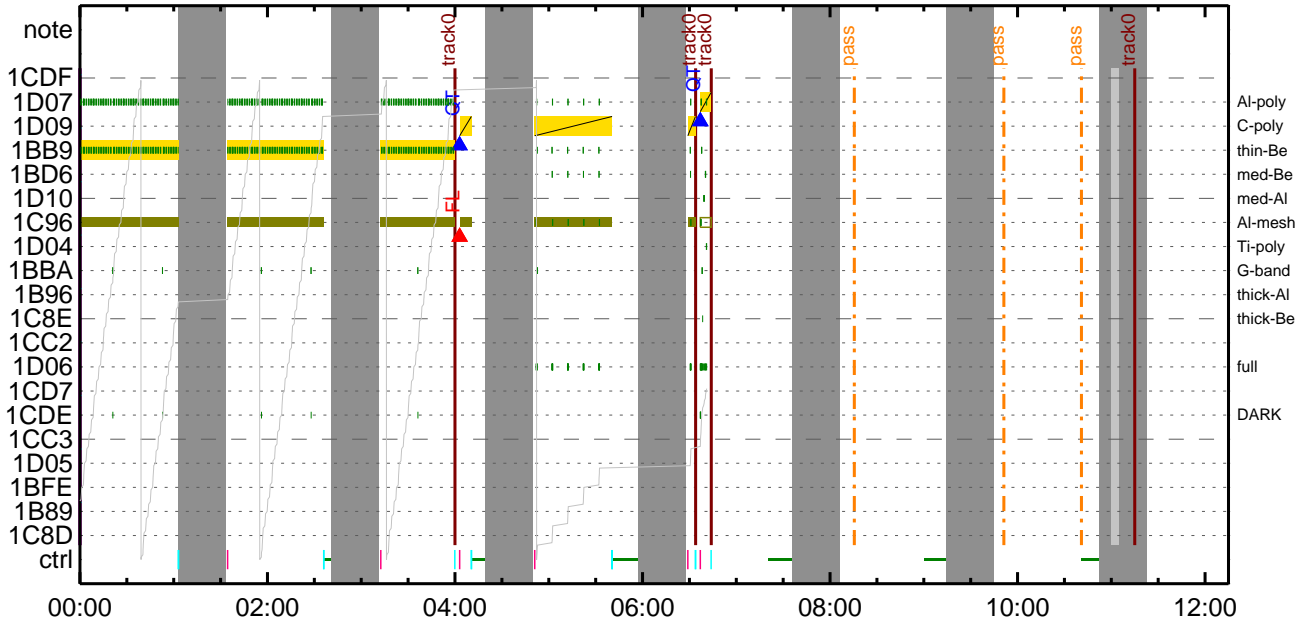
CMDI #0877 2024/06/10



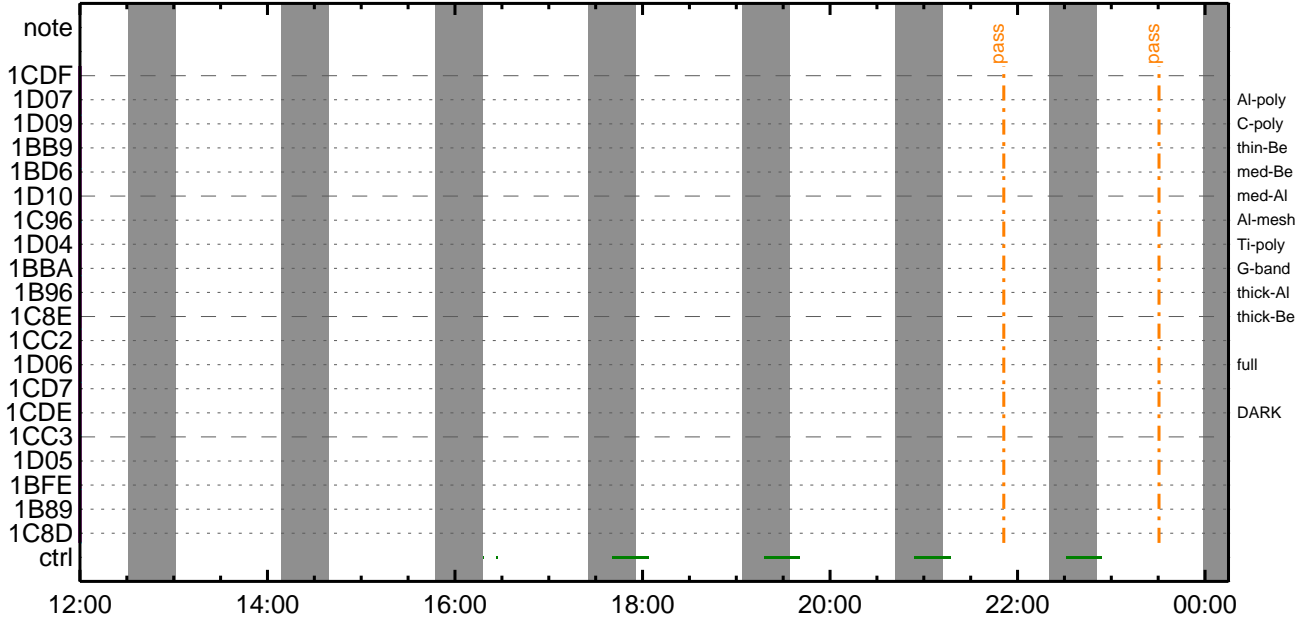
CMDI #0877 2024/06/10



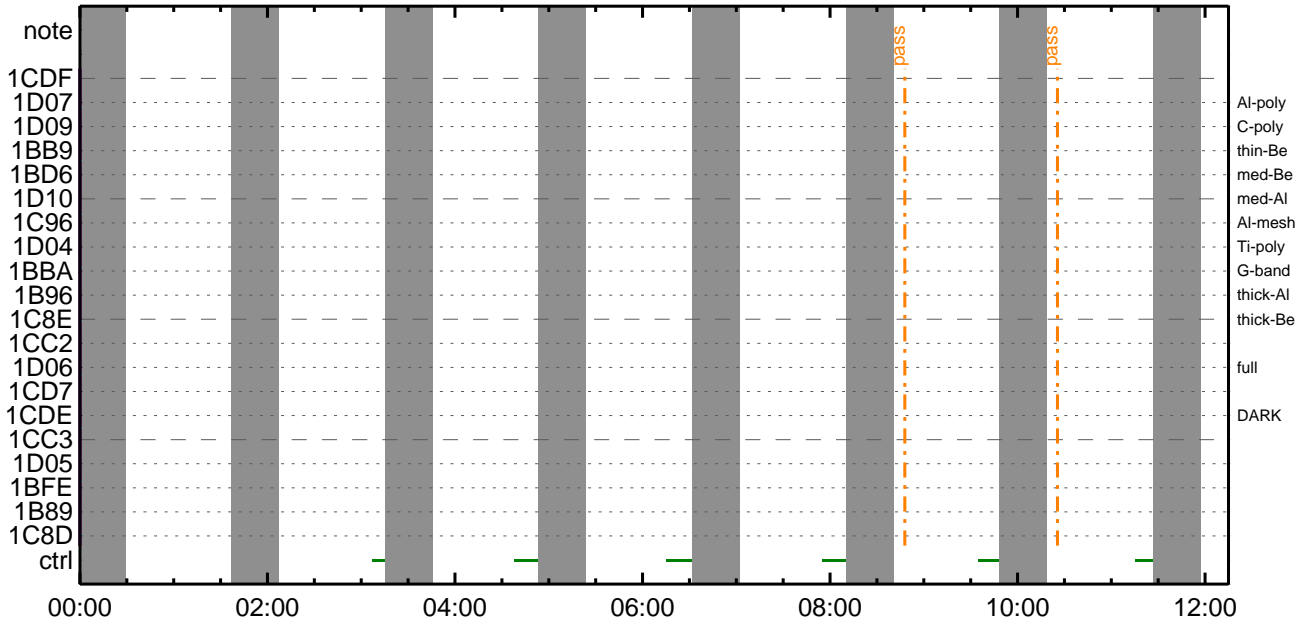
CMDI #0877 2024/06/11



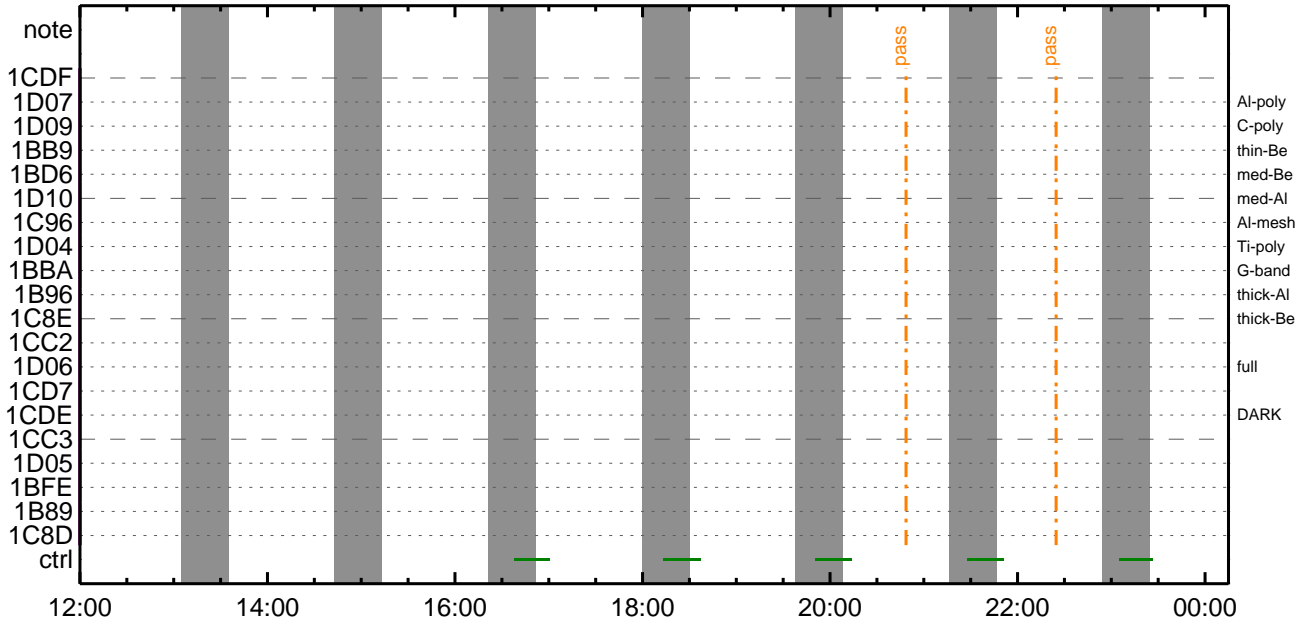
CMDI #0877 2024/06/11



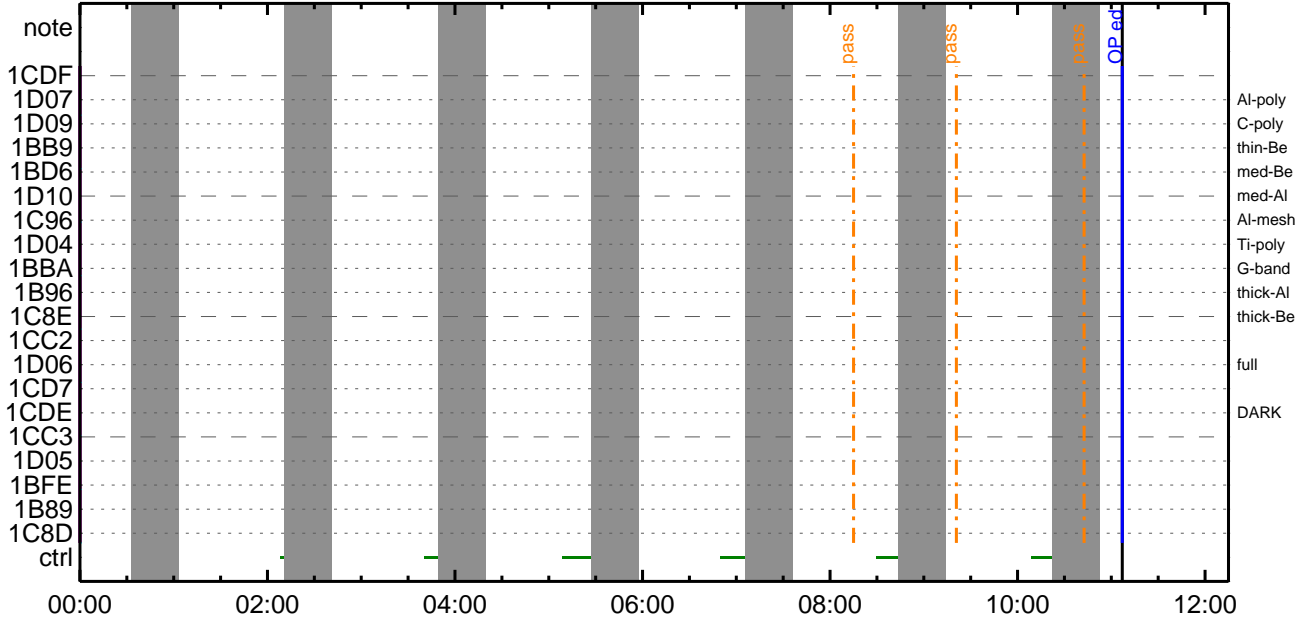
CMDI #0877 2024/06/12



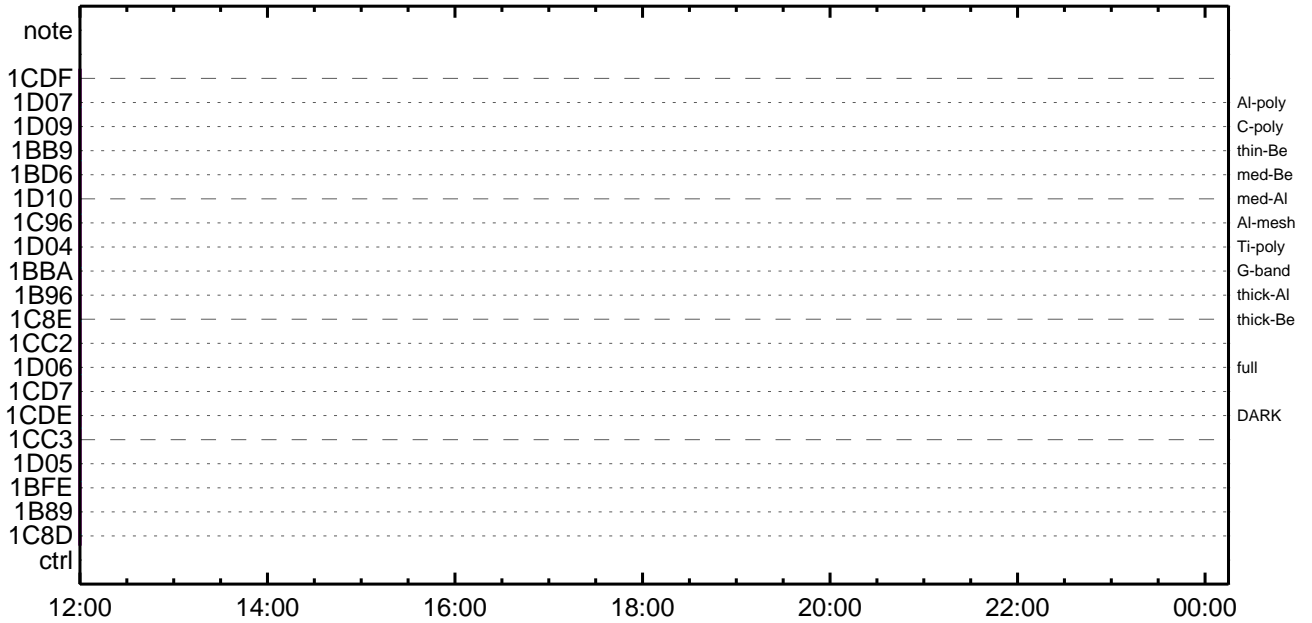
CMDI #0877 2024/06/12



CMDI #0877 2024/06/13



CMDI #0877 2024/06/13



(a) Spacecraft Operation Procedure (real-commands)

```
main-615 2024-06-08 11:48:19 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë>Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËÈµ•ÍÍË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÁ®, ùµ¹µÈµDµÇÁ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç° ÒÈÀ, Í×ÈYµÄLOSµDµÇµÍ»p´Öµò¹ÍÍ, µ•; ÇÉÖÍ×µÈXÁÓONµÍ¹ÖµÈµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY~¾ÖÁÖµ¬°ÄÄÈµ•µ¿µé; Ç°È²¼µÍ°ÈÀ, ¼È½Çµò¼Á¹Öµ¹µé; £
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÍ¼í°ÈÀ,
0033 C. *****
0034 C. Ç° RESTART;ËPT1;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°È²¼µÍ¼Á¹Öµµ°; ÇDCBC-150µØ¿Èµà; £
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈò;Ë, áµÍ°ÈÀ, °È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, µ¬¼«°È°Äá»ßµ•µ¿, á; Ç°È²¼µò¼Á¹Öµ¹µé; £
0055 C. YÇYÓYÉYËÁÚÁØµàÁ•Á°²óÈòµ¬¼áµ¾¼í¹ÇµÍ´°Í°Í»µ¹µÈµDµÇÁÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÍ¼í°ÈÀ,
0059 C. *****
0060 C. Ç° RESTART;ËPT2;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°È²¼µÍ¼Á¹Öµµ°; ÇDCBC-151µØ¿Èµà; £
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÁÚÁØ;ËÁ•Á°²óÈò;Ë, áµÍ°ÈÀ, °È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÀ, Áá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÀ, Áá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OG¶ı;¼¶É;ı;¼¶×
0100 C. *****
0101 C.
0102 . C. ;ãOP/OG¶ı;¼¶É;ã
0103 . S. OP      op-615:OP
0104 ()
0105 . S. OG      og-615:OG
0106 ()
0107 C.
0108 . C. ;ãNMOG&OPîî°e¼¶×;ã
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 C.                çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.                çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.                çç[HK1_DMP_BLOCK_NUM]      EQ     127
0115 C.                çç[HK1_DMP_REPEAT_NUM]     EQ      0
0116 C.                çç[HK1_DMA_DMP_PIM]        EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 C.                çç[HK1_PKT_FORM_NO]        EQ      7
0120 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0121 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0122 C.                çç[HK1_X_TLM_BIT_RATE]    EQ     4M
0123 C.                çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0124 . C. ¼¶×½ª î»²³ İÇŞ
0125 C.                çç[HK1_DMP_CHK_FLG]      EQ     NON
0126 . C. RAM ID=NMOG¼î¼Ë¹ç•ë² İOK²³ İÇŞ
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 C.                çç[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.                çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.                çç[HK1_DMP_BLOCK_NUM]      EQ     127
0134 C.                çç[HK1_DMP_REPEAT_NUM]     EQ      0
0135 C.                çç[HK1_DMA_DMP_PIM]        EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 C.                çç[HK1_PKT_FORM_NO]        EQ      7
0139 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0140 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0141 C.                çç[HK1_X_TLM_BIT_RATE]    EQ     4M
0142 C.                çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0143 . C. ¼¶×½ª î»²³ İÇŞ
0144 C.                çç[HK1_DMP_CHK_FLG]      EQ     NON
0145 . C. RAM ID=NMOG¼î¼Ë¹ç•ë² İOK²³ İÇŞ
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 C.                çç[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.                çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.                çç[HK1_DMP_BLOCK_NUM]      EQ     65
0153 C.                çç[HK1_DMP_REPEAT_NUM]     EQ      0
0154 C.                çç[HK1_DMA_DMP_PIM]        EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 C.                çç[HK1_PKT_FORM_NO]        EQ      7
0158 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.25 s
0159 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32k
0160 C.                çç[HK1_X_TLM_BIT_RATE]    EQ     4M
0161 C.                çç[HK1_DMP_CHK_FLG]      EQ     EXEC
0162 . C. ¼¶×½ª î»²³ İÇŞ
0163 C.                çç[HK1_DMP_CHK_FLG]      EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OP¼î¼Ë¹ç•ë² İOK²³ İÇŞ
0165 C.
0166 . C. ***** °Ë²¼¼İ¼Ā'¶Ā°ÉĒ¬°Ā÷ç® (¼Āµ-¼¶×½ª¼êç²²ĀĀæ²ç¼ªª°·²ë¼i¹ç²ç¼ª) *****
0167 C. DHU¼Ā;¼¶É;Ē¼ý½,¼ı;¼¶É;Ē²İĀ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 C.                çç[HK1_PKT_FORM_NO]        EQ      2
0171 C.                çç[HK1_PKT_GEN_TIME]      EQ     0.5S
0172 C.                çç[HK1_S_TLM_BIT_RATE]    EQ     32K
0173 C.                çç[HK1_X_TLM_BIT_RATE]    EQ     4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;S OPOG UPLOAD¼Ā÷ç®NG¼İ¼i¹ç;ç°Ë²¼¼İTI-CMDĀ÷ç®İ¼Ā¹Ô¼•¼Ē²²²³²ë;£
0180 C.                ²²²ç;çSET¼ĒDUMP¼İĒ±°i¼Œ¼¹ç¹Ô¼ı²³²ë;£
0181 C.
0182 . C. TI¼³¼Ē¼ó¼É²²Āİç (UT)
0183 +. TI 2024-06-08 11:29:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.                çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0186 C.
0187 +. TI 2024-06-08 11:29:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.                çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP
0190 C.
0191 +. TI 2024-06-08 11:29:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.                çç[HK1_TI_CMD_NUM]        EQ     1COUNTUP

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0194 C.
0195 +. TI 2024-06-08 11:33:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ☺☺ [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼ªîÄë¼îÑªîŷÄŷ$ŷÄŷ-¹ãîŰ
0200 C.          ☺☺ [HK1_TI_CMD_ENA/DIS]      EQ          ENA
0201 C.          ☺☺ [HK1_TI_CMD_NUM]          EQ          4
0202 C.          ☺☺ [HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          ☺☺ [HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷóŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ☺☺ [HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          ☺☺ [HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          ☺☺ [HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          ☺☺ [HK1_DMP_REPEAT_NUM]    EQ          0
0216 C.          ☺☺ [HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ☺☺ [HK1_PKT_FORM_NO]       EQ          7
0220 C.          ☺☺ [HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          ☺☺ [HK1_S_TLM_BIT_RATE]    EQ          32K
0222 C.          ☺☺ [HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          ☺☺ [HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ŷÄŷóŷ×½ªî»ªò³îÇ$
0226 C.          ☺☺ [HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLªîŷ¾¹Çªè²ìOKªò³îÇ$
0229 C.
0230 C. DHUŷª;¼ŷÉ;Ê¼ŷ½,ŷi;¼ŷÊ;Êªòîªª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ☺☺ [HK1_PKT_FORM_NO]       EQ          2
0234 C.          ☺☺ [HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          ☺☺ [HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          ☺☺ [HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C.
0239 C. ***** XRT START *****
0240 C. Execute, after the success of OP upload.
0241 +. TI 2024-06-08 11:33:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC      (c3)
0244 C.          [ ] [HK1_TI_CMD_NUM]       EQ          1COUNTUP
0245 C.
0246 C. ***** XRT END *****
0247 C. Stop EIS observation and temporarily disable EIS mode changes
0248 C.
0249 C.
0250 C. ***** Start EIS operation (TI set) *****
0251 C. Execute, after the success of OP upload.
0252 C. Set EIS TI-commands
0253 +. TI 2024-06-08 11:33:30.0
0254 DC 07-FC EIS_MODE_MANU
0255 BC      (21 02)
0256 +. TI 2024-06-08 11:33:40.0
0257 DC 07-FC EIS_MODE_CHG_DIS
0258 BC      (22)
0259 C.          [ ] [HK1_TI_CMD_NUM]       EQ          2 COUNTUP
0260 C. ***** End EIS operation (TI set) *****
0261 C.
0262 C.
0263 C.
0264 C. ***** MDP `ûÃîîî»ó¼ŷªÊÄª¹ªèDCBC•×²è *****
0265 C. (¾ª°îŷóŷÄŷÈŷPŷÈŷáŷçŷèªÈ¼ªª¼Äª»Üª¹ªè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷÐŷ¹•î Daily±;îÑªÈ'Øª¹ªèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;ãLOSŷÄŷ$ŷÄŷ-¼Äª»Ü;ã
0276 C.
0277 C. ***** LOS *****
0278 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-616 2024-06-08 11:48:19 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŚYÁY~¼Ã»Û;ã
0005 C.
0006 C. YÀYŞ;¼Y³YDÝÓYÉÁ÷¿©
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ēꝑ¿ꝑÃꝑ•µ°Ē»Í×ÁÇꝑĪY¿ÇYÁY×YÍ;¼YÉ;ĒĒĒµ•ĪÍĒ;ĒĒĒ¼°ÇÓꝑ•ꝑ¿¼Ī¹ÇꝑĪ; ÇÃ, ùꝑ¹ꝑĒꝑꝑÇÃ÷¿©ꝑ•ꝑĒꝑꝑꝑĒ; Æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿@µ;ON
0016 C. *****
0017 C. °ĒÀ, Ī×ĒÝꝑÀLOSꝑꝑÇꝑĪ»ꝑ´°ꝑòĪÍĪ, ꝑ•; ÇĒÓĪ×ꝑĒXÁÓONꝑĪ¹°ꝑĒꝑĪꝑĒꝑꝑꝑĒ; Æ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C.          ÇÇ [HK1_XPA_ON/OFF]          EQ       ON
0025 C.          ÇÇ [HK1_XPA_PWR_HI/LO]      EQ       HI
0026 C.          ÇÇ [HK1_XMOD_ON/OFF]        EQ       ON
0027 C.          ÇÇ [HK1_XMOD_QPSK/PM]       EQ       QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÁY~¼ÓÃÖꝑ¬°ÃĀĒꝑ•ꝑ¿ꝑ; Ç°Ē²¼ꝑĪ°ĒÀ, ¼Ē¼Çꝑò¼Ā¹°ꝑ¹ꝑĒ; Æ
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁĪ¼Ī°ĒÀ
0033 C. *****
0034 C. °Ē RESTART;ĒPT1;Ēꝑ•ꝑ¿ꝑ¼Ī¹ÇꝑĪ; Ç°Ē²¼ꝑĪ¼Ā¹°ꝑꝑꝑ; ÇDCBC-150ꝑø¿Ēꝑ; Æ
0035 C.
0036 . C. ;ãPT1°ĒÀ, ³«»Ī;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC          (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C.          ÇÇ [HK1_REP_PT_1/2]          EQ       PT1  (¼Ā¹°; ¼Ú)
0043 C.          ÇÇ [HK1_REP_STA/STP]        EQ       START (¼Ā¹°; ¼Ú)
0044 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ       ON    (¼Ā¹°; ¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYĒYÉÁÚÁØ;ĒÃ•Ã°²ÓĒò;Ē, áꝑĪ°ĒÀ, °Ē³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C.          ÇÇ [HK1_REP_PT_1/2]          EQ       PT1  (¼Ā¹°; ¼Ú)
0050 C.          ÇÇ [HK1_REP_STA/STP]        EQ       START (¼Ā¹°; ¼Ú)
0051 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ       ON    (¼Ā¹°; ¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ĒÀ, ꝑ¬¼«°Ēã»ꝑꝑꝑꝑ¿, á; Ç°Ē²¼ꝑò¼Ā¹°ꝑ¹ꝑĒ; Æ
0055 C. YÇYÓYĒYÉÁÚÁØꝑãÃ•Ã°²ÓĒòꝑ¬¾áꝑ¼Ī¹ÇꝑĪ´°Ī»ꝑ¹ꝑĒꝑꝑÇÃØꝑ; Æ
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁĪ¼Ī°ĒÀ
0059 C. *****
0060 C. °Ē RESTART;ĒPT2;Ēꝑ•ꝑ¿ꝑ¼Ī¹ÇꝑĪ; Ç°Ē²¼ꝑĪ¼Ā¹°ꝑꝑꝑ; ÇDCBC-151ꝑø¿Ēꝑ; Æ
0061 C.
0062 . C. ;ãPT2°ĒÀ, ³«»Ī;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC          (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C.          ÇÇ [HK1_REP_PT_1/2]          EQ       PT2  (¼Ā¹°; ¼Ú)
0069 C.          ÇÇ [HK1_REP_STA/STP]        EQ       START (¼Ā¹°; ¼Ú)
0070 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ       ON    (¼Ā¹°; ¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYĒYÉÁÚÁØ;ĒÃ•Ã°²ÓĒò;Ē, áꝑĪ°ĒÀ, °Ē³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C.          ÇÇ [HK1_REP_PT_1/2]          EQ       PT2  (¼Ā¹°; ¼Ú)
0076 C.          ÇÇ [HK1_REP_STA/STP]        EQ       START (¼Ā¹°; ¼Ú)
0077 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ       ON    (¼Ā¹°; ¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ĒÀ, Āã»ꝑ; ÇXÁ÷¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ĒÀ, Āã»ꝑ;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C.          ÇÇ [HK1_REP_STA/STP]        EQ       STOP
0087 C.          ÇÇ [HK1_S_VC4_ON/OFF]       EQ       OFF
0088 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ       OFF
0089 C.
0090 . C. ;ãXÁ÷¿@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C.          ÇÇ [HK1_XMOD_ON/OFF]        EQ       OFF
0095 C.          ÇÇ [HK1_XPA_ON/OFF]        EQ       OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 30s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2024-06-08 11:33:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAÎI»ö¼YnEÄDn¹nëDCBC•x²è *****
0156 C. (%â°I¥ÓYÄYÉYÐYËYÄYÇYÈnÈ¼n¼Ä»Û¹në)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•Ï Daily±çÍÑnÈ'Øn¹nëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Ä¥~¼Ä»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-617 2024-06-08 11:48:19 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÄŸ~¼Ä»Û;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Ëñ¿ñÄñ•µ°È»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèµ•íÍË;ËñÈ¼°ÇÖñ•ñ¿¼í¹ÇñÍ;çÄ®, ùñ¹ñèñDñÇÄ÷¿®ñ•ñËñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 + DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG_____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR _____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 03)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 0e)
0072 . C. ----- Success Verify ? OK / NG _____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2024-06-08 11:33:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG _____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP ´ûÄÍñÎ»ò¾ŸñËÄDñ¹ñèDCBC•×²è *****
0087 C. (¾â°íŸóŸÄŸËŸDŸËŸáŸçŸËñÈ¼¾ññ¼Ä»Ûñ¹ñè)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** ŸDŸ¹!•Ï Daily±¿;ÍññË´Øñ¹ñèDCBC•×²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
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0096 C.
0097 . C. ;ãLOS¥Á¥S¥Ã¥¼Å»Ü;ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

*** OP Sequence for XRT ***

2024/06/08	11:37:00.0	XRT_CTRL_MANU_442_OG [0x1ba]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	11:37:10.0	XRT_FOCUS_RECALIBRATE_405_OG [0x195]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2024/06/08	11:41:10.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/06/08	11:43:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	11:43:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	11:43:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/06/08	11:44:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 06 24 01 81
2024/06/08	11:44:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/06/08	11:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/06/08	11:44:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/06/08	11:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/08	11:44:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	11:46:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2024/06/08	11:46:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/08	11:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	12:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	12:27:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	12:27:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	12:27:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	12:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	12:58:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/08	12:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	14:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	14:06:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	14:06:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	14:06:06.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	14:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	14:36:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/08	14:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	15:44:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	15:44:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	15:44:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	15:44:06.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	15:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	16:14:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/08	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	17:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	17:22:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	17:22:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	17:22:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	17:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	18:00:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/06/08	18:01:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2024/06/08	18:01:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2024/06/08	18:01:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

2024/06/08	18:01:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/08	18:03:58.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2024/06/08	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	18:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	18:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/06/08	18:11:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 06 24 01 81
2024/06/08	18:11:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/06/08	18:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/06/08	18:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/06/08	18:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/08	18:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	18:13:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2024/06/08	18:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/08	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	19:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	19:00:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	19:00:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	19:00:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	19:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	19:37:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/08	19:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	20:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	20:39:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	20:39:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	20:39:06.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	20:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	21:14:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/08	21:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	22:17:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	22:17:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	22:17:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	22:17:06.5	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	22:20:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/08	22:51:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/08	22:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/08	23:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	23:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/08	23:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/08	23:55:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/08	23:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	00:26:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/09	00:27:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	01:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	01:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	01:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	01:33:36.0	XRT_PREFLR_STRT_403_OG [0x193]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	01:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	02:04:00.0	XRT_Custom_430_OG [0x1ae]			

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2024/06/09	02:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/09	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	03:04:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	03:04:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/09	03:04:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/09	03:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/09	03:42:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/09	03:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/09	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/06/09	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2024/06/09	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/06/09	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/06/09	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/06/09	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/09	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/09	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2024/06/09	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/06/09	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/09	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	04:36:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	04:36:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/09	04:36:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/09	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/09	05:20:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/09	05:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/09	05:44:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	05:44:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	05:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/06/09	05:45:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2024/06/09	05:45:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/06/09	05:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/06/09	05:45:22.0	XRT_ARS_DIS_435_OG [0x1b3] MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/09	05:47:58.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2024/06/09	05:48:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/09	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	05:54:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/09	05:54:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/06/09	05:55:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01	06	24	01	81
2024/06/09	05:55:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/06/09	05:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/06/09	05:55:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/06/09	05:55:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/09	05:55:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/09	05:57:56.0	XRT_QT_PROG_SET_447_OG [0x1bf] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2024/06/09	05:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/06/09	05:58:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/09	06:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							

2024/06/09	06:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	06:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	06:11:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	06:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	06:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	07:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	07:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	07:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	07:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	07:51:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	07:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	08:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	08:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	09:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	09:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	09:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	09:31:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	09:31:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	09:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	09:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	10:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	10:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	11:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	11:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	11:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	11:12:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	11:12:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	11:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	11:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	11:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	11:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	13:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	13:01:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	13:01:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	13:01:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	13:01:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	13:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	13:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	13:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	13:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	14:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	14:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	14:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	14:39:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	14:39:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	14:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	14:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	15:10:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	15:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	16:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	16:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	16:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	16:18:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	16:18:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	16:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	16:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	16:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	16:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	17:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	17:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	17:41:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

2024/06/09	17:41:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	17:41:30.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/06/09	17:41:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00
2024/06/09	17:41:50.5	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2024/06/09	17:41:52.5	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/06/09	17:44:28.5	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/09	17:44:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2024/06/09	17:51:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	17:51:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	17:51:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	17:51:30.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/06/09	17:51:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 06 24 01 81
2024/06/09	17:51:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2024/06/09	17:51:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/06/09	17:51:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2024/06/09	17:51:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/09	17:54:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	17:54:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2024/06/09	17:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/09	17:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	17:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	17:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	17:56:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	17:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	18:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	18:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/09	19:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2024/06/09	19:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	19:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	19:34:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	19:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	20:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	20:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	21:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2024/06/09	21:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	21:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	21:12:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	21:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	21:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	21:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/09	22:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2024/06/09	22:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/09	22:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	22:51:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/09	22:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/09	23:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/09	23:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/10	00:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1

2024/06/10	00:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	00:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/10	00:29:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/10	00:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/10	00:59:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	01:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/10	02:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	02:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	02:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/10	02:05:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/10	02:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/10	02:38:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	02:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/10	03:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	03:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	03:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/10	03:37:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/10	03:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/10	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/06/10	04:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2024/06/10	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/06/10	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/06/10	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/06/10	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/06/10	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/10	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12			
2024/06/10	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/06/10	04:16:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	04:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/10	05:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	05:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	05:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2024/06/10	05:06:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/06/10	05:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/06/10	05:54:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	05:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/10	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/06/10	06:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2024/06/10	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2024/06/10	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2024/06/10	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/06/10	06:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2024/06/10	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/06/10	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/06/10	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							

2024/06/10	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	00	20	18	b1 bb
2024/06/10	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2024/06/10	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2024/06/10	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2024/06/10	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2024/06/10	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	06:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	0c	
2024/06/10	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	0e	
2024/06/10	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2024/06/10	06:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	06:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	06:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	06:46:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2024/06/10	06:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2024/06/10	07:32:30.5	XRT_Custom_430_OG [0x1ae]							
2024/06/10	07:33:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2024/06/10	08:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	08:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	08:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	08:26:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2024/06/10	08:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2024/06/10	09:11:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	09:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2024/06/10	10:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	10:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	10:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	10:05:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2024/06/10	10:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2024/06/10	10:49:00.5	XRT_Custom_430_OG [0x1ae]							
2024/06/10	10:50:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2024/06/10	11:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	11:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	11:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	11:48:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2024/06/10	11:50:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	11:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2024/06/10	11:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2024/06/10	11:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	11:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	11:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	11:57:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2024/06/10	12:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2024/06/10	12:27:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/10	12:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2024/06/10	13:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	13:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2024/06/10	13:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2024/06/10	13:35:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2024/06/10	13:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2024/06/10	14:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/10	14:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/06/10	15:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/10	15:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	15:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/10	15:13:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/10	15:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/10	15:44:00.0	XRT_Custom_430_OG [0x1ae]								
2024/06/10	15:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/06/10	16:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/10	16:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	16:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/10	16:51:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/10	16:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/10	17:31:00.0	XRT_Custom_430_OG [0x1ae]								
2024/06/10	17:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/06/10	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	18:00:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/06/10	18:01:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2024/06/10	18:01:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/06/10	18:01:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/06/10	18:01:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/10	18:03:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/06/10	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/10	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	18:10:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	18:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/06/10	18:11:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 20 18 b1 bb				
2024/06/10	18:11:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/06/10	18:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/06/10	18:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/06/10	18:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/10	18:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/10	18:13:56.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2024/06/10	18:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/06/10	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/10	18:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	18:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	18:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/10	18:30:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/10	18:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/10	19:07:30.0	XRT_Custom_430_OG [0x1ae]								
2024/06/10	19:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/06/10	20:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	20:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/10	20:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/10	20:08:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2024/06/10	20:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/10	20:44:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/10	20:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/10	21:46:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/10	21:46:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/10	21:46:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/10	21:46:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/10	21:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/10	22:21:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/10	22:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/10	23:24:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/10	23:24:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/10	23:24:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/10	23:24:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/10	23:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/10	23:56:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/10	23:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	01:03:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	01:03:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	01:03:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	01:03:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/11	01:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/11	01:33:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/11	01:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	02:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	02:36:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	02:36:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	02:36:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/11	02:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/11	03:11:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/11	03:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/06/11	04:00:00.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2024/06/11	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2024/06/11	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/06/11	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2024/06/11	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/11	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2024/06/11	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/11	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	04:10:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	04:10:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	04:10:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	04:10:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/11	04:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/11	04:50:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/11	04:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	05:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			

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2024/06/11	05:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/11	05:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/11	05:40:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/06/11	05:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/06/11	06:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/06/11	06:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2024/06/11	06:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2024/06/11	06:33:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/06/11	06:33:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/11	06:34:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2024/06/11	06:34:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2024/06/11	06:34:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2024/06/11	06:34:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2024/06/11	06:36:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/06/11	06:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2024/06/11	06:43:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/06/11	06:44:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/11	11:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 20 18 b1 bb	
			AOCU_NM	5	02-76	00 00 00 00 00	