

XRT Timeline to be uploaded on 2024/06/11

Period: 2024/06/11 11:05:00 - 2024/06/15 10:36:00

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Normal mode

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XOB #1BB9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
06/11 11:24:00 - 06/11 17:25:30	Fixed (880.4, -361.0)	# OP start + 10min fixed pointing for AR13697 (limb obs.)
06/11 18:17:00 - 06/12 03:59:54	Fixed (880.4, -361.0)	# fixed pointingfor AR13697 (limb obs.)
06/12 06:01:30 - 06/12 17:44:24	Track (-207.7, -253.1) @ 06/12 05:58:30	# track AR13709
06/12 17:57:30 - 06/13 03:40:30	Track (-101.5, -254.2) @ 06/12 17:54:30	# track AR13709
06/13 06:16:30 - 06/13 08:00:00	Track (9.2, -255.3) @ 06/13 06:13:30	# track AR13709

PROG= 17 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
Seqn= 92	1-time(s)	2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	5-time(s)	2.0sec											
Seqn= 47	1-time(s)	2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96	4-time(s)	90.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	1	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)

Term	Pointing (x, y)	Comment
06/11 18:07:00 - 06/11 18:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min
06/12 05:51:30 - 06/12 05:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min
06/12 17:47:30 - 06/12 17:54:24	Fixed (0.0, 0.0)	synoptic, shifted -15.5 min
06/13 06:06:30 - 06/13 06:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min

PROG= 19 1-time(s)

Subr= 1	1-time(s)	2.0sec											
Seqn= 5	1-time(s)	2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26	1-time(s)	2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83	1-time(s)	2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23	1-time(s)	4.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec											
Seqn= 41	1-time(s)	2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17	1-time(s)	2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33	1-time(s)	2.0sec											
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75	1-time(s)	2.0sec											
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wat

Term	Pointing (x, y)	Comment
06/12 04:03:00 - 06/12 05:48:24	Fixed (0.0, 0.0)	HOP 349
06/13 04:20:30 - 06/13 06:03:24	Fixed (0.0, 0.0)	HOP 349

PROG= 18 Inf.-time(s)

Subr= 1	1-time(s)	600.0sec
Seqn= 55	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2ms
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms
Seqn= 98	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 5ms
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s
Seqn= 79	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 16ms
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s
Seqn= 30	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms
Open/G-band	Open/G-band close	Safe Norm 1ms

Subr= 2	7-time(s)	600.0sec
Seqn= 8	1-time(s)	2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s
Seqn= 74	1-time(s)	2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 500ms
med-Be/Open	med-Be/Open close	Safe Norm 2.00s
Seqn= 6	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s
Seqn= 29	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
06/11 11:24:00 - 06/11 17:25:30	Fixed (880.4, -361.0)	# OP start + 10min fixed pointing for AR13697 (limb obs.)
06/11 18:17:00 - 06/12 03:59:54	Fixed (880.4, -361.0)	# fixed pointingfor AR13697 (limb obs.)
06/12 04:03:00 - 06/12 05:48:24	Fixed (0.0, 0.0)	HOP 349
06/12 06:01:30 - 06/12 17:44:24	Track (-207.7, -253.1) @ 06/12 05:58:30	# track AR13709
06/12 17:57:30 - 06/13 03:40:30	Track (-101.5, -254.2) @ 06/12 17:54:30	# track AR13709
06/13 04:20:30 - 06/13 06:03:24	Fixed (0.0, 0.0)	HOP 349
06/13 06:16:30 - 06/13 08:00:00	Track (9.2, -255.3) @ 06/13 06:13:30	# track AR13709

PROG= 14 30-time(s)

Subr= 1	20-time(s)	2.0sec
Seqn= 11	1-time(s)	2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms
Seqn= 73	1-time(s)	10.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 125ms
med-Be/Open	Open/thick-Al close	Safe Norm 250ms
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s
Subr= 2	1-time(s)	2.0sec
Seqn= 10	1-time(s)	2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s
Seqn= 11	1-time(s)	2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms
Seqn= 87	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms
Open/G-band	Open/G-band close	Safe Norm 1ms
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s
Open/thick-Al	Open/thick-Al close	Safe Dark 1.00s

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Active Region Search

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NOT USED

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Flare Detection

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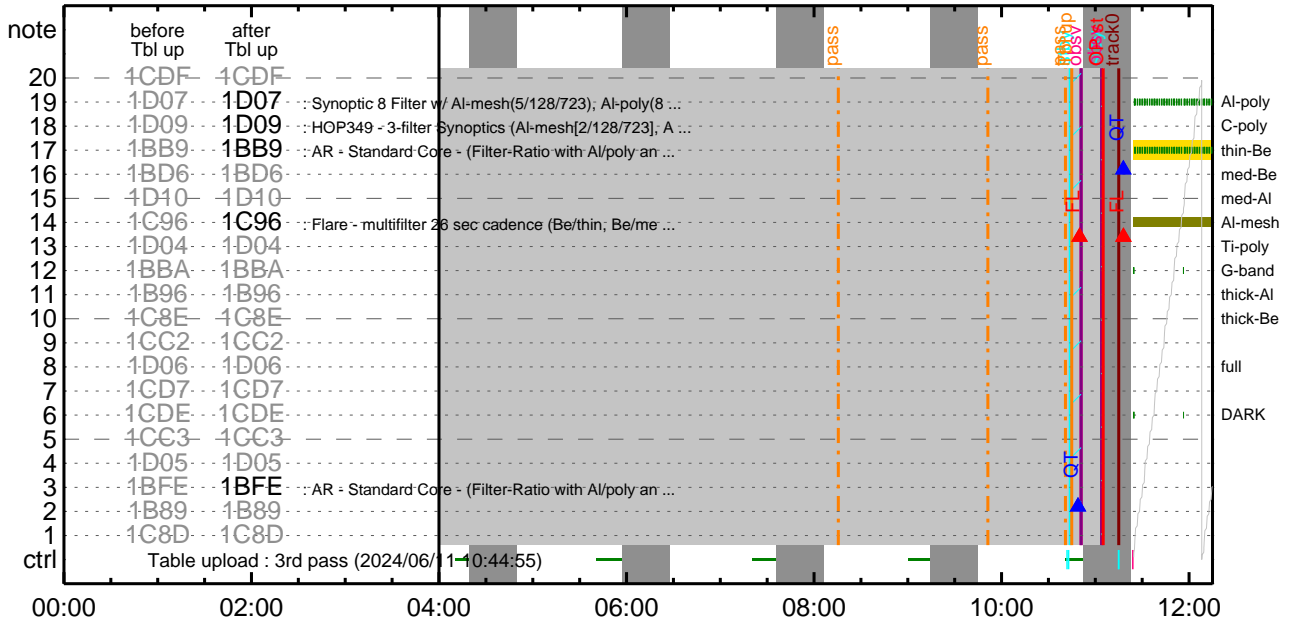
FLD Patrol

Term	Pointing (x, y)	Comment
06/11 10:45:55 - 06/11 18:04:18	cannot be identified	

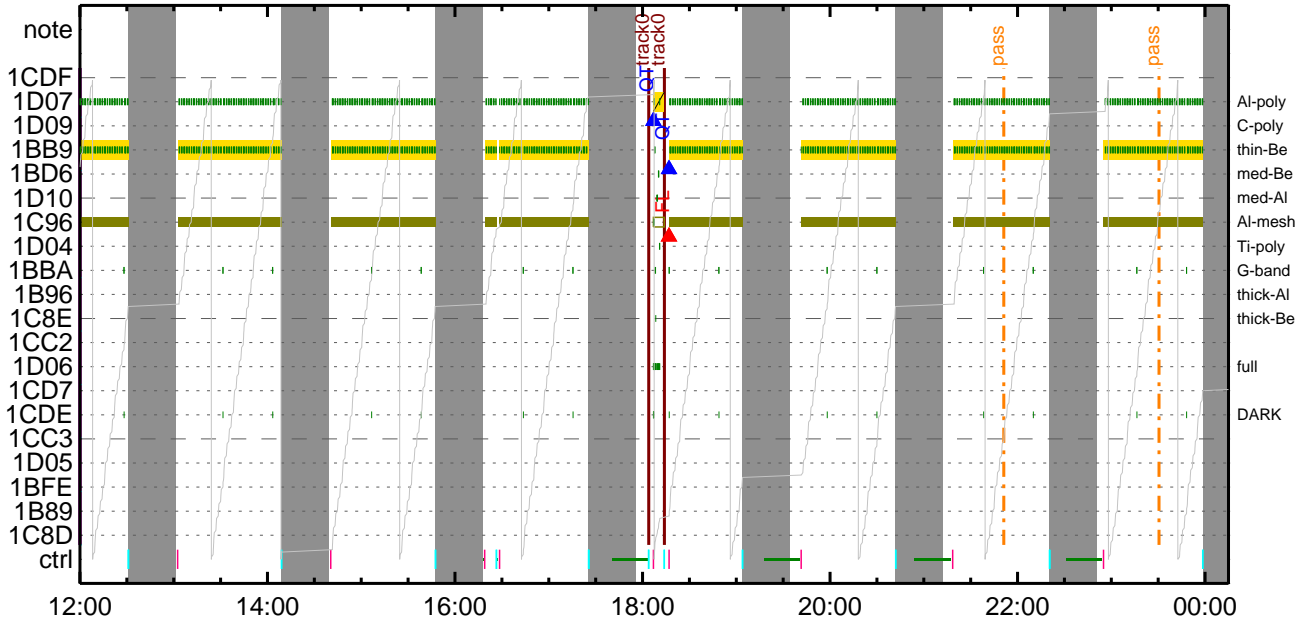
06/11 18:14:18 - 06/12 05:48:48 Fixed (880.4, -361.0) # fixed pointingfor AR13697 (limb obs.)
 06/12 05:58:48 - 06/12 17:44:48 Track (-207.7, -253.1) @ 06/12 05:58:30 # track AR13709
 06/12 17:54:48 - 06/13 06:03:48 Track (-101.5, -254.2) @ 06/12 17:54:30 # track AR13709
 06/13 06:13:48 - 06/15 10:36:00 Track (9.2, -255.3) @ 06/13 06:13:30 # track AR13709

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

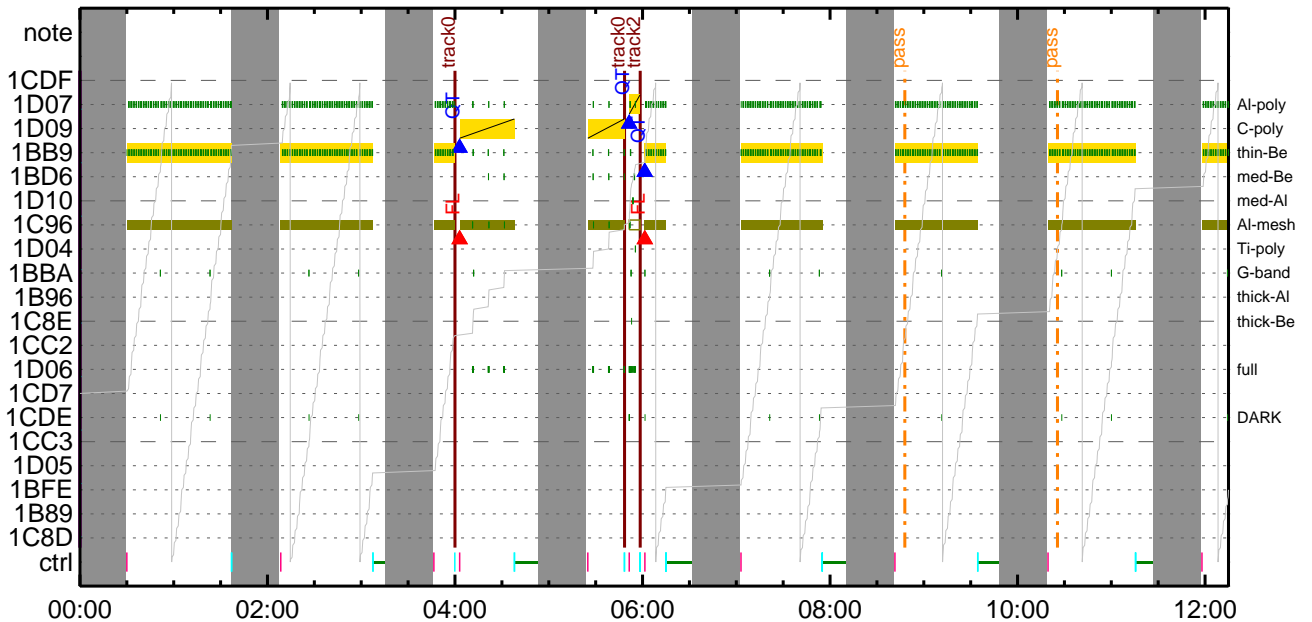
CMDI #0886 2024/06/11



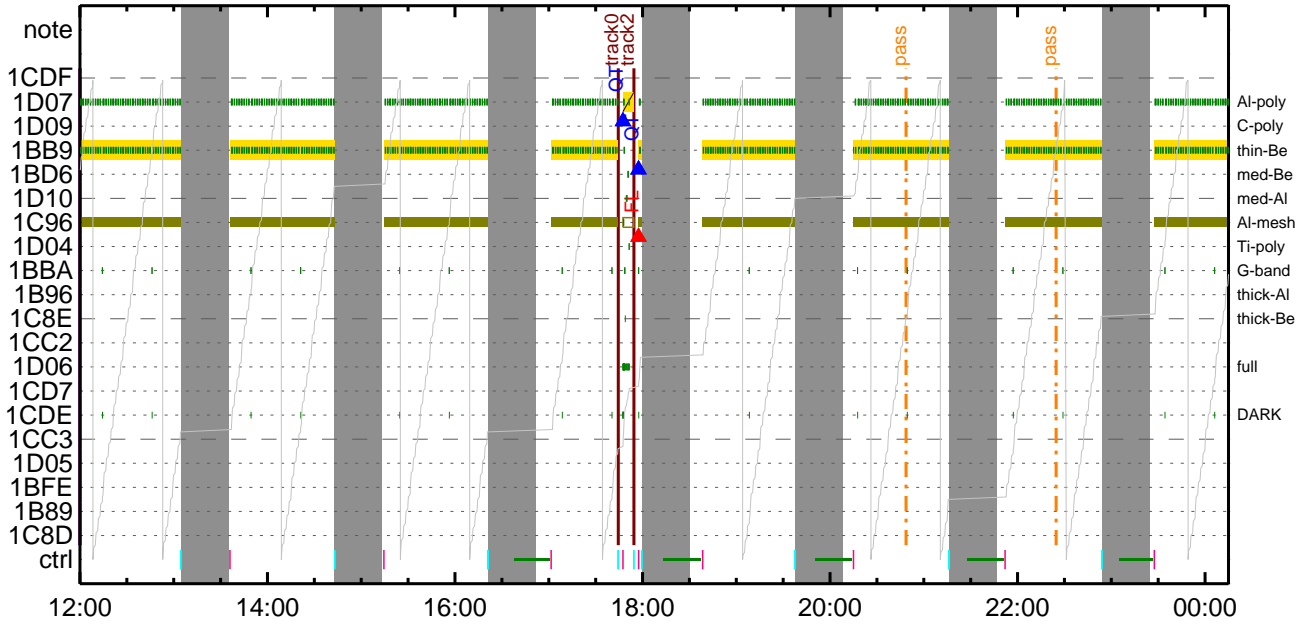
CMDI #0886 2024/06/11



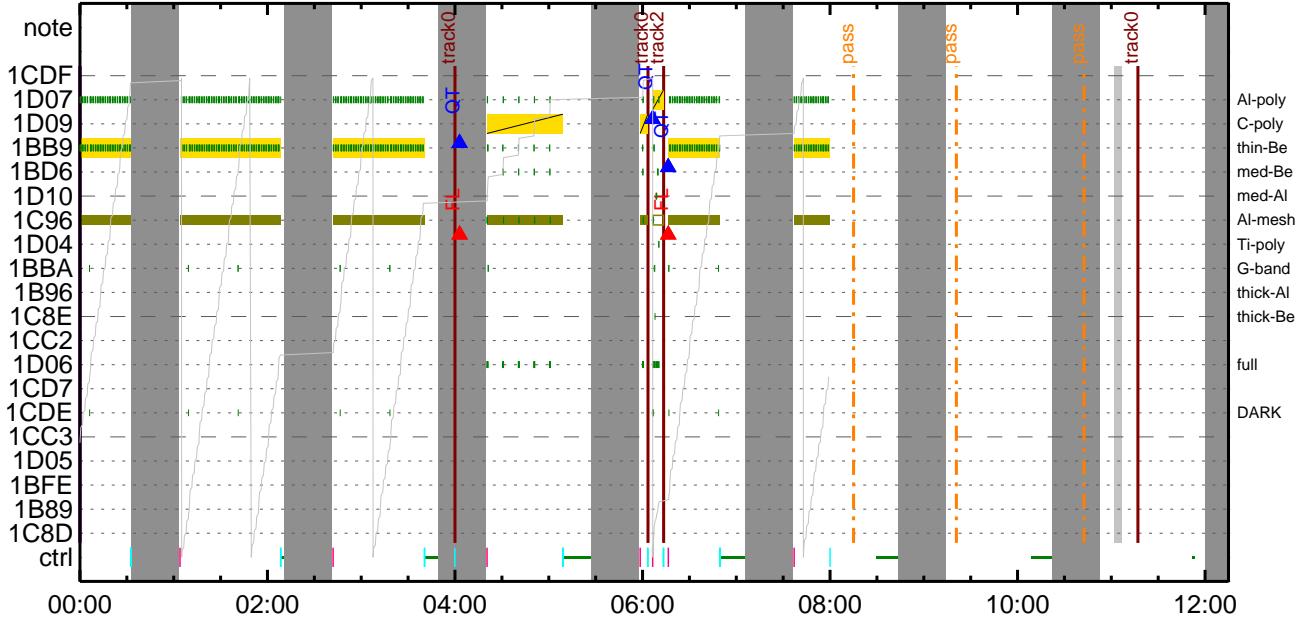
CMDI #0886 2024/06/12



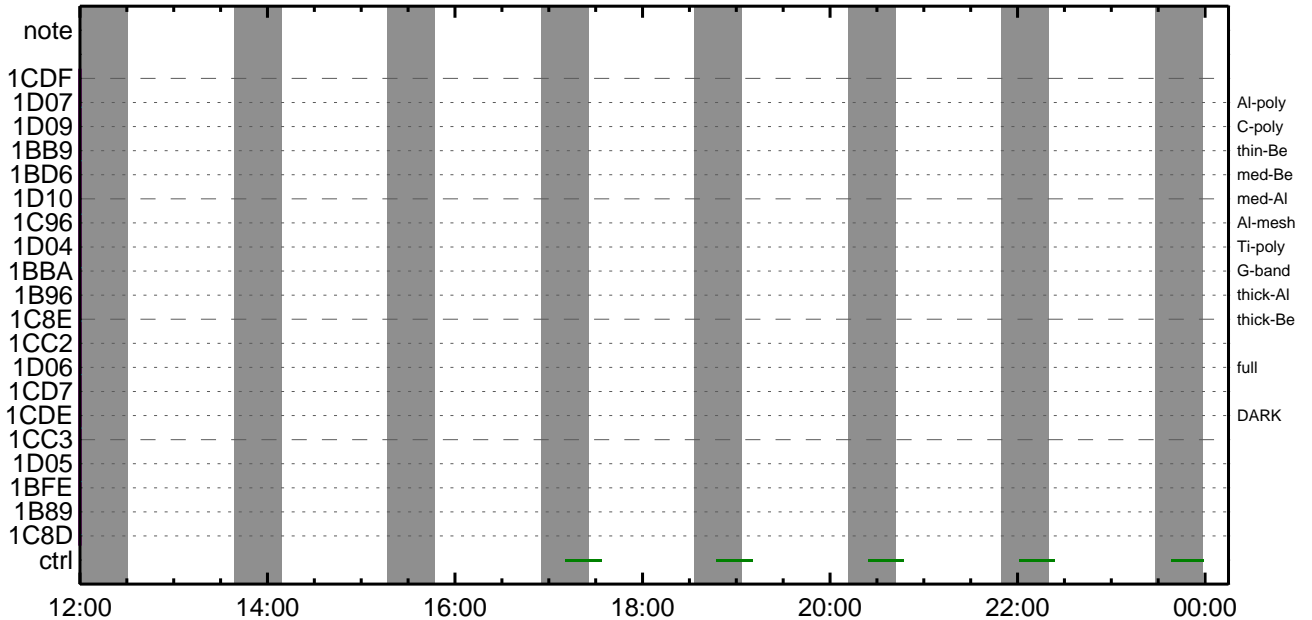
CMDI #0886 2024/06/12



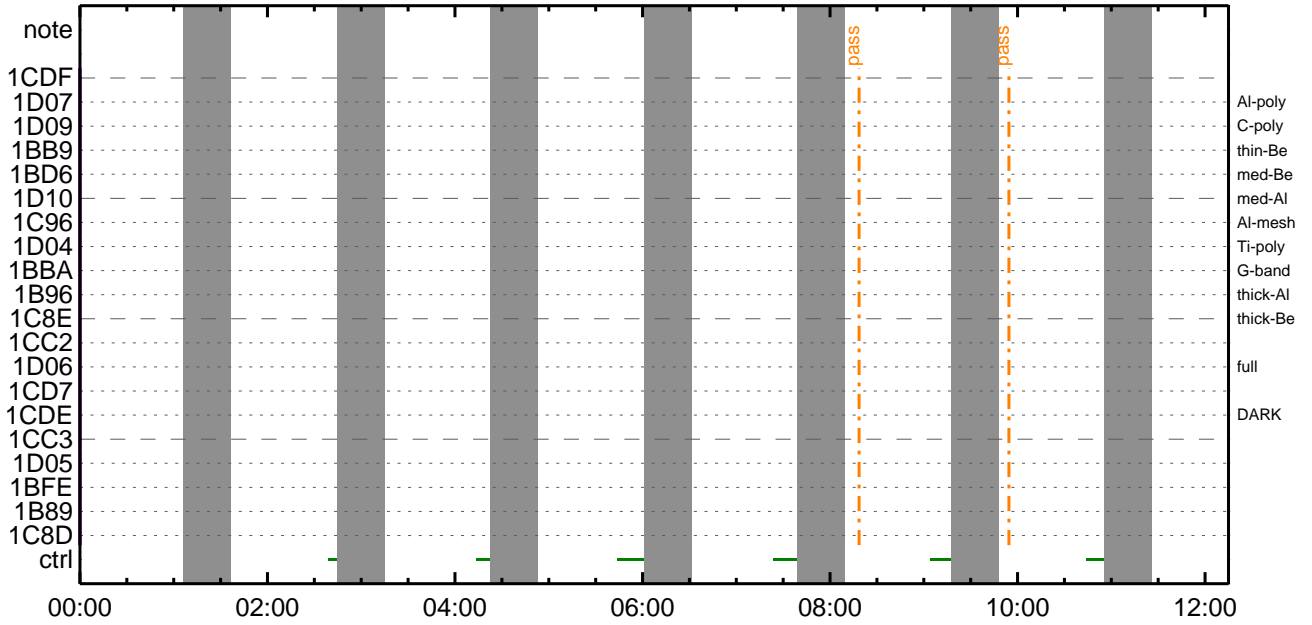
CMDI #0886 2024/06/13



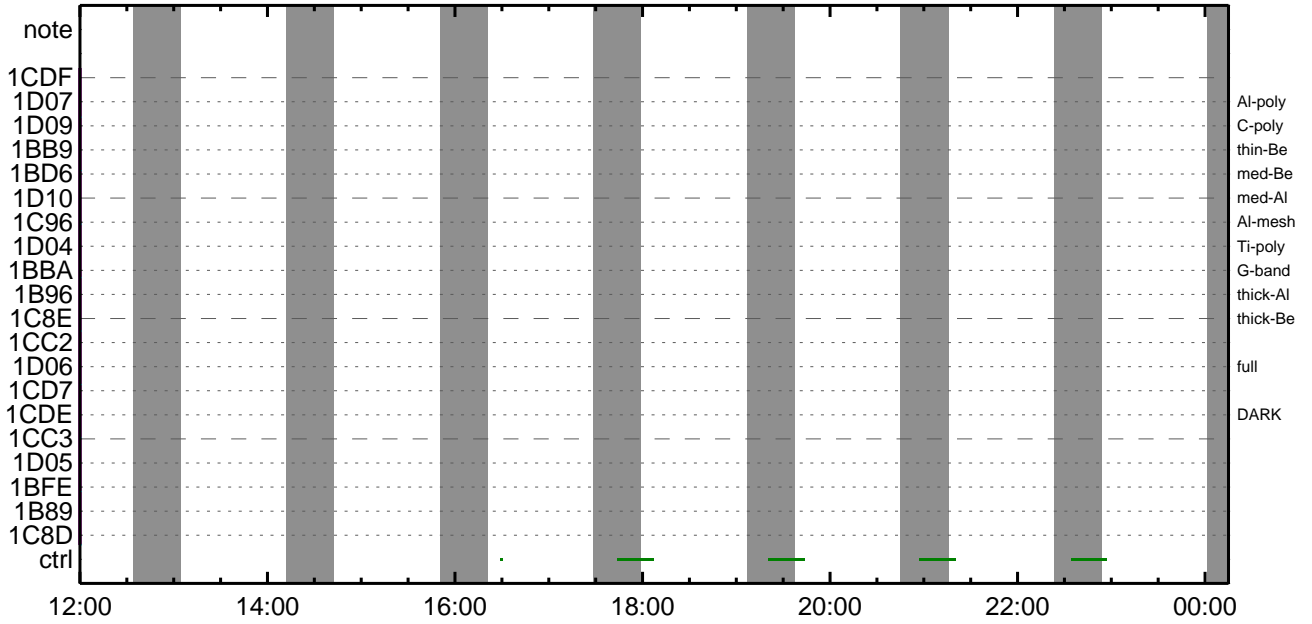
CMDI #0886 2024/06/13



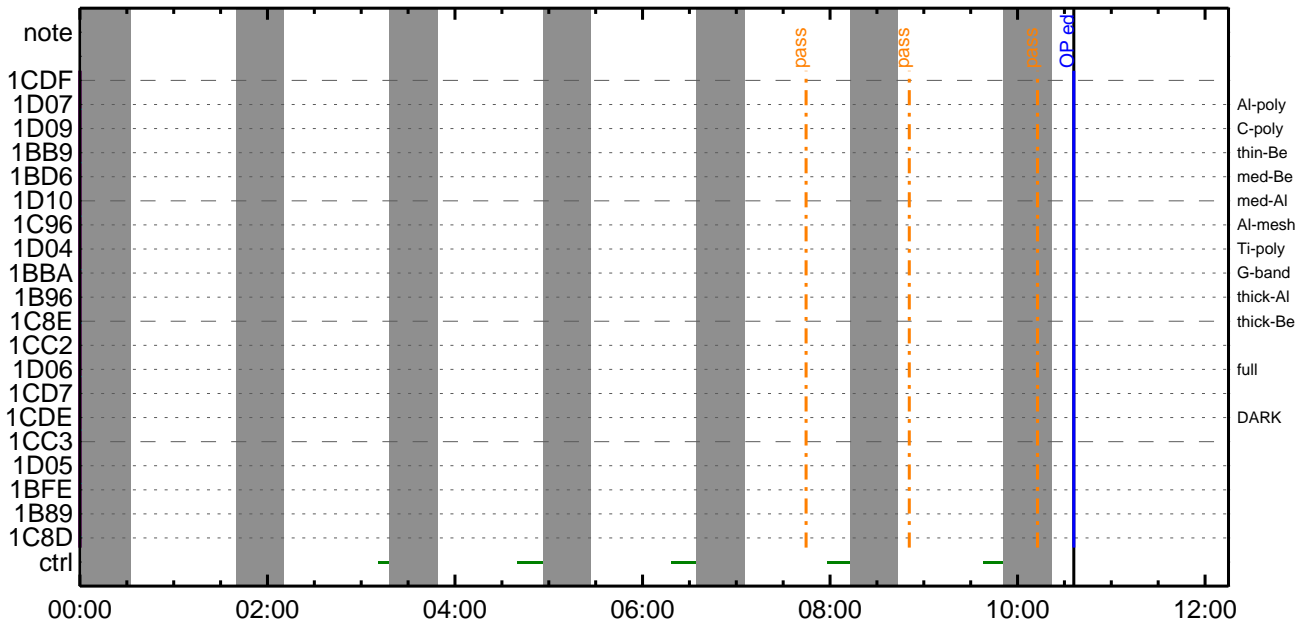
CMDI #0886 2024/06/14



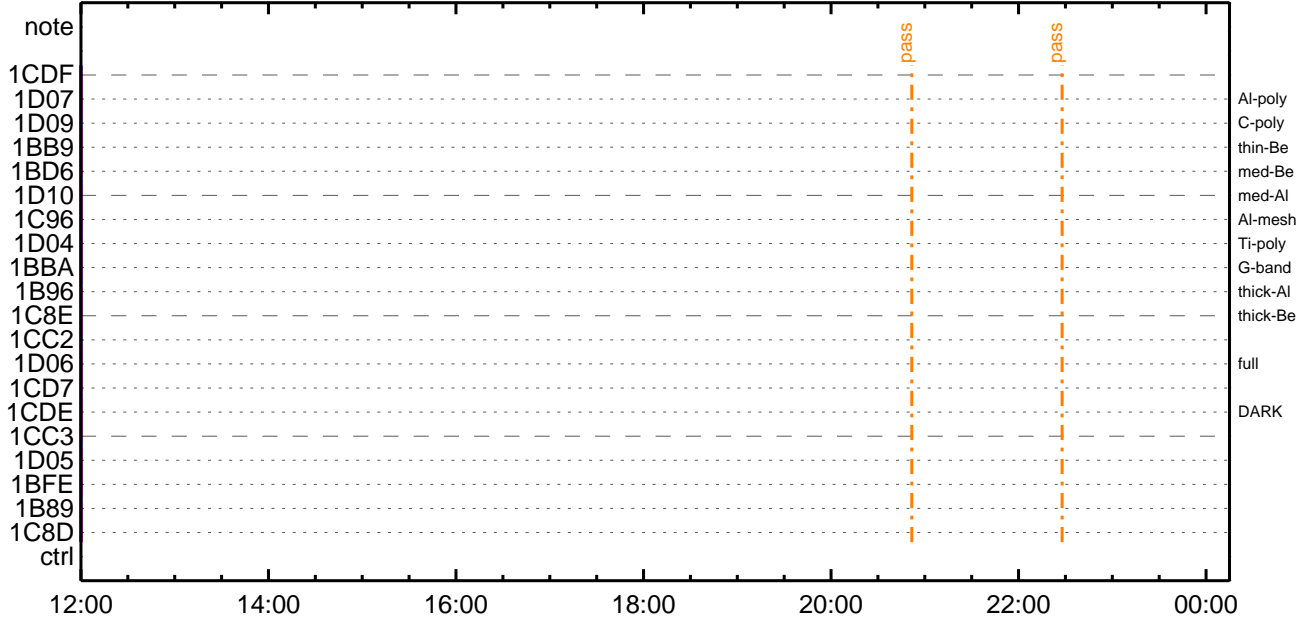
CMDI #0886 2024/06/14



CMDI #0886 2024/06/15



CMDI #0886 2024/06/15



(a) Spacecraft Operation Procedure (real-commands)

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main-620 2024-06-11 12:57:40 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Ä»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝóYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°E»Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËËµ•íÍË;ËËË¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÄ®, ùµ¹µÈµDµÇÄ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç " °EÀ, Í×ËYµÄLOSµDµÇµÍ»p´Öµò¹íÍ, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝóYÉYíYÁY~¾ÖÄÖµ¬°ÄÄêµ•µ¿µé; Ç°Ê²¼µÍ°EÀ, ¼ê¼Çµò¼Ä¹Öµ¹µé; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÀ,
0033 C. *****
0034 C. Ç " RESTART;ËPT1;Ëµ•µ¿µµ¼í¹ÇµÍ; Ç°Ê²¼µÍ¼Ä¹Öµµ°; ÇDCBC-150µØ¿Ëµà; £
0035 C.
0036 . C. ;ãPT1°EÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYóYÉYËÄÜÄØ;ËÄ•Ä°²óÈò;Ë, áµÍ°EÀ, °E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÀ, µ¬¼«°E°Äá»ßµ•µ¿, á; Ç°Ê²¼µò¼Ä¹Öµ¹µé; £
0055 C. YÇYóYÉYËÄÜÄØµÄÄ•Ä°²óÈòµ¬¼áµµ¼í¹ÇµÍ´°Í»µ¹µÈµDµÇÄÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÀ,
0059 C. *****
0060 C. Ç " RESTART;ËPT2;Ëµ•µ¿µµ¼í¹ÇµÍ; Ç°Ê²¼µÍ¼Ä¹Öµµ°; ÇDCBC-151µØ¿Ëµà; £
0061 C.
0062 . C. ;ãPT2°EÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYóYÉYËÄÜÄØ;ËÄ•Ä°²óÈò;Ë, áµÍ°EÀ, °E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÀ, Äá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÀ, Äá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF

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0096 C.
0097 C.
0098 . C. *****
0099 C. OP/OGY1;4YE;4Y6Yx
0100 C. *****
0101 C.
0102 . C. ;aOP/OGY1;4YE;a
0103 . S. OP op-620:OP
0104 ()
0105 . S. OG og-620:OG
0106 ()
0107 C.
0108 . C. ;aNMOG&OPi^eYAY6Yx;a
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.                c[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.                c[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.                c[HK1_DMP_BLOCK_NUM]       EQ     127
0115 C.                c[HK1_DMP_REPEAT_NUM]      EQ      0
0116 C.                c[HK1_DMA_DMP_PIM]         EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.                c[HK1_PKT_FORM_NO]         EQ      7
0120 C.                c[HK1_PKT_GEN_TIME]        EQ     0.25 s
0121 C.                c[HK1_S_TLM_BIT_RATE]      EQ     32k
0122 C.                c[HK1_X_TLM_BIT_RATE]      EQ      4M
0123 C.                c[HK1_DMP_CHK_FLG]         EQ     EXEC
0124 . C. YAY6Yx2^i>=0^3iCS
0125 C.                c[HK1_DMP_CHK_FLG]         EQ     NON
0126 . C. RAM ID=NMOG=i3E^1c•e^2iOK=0^3iCS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.                c[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.                c[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.                c[HK1_DMP_BLOCK_NUM]       EQ     127
0134 C.                c[HK1_DMP_REPEAT_NUM]      EQ      0
0135 C.                c[HK1_DMA_DMP_PIM]         EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.                c[HK1_PKT_FORM_NO]         EQ      7
0139 C.                c[HK1_PKT_GEN_TIME]        EQ     0.25 s
0140 C.                c[HK1_S_TLM_BIT_RATE]      EQ     32k
0141 C.                c[HK1_X_TLM_BIT_RATE]      EQ      4M
0142 C.                c[HK1_DMP_CHK_FLG]         EQ     EXEC
0143 . C. YAY6Yx2^i>=0^3iCS
0144 C.                c[HK1_DMP_CHK_FLG]         EQ     NON
0145 . C. RAM ID=NMOG=i3E^1c•e^2iOK=0^3iCS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.                c[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.                c[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.                c[HK1_DMP_BLOCK_NUM]       EQ     65
0153 C.                c[HK1_DMP_REPEAT_NUM]      EQ      0
0154 C.                c[HK1_DMA_DMP_PIM]         EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.                c[HK1_PKT_FORM_NO]         EQ      7
0158 C.                c[HK1_PKT_GEN_TIME]        EQ     0.25 s
0159 C.                c[HK1_S_TLM_BIT_RATE]      EQ     32k
0160 C.                c[HK1_X_TLM_BIT_RATE]      EQ      4M
0161 C.                c[HK1_DMP_CHK_FLG]         EQ     EXEC
0162 . C. YAY6Yx2^i>=0^3iCS
0163 C.                c[HK1_DMP_CHK_FLG]         EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OP=i3E^1c•e^2iOK=0^3iCS
0165 C.
0166 . C. ***** °E^24i34A^1A^oE^E^~oA^:z® (34u-YAY6Yx4e2c=0A0Aæc2^a^~^e34i^1c=2c^a) *****
0167 C. DHU=â;4YE;E4Y2;Y1;4YE;E=0i^a^1
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.                c[HK1_PKT_FORM_NO]         EQ      2
0171 C.                c[HK1_PKT_GEN_TIME]        EQ     0.5S
0172 C.                c[HK1_S_TLM_BIT_RATE]      EQ     32K
0173 C.                c[HK1_X_TLM_BIT_RATE]      EQ      4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;S OPOG UPLOAD=i3E^1c;°E^24iTI-CMDA^:z®i34A^1O•mE^m^3E;£
0180 C.                mP=;cSET=EDUMP=iE+°iY^1c^1O;^3E;£
0181 C.
0182 . C. TIY^3YD4Y6YE^m0Adi^z (UT)
0183 +. TI 2024-06-11 11:00:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.                c[HK1_TI_CMD_NUM]          EQ     1COUNTUP
0186 C.
0187 +. TI 2024-06-11 11:00:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.                c[HK1_TI_CMD_NUM]          EQ     1COUNTUP
0190 C.
0191 +. TI 2024-06-11 11:00:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.                c[HK1_TI_CMD_NUM]          EQ     1COUNTUP
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0194 C.
0195 +. TI 2024-06-11 11:04:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼¼ñîÄë¾íññîîÿÄÿ\$ÿÄÿ¹àîÛ
0200 C. ¢¢[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. ¢¢[HK1_TI_CMD_NUM] EQ 4
0202 C. ¢¢[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. ¢¢[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°èÿÄÿÓÿ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0220 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ÿÄÿÓÿ×½ªî»ñò³îÇ\$
0226 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLñî¾Ê¹Ç•è²îOKñò³îÇ\$
0229 C.
0230 . C. DHUÿâ;¼ÿÉ;Ê¼ÿ½;ÿi;¼ÿË;Ëñòîáñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0234 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C.
0239 C. ***** XRT START *****
0240 C. Execute, after the success of OP upload.
0241 +. TI 2024-06-11 11:04:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC (c3)
0244 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0245 C.
0246 C. ***** XRT END *****
0247 . C. Stop EIS observation and temporarily disable EIS mode changes
0248 C.
0249 C.
0250 C. ***** Start EIS operation (TI set) *****
0251 C. Execute, after the success of OP upload.
0252 C. Set EIS TI-commands
0253 +. TI 2024-06-11 11:04:30.0
0254 DC 07-FC EIS_MODE_MANU
0255 BC (21 02)
0256 +. TI 2024-06-11 11:04:40.0
0257 DC 07-FC EIS_MODE_CHG_DIS
0258 BC (22)
0259 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0260 C. ***** End EIS operation (TI set) *****
0261 C.
0262 C.
0263 C.
0264 . C. ***** MDP ´ñÄîñî»ó¾ÿñÊÄñ¹ñèDCBC•×²è *****
0265 C. (¾ª°îÿÓÿÄÿËÿËÿËÿáÿçÿèñ¾¾¾ñ¾¾¾Ä»ñ¹ñè)
0266 . S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 . C. ***** ÿÐÿ¹•î Daily±;îññË´Øñ¹ñèDCBC•×²è *****
0271 . S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 . C. ;ãLOSÿÄÿ\$ÿÿÄÿ¹¼Ä»Û;ã
0276 C.
0277 . C. ***** LOS *****
0278 C.

(a) Spacecraft Operation Procedure (real-commands)

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main-621 2024-06-11 12:57:40 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥S¥Á¥~¼Ä»Û;ä
0005 C.
0006 C. ¥À¥ß;¼¥³¥Ð¥ó¥ÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄµ•µ°Ë>Í×ÁÇµÍ¥ç¥Á¥×¥í;¼¥É;ËËËµ•ííË;ËËË¼°ÇÖµ•µç¼í¹çµí;çÄ®, ùµ¹µèµÐµÇÄ÷ç®µ•µËµµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷çµ;ON
0016 C. *****
0017 C. ç °ËÀ, í×ËÝµäLOSµÐµÇµí»p´Öµò¹íí, µ•; çÉÖÍ×µËXÁÓONµí¹ÖµËµíµËµµµ³µÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥ó¥É¥í¥Á¥~¾òÄÖµ¬°ÄÄèµ•µçµé; ç°Ë²¼µí°ËÀ, ¼è¼çµò¼Ä¹Öµ¹µé;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ËÀ,
0033 C. *****
0034 C. ç ° RESTART;ËPT1;Ëµ•µçµ¼¼í¹çµí; ç°Ë²¼µí¼Ä¹Öµµ°; çDCBC-150µØçËµà;£
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ã¥ç¥ó¥É¥ËÄÙÄØ;ËÄ•Ä°²óÈò;Ë, äµí°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, µ¬¼«°ËÄ»ßµ•µç, ä; ç°Ë²¼µò¼Ä¹Öµ¹µé;£
0055 C. ¥ç¥ó¥É¥ËÄÙÄØµäÄ•Ä°²óÈòµ¬¼áµ¼¼í¹çµí´°í´°í»µ¹µèµÐµÇÄÖµÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ËÀ,
0059 C. *****
0060 C. ç ° RESTART;ËPT2;Ëµ•µçµ¼¼í¹çµí; ç°Ë²¼µí¼Ä¹Öµµ°; çDCBC-151µØçËµà;£
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ã¥ç¥ó¥É¥ËÄÙÄØ;ËÄ•Ä°²óÈò;Ë, äµí°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Ää»ß; çXÁ÷çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷çµ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1_XPA_ON/OFF] EQ OFF

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```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5119836.0 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 21s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2024-06-11 11:04:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÃî»ö¼ÝñËÄñ¹ëDCBC•x²è *****
0165 C. (¾á°îÿÓÿÄÿËÿÐÿËÿÄÿÇÿËëñ¼¾ñ¼Ä»Û¹ë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¼Ðÿ¹•Ï Daily±¿ÍÑë'Øñ¹ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÿÁÿSÿÄÿÿ¼Ä»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-622 2024-06-11 12:57:40 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Ü;ä
0005 C.
0006 C. YÁY$;¼Y³YD¥óYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¼¼Añ•µ°E»Í×ÁÇñÍYçYÁY×Yí;¼YÉ;ËËè¼µ•íÍË;ËñÈ¼°ÇÔñ•ñ¿¼í¹ÇñÍ;çÁ®, ùñ¹ñèñDñÇÁ÷¿®ñ•ñËñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 + DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 + DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 + DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 03)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 0e)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2024-06-11 11:04:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP `ûÃÍñÎ»ò¾YñËÄDñ¹ñèDCBC•×²è *****
0087 C. (¾á°íYóYÁYËYD¥YÉYáYçYëñË¼¼ññ¼¼Á»Üñ¹ñè)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** YD¥¹•Í Daily±¿ÍÑñË´Øñ¹ñèDCBC•×²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
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0096 C.
0097 . C. ;ãLOS¥Á¥S¥Ã¥¼Å»Ü;ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

*** OP Sequence for XRT ***

2024/06/11	11:14:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	11:14:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	11:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2024/06/11	11:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 20 18 b1 bb		
2024/06/11	11:15:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/06/11	11:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/06/11	11:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/06/11	11:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/06/11	11:15:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/11	11:17:56.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2024/06/11	11:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2024/06/11	11:23:00.0	XRT_Custom_430_OG [0x1ae]					
2024/06/11	11:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/11	12:31:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	12:31:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	12:31:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/11	12:31:06.0	XRT_PREFLR_STRT_403_OG [0x193]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/11	12:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/11	13:01:30.0	XRT_Custom_430_OG [0x1ae]					
2024/06/11	13:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/11	14:09:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	14:09:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	14:09:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/11	14:09:06.0	XRT_PREFLR_STRT_403_OG [0x193]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/11	14:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/11	14:39:30.0	XRT_Custom_430_OG [0x1ae]					
2024/06/11	14:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/11	15:47:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	15:47:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	15:47:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/11	15:47:36.0	XRT_PREFLR_STRT_403_OG [0x193]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/11	15:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/11	16:18:00.0	XRT_Custom_430_OG [0x1ae]					
2024/06/11	16:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/11	16:26:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	16:26:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	16:26:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/11	16:26:36.0	XRT_PREFLR_STRT_403_OG [0x193]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/11	16:27:30.0	XRT_Custom_430_OG [0x1ae]					
2024/06/11	16:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/11	16:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/11	17:25:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	17:25:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	17:25:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/11	17:25:36.0	XRT_PREFLR_STRT_403_OG [0x193]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/11	17:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/11	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/11	18:03:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		

2024/06/11	18:03:58.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/06/11	18:04:00.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2024/06/11	18:04:18.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2024/06/11	18:04:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/06/11	18:04:22.0	XRT_ARS_DIS_435_OG [0x1b3] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/11	18:06:58.0	XRT_QT_PROG_SET_401_OG [0x191] MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2024/06/11	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	18:13:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	18:13:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	18:13:58.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/06/11	18:14:00.5	AOCS_Ore-point_Start_1_OG [0x097] AOCU_NM	5	02-76	00 20 18 b1 bb
2024/06/11	18:14:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2024/06/11	18:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/06/11	18:14:22.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2024/06/11	18:14:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/06/11	18:14:26.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	18:16:56.0	XRT_QT_PROG_SET_404_OG [0x194] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2024/06/11	18:16:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/11	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	19:04:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	19:04:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	19:04:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	19:04:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/11	19:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/11	19:40:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/11	19:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	20:42:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	20:42:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	20:42:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	20:42:06.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/11	20:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/11	21:17:30.0	XRT_Custom_430_OG [0x1ae]			
2024/06/11	21:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	22:20:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	22:20:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	22:20:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	22:20:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/11	22:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/11	22:54:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/11	22:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/11	23:58:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	23:58:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/11	23:58:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/11	23:58:36.0	XRT_PREFLR_STRT_403_OG [0x193] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	00:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	00:29:00.0	XRT_Custom_430_OG [0x1ae]			
2024/06/12	00:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	01:37:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	01:37:02.0	XRT_CTRL_MANU_402_OG [0x192]			

2024/06/12	01:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	01:37:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/12	01:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	02:07:30.0	XRT_Custom_430_OG [0x1ae]								
2024/06/12	02:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	03:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	03:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	03:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	03:07:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/12	03:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	03:45:30.0	XRT_Custom_430_OG [0x1ae]								
2024/06/12	03:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/06/12	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2024/06/12	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/06/12	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/06/12	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/06/12	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/12	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2024/06/12	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/06/12	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	04:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	04:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	04:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	04:38:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/12	04:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	05:24:00.0	XRT_Custom_430_OG [0x1ae]								
2024/06/12	05:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	05:48:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	05:48:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/06/12	05:48:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2024/06/12	05:48:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/06/12	05:48:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/06/12	05:48:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/12	05:51:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/06/12	05:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	05:58:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	05:58:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/06/12	05:58:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 06 24 01 81				
2024/06/12	05:58:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/06/12	05:58:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/06/12	05:58:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/06/12	05:58:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/12	05:58:56.0	XRT_FLD_RESET_434_OG [0x1b2]								

2024/06/12	06:01:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	06:01:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2024/06/12	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/06/12	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	06:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	06:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	06:15:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	06:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	07:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	07:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/12	07:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	07:55:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	07:55:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	07:55:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	07:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	08:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	08:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/12	09:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	09:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	09:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	09:34:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	09:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	10:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	10:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/12	11:15:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	11:15:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	11:15:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	11:15:36.5	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	11:18:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	11:57:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	11:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/12	13:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	13:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	13:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	13:04:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	13:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	13:35:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	13:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/12	14:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	14:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	14:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	14:43:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12	14:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/06/12	15:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/06/12	15:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/06/12	16:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/06/12	16:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	16:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/06/12	16:21:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/06/12			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2024/06/12	16:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	17:00:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/12	17:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	17:44:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	17:44:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	17:44:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/06/12	17:44:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/06/12	17:44:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/06/12	17:44:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/06/12	17:44:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/12	17:47:28.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2024/06/12	17:47:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	17:54:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/06/12	17:54:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 06 24 01 81				
2024/06/12	17:54:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/06/12	17:54:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/06/12	17:54:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/06/12	17:54:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/06/12	17:54:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	17:57:26.0	XRT_QT_PROG_SET_404_OG [0x194]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2024/06/12	17:57:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/06/12	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	17:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	17:59:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	17:59:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	17:59:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/12	18:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	18:37:30.0	XRT_Custom_430_OG [0x1ae]							
2024/06/12	18:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	19:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	19:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	19:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	19:37:36.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/12	19:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	20:14:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/12	20:15:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	21:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	21:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	21:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	21:16:06.0	XRT_PREFLR_STRT_403_OG [0x193]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/06/12	21:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/06/12	21:51:00.0	XRT_Custom_430_OG [0x1ae]							
2024/06/12	21:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/06/12	22:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	22:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/06/12	22:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/06/12	22:54:06.0	XRT_PREFLR_STRT_403_OG [0x193]							

2024/06/12	22:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8		
2024/06/12	23:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/12	23:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/13	00:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	00:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	00:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/13	00:32:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/13	00:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	01:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	01:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/13	02:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	02:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	02:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	02:08:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/13	02:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/13	02:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	02:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	03:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/13	03:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	03:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	03:40:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/13	03:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/13	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	04:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/06/13	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00		
2024/06/13	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/06/13	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/06/13	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/06/13	04:00:24.0	XRT_FLD_RESET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/06/13	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/13	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12		
2024/06/13	04:19:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2024/06/13	04:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0		
2024/06/13	05:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/13	05:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	05:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	05:09:06.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/06/13	05:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/06/13	05:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	05:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/06/13	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/06/13	06:03:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	06:03:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/06/13	06:03:30.0	AOCs_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/06/13	06:03:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00		
2024/06/13	06:03:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/06/13	06:03:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/06/13	06:06:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5		

2024/06/13	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/06/13	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/13	06:13:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/13	06:13:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2024/06/13	06:13:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 06 24 01	81
2024/06/13	06:13:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/06/13	06:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/06/13	06:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/06/13	06:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/06/13	06:13:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/06/13	06:16:26.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2024/06/13	06:16:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/06/13	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/06/13	06:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/13	06:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/13	06:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/06/13	06:49:36.0	XRT_PREFLR_STRT_403_OG [0x193]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/06/13	06:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/06/13	07:36:00.0	XRT_Custom_430_OG [0x1ae]					
2024/06/13	07:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/06/13	08:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/06/13	11:17:00.5	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00