XRT Timeline to be uploaded on 2024/10/22

Period: 2024/10/22 11:13:00 - 2024/10/26 11:16:00

* * * * * **Normal mode** * * * * *

XOB #1D03: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term	Pointing (x, y)	Comment
10/23 12:03:00 - 10/23 12:09:54	Fixed (-528.4, -528.4)	Post bakeout Q1

PROG= 17 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 51 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D04: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 2nd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC

Term	Pointing (x, y)	Comment
10/23 12:13:00 - 10/23 12:19:54	Fixed (528.4, -528.4)	Post bakeout Q2

PROG= 06 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 38 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D05: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 3rd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term	Pointing (x, y)	Comment
10/23 12:23:00 - 10/23 12:29:54	Fixed (528.4, 528.4)	Post bakeout Q3

PROG= 18 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 21 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term	Pointing (x, y)	Comment
10/23 12:33:00 - 10/23 12:39:54	Fixed (-528.4, 528.4)	Post bakeout Q4

PROG= 05 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 14 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec													
Seqn= 34 1-time(s) 60.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CC9: Synoptic 7 Filter w/ Al-mesh(2/128/723), Al-poly(12/181/1443), Thin-Be(64/1024/5795) - Thick-Be(32768), Al-poly+Ti-poly(128/2048), Med-Al(289

Term	Pointing (x, y)	Comment
10/23 12:43:00 - 10/23 12:49:54	Fixed (0.0, 0.0)	Post bakeout synoptics
10/23 18:01:00 - 10/23 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min
10/24 06:32:00 - 10/24 06:38:54	Fixed (0.0, 0.0)	synoptic, shifted 29.0 min

PROG= 10 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 78 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 45 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 41 1-time(s) 2.0sec													
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 85 1-time(s) 2.0sec													
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 4 1-time(s) 2.0sec													
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
10/23 12:53:00 - 10/23 17:57:54	Track (-212.8, -278.5) @ 10/23 12:50:00	# Track AR 13863
10/23 18:11:00 - 10/24 03:59:54	Track (-164.6, -279.0) @ 10/23 18:08:00	# Track AR 13863
10/24 06:42:00 - 10/24 10:51:30	Track (-49.4, -279.2) @ 10/24 06:39:00	# Track AR 13863

PROG= 12 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 92 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec													
Seqn= 47 1-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77 4-time(s) 300.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
10/24 04:03:00 - 10/24 06:28:54	Fixed (0.0, 0.0)	HOP 349

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 55 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 1500.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 74 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

* * * * * Flare mode * * * * *

XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384

Term	Pointing (x, y)	Comment
10/23 12:53:00 - 10/23 17:57:54	Track (-212.8, -278.5) @ 10/23 12:50:00	# Track AR 13863
10/23 18:11:00 - 10/24 03:59:54	Track (-164.6, -279.0) @ 10/23 18:08:00	# Track AR 13863
10/24 04:03:00 - 10/24 06:28:54	Fixed (0.0, 0.0)	HOP 349
10/24 06:42:00 - 10/24 10:51:30	Track (-49.4, -279.2) @ 10/24 06:39:00	# Track AR 13863

PROG= 19 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 9 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 49 255-time(s) 5.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

* * * * * Active Region Search * * * * *

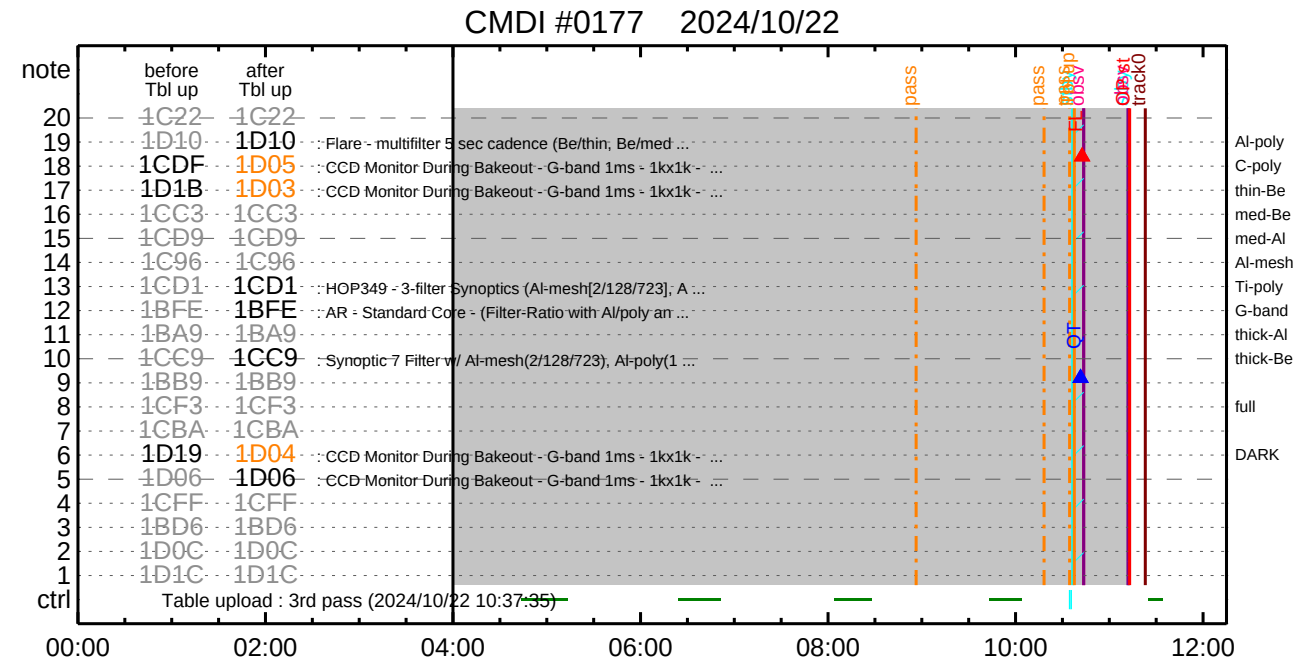
NOT USED

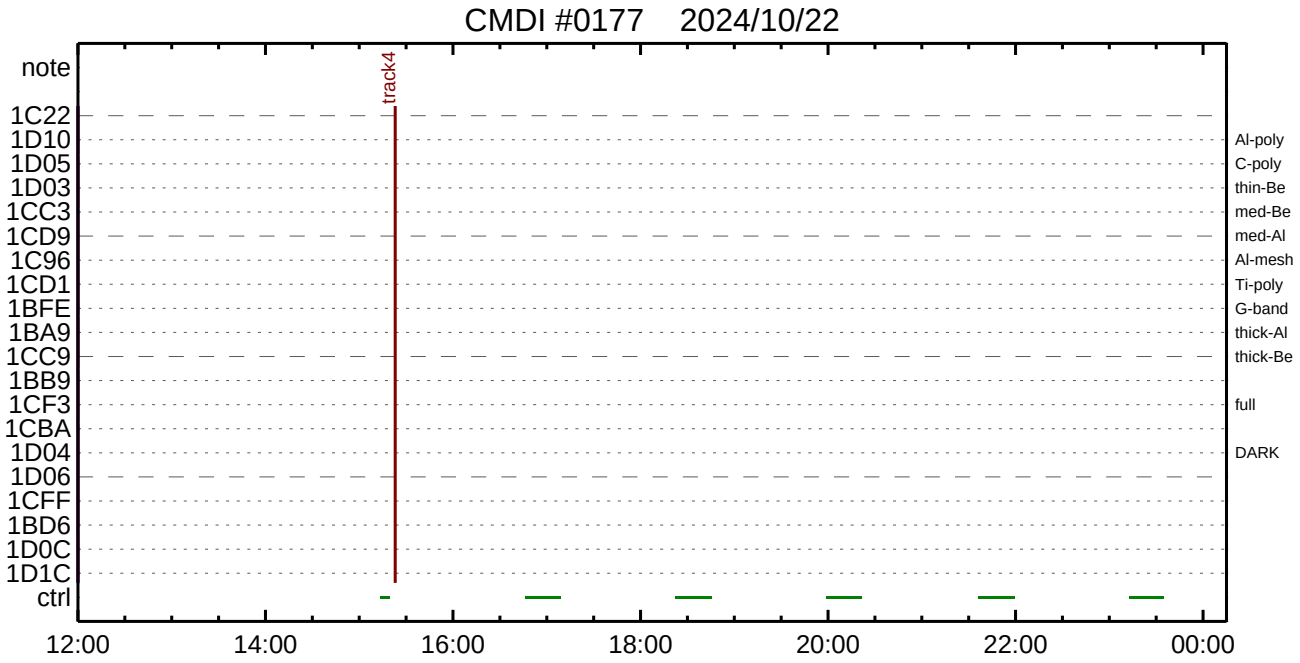
* * * * * Flare Detection * * * * *

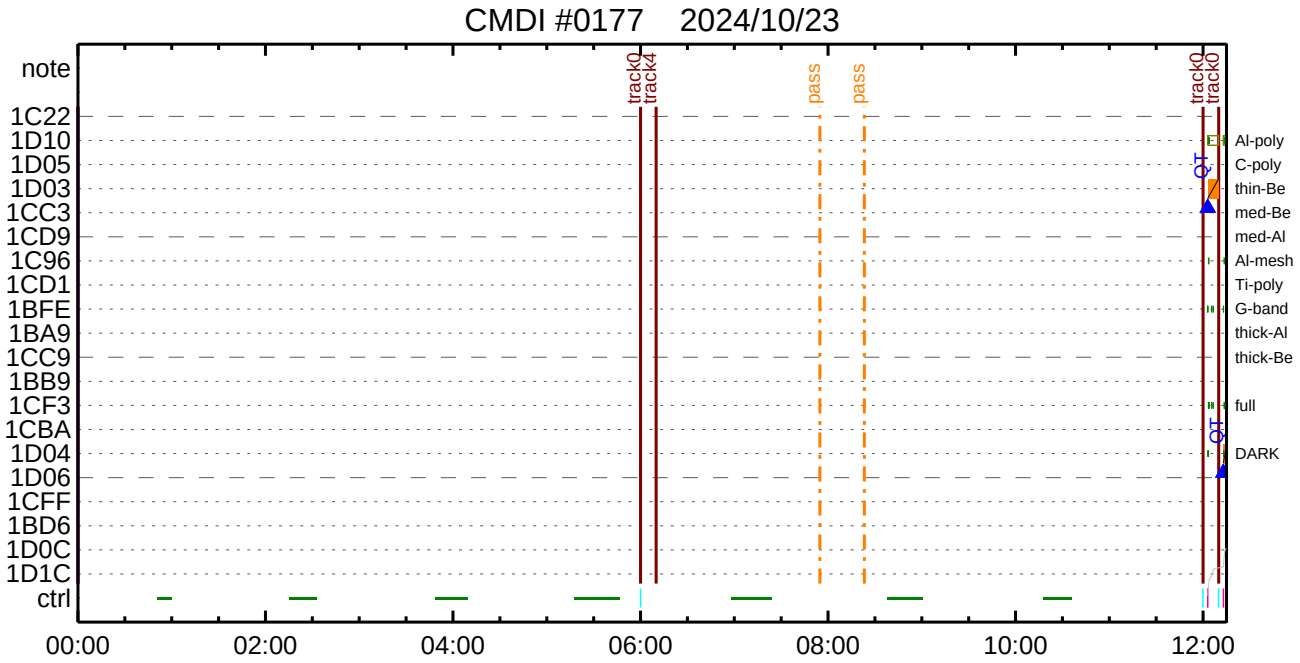
FLD Patrol

Term	Pointing (x, y)	Comment
10/22 10:38:35 - 10/23 12:02:56	cannot be identified	
10/23 12:50:18 - 10/23 17:58:18	Track (-212.8, -278.5) @ 10/23 12:50:00	# Track AR 13863
10/23 18:08:18 - 10/24 06:29:18	Track (-164.6, -279.0) @ 10/23 18:08:00	# Track AR 13863
10/24 06:39:18 - 10/26 11:16:00	Track (-49.4, -279.2) @ 10/24 06:39:00	# Track AR 13863

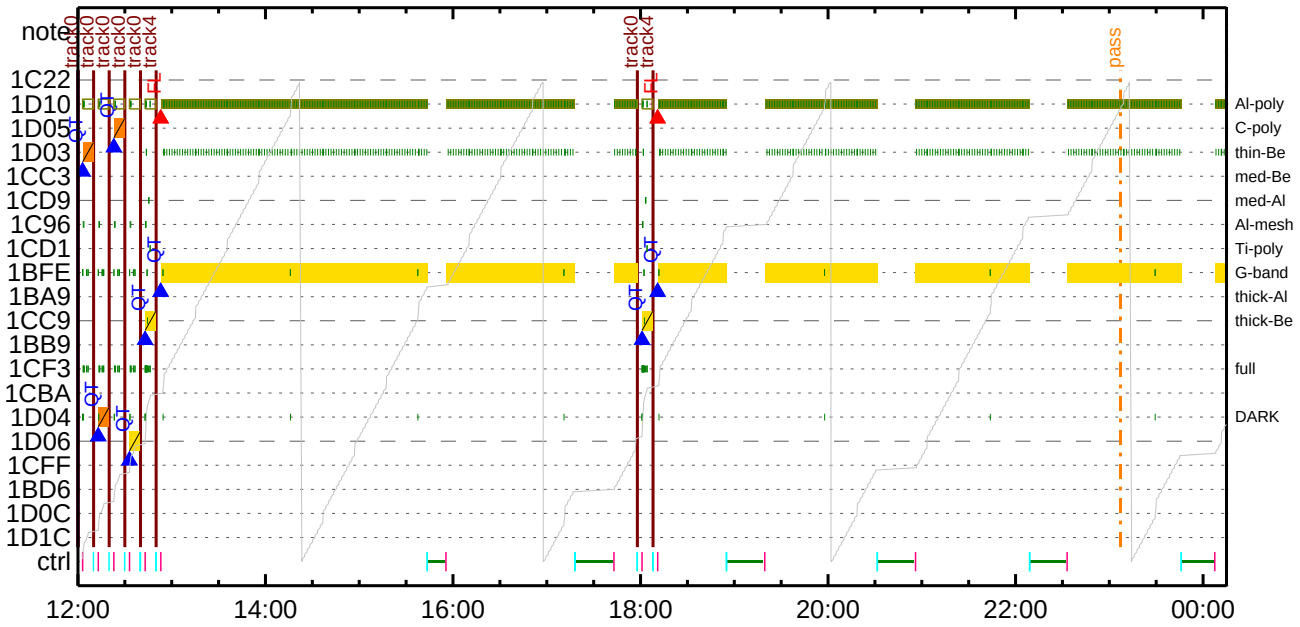
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec


CMDI #0177 2024/10/22
note
before Tbl up
after Tbl up
20 1C22 1C22
19 1D10 1D10 : Flare - multifilter 5 sec cadence (Be/thin, Be/med ...
18 1CDF 1D05 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
17 1D1B 1D03 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
16 1CC3 1CC3
15 1CD9 1CD9
14 1C96 1C96
13 1CD1 1CD1 : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
12 1BFE 1BFE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
11 1BA9 1BA9
10 1CC9 1CC9 : Synoptic 7 Filter w/ Al-mesh(2/128/723), Al-poly(1 ...
9 1BB9 1BB9
8 1CF3 1CF3
7 1CBA 1CBA
6 1D19 1D04 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
5 1D06 1D06 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
4 1CFF 1CFF
3 1BD6 1BD6
2 1D0C 1D0C
1 1D1C 1D1C
ctrl Table upload : 3rd pass (2024/10/22 10:37:35)
pass
pass
track0
EF
OT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

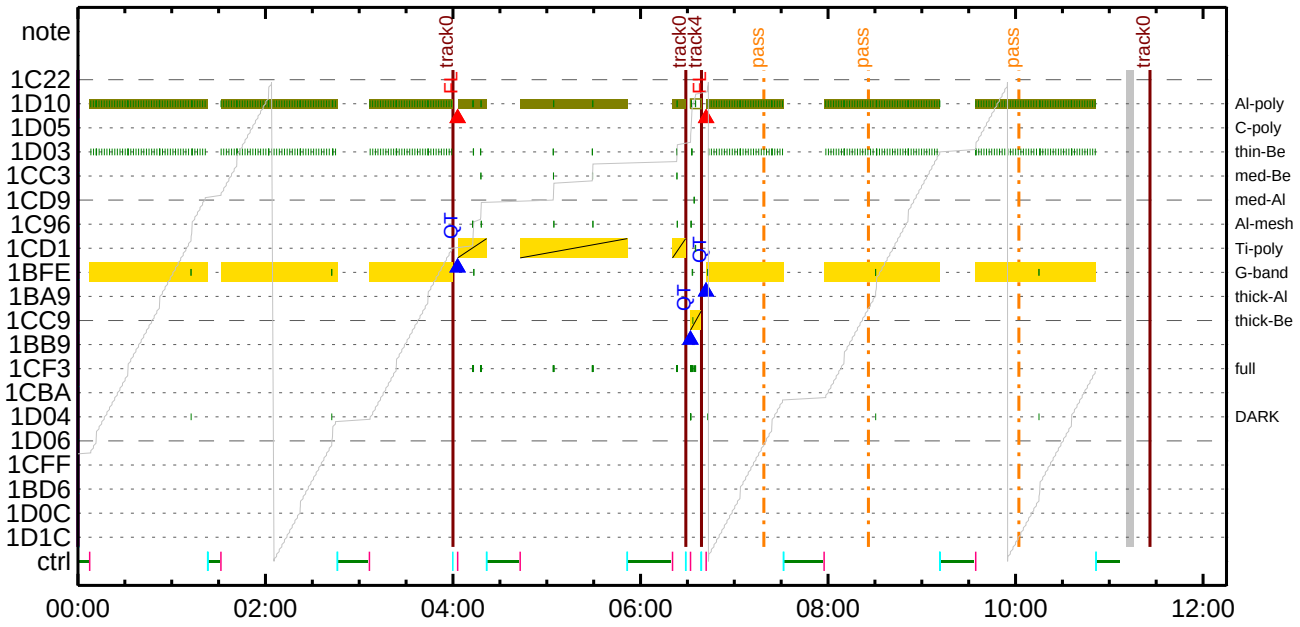

CMDI #0177 2024/10/22
note
track4
1C22
1D10
1D05
1D03
1CC3
1CD9
1C96
1CD1
1BFE
1BA9
1CC9
1BB9
1CF3
1CBA
1D04
1D06
1CFF
1BD6
1D0C
1D1C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0177 2024/10/23
note
track0
track4
pass
pass
track0
track0
EF
OT
OT
1C22
1D10
1D05
1D03
1CC3
1CD9
1C96
1CD1
1BFE
1BA9
1CC9
1BB9
1CF3
1CBA
1D04
1D06
1CFF
1BD6
1D0C
1D1C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

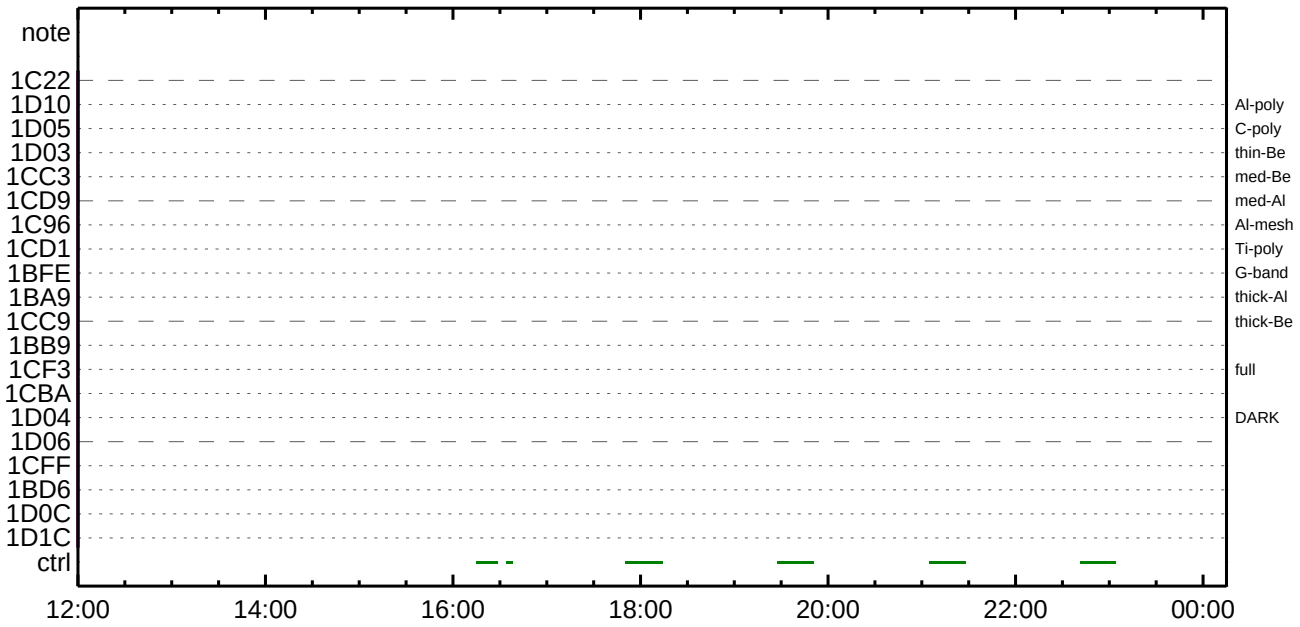
CMDI #0177 2024/10/23

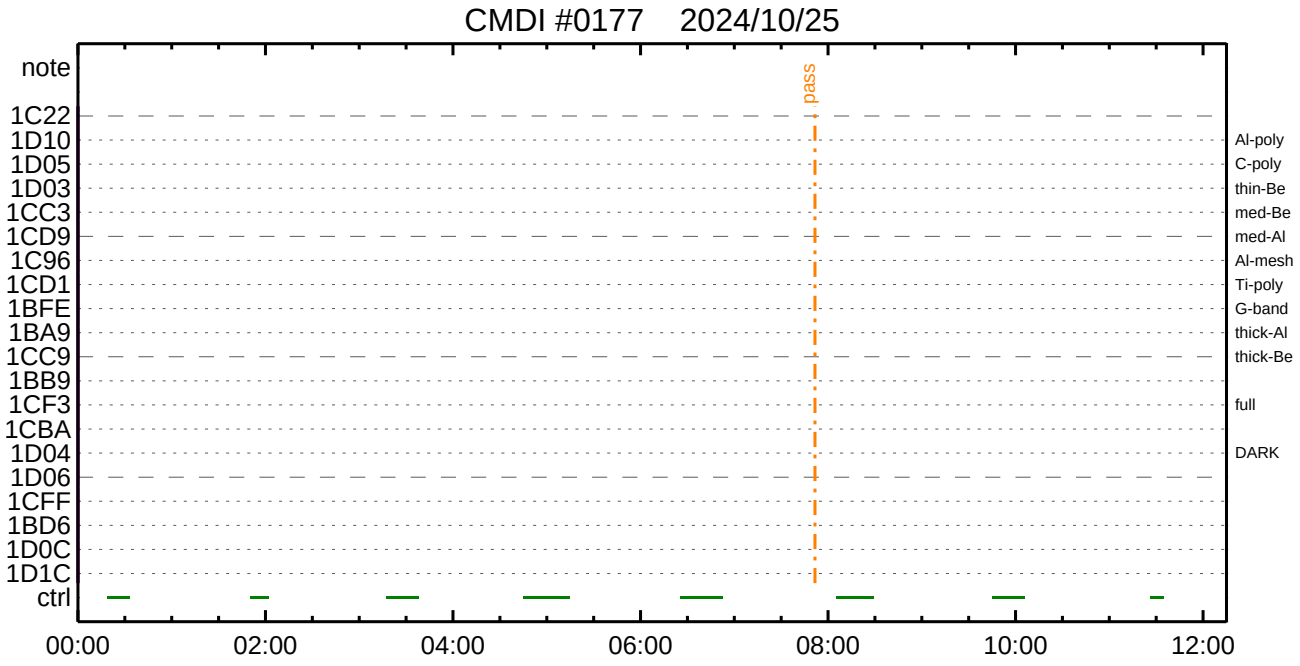


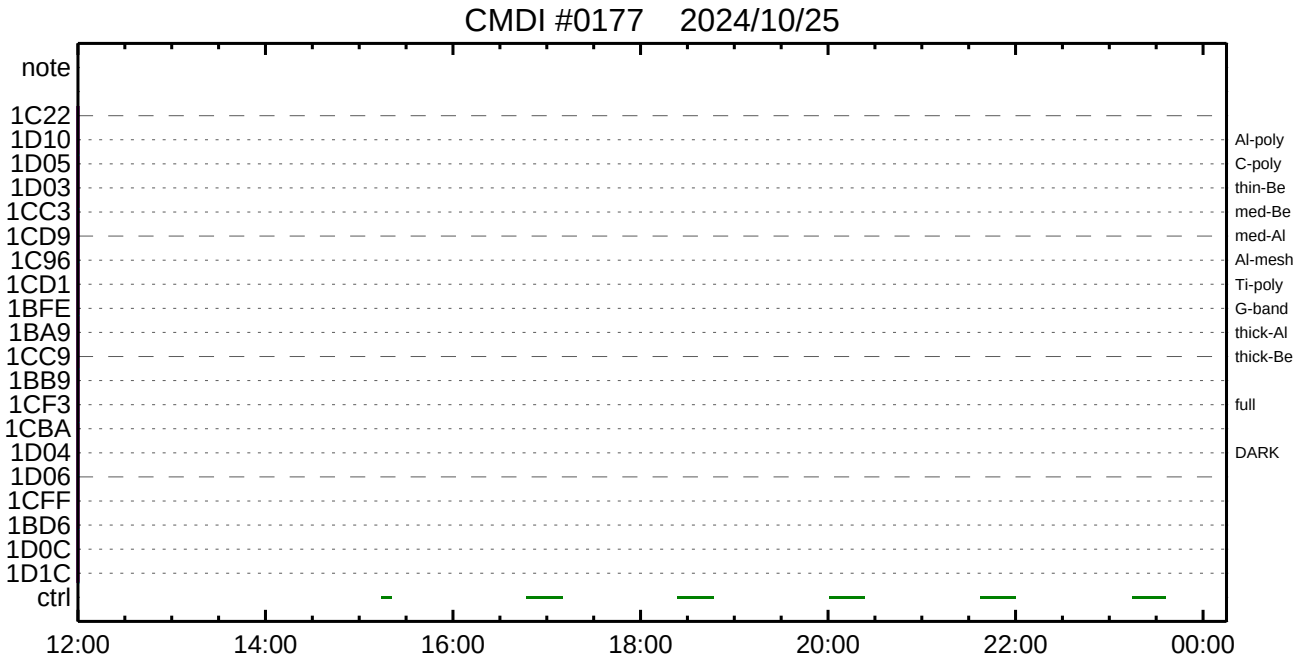
CMDI #0177 2024/10/24

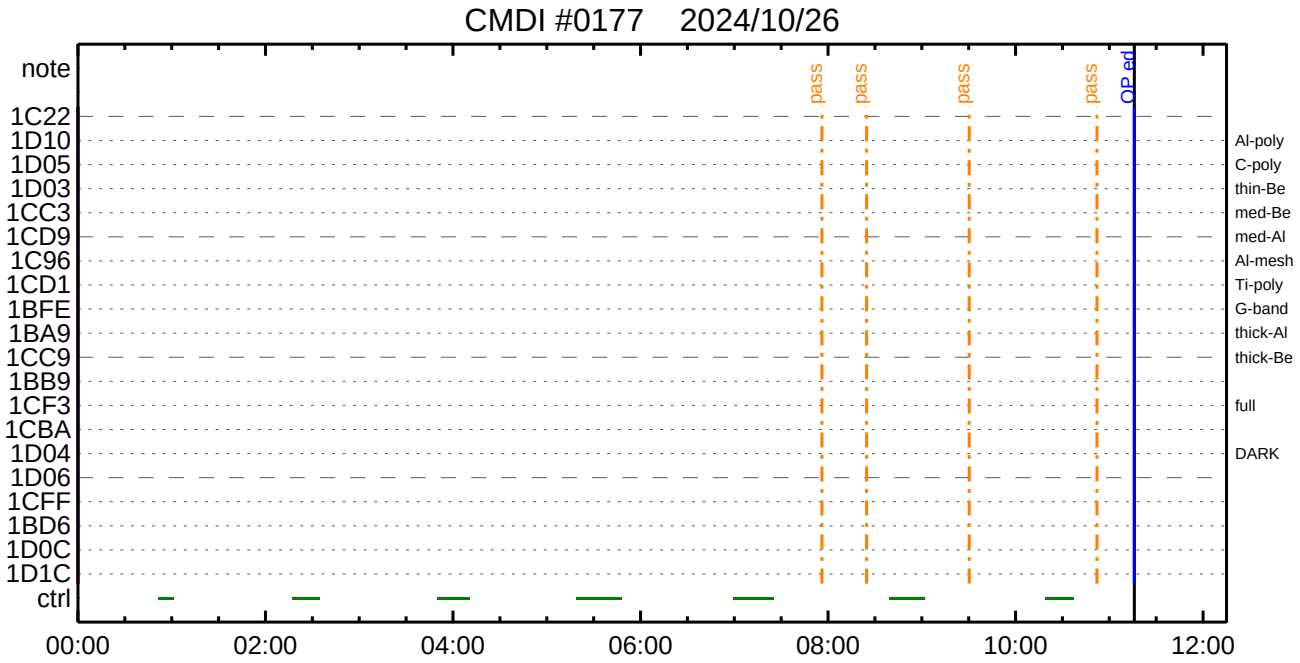


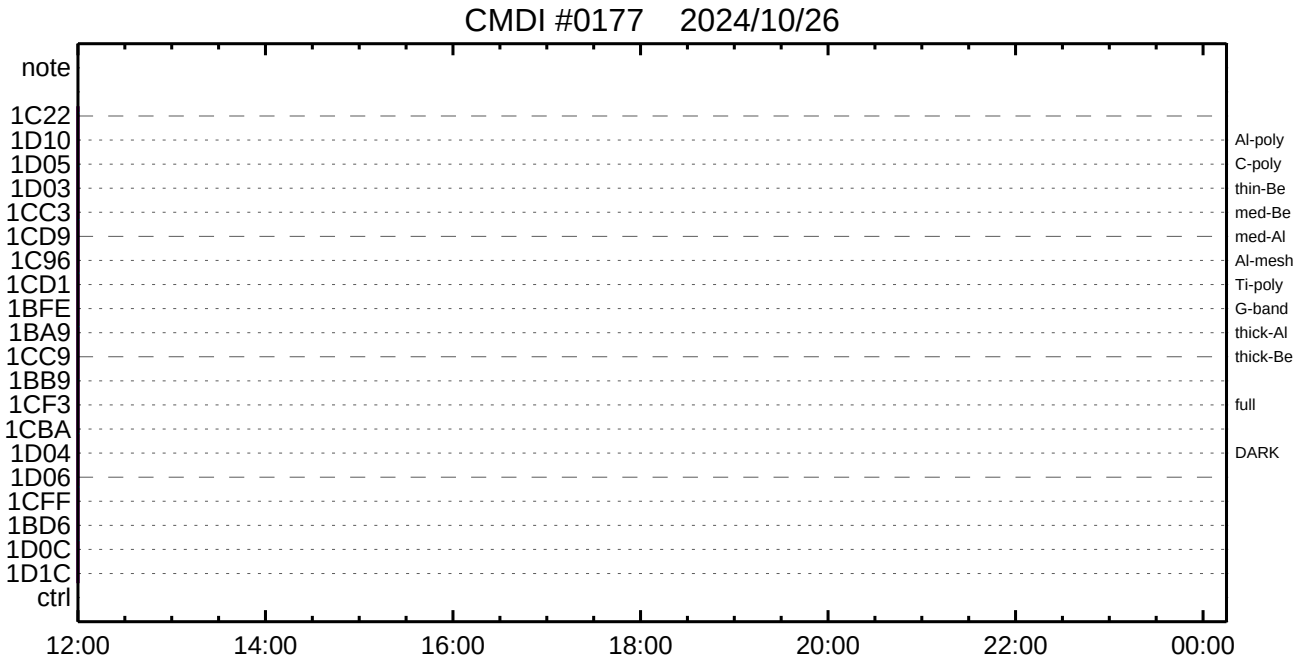
CMDI #0177 2024/10/24




CMDI #0177 2024/10/25
note
1C22
1D10
1D05
1D03
1CC3
1CD9
1C96
1CD1
1BFE
1BA9
1CC9
1BB9
1CF3
1CBA
1D04
1D06
1CFF
1BD6
1D0C
1D1C
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0177 2024/10/25
note
1C22
1D10
1D05
1D03
1CC3
1CD9
1C96
1CD1
1BFE
1BA9
1CC9
1BB9
1CF3
1CBA
1D04
1D06
1CFF
1BD6
1D0C
1D1C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0177 2024/10/26
note
1C22
1D10
1D05
1D03
1CC3
1CD9
1C96
1CD1
1BFE
1BA9
1CC9
1BB9
1CF3
1CBA
1D04
1D06
1CFF
1BD6
1D0C
1D1C
ctrl
pass
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0177 2024/10/26
note
1C22
1D10
1D05
1D03
1CC3
1CD9
1C96
1CD1
1BFE
1BA9
1CC9
1BB9
1CF3
1CBA
1D04
1D06
1CFF
1BD6
1D0C
1D1C
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-882 2024-10-22 12:32:58 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
     C.
     C.
   . C. ****************************
     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
     C. ****************************
     C.
   . C. ¡ãOP/OG¥í¡¼¥É¡ä
   . S. OP     op-882:OP
     ()
   . S. OG     og-882:OG
     ()
     C.
   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC         (20 00 7f 01 02)
     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ      40
     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ      0
     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ      127
     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ      DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC         (07 0b f8)
     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ      7
     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ      0.25 s
     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ      32k
     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ      EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ      NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC         (20 80 7f 01 02)
     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ      41
     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ      0
     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ      127
     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ      DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC         (07 0b f8)
     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ      7
     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ      0.25 s
     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ      32k
     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ      EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ      NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC         (21 00 41 01 02)
     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ      42
     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ      0
     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ      65
     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ      0
     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ      DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC         (07 0b f8)
     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ      7
     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ      0.25 s
     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ      32k
     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ      EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ      NON
   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
  +. DC 01-22 DHU_MODE_CHNG
     BC         (02 0a f8)
     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ      2
     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ      0.5S
     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ      32K
     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ      4M
     C.
   . C. *********************************
     C. TI-CMD SET (OPOG STOP/COPY/START)
     C. *********************************
     C.
   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
     C.
   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
  +. TI 2024-10-22 11:08:00.0
     DC 01-B3 DHU_OP_STOP
     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ      1COUNTUP
     C.
  +. TI 2024-10-22 11:08:01.0
     DC 01-B4 DHU_OP_COPY
     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ      1COUNTUP
     C.
  +. TI 2024-10-22 11:08:01.0
     DC 01-B5 DHU_OPOG_COPY
     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ      1COUNTUP
```

```
0194     C.
0195   +. TI 2024-10-22 11:12:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2024-10-22 11:12:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2024-10-22 11:12:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ************
0256     C. Execute, after the success of OP upload.
0257   +. TI 2024-10-22 11:12:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ************
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-883 2024-10-22 12:32:58 94 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047  +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049  +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 8179311.8 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055  +. DC 07-FC EIS_MODE_CHG_ENA
0056     BC       (20)
0057   . C. Verify EIS_MODE_CHG_FLG is ENA
0058  +. DC 07-FC EIS_MODE_MANU
0059     BC       (21 02)
0060   . C. Verify EIS in MANUAL mode
0061   . C. Estimated OBSTBL upload time is 19s
0062     C. *********************************
0063     C. EIS START OBSTBL LOAD
0064     C. *********************************
0065   . S. RAM   ram-820:EIS_OBSTBL
0066     ()
0067  +. DC 07-FC EIS_DUMP_OBSTBL
0068     BC       (07 07 07 00 00 70 00)
0069     C.
0070     C. Execute, after the success of OBSTBL upload.
0071     C. Set EIS TI-commands
0072  +. TI 2024-10-22 11:12:50.0
0073     DC 07-FC EIS_MODE_CHG_ENA
0074     BC       (20)
0075   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0076     C. *********************************
0077     C. EIS END OBSTBL LOAD
0078     C. *********************************
0079     C.
0080   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0081     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0082   . S. DC-BC dcbc-402:DCBC
0083     (MDP_known_event)
0084     C.
0085     C.
0086   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0087   . S. DC-BC dcbc-153:DCBC
0088     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089     C.
0090     C.
0091   . C. ¡ãLOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0092     C.
0093   . C. *********** LOS ***********
0094     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-884 2024-10-22 12:32:58 188 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +. DC 07-F0 MDP_XRT_CTRL_MANU
0104     BC       (c1)
0105  +  DC 07-F0 MDP_XRT_MODE_STBY
0106     BC       (c3)
0107   . C. ----------- Success Verify ?              OK / NG____
0108     C.
0109     C. XRT Obs. Table Upload
0110   . S. RAM   ram-291:MDP_OBS_X
0111     ()
0112     C.
0113  +. DC 07-F0 MDP_DUMP_XRTTBL
0114     BC       (84 07 00 00 00 3a d4)
0115   . C. ----------- Comparison Check ?                        OK / ERR ____
0116     C.
0117     C.
0118  +. DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 01 b1 b1 04 04)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 02 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 03 b1 b1 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 04 b1 b1 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 06 80 80 20 20)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 07 80 80 20 08)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 08 80 80 08 20)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 09 c0 c0 10 10)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 0a 40 c0 10 10)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0b 40 40 10 10)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0c c0 40 10 10)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 0d 85 83 06 06)
0142  +  DC 07-F0 MDP_XRT_ROI_SET
0143     BC       (cd 0e 80 80 08 08)
0144  +  DC 07-F0 MDP_XRT_ROI_SET
0145     BC       (cd 0f 80 80 06 06)
0146  +  DC 07-F0 MDP_XRT_FLD_ENA
0147     BC       (d8)
0148  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0149     BC       (c8)
0150  +  DC 07-F0 MDP_XRT_ARS_DIS
0151     BC       (d5)
0152  +  DC 07-F0 MDP_XRT_AEC_RESET
0153     BC       (d0)
0154  +  DC 07-F0 MDP_XRT_FLD_RESET
0155     BC       (da)
0156  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0157     BC       (c4 0a)
0158  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0159     BC       (c5 13)
0160   . C. ---------- Success Verify ?            OK / NG ____
0161     C.
0162     C.
0163   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0164     C.
0165  +. DC 07-F0 MDP_XRT_MODE_OBSV
0166     BC       (c2)
0167  +. TI 2024-10-22 11:12:02.0
0168     DC 07-F0 MDP_XRT_MODE_OBSV
0169     BC       (c2)
0170   . C. ---------- Success Verify ?            OK / NG ____
0171     C.
0172     C. ************ XRT END ***********
0173     C.
0174   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0175     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0176   . S. DC-BC dcbc-402:DCBC
0177     (MDP_known_event)
0178     C.
0179     C.
0180   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0181   . S. DC-BC dcbc-153:DCBC
0182     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0183     C.
0184     C.
0185   . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
0186     C.
0187   . C. ************ LOS ************
0188     C.
```

```
*** OP Sequence for XRT ***

2024/10/22  11:23:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 bd b3 01 f3
2024/10/22  15:23:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 03 ce 01 f3
2024/10/23  06:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/10/23  06:00:00.5  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  06:00:02.5  XRT_TCIB_XRT_S_HTR_A_DIS_432_OG [0x1b0]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2024/10/23  06:10:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 04 03 ce 01 f3
2024/10/23  11:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  11:59:56.0  XRT_FOCUS_POSITION_445_OG [0x1bd]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/23  12:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2024/10/23  12:02:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  12:02:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/23  12:02:56.0  XRT_FLD_DIS_446_OG [0x1be]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/23  12:02:58.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2024/10/23  12:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  12:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:09:56.0  XRT_FOCUS_POSITION_445_OG [0x1bd]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/23  12:10:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2024/10/23  12:12:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  12:12:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/23  12:12:56.0  XRT_FLD_DIS_446_OG [0x1be]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/23  12:12:58.0  XRT_QT_PROG_SET_401_OG [0x191]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2024/10/23  12:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  12:19:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:19:56.0  XRT_FOCUS_POSITION_445_OG [0x1bd]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/23  12:20:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2024/10/23  12:22:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  12:22:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/23  12:22:56.0  XRT_FLD_DIS_446_OG [0x1be]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/23  12:22:58.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2024/10/23  12:23:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  12:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:29:56.0  XRT_FOCUS_POSITION_445_OG [0x1bd]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/23  12:30:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 d1 07 2e f9
2024/10/23  12:32:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  12:32:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/23  12:32:56.0  XRT_FLD_DIS_446_OG [0x1be]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/23  12:32:58.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2024/10/23  12:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  12:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:39:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/23  12:40:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/10/23  12:40:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/23  12:40:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/23  12:40:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  12:42:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
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                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2024/10/23  12:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  12:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:49:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  12:49:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/10/23  12:50:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 04 03 ce 01 f3
2024/10/23  12:50:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2024/10/23  12:50:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/10/23  12:50:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2024/10/23  12:50:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  12:50:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  12:52:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2024/10/23  12:52:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2024/10/23  12:53:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  15:43:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  15:43:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  15:43:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  15:43:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/23  15:46:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/23  15:54:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/23  15:55:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  17:18:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  17:18:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  17:18:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  17:18:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/23  17:21:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/23  17:42:00.0  XRT_Custom_430_OG [0x1ae]
2024/10/23  17:43:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  17:57:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  17:57:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  17:57:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/23  17:58:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2024/10/23  17:58:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/23  17:58:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/23  17:58:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  18:00:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2024/10/23  18:01:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  18:07:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  18:07:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  18:07:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/10/23  18:08:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 04 03 ce 01 f3
2024/10/23  18:08:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2024/10/23  18:08:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/10/23  18:08:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2024/10/23  18:08:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/23  18:08:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  18:10:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2024/10/23  18:10:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
```

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2024/10/23  18:11:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  18:55:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  18:55:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  18:55:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  18:55:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/23  18:58:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/23  19:18:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/23  19:19:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  20:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  20:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  20:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  20:31:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/23  20:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/23  20:55:00.0  XRT_Custom_430_OG [0x1ae]
2024/10/23  20:56:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  22:09:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  22:09:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  22:09:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  22:09:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/23  22:12:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/23  22:32:00.0  XRT_Custom_430_OG [0x1ae]
2024/10/23  22:33:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/23  23:46:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  23:46:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/23  23:46:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/23  23:46:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/23  23:49:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  00:06:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  00:07:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  01:23:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  01:23:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  01:23:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  01:23:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  01:26:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  01:30:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  01:31:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  02:46:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  02:46:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  02:46:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  02:46:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  02:49:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  03:05:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  03:06:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  03:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/24  04:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/10/24  04:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/10/24  04:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/10/24  04:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/10/24  04:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
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                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/24  04:00:24.0  XRT_FLD_RESET_428_OG [0x1ac]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  04:02:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2024/10/24  04:02:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2024/10/24  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  04:21:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  04:21:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  04:21:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  04:21:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  04:24:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  04:42:00.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  04:43:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  05:51:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  05:51:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  05:51:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  05:51:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  05:54:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  06:19:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  06:20:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  06:28:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  06:28:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  06:28:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/10/24  06:29:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2024/10/24  06:29:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2024/10/24  06:29:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/10/24  06:29:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/24  06:31:58.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2024/10/24  06:32:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  06:38:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  06:38:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  06:38:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/10/24  06:39:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 04 03 ce 01 f3
2024/10/24  06:39:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2024/10/24  06:39:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/10/24  06:39:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2024/10/24  06:39:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/10/24  06:39:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  06:41:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2024/10/24  06:41:58.0  XRT_FL_PROG_SET_418_OG [0x1a2]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 13
2024/10/24  06:42:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  07:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  07:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  07:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  07:31:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  07:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  07:56:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  07:57:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  09:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  09:11:32.0  XRT_CTRL_MANU_402_OG [0x192]
```

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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  09:11:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  09:11:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  09:14:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  09:33:30.0  XRT_Custom_430_OG [0x1ae]
2024/10/24  09:34:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/10/24  10:51:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  10:51:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/10/24  10:51:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/10/24  10:51:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/10/24  10:54:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/10/24  11:26:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```