

XRT Timeline to be uploaded on 2024/11/02

Period: 2024/11/02 10:47:00 - 2024/11/07 11:21:00

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Normal mode

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XOB #1BF: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/02 11:11:30 - 11/02 17:54:30	Track (-146.6, 153.1) @ 11/02 10:57:00	# OP start + 10min. AR 13878 Major Flare Watch.
11/02 18:31:30 - 11/03 06:00:54	Track (-79.6, 153.2) @ 11/02 18:28:30	# AR cont.
11/03 06:57:30 - 11/03 17:59:54	Track (25.1, 154.0) @ 11/03 06:11:00	# AR cont.
11/03 18:52:00 - 11/04 05:23:00	Track (131.7, 155.7) @ 11/03 18:10:00	# AR cont.
11/04 06:18:00 - 11/04 17:23:30	Track (236.6, 158.3) @ 11/04 06:11:00	# AR cont.
11/04 18:14:30 - 11/05 03:59:54	Track (337.9, 161.6) @ 11/04 18:11:30	# AR cont.
11/05 06:26:00 - 11/05 10:41:00	Track (430.4, 165.4) @ 11/05 05:40:30	# AR cont.

PROG= 12 Inf-time(s)

Subr= 1	1-time(s)	2.0sec													
Seqn= 92 1-time(s) 2.0sec			Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec															
Seqn= 47 1-time(s) 2.0sec			Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77 4-time(s) 300.0sec															
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
			Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
			thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
			Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795)

Term	Pointing (x, y)	Comment
11/02 18:21:30 - 11/02 18:28:24	Fixed (0.0, 0.0)	synoptic, shifted 18.5 min
11/03 06:04:00 - 11/03 06:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min
11/03 18:03:00 - 11/03 18:09:54	Fixed (0.0, 0.0)	synoptic
11/04 06:04:00 - 11/04 06:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min
11/04 18:04:30 - 11/04 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
11/05 05:33:30 - 11/05 05:40:24	Fixed (0.0, 0.0)	synoptic, shifted -29.5 min + HOP 349

PROG= 01 1-time(s)

Subr= 1	1-time(s)	2.0sec													
Seqn= 5 1-time(s) 2.0sec			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
			Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26 1-time(s) 2.0sec															
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99 1-time(s) 2.0sec															
			Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83 1-time(s) 2.0sec															
			thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec															
			Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
			Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec															
Seqn= 41 1-time(s) 2.0sec			Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec															
			med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec															
			med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
			med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec															

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
11/05 04:03:00 - 11/05 05:30:24	Fixed (0.0, 0.0)	synoptic, shifted -29.5 min + HOP 349

PROG= 13 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec										
Seqn= 55	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	15-time(s)	1500.0sec										
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74	1-time(s)	2.0sec										
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
11/02 11:11:30 - 11/02 17:54:30	Track (-146.6, 153.1) @ 11/02 10:57:00	# OP start + 10min. AR 13878 Major Flare Watch.
11/02 18:31:30 - 11/03 06:00:54	Track (-79.6, 153.2) @ 11/02 18:28:30	# AR cont.
11/03 06:57:30 - 11/03 17:59:54	Track (25.1, 154.0) @ 11/03 06:11:00	# AR cont.
11/03 18:52:00 - 11/04 05:23:00	Track (131.7, 155.7) @ 11/03 18:10:00	# AR cont.
11/04 06:18:00 - 11/04 17:23:30	Track (236.6, 158.3) @ 11/04 06:11:00	# AR cont.
11/04 18:14:30 - 11/05 03:59:54	Track (337.9, 161.6) @ 11/04 18:11:30	# AR cont.
11/05 04:03:00 - 11/05 05:30:24	Fixed (0.0, 0.0)	synoptic, shifted -29.5 min + HOP 349
11/05 06:26:00 - 11/05 10:41:00	Track (430.4, 165.4) @ 11/05 05:40:30	# AR cont.

PROG= 14 30-time(s)

Subr= 1	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

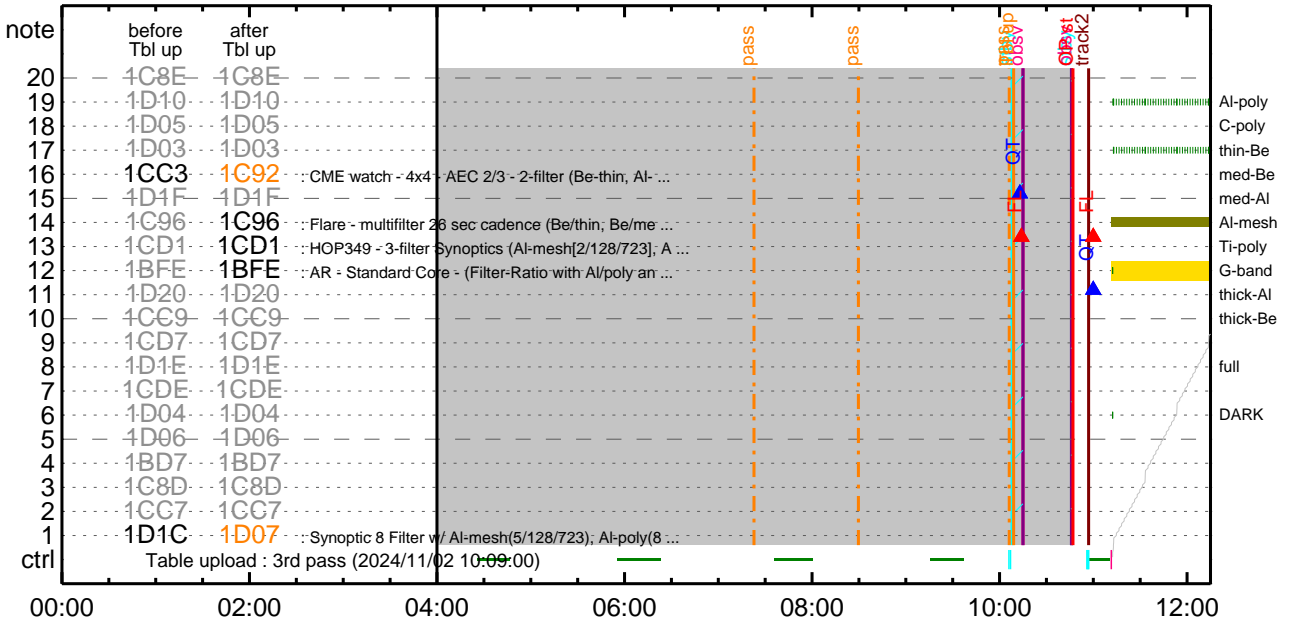
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Flare Detection

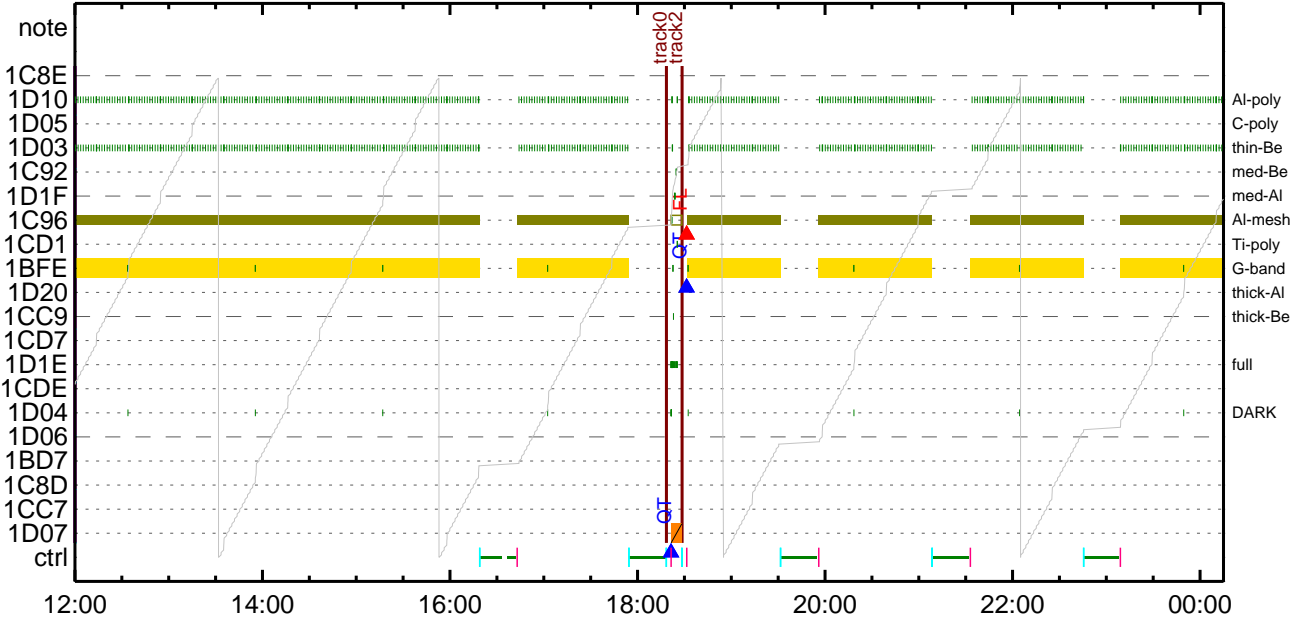
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FLD Patrol												
Term		Pointing (x, y)						Comment				
11/02 10:10:00 - 11/02 18:18:48		cannot be identified										
11/02 18:28:48 - 11/03 06:01:18		Track (-79.6, 153.2) @ 11/02 18:28:30						# AR cont.				
11/03 06:11:18 - 11/03 18:00:18		Track (25.1, 154.0) @ 11/03 06:11:00						# AR cont.				
11/03 18:10:18 - 11/04 06:01:18		Track (131.7, 155.7) @ 11/03 18:10:00						# AR cont.				
11/04 06:15:18 - 11/04 18:01:48		Track (236.6, 158.3) @ 11/04 06:11:00						# AR cont.				
11/04 18:11:48 - 11/05 05:30:48		Track (337.9, 161.6) @ 11/04 18:11:30						# AR cont.				
11/05 05:40:48 - 11/07 11:21:00		Track (430.4, 165.4) @ 11/05 05:40:30						# AR cont.				
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval	

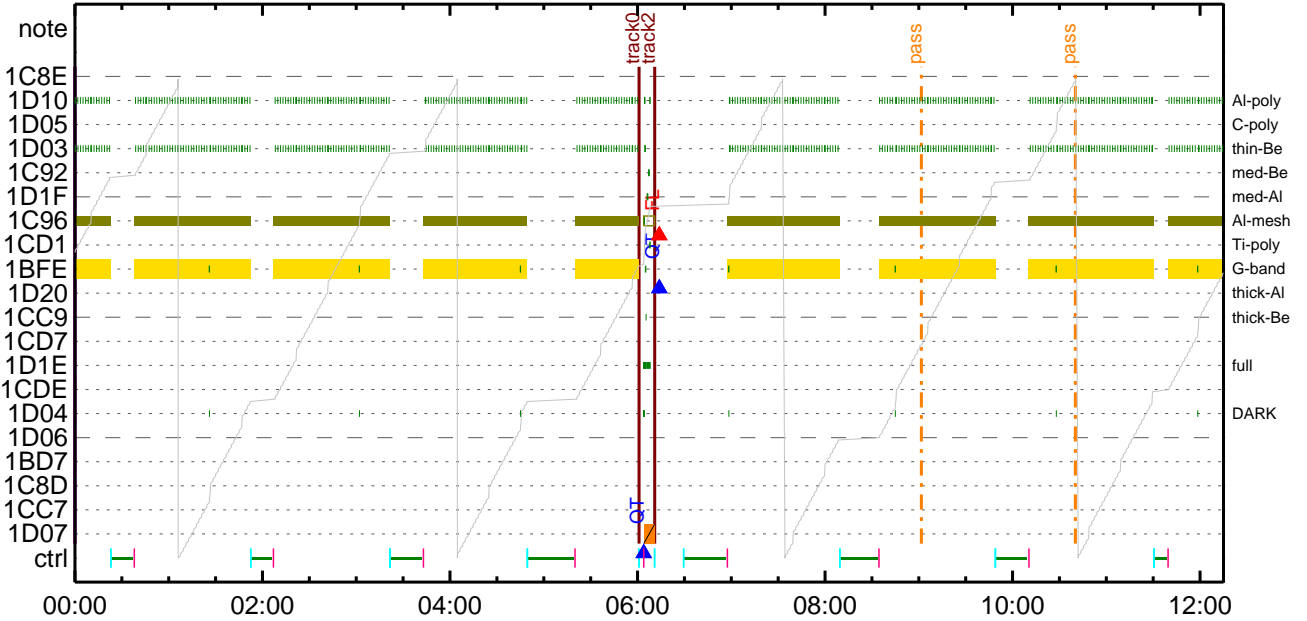
CMDI #0210 2024/11/02



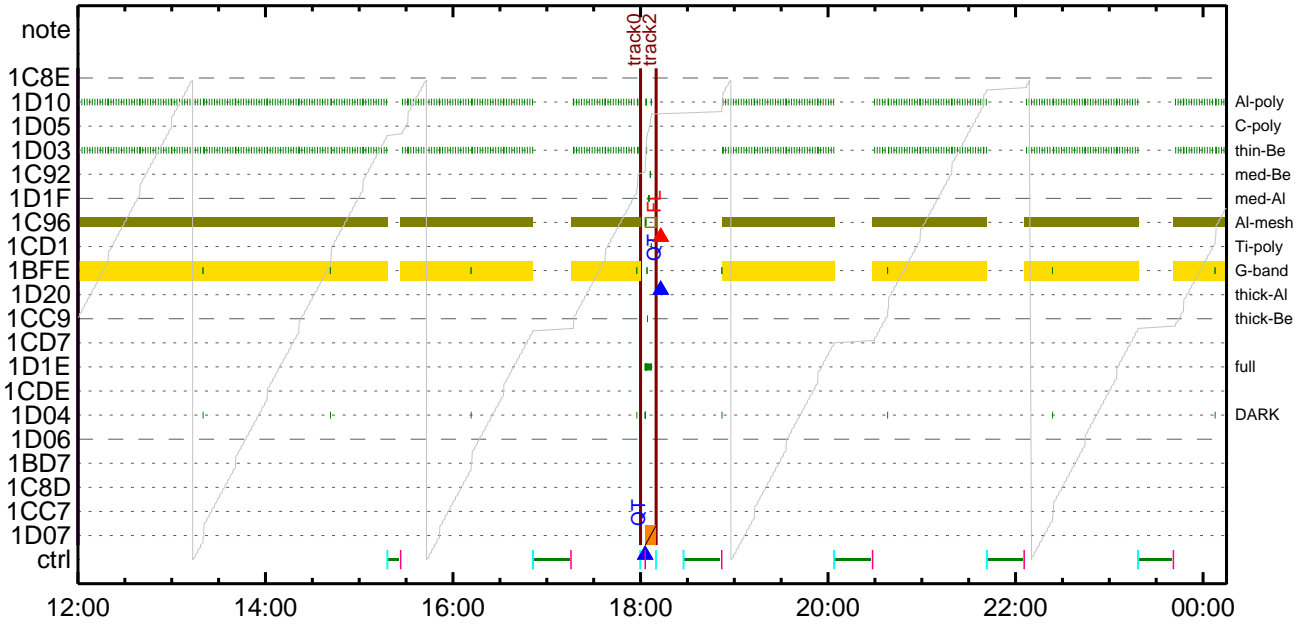
CMDI #0210 2024/11/02



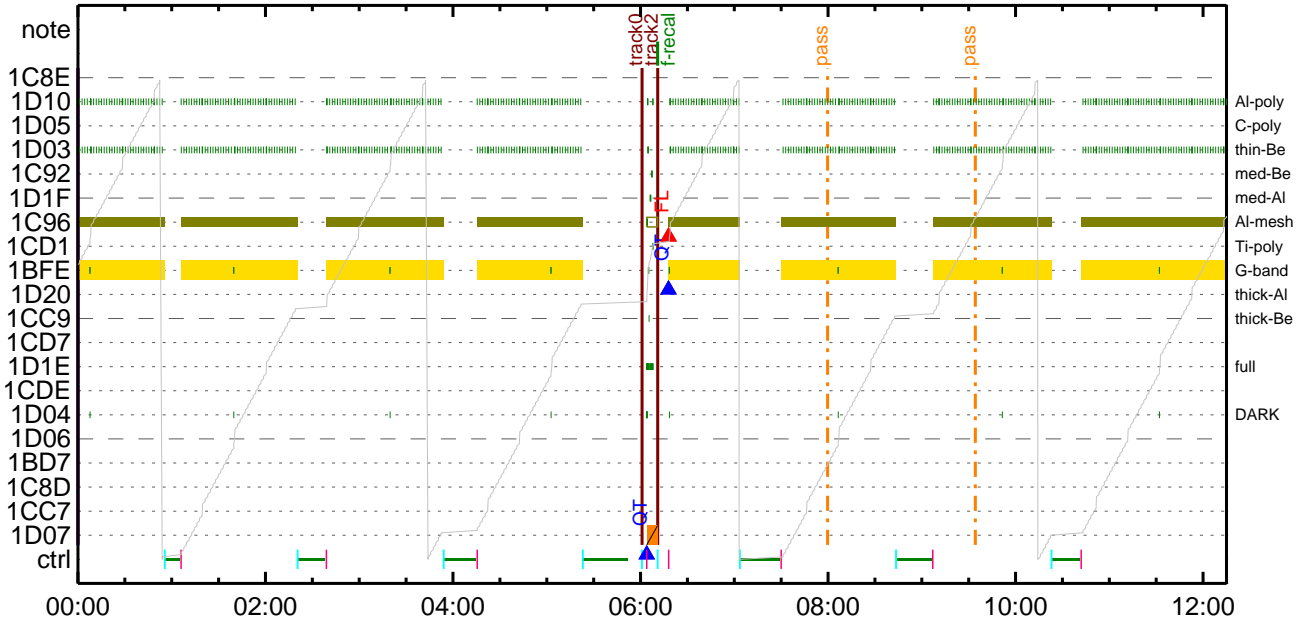
CMDI #0210 2024/11/03



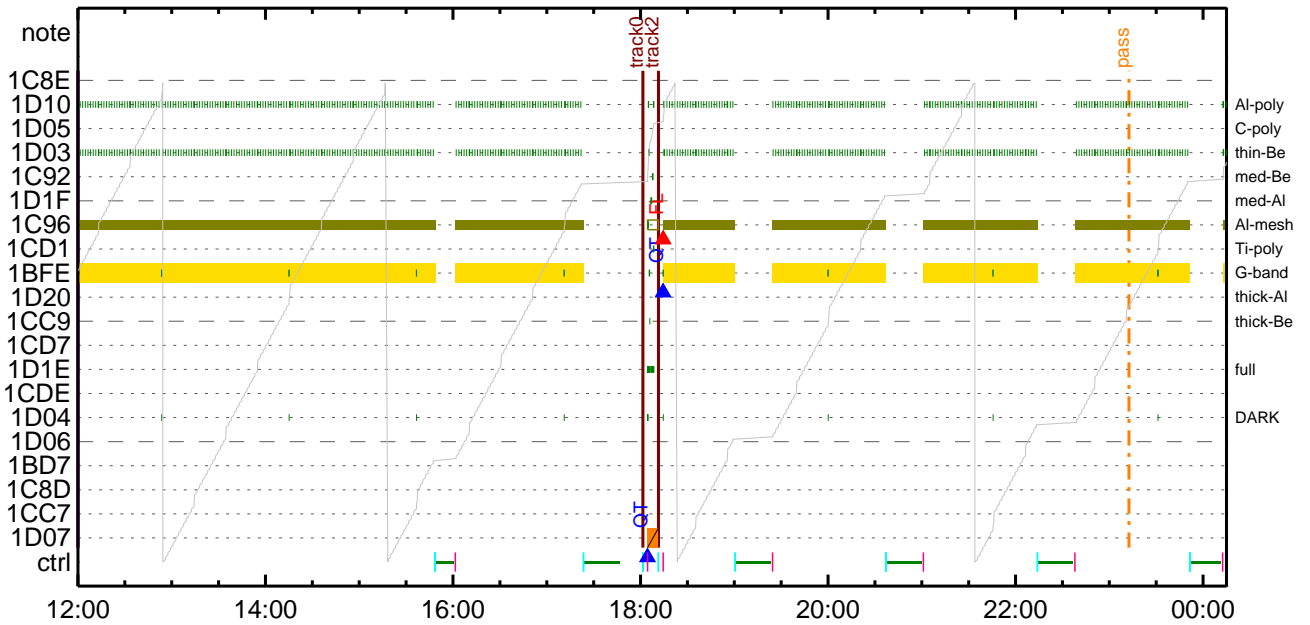
CMDI #0210 2024/11/03



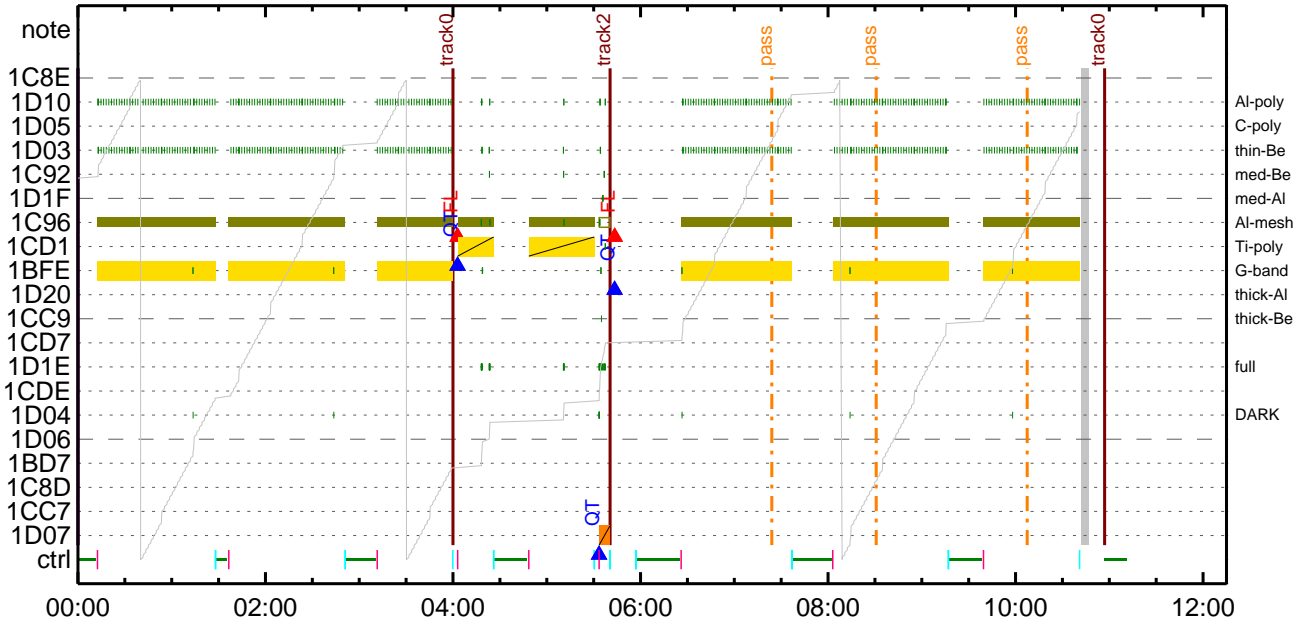
CMDI #0210 2024/11/04



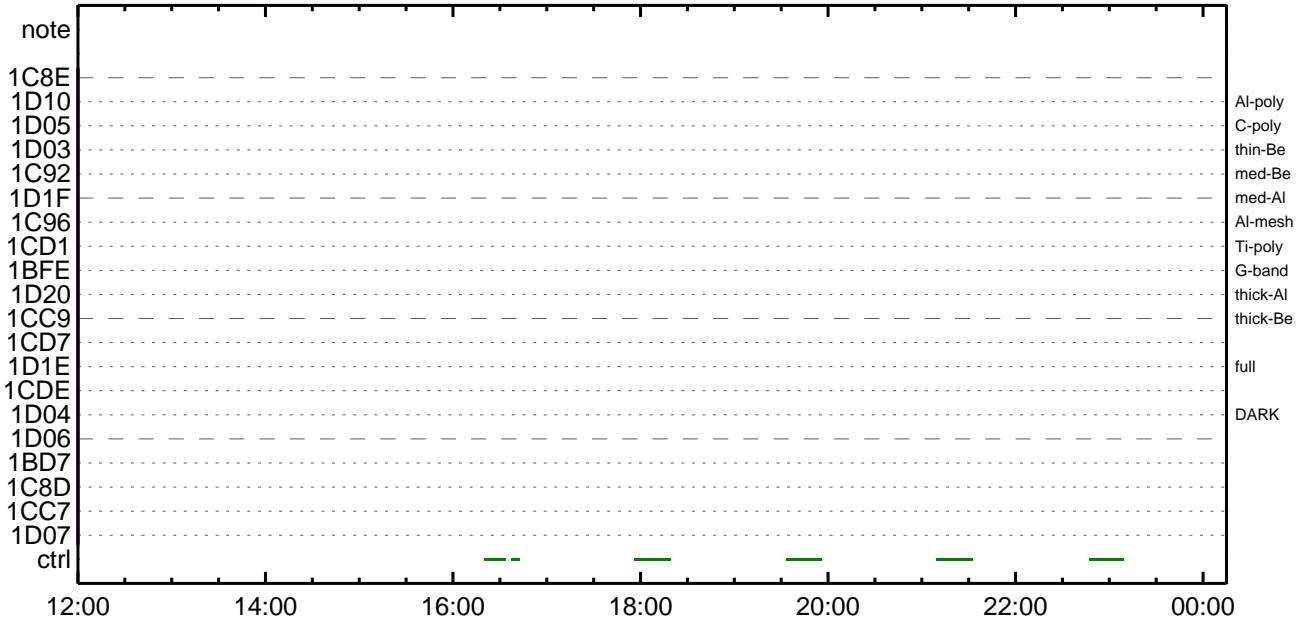
CMDI #0210 2024/11/04



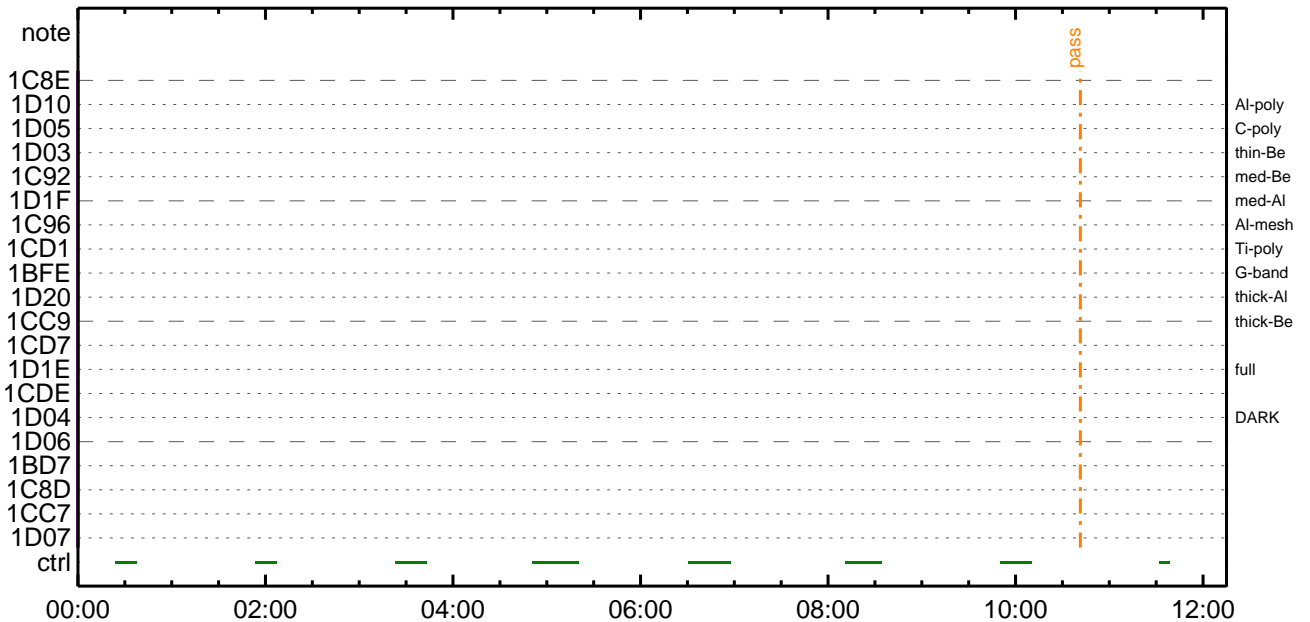
CMDI #0210 2024/11/05



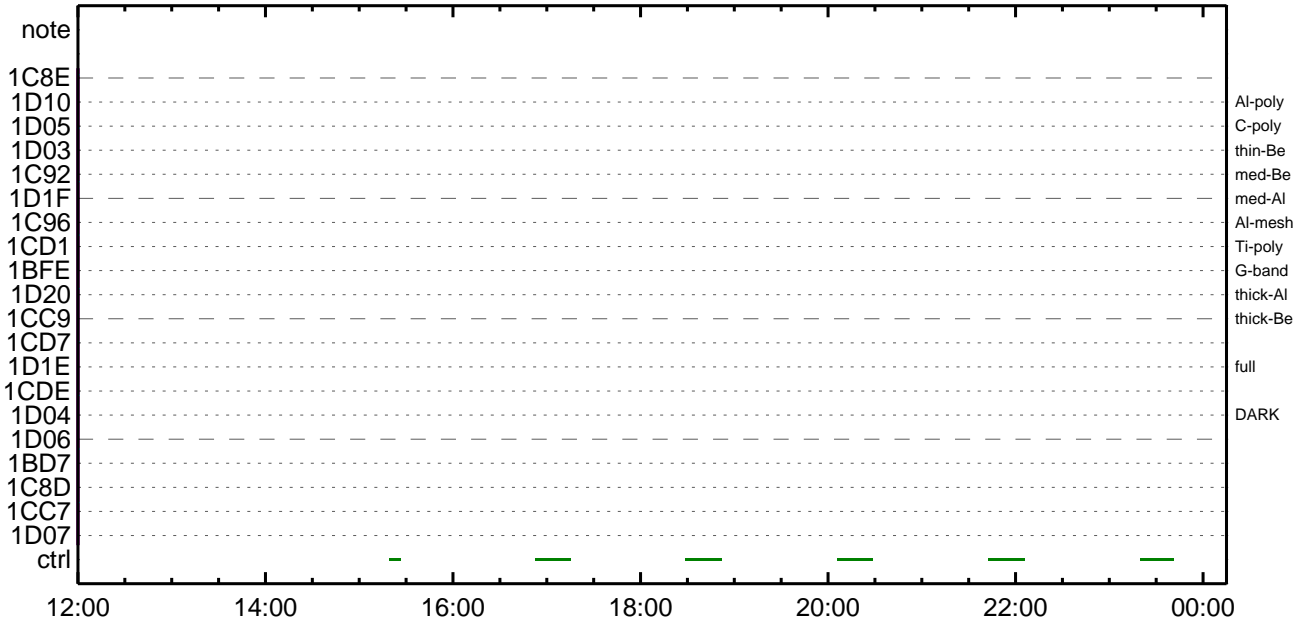
CMDI #0210 2024/11/05



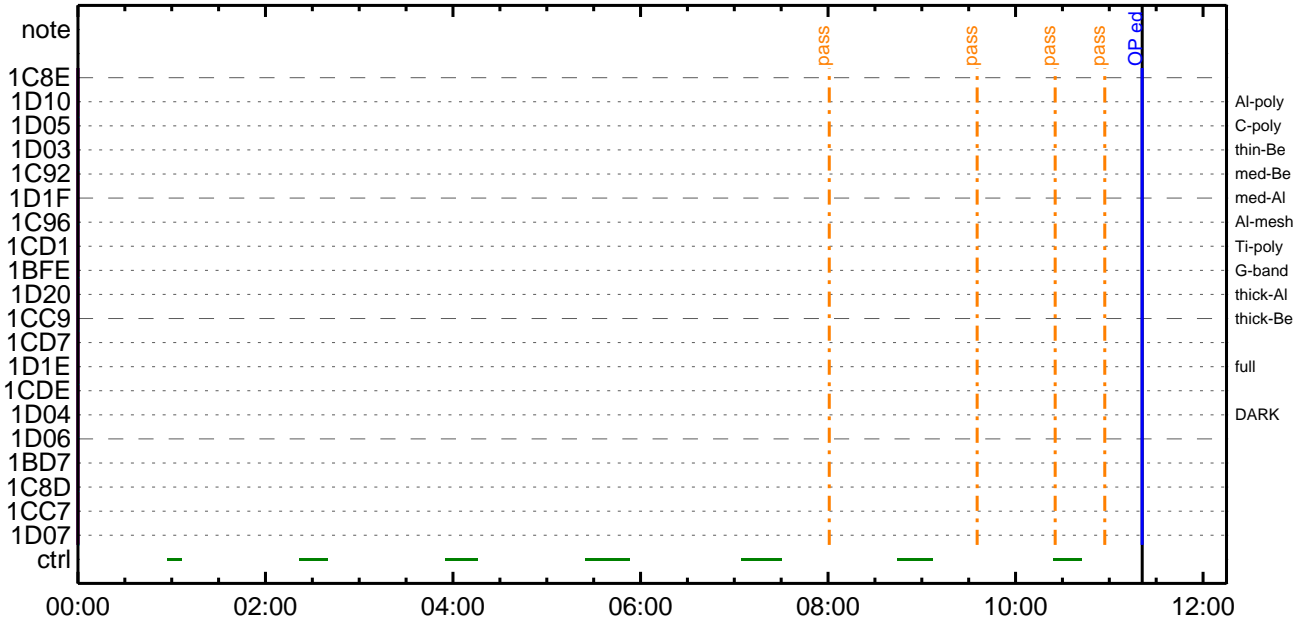
CMDI #0210 2024/11/06



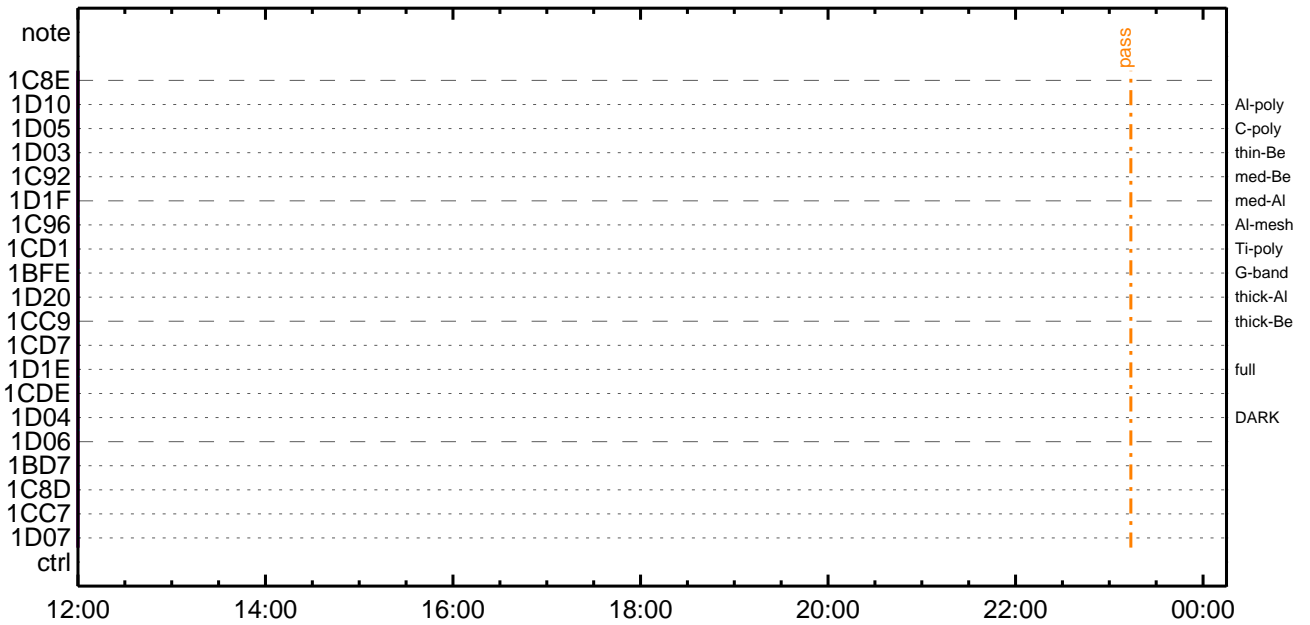
CMDI #0210 2024/11/06



CMDI #0210 2024/11/07



CMDI #0210 2024/11/07




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0096 C.          SET EEDUMP I  E  I  Y  N  Y  1  C  1  O  M  |  M  3  M  E  ;  E
0097 C.
0098 . C. TTY 3  Y  D  Y  6  Y  E  M  0  A  D  I  j  (UT)
0099 +. TI 2024-11-02 10:42:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          C  C  [HK1_TI_CMD_NUM]        EQ        1COUNTUP
0102 C.
0103 +. TI 2024-11-02 10:42:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          C  C  [HK1_TI_CMD_NUM]        EQ        1COUNTUP
0106 C.
0107 +. TI 2024-11-02 10:42:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          C  C  [HK1_TI_CMD_NUM]        EQ        1COUNTUP
0110 C.
0111 +. TI 2024-11-02 10:46:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          C  C  [HK1_TI_CMD_NUM]        EQ        1COUNTUP
0114 C.
0115 C.   E  2  M  I  A  e  3  i  I  N  M  I  V  A  Y  S  Y  A  Y  -  1  a  i  U
0116 C.          C  C  [HK1_TI_CMD_ENA/DIS]      EQ        ENA
0117 C.          C  C  [HK1_TI_CMD_NUM]        EQ        4
0118 C.          C  C  [HK1_NEXT_EXEC_PIM]      EQ        DHU
0119 C.          C  C  [HK1_NEXT_EXEC_DC]      EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TTY I  I  e  Y  A  6  Y  x
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.          C  C  [HK1_DMP_TOP_ADDR_1]      EQ        07
0129 C.          C  C  [HK1_DMP_TOP_ADDR_0]      EQ        2B
0130 C.          C  C  [HK1_DMP_BLOCK_NUM]      EQ        3
0131 C.          C  C  [HK1_DMP_REPEAT_NUM]     EQ        0
0132 C.          C  C  [HK1_DMA_DMP_PIM]       EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.          C  C  [HK1_PKT_FORM_NO]        EQ        7
0136 C.          C  C  [HK1_PKT_GEN_TIME]       EQ        0.25 s
0137 C.          C  C  [HK1_S_TLM_BIT_RATE]     EQ        32k
0138 C.          C  C  [HK1_X_TLM_BIT_RATE]    EQ        4M
0139 C.          C  C  [HK1_DMP_CHK_FLG]      EQ        EXEC
0140 C.
0141 . C.  Y  A  6  Y  x  M  A  I  M  0  3  I  C  S
0142 C.          C  C  [HK1_DMP_CHK_FLG]      EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL M  I  M  E  1  C  •  e  2  I  O  K  M  0  3  I  C  S
0145 C.
0146 . C. DHU M  a  ;  M  Y  E  ;  E  M  Y  M  ,  Y  i  ;  M  Y  E  ;  E  M  0  I  a  M  1
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.          C  C  [HK1_PKT_FORM_NO]        EQ        2
0150 C.          C  C  [HK1_PKT_GEN_TIME]       EQ        0.5S
0151 C.          C  C  [HK1_S_TLM_BIT_RATE]     EQ        32K
0152 C.          C  C  [HK1_X_TLM_BIT_RATE]    EQ        4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2024-11-02 10:46:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC      (21 02)
0163 +. TI 2024-11-02 10:46:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC      (22)
0166 . C.          [ ] [HK1_TI_CMD_NUM]      EQ        2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2024-11-02 10:46:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC      (c3)
0176 . C.          [ ] [HK1_TI_CMD_NUM]      EQ        1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP u  A  I  M  I  M  0  3  Y  M  E  A  D  M  1  M  e  DCBC  •  x  2  e  *****
0181 C. ( M  a  I  Y  O  Y  A  Y  E  Y  D  Y  E  Y  A  Y  C  Y  e  M  E  M  M  M  A  M  U  M  1  M  e  )
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** Y  D  Y  1  •  I  Daily  z  i  N  M  E  '  O  M  1  M  e  DCBC  •  x  2  e  *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;  a  L  O  S  Y  A  Y  S  Y  A  Y  -  M  A  M  U  ;  a
0192 C.
0193 . C. ***** LOS *****

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(a) Spacecraft Operation Procedure (real-commands)

```
main-909 2024-11-02 12:23:23 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸVŸÄŸ`¼Ä»Û;ã
0005 C.
0006 C. ŸÀŸß;¼Ÿ³ŸÐŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOS : Reload orbital element (send every contact) *****
0010 C. Áí;Ëñ¿µÄñ•µ°Æ»Í×ÁÇñÍŸÇŸŸVŸ×Ÿí;¼ŸÉ;ËËÈµ•ÍÍË;ËñÈ¼°ÇÖñ•ñ¿¼í¹¿ñİ;ÇÄ®, ùñ¹ñÈñÐñÇÄ÷¿®ñ•ñÈñññññÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç` °ÆÀ, Í×ÈŸñÄLOSñÐñÇñİ»p´Öñó¹ÍÍ, ñ•; ÇÉÖÍ×ñÈXÁÓONñİ¹ÓñÈñİñÈñññññÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.                ÇÇ [HK1_XPA_ON/OFF]                EQ        ON
0025 C.                ÇÇ [HK1_XPA_PWR_HI/LO]            EQ        HI
0026 C.                ÇÇ [HK1_XMOD_ON/OFF]              EQ        ON
0027 C.                ÇÇ [HK1_XMOD_QPSK/PM]              EQ        QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ`¾ÒÄÖñ¬°ÄÄÈñ•ñ¿;ñé; Ç°È²¼ñİ°ÆÀ, ¼é½Çñó¼Ä¹Óñ¹ñé;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°ÆÀ,
0033 C. *****
0034 C. Ç` RESTART;ËPT1;Ëñ•ñ¿ñ¾¼í¹¿ñİ; Ç°È²¼ñİ¼Ä¹Óñ»ñ°; ÇDCBC-150ñØ¿ËÈñà;£
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»İ;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC               (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.                ÇÇ [HK1_REP_PT_1/2]                EQ        PT1   (¼Ä¹Ó, ;¾Ú)
0043 C.                ÇÇ [HK1_REP_STA/STP]              EQ        START (¼Ä¹Ó, ;¾Ú)
0044 C.                ÇÇ [HK1_X_VC4_ON/OFF]              EQ        ON    (¼Ä¹Ó, ;¾Ú)
0045 C.
0046 . C. ;ãŸÇŸóŸŸŸÈÄÚÄØ;ÈÄ•Ä°²óÈò;Ë, ãñİ°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.                ÇÇ [HK1_REP_PT_1/2]                EQ        PT1   (¼Ä¹Ó, ;¾Ú)
0050 C.                ÇÇ [HK1_REP_STA/STP]              EQ        START (¼Ä¹Ó, ;¾Ú)
0051 C.                ÇÇ [HK1_X_VC4_ON/OFF]              EQ        ON    (¼Ä¹Ó, ;¾Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ñ¬¼«°Æ°Äã»ñ•ñ¿, ã; Ç°È²¼ñó¼Ä¹Óñ¹ñé;£
0055 C. ŸÇŸóŸŸŸÈÄÚÄØñãÄ•Ä°²óÈòñ¬¼«áñ¾¼í¹¿ñİ´°Í»ñ¹ñÈñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°ÆÀ,
0059 C. *****
0060 C. Ç` RESTART;ËPT2;Ëñ•ñ¿ñ¾¼í¹¿ñİ; Ç°È²¼ñİ¼Ä¹Óñ»ñ°; ÇDCBC-151ñØ¿ËÈñà;£
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»İ;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC               (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.                ÇÇ [HK1_REP_PT_1/2]                EQ        PT2   (¼Ä¹Ó, ;¾Ú)
0069 C.                ÇÇ [HK1_REP_STA/STP]              EQ        START (¼Ä¹Ó, ;¾Ú)
0070 C.                ÇÇ [HK1_X_VC4_ON/OFF]              EQ        ON    (¼Ä¹Ó, ;¾Ú)
0071 C.
0072 . C. ;ãŸÇŸóŸŸŸÈÄÚÄØ;ÈÄ•Ä°²óÈò;Ë, ãñİ°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.                ÇÇ [HK1_REP_PT_1/2]                EQ        PT2   (¼Ä¹Ó, ;¾Ú)
0076 C.                ÇÇ [HK1_REP_STA/STP]              EQ        START (¼Ä¹Ó, ;¾Ú)
0077 C.                ÇÇ [HK1_X_VC4_ON/OFF]              EQ        ON    (¼Ä¹Ó, ;¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Äã»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Äã»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.                ÇÇ [HK1_REP_STA/STP]              EQ        STOP
0087 C.                ÇÇ [HK1_S_VC4_ON/OFF]              EQ        OFF
0088 C.                ÇÇ [HK1_X_VC4_ON/OFF]              EQ        OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.                ÇÇ [HK1_XMOD_ON/OFF]                EQ        OFF
0095 C.                ÇÇ [HK1_XPA_ON/OFF]                EQ        OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 14s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2024-11-02 10:46:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAÎI»ö¼YnEÄDn¹nëDCBC•x²è *****
0156 C. (%ã°I¥ÓYAYEYD¥EYáYçYèE¼n¼A»Ün¹në)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥D¥¹•I Daily±çÍÑE'Øn¹nëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Á¥¹¼A»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-910 2024-11-02 12:23:23 182 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë»Í×ÁÇµÍYÇYÁY×Yí;¼YÉ;ËËÈµ•ííË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ;ÇÀ®, ùµ¹µÈµDµÇÁ÷¿®µ•µËµµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. °ËÀ, Í×ËÝµÄLOSµDµÇµÍ»p´Öµò¹íí, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµíµËµµµ³µÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY~¾ÖÁÖµ¬°ÄÄÈµ•µ¿µé; Ç°Ë²¼µí°ËÀ, ¼È½Çµò¼Á¹Öµ¹µé;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ËÀ,
0033 C. *****
0034 C. °Ë RESTART;ËPT1;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µí¼Á¹Öµµ°; ÇDCBC-150µØ¿ËÈµà;£
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÀÚÁØ;ËÁ•Ä°²óÈò;Ë, áµí°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, µ¬¼«°ËÄ»ßµ•µ¿, á; Ç°Ë²¼µò¼Á¹Öµ¹µé;£
0055 C. YÇYÓYÉYËÀÚÁØµÄÁ•Ä°²óÈòµ¬¼«µ¾¼í¹ÇµÍ´°í»µ¹µÈµDµÇÁÖµÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ËÀ,
0059 C. *****
0060 C. °Ë RESTART;ËPT2;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µí¼Á¹Öµµ°; ÇDCBC-151µØ¿ËÈµà;£
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÀÚÁØ;ËÁ•Ä°²óÈò;Ë, áµí°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Äá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Äá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 08 08)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 10)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 0e)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2024-11-02 10:46:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîñÏ»ö¼Ý±ÉÄÐ±¹±èDCBC•x²è *****
0169 C. (¼å°îÿÓÿÁÿÉÿÐÿËÿÀÿÇÿé±É½¼±¼Ä»Û¹±è)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥Ðÿ¹•Ï Daily±çÍÑ±È´Ø±¹±èDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥~¼Ä»Û;ä
0180 C.
0181 . C. ***** LOS *****
0182 C.
```

*** OP Sequence for XRT ***

2024/11/02	10:56:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	10:56:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	10:56:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/11/02	10:56:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/11/02	10:56:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	10:56:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	10:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/11/02	10:57:00.5	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 03 ce 01 f3			
2024/11/02	10:57:18.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/11/02	10:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/11/02	10:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/11/02	10:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/11/02	10:57:26.0	XRT_FLD_RESET_434_OG [0x1b2]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/11/02	10:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/11/02	10:59:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c			
2024/11/02	10:59:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2024/11/02	11:10:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/02	11:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/11/02	16:19:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	16:19:02.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	16:19:04.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/11/02	16:19:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/11/02	16:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/11/02	16:42:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/02	16:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/11/02	17:54:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	17:54:32.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	17:54:34.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2024/11/02	17:54:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2024/11/02	17:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2024/11/02	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	18:18:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	18:18:28.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2024/11/02	18:18:30.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2024/11/02	18:18:48.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2024/11/02	18:18:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2024/11/02	18:18:52.0	XRT_ARS_DIS_435_OG [0x1b3]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2024/11/02	18:21:28.0	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
2024/11/02	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2024/11/02	18:28:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	18:28:26.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2024/11/02	18:28:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2024/11/02	18:28:30.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 03 ce 01 f3			
2024/11/02	18:28:48.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2024/11/02	18:28:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2024/11/02	18:28:52.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2024/11/02	18:28:54.0	XRT_ARS_DIS_423_OG [0x1a7]						

2024/11/02	18:28:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/02	18:31:26.0	XRT_OT_PROG_SET_447_OG [0x1bf]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 0c
2024/11/02	18:31:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/11/02	18:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/02	19:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/02	19:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/02	19:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/02	19:31:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/02	19:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/02	19:55:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/02	19:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/02	21:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/02	21:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/02	21:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/02	21:08:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/02	21:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/02	21:32:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/02	21:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/02	22:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/02	22:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/02	22:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/02	22:45:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/02	22:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/02	23:08:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/02	23:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/03	00:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	00:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	00:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/03	00:23:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/03	00:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/03	00:37:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/03	00:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/03	01:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	01:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	01:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/03	01:52:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/03	01:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/03	02:06:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/03	02:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/03	03:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	03:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	03:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/03	03:21:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/03	03:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/03	03:42:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/03	03:43:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/03	04:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	04:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/03	04:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da

2024/11/03	04:49:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	04:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	05:19:00.5	XRT_Custom_430_OG [0x1ae]							
2024/11/03	05:20:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	06:00:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	06:00:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	06:00:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/03	06:01:00.0	AOCS_Or-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/11/03	06:01:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/03	06:01:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/03	06:01:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/03	06:03:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2024/11/03	06:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	06:10:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	06:10:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	06:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/11/03	06:11:00.0	AOCS_Or-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 03 ce 01 f3				
2024/11/03	06:11:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/03	06:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/03	06:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/03	06:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/03	06:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	06:13:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2024/11/03	06:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/11/03	06:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	06:29:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	06:29:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	06:29:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	06:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	06:56:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/03	06:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	08:09:30.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	08:09:32.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	08:09:34.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	08:09:36.5	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	08:12:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	08:33:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/03	08:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	09:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	09:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	09:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	09:49:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	09:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	10:09:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/03	10:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	11:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	11:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	11:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	11:30:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							

2024/11/03	11:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	11:38:30.0	XRT_Custom_430_OG [0x1ae]								
2024/11/03	11:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	15:18:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	15:18:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	15:18:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	15:18:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	15:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	15:25:30.0	XRT_Custom_430_OG [0x1ae]								
2024/11/03	15:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	16:51:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	16:51:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	16:51:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	16:51:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	16:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	17:14:30.0	XRT_Custom_430_OG [0x1ae]								
2024/11/03	17:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]								
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/03	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]								
			AOCU_NM	5	02-76	00 00 00 00 00				
2024/11/03	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/03	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/03	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/03	18:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2024/11/03	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/11/03	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	02 03 ce 01 f3				
2024/11/03	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/03	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/03	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/03	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/03	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	18:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2024/11/03	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/11/03	18:27:30.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	18:27:32.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	18:27:34.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	18:27:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	18:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	18:51:00.0	XRT_Custom_430_OG [0x1ae]								
2024/11/03	18:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	20:04:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	20:04:02.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	20:04:04.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	20:04:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				

2024/11/03	20:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	20:27:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/03	20:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	21:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	21:41:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	21:41:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	21:41:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	21:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	22:04:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/03	22:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/03	23:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	23:18:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/03	23:18:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/03	23:18:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/03	23:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/03	23:40:00.0	XRT_Custom_430_OG [0x1ae]							
2024/11/03	23:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/04	00:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	00:55:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	00:55:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/04	00:55:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/04	00:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/04	01:05:00.0	XRT_Custom_430_OG [0x1ae]							
2024/11/04	01:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/04	02:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	02:20:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	02:20:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/04	02:20:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/04	02:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/04	02:38:00.0	XRT_Custom_430_OG [0x1ae]							
2024/11/04	02:39:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/04	03:54:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	03:54:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	03:54:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/04	03:54:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/04	03:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/04	04:14:30.5	XRT_Custom_430_OG [0x1ae]							
2024/11/04	04:15:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/04	05:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	05:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	05:23:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/04	05:23:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/04	05:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/04	06:00:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	06:00:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/04	06:00:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/04	06:01:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/11/04	06:01:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/04	06:01:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/04	06:01:22.0	XRT_ARS_DIS_435_OG [0x1b3]							

2024/11/04	06:03:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2024/11/04	06:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/04	06:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	06:10:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	06:10:58.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]	XRT_FOCUS_RECAL	2	07-F8	78	00	
2024/11/04	06:11:00.0	AOCs_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	03	ce 01 f3
2024/11/04	06:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2024/11/04	06:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/04	06:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/04	06:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/11/04	06:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/11/04	06:15:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/04	06:17:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2024/11/04	06:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e	
2024/11/04	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/04	07:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	07:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	07:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/04	07:03:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/04	07:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/04	07:29:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/04	07:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/04	08:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	08:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	08:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/04	08:43:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/04	08:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/04	09:06:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/04	09:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/04	10:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	10:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	10:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/04	10:23:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/04	10:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/04	10:41:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/04	10:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/04	15:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	15:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	15:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/04	15:48:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/04	15:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/04	16:00:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/04	16:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/04	17:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	17:23:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/04	17:23:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/04	17:23:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/04	17:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		

2024/11/04	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	18:01:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	18:01:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/11/04	18:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2024/11/04	18:01:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2024/11/04	18:01:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/11/04	18:01:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/11/04	18:04:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2024/11/04	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/04	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	18:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/11/04	18:11:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 03 ce 01 f3
2024/11/04	18:11:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2024/11/04	18:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/11/04	18:11:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2024/11/04	18:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/11/04	18:11:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/04	18:14:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2024/11/04	18:14:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/11/04	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/04	19:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	19:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	19:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/04	19:00:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/04	19:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/04	19:23:30.0	XRT_Custom_430_OG [0x1ae]				
2024/11/04	19:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/04	20:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	20:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	20:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/04	20:37:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/04	20:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/04	21:00:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/04	21:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/04	22:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	22:14:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	22:14:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/04	22:14:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/04	22:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/04	22:37:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/04	22:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/04	23:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	23:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/04	23:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/04	23:51:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/04	23:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/05	00:11:30.0	XRT_Custom_430_OG [0x1ae]				
2024/11/05	00:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				

2024/11/05	01:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	01:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	01:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	01:28:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/05	01:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/05	01:35:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/05	01:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	02:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	02:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	02:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	02:51:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/05	02:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/05	03:10:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/05	03:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/11/05	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2024/11/05	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/05	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/05	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/11/05	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/11/05	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d		
2024/11/05	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2024/11/05	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	04:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	04:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	04:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	04:26:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/05	04:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/05	04:47:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/05	04:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	05:30:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	05:30:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	05:30:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/11/05	05:30:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/11/05	05:30:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/11/05	05:30:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/11/05	05:33:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2024/11/05	05:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	05:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	05:40:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	05:40:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2024/11/05	05:40:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 03 ce 01 f3		
2024/11/05	05:40:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/05	05:40:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/05	05:40:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/11/05	05:40:54.0	XRT_ARS_DIS_423_OG [0x1a7]						

2024/11/05	05:40:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2024/11/05	05:43:26.0	XRT_OT_PROG_SET_447_OG [0x1bf]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/05	05:43:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 0c
2024/11/05	05:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/11/05	05:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/05	05:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/05	05:57:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/05	06:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/05	06:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/05	06:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/11/05	07:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/05	07:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/05	07:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/05	07:37:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/05	07:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/05	08:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/05	08:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/11/05	09:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/05	09:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/05	09:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/05	09:17:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/05	09:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/05	09:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/05	09:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2024/11/05	10:41:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/05	10:57:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00