

# XRT Timeline to be uploaded on 2024/11/05

Period: 2024/11/05 10:47:00 - 2024/11/09 11:21:00

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## Normal mode

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XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms												
Term	Pointing (x, y)							Comment				
11/05 11:12:30 - 11/05 17:56:00	Fixed ( -22.0, -963.0)							# OP start + 10min + HOP081(S-pole)				
<b>PROG= 17 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 16 2-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 90 1-time(s) 30.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 60-time(s) 60.0sec												
└─ Seqn= 24 1-time(s) 30.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs 1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)												
Term	Pointing (x, y)							Comment				
11/05 18:22:30 - 11/05 18:29:24	Fixed ( 0.0, 0.0)							synoptic, shifted 19.5 min				
11/06 06:05:30 - 11/06 06:12:24	Fixed ( 0.0, 0.0)							HOP349 + synoptic, shifted 2.5 min				
11/06 18:03:00 - 11/06 18:09:54	Fixed ( 0.0, 0.0)							synoptic				
11/07 06:05:00 - 11/07 06:11:54	Fixed ( 0.0, 0.0)							HOP349 + synoptic, shifted 2.0 min				

<b>PROG= 01 1-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 5 1-time(s) 2.0sec												
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 26 1-time(s) 2.0sec												
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 99 1-time(s) 2.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 83 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 23 1-time(s) 4.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 41 1-time(s) 2.0sec												
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Seqn= 17 1-time(s) 2.0sec												
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 33 1-time(s) 2.0sec												
	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 75 1-time(s) 2.0sec												
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with												
Term	Pointing (x, y)							Comment				
11/05 18:32:30 - 11/06 03:59:54	Track ( -416.4, -200.7) @ 11/05 18:29:30							AR13883				
11/06 06:15:30 - 11/06 17:59:54	Track ( -314.1, -202.6) @ 11/06 06:12:30							AR13883				
11/06 18:13:00 - 11/07 03:55:00	Track ( -205.7, -203.6) @ 11/06 18:10:00							AR13883				
11/07 06:15:00 - 11/07 11:10:15	Track ( -94.0, -203.7) @ 11/07 06:12:00							AR13883				

<b>PROG= 12 Inf.-time(s)</b>												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 92 1-time(s) 2.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2		4-time(s)		2.0sec											
Seqn= 47		1-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Seqn= 77		4-time(s)		300.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

<b>XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w</b>												
Term		Pointing (x, y)				Comment						
11/06 04:03:00 - 11/06 06:02:24		Fixed ( 0.0, 0.0)				HOP349 + synoptic, shifted 2.5 min						
11/07 04:17:00 - 11/07 06:01:54		Fixed ( 0.0, 0.0)				HOP349 + synoptic, shifted 2.0 min						

<b>PROG= 13 Inf.-time(s)</b>															
Subr= 1		1-time(s)		300.0sec											
Seqn= 55		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 15		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 79		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec			
Seqn= 30		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec			
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec			
Subr= 2		15-time(s)		1500.0sec											
Seqn= 8		1-time(s)		2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec			
Seqn= 74		1-time(s)		2.0sec											
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec			
Seqn= 6		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec			
Seqn= 29		1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec			
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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**Flare mode**

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<b>XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G</b>												
Term		Pointing (x, y)				Comment						
11/05 11:12:30 - 11/05 17:56:00		Fixed ( -22.0, -963.0)				# OP start + 10min + HOP081(S-pole)						
11/05 18:32:30 - 11/06 03:59:54		Track ( -416.4, -200.7) @ 11/05 18:29:30				AR13883						
11/06 04:03:00 - 11/06 06:02:24		Fixed ( 0.0, 0.0)				HOP349 + synoptic, shifted 2.5 min						
11/06 06:15:30 - 11/06 17:59:54		Track ( -314.1, -202.6) @ 11/06 06:12:30				AR13883						
11/06 18:13:00 - 11/07 03:55:00		Track ( -205.7, -203.6) @ 11/06 18:10:00				AR13883						
11/07 04:17:00 - 11/07 06:01:54		Fixed ( 0.0, 0.0)				HOP349 + synoptic, shifted 2.0 min						
11/07 06:15:00 - 11/07 11:10:15		Track ( -94.0, -203.7) @ 11/07 06:12:00				AR13883						

<b>PROG= 14 30-time(s)</b>															
Subr= 1		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 73		1-time(s)		10.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 87		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

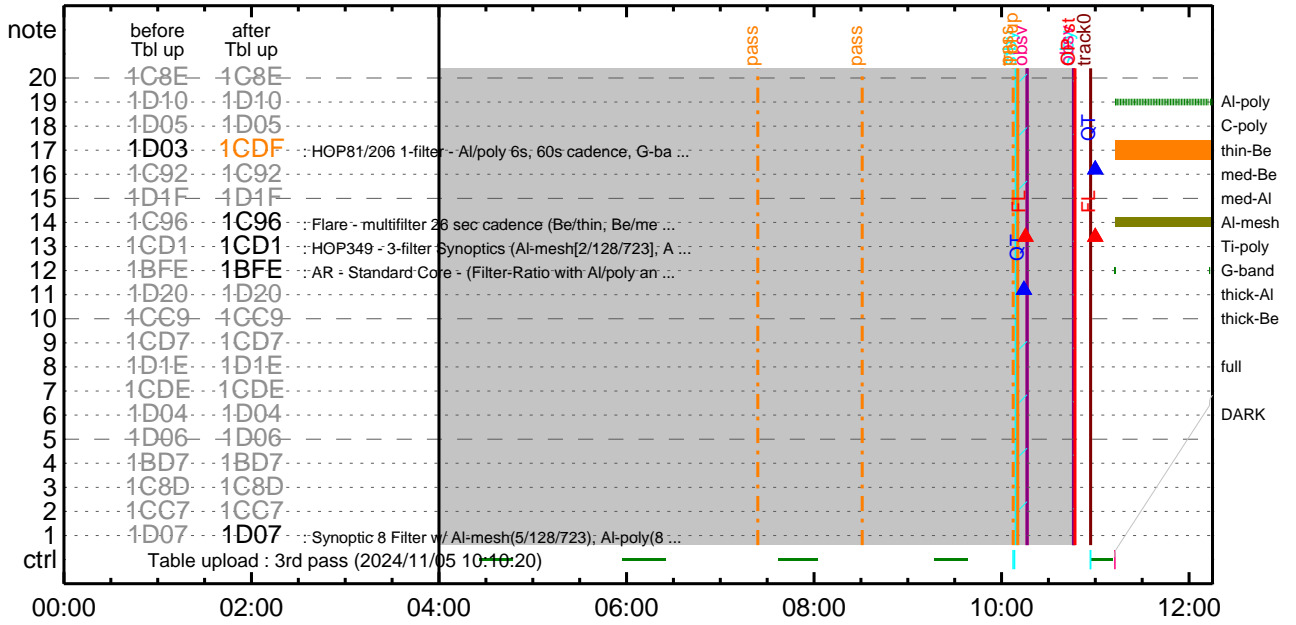
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### Flare Detection

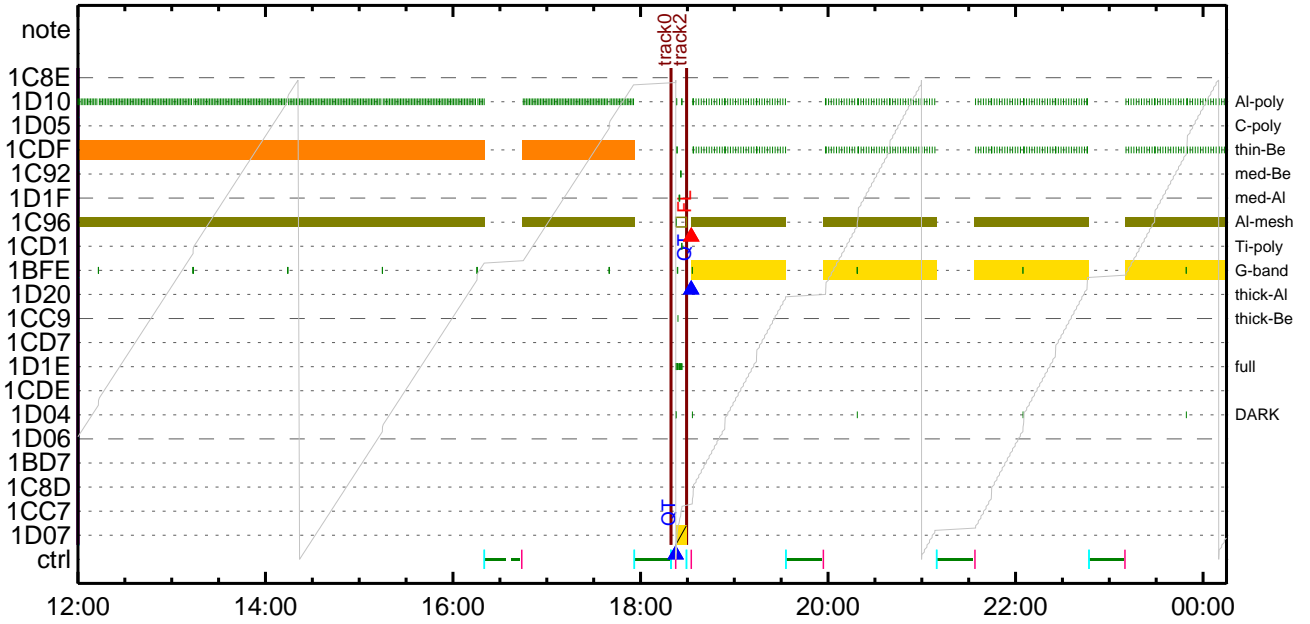
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FLD Patrol												
Term	Pointing (x, y)							Comment				
11/05 10:11:20 - 11/05 18:19:48	cannot be identified											
11/05 18:29:48 - 11/06 06:02:48	Track ( -416.4, -200.7)	@ 11/05 18:29:30	AR13883									
11/06 06:12:48 - 11/06 18:00:18	Track ( -314.1, -202.6)	@ 11/06 06:12:30	AR13883									
11/06 18:10:18 - 11/07 06:02:18	Track ( -205.7, -203.6)	@ 11/06 18:10:00	AR13883									
11/07 06:12:18 - 11/09 11:21:00	Track ( -94.0, -203.7)	@ 11/07 06:12:00	AR13883									
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

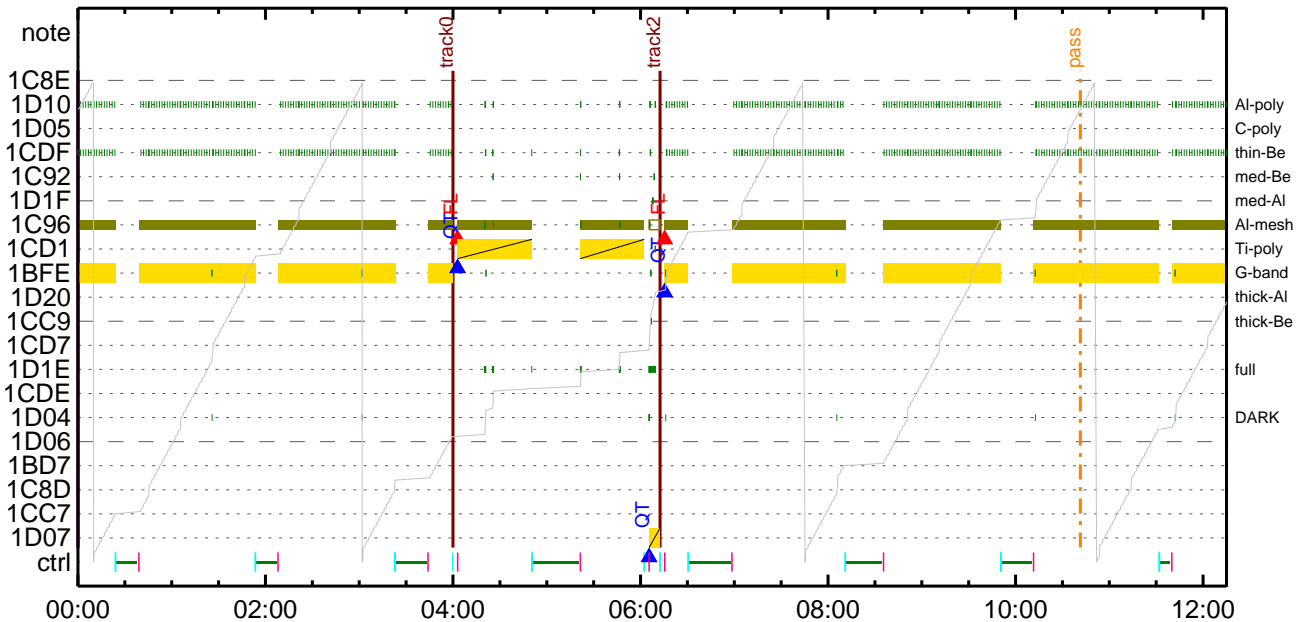
### CMDI #0217 2024/11/05



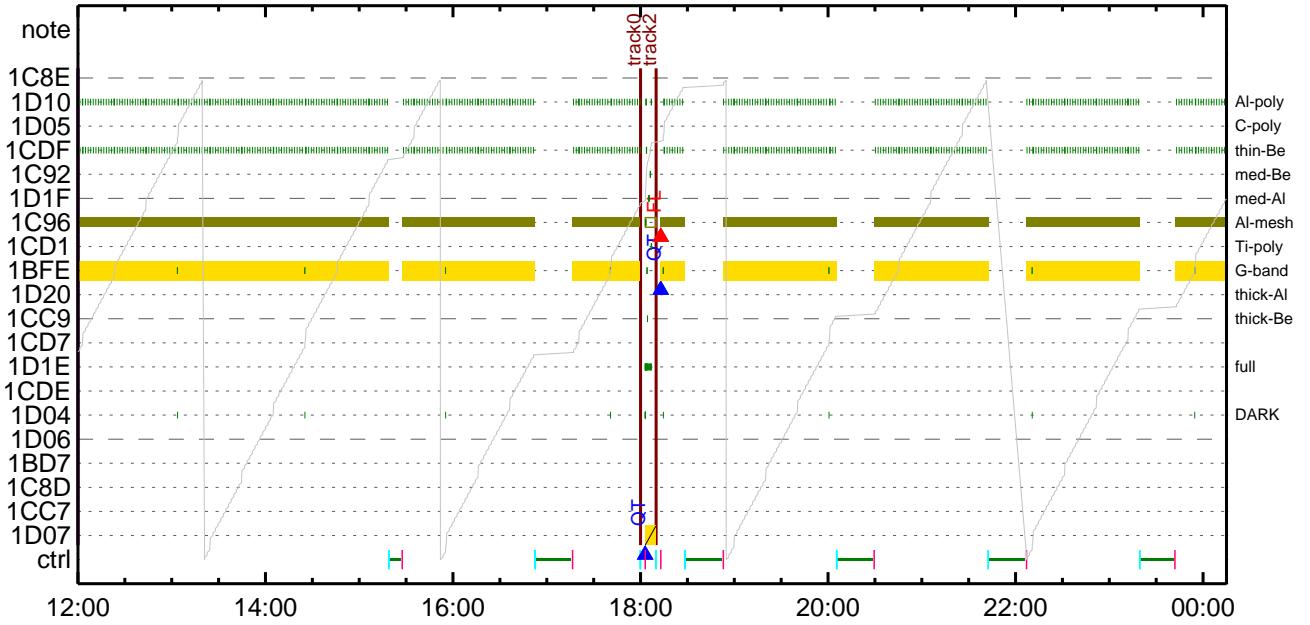
### CMDI #0217 2024/11/05



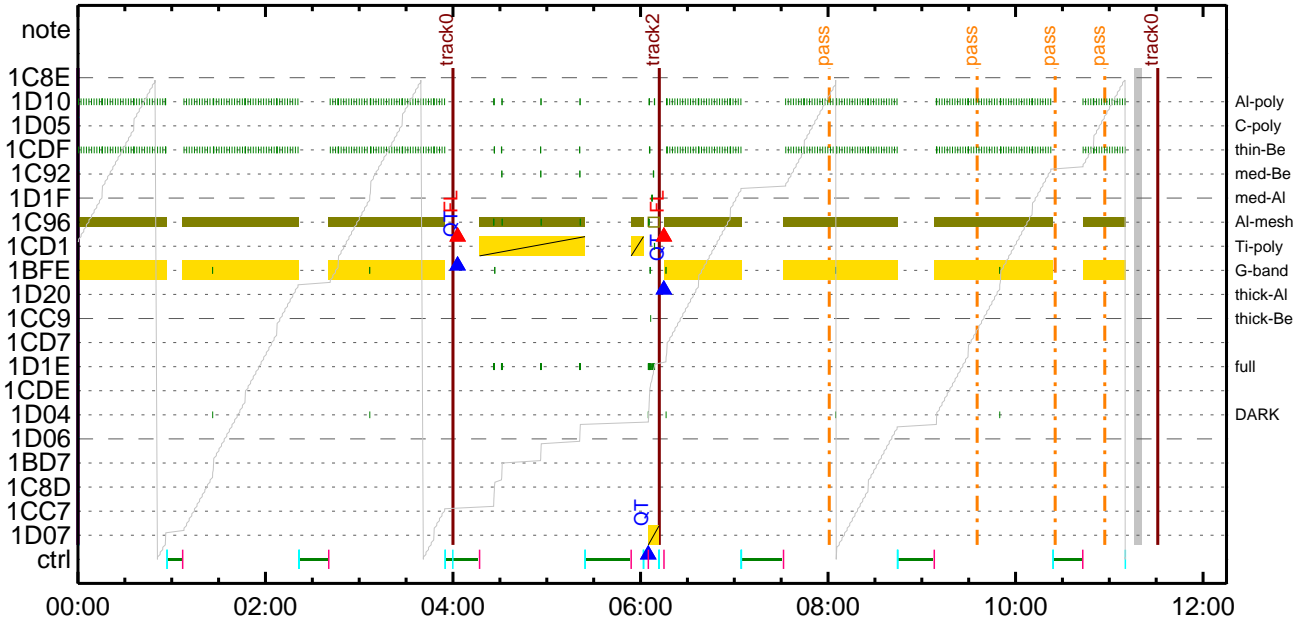
### CMDI #0217 2024/11/06



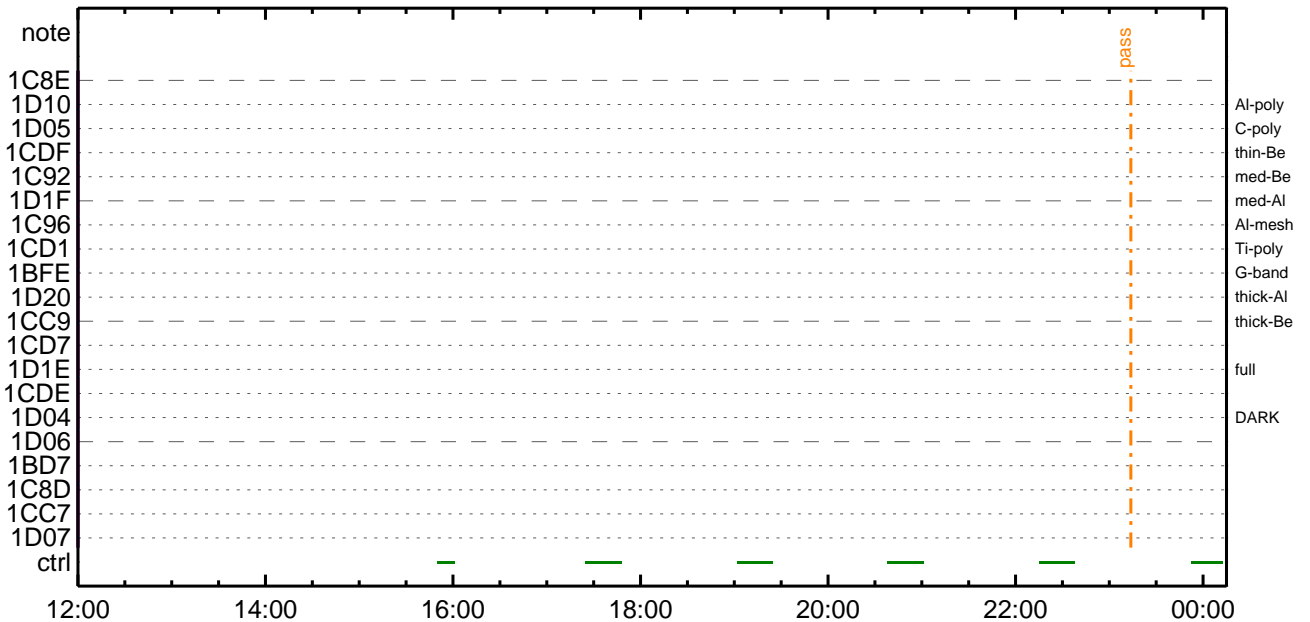
### CMDI #0217 2024/11/06



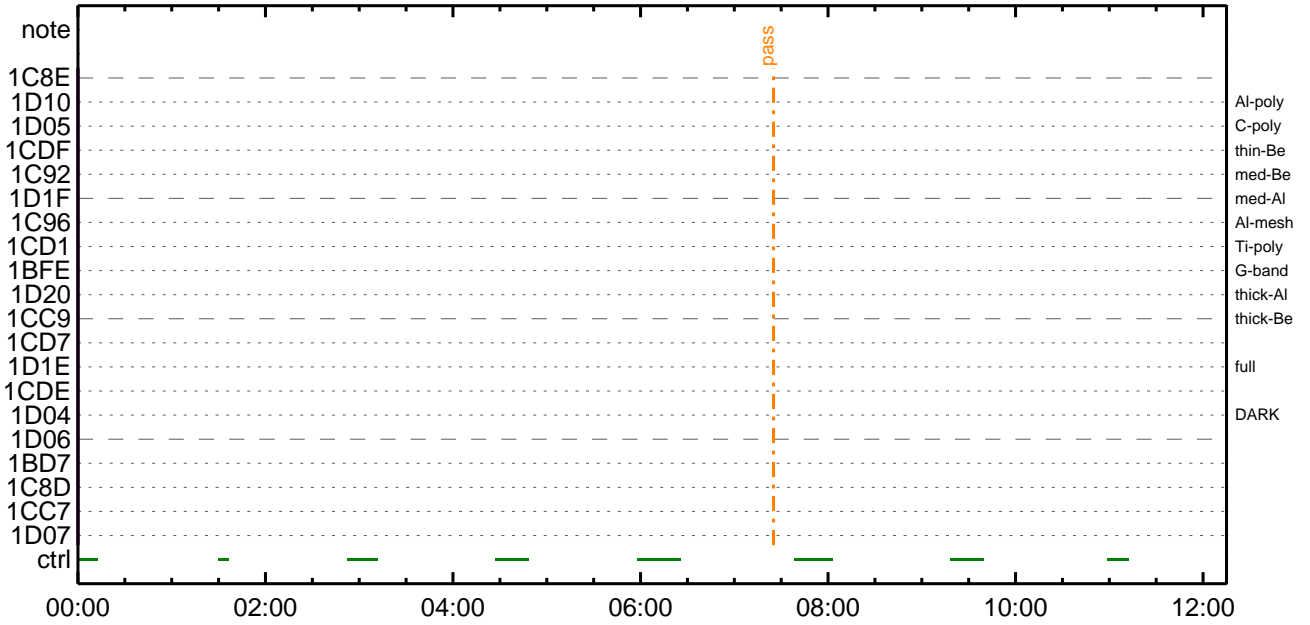
### CMDI #0217 2024/11/07



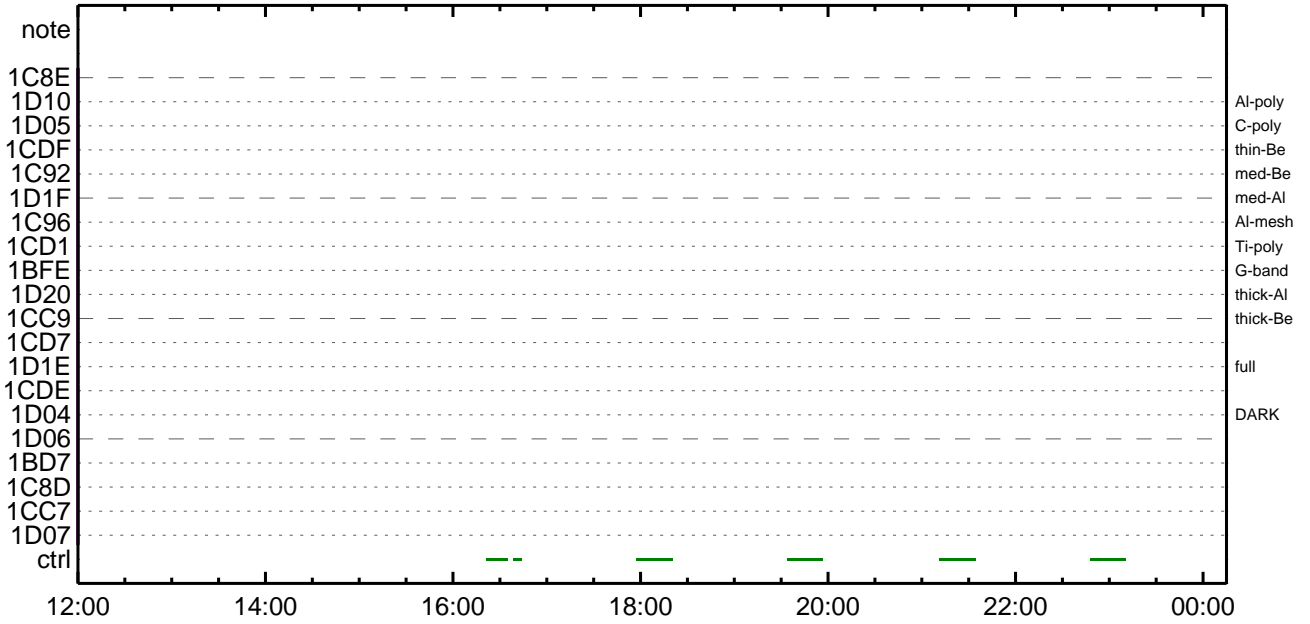
### CMDI #0217 2024/11/07



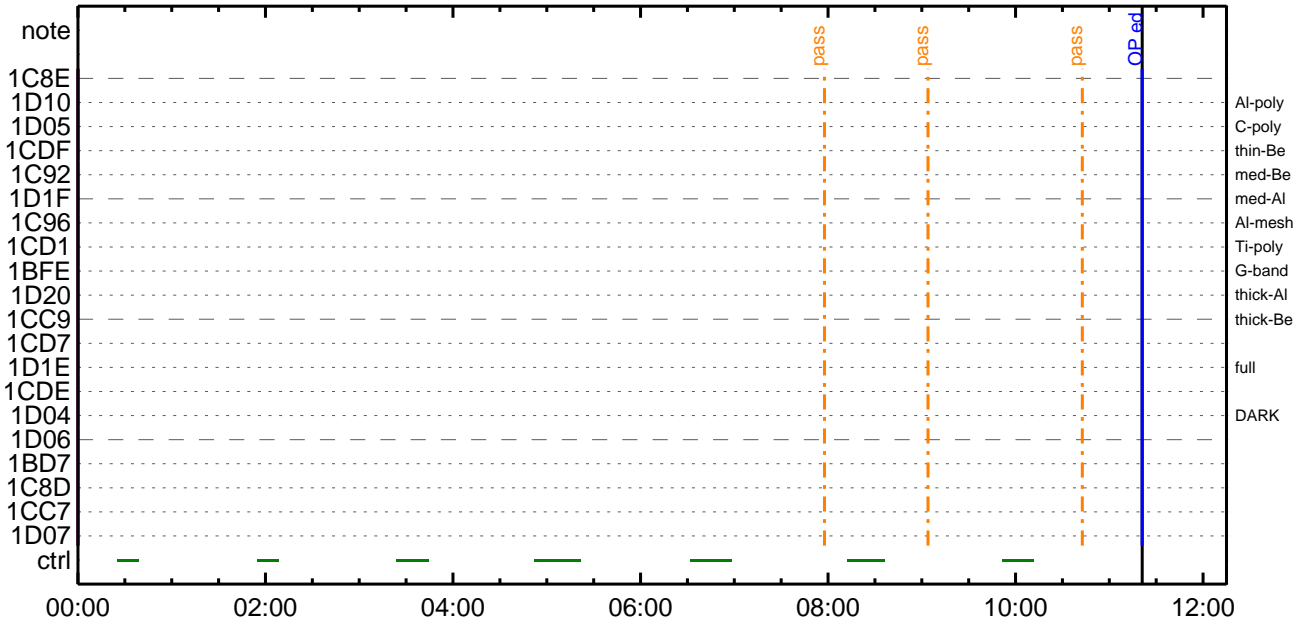
CMDI #0217 2024/11/08

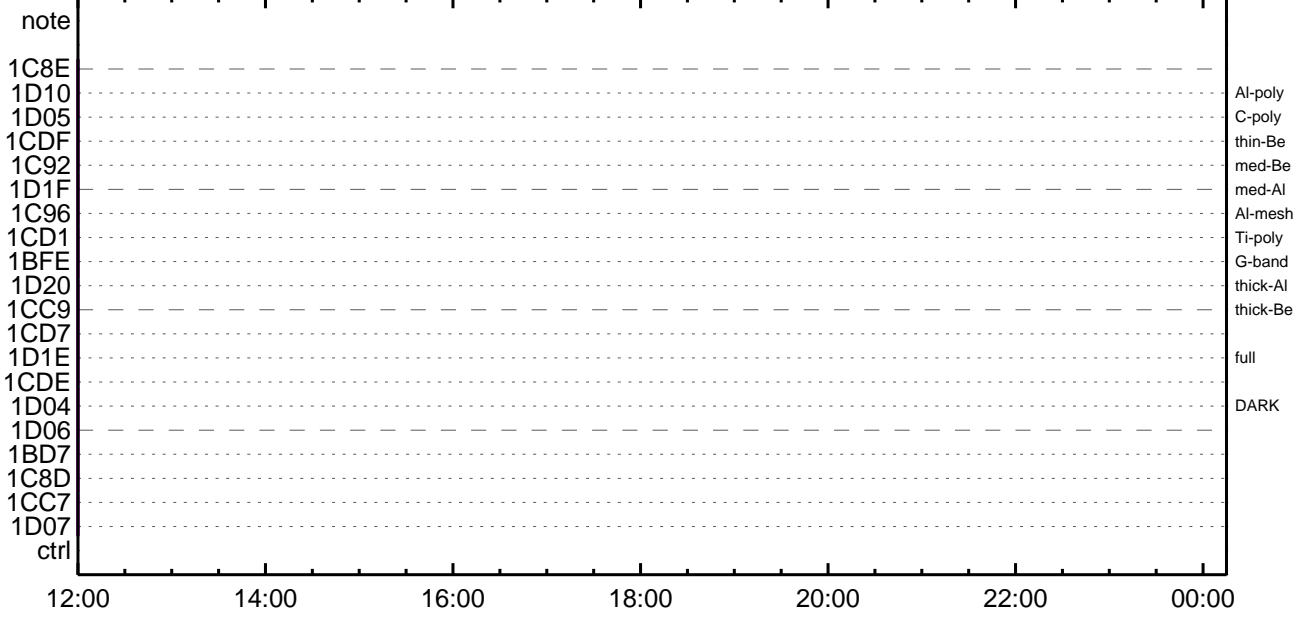


CMDI #0217 2024/11/08



CMDI #0217 2024/11/09





(a) Spacecraft Operation Procedure (real-commands)

```
main-912 2024-11-05 14:37:21 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÿÁÿ$ÿÄÿ~¼Ä»Û;ä
0005 C.
0006 C. ÿÀÿß;¼ÿ³ÿDÿóÿÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËñçµÄñ•µ°Æ»Í×ÁÇñÍÿçÿÿÄÿ×ÿÿí;¼ÿÉ;ËËËµ•ííË;ËñË¼°ÇÓñ•ñç¼í¹çñí;çÀ®, ùñ¹ñËñDñÇÄ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGÿí;¼ÿÉ;ÿÿÿÄÿóÿ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGÿí;¼ÿÉ;ä
0019 . S. OP op-912:OP
0020 ()
0021 . S. OG og-912:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPíî°èÿÿÄÿóÿ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ÿÀÿóÿ×½ªí»ð³íÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñí¼Ë¹ç•ë²íOKñð³íÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ÿÀÿóÿ×½ªí»ð³íÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñí¼Ë¹ç•ë²íOKñð³íÇ$
0062 C.
0063 C. NMOG(0x210000-0x210FFF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ÿÀÿóÿ×½ªí»ð³íÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPñí¼Ë¹ç•ë²íOKñð³íÇ$
0081 C.
0082 . C. ***** °Ë²¼ñí¼Ä´¶¼°ñËË¬ñ°Á÷ç® (¼ãµ-ÿÀÿóÿ×½ªí»ð³íÇñðÁÓÃñç½ªñ¬ñë¼í¹çñçñá) *****
0083 C. DHUÿá;¼ÿÉ;Ë¼ÿ½;ÿí;¼ÿÉ;Ëñðíáñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®ñGñí¼í¹ç;ç°Ë²¼ñíTI-CMDÁ÷ç®ñí¼Á¹Ôñ•ñËñññ³ñË;£
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0096 C.          SET EDUMP I±°iNŸ¹qÇ¹Ôm|³mÈ;£
0097 C.
0098 . C. TTY³ŸDŸÓŸÉ±ðÀDî¿ (UT)
0099 +. TI 2024-11-05 10:42:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0102 C.
0103 +. TI 2024-11-05 10:42:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0106 C.
0107 +. TI 2024-11-05 10:42:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0110 C.
0111 +. TI 2024-11-05 10:46:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]           EQ      1COUNTUP
0114 C.
0115 C. °Ê¼mİÄê¼iÍŸmİŸÁŸSŸÄŸ¹àÛ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.          çç[HK1_TI_CMD_NUM]         EQ      4
0118 C.          çç[HK1_NEXT_EXEC_PIM]      EQ      DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0120 C.
0121 . C. *****
0122 C. TIIÎ°èŸÄŸÓŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0131 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0132 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0136 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0139 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0140 C.
0141 . C. ŸÄŸÓŸ×¼²¹İ»¼ð³ÍÇS
0142 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0143 C.
0144 . C. RAM ID=TI_TBL±İ¼È¹Ç•è²İOK¼ð³ÍÇS
0145 C.
0146 . C. DHUŸâ;¼ŸÉ;È¼Ÿ¼,Ÿi;¼ŸÈ;È¼ðİá±¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0150 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0153 C.
0154 . C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2024-11-05 10:46:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2024-11-05 10:46:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 . C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2024-11-05 10:46:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 . C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´ûÃî±İ»ð¼Ÿ±ÈÄð±¹èDCBC•×²è *****
0181 C. (¼å°İŸÓŸÄŸÉŸDŸŸÈŸÄŸÇŸÈ±È¼¼m¼Ä»Û±¼è)
0182 . S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 . C. ***** ŸDŸ¹•İ Daily±¿İŸ±È´Ø±¹èDCBC•×²è *****
0187 . S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 . C. ;ãLOSŸÁŸŸÄŸ¹¼Ä»Û;ä
0192 C.
0193 . C. ***** LOS *****

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(a) Spacecraft Operation Procedure (real-commands)

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main-913 2024-11-05 14:37:21 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Ä»Ü;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ËËÈµ•ÍÍË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÄ®, ùµ¹µÈµDµÇÄ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç¨ °ËÀ, Í×ËÝµÄLOSµDµÇµÍ»p´Öµò¹ÍÍ, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYÍYÁY~¾ÖÄÖµ¬°ÄÄÈµ•µ¿µé; Ç°Ë²¼µÍ°ËÀ, ¼È½Çµò¼Ä¹Öµ¹µé; £
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÌ¼Í°ËÀ,
0033 C. *****
0034 C. Ç¨ RESTART;ËPT1;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Ä¹Öµµ°; ÇDCBC-150µØ¿ËÈµà; £
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¾Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¾Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¾Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYËÄÜÄØ;ËÄ•Ä°²óÈò;Ë, äµÍ°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¾Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¾Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¾Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, µ¬¼«°ËÄ»ßµ•µ¿, ä; Ç°Ë²¼µò¼Ä¹Öµ¹µé; £
0055 C. YÇYÓYÉYËÄÜÄØµÄÄ•Ä°²óÈòµ¬¼«µ¾¼í¹ÇµÍ´°Í°Í»µ¹µÈµDµÇÄÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÌ¼Í°ËÀ,
0059 C. *****
0060 C. Ç¨ RESTART;ËPT2;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Ä¹Öµµ°; ÇDCBC-151µØ¿ËÈµà; £
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¾Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¾Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¾Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYËÄÜÄØ;ËÄ•Ä°²óÈò;Ë, äµÍ°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¾Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¾Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, ÄÄ»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, ÄÄ»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 + DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 1003913.0 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 9s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2024-11-05 10:46:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'úÁî=Î»ö¼Ý=ÉÄÐ=¹=ëDCBC•x²è *****
0165 C. (¼á°îÝÓÝÁÝÉÝÐÝÉÝÁÝçÝè=É¼¼=¼Á»Û¹=ë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•İ Daily±çİÑ=É'Ø¹=ëDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÁÝSÝÁÝ¹¼Á»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-914 2024-11-05 14:37:21 182 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝÓYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÄµ•µ°Ë>Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ËËÈµ•íÍË;ËËÈ¼°ÇÖµ•µ¿¼í¹ÇµÍ; ÇÀ®, ùµ¹µÈµDµÇÁ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. Ç“ °ËÀ, Í×ÈYµÄLOSµDµÇµÍ»p´Öµò¹ÍÍ, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝÓYÉYíYÁY-¾ÖÁÖµ¬°ÄÄÈµ•µ¿µé; Ç°Ë²¼µÍ°ËÀ, ¼È½Çµò¼Á¹Öµ¹µé; £
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ËÀ,
0033 C. *****
0034 C. Ç“ RESTART;ËPT1;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Á¹Öµµ°; ÇDCBC-150µØ¿ËÈµà; £
0035 C.
0036 . C. ;ãPT1°ËÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÉYËÀÙÁØ;ËÁ•Ä°²óÈò;Ë, áµÍ°ËÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÀ, µ¬¼«°ËÄ»ßµ•µ¿, á; Ç°Ë²¼µò¼Á¹Öµ¹µé; £
0055 C. YçYóYÉYËÀÙÁØµÄÁ•Ä°²óÈòµ¬¼áµ¾¼í¹ÇµÍ´°Í´°Í»µ¹µÈµDµÇÁÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ËÀ,
0059 C. *****
0060 C. Ç“ RESTART;ËPT2;Ëµ•µ¿µ¾¼í¹ÇµÍ; Ç°Ë²¼µÍ¼Á¹Öµµ°; ÇDCBC-151µØ¿ËÈµà; £
0061 C.
0062 . C. ;ãPT2°ËÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÉYËÀÙÁØ;ËÁ•Ä°²óÈò;Ë, áµÍ°ËÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÀ, Äá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÀ, Äá»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_ARS_DIS
0145 BC (d5)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 0c)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 0e)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2024-11-05 10:46:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP 'ûÃîñÍ»ö¼Ý²ÈÄÐñ¹²³DCBC•x²è *****
0169 C. (¼å°îÿÓÿÃÿÈÿÐÿÿ¼ÿçÿè²¼³ñ¼Å»Û¹²è)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥Ðÿ¹•Ï Daily±çÍÑ²È´Øñ¹²³DCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥´¼Å»Û;ä
0180 C.
0181 . C. ***** LOS *****
0182 C.
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\*\*\* OP Sequence for XRT \*\*\*

2024/11/05	10:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	10:56:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	10:56:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/11/05	10:57:00.0	AOCs_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	55	99	01	f3
2024/11/05	10:57:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/05	10:57:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/05	10:57:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/05	10:57:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/05	10:57:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/05	10:59:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2024/11/05	10:59:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/11/05	11:11:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/05	11:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/05	16:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	16:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	16:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/05	16:20:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/05	16:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/05	16:43:00.0	XRT_Custom_430_OG [0x1ae]							
2024/11/05	16:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/05	17:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	17:56:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	17:56:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/05	17:56:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/05	17:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/05	18:19:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	18:19:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	18:19:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/11/05	18:19:30.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2024/11/05	18:19:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/05	18:19:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/05	18:19:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/05	18:22:28.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2024/11/05	18:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/05	18:29:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	18:29:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/05	18:29:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/11/05	18:29:30.0	AOCs_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	03	ce	01	f3
2024/11/05	18:29:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/05	18:29:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/05	18:29:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/05	18:29:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/05	18:29:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/05	18:32:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2024/11/05	18:32:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/11/05	18:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/05	19:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							

2024/11/05	19:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	19:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	19:33:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	19:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/05	19:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/05	19:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	21:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	21:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	21:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	21:09:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	21:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/05	21:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/05	21:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/05	22:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	22:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	22:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/05	22:47:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/05	22:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/05	23:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/05	23:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	00:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	00:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	00:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	00:24:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/06	00:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/06	00:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	00:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	01:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	01:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	01:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	01:53:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/06	01:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/06	02:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	02:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	03:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	03:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	03:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	03:23:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/06	03:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/06	03:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	03:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	04:00:00.0	AOCs_Or-e-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/11/06	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00		
2024/11/06	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/06	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/06	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/11/06	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/11/06			MDP_XRT_FLD_RESET	1	07-F0	da		



2024/11/06	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2024/11/06	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/06	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/06	04:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	04:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	04:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/06	04:50:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/06	04:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/06	05:20:30.0	XRT_Custom_430_OG [0x1ae]					
2024/11/06	05:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/06	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	06:02:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	06:02:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2024/11/06	06:02:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2024/11/06	06:02:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2024/11/06	06:02:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/06	06:05:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2024/11/06	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/06	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	06:12:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	06:12:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2024/11/06	06:12:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02	03 ce 01 f3
2024/11/06	06:12:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/11/06	06:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/11/06	06:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/11/06	06:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/06	06:12:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/06	06:15:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2024/11/06	06:15:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/06	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/06	06:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	06:30:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	06:30:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/06	06:30:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/06	06:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/06	06:57:30.0	XRT_Custom_430_OG [0x1ae]					
2024/11/06	06:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/06	08:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	08:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	08:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/06	08:11:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/06	08:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/06	08:34:30.0	XRT_Custom_430_OG [0x1ae]					
2024/11/06	08:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/06	09:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	09:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/06	09:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/06	09:50:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

2024/11/06	09:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/06	10:10:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/06	10:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/06	11:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	11:32:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	11:32:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/06	11:32:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/06	11:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/06	11:39:00.0	XRT_Custom_430_OG [0x1ae]							
2024/11/06	11:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/06	15:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	15:19:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	15:19:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/06	15:19:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/06	15:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/06	15:26:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/06	15:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/06	16:52:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	16:52:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	16:52:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/06	16:52:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/06	16:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/06	17:15:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/06	17:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/06	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/06	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2024/11/06	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/06	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/06	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/06	18:02:58.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2024/11/06	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/06	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/11/06	18:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 03 ce 01 f3				
2024/11/06	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/06	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/06	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/06	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/06	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/06	18:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2024/11/06	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/11/06	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/06	18:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	18:28:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/06	18:28:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/06	18:28:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							

2024/11/06	18:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	18:52:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/06	18:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	20:05:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	20:05:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	20:05:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/06	20:05:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/06	20:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	20:28:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/06	20:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	21:42:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	21:42:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	21:42:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/06	21:42:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/06	21:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	22:06:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/06	22:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/06	23:19:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	23:19:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/06	23:19:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/06	23:19:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/06	23:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/06	23:41:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/06	23:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/07	00:57:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	00:57:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	00:57:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/07	00:57:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/07	01:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/07	01:06:00.0	XRT_Custom_430_OG [0x1ae]						
2024/11/07	01:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/07	02:21:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	02:21:32.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	02:21:34.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/07	02:21:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/07	02:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/07	02:39:30.0	XRT_Custom_430_OG [0x1ae]						
2024/11/07	02:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/07	03:55:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	03:55:02.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	03:55:04.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/07	03:55:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/07	03:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/07	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/07	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/11/07	04:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2024/11/07	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/07	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/07	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		

2024/11/07	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/07	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/07	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2024/11/07	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/07	04:16:00.0	XRT_Custom_430_OG [0x1ae]					
2024/11/07	04:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/07	05:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	05:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	05:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/07	05:24:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/07	05:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/07	05:53:00.0	XRT_Custom_430_OG [0x1ae]					
2024/11/07	05:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/07	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	06:01:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	06:01:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2024/11/07	06:02:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2024/11/07	06:02:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2024/11/07	06:02:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/07	06:04:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2024/11/07	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/07	06:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	06:11:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	06:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2024/11/07	06:12:00.0	AOCs_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02	03 ce 01 f3
2024/11/07	06:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/11/07	06:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/11/07	06:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/11/07	06:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/07	06:12:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/07	06:14:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2024/11/07	06:14:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/07	06:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/07	07:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	07:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	07:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/07	07:04:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/07	07:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/07	07:30:30.0	XRT_Custom_430_OG [0x1ae]					
2024/11/07	07:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/07	08:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	08:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	08:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/07	08:44:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/07	08:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/07	09:07:00.0	XRT_Custom_430_OG [0x1ae]					
2024/11/07	09:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/07	10:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/07	10:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

2024/11/07	10:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/07	10:24:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/07	10:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/07	10:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/07	10:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/07	11:10:15.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/07	11:31:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00