XRT Timeline to be uploaded on 2024/11/12

Period: 2024/11/12 11:56:00 - 2024/11/16 11:10:00

* * * * * **Normal mode** * * * * *

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
11/12 15:30:00 - 11/12 16:54:30	Fixed (-22.0, -288.0)	HOP412 08/20
11/12 17:18:30 - 11/12 18:30:30	Fixed (-22.0, 2.0)	HOP412 11/20
11/12 18:55:00 - 11/12 20:07:30	Fixed (-22.0, 302.0)	HOP412 14/20
11/12 20:31:30 - 11/12 21:44:30	Fixed (-22.0, 602.0)	HOP412 17/20
11/12 22:09:00 - 11/12 23:22:00	Fixed (-22.0, 902.0)	HOP412 20/20
11/12 23:44:00 - 11/13 11:59:54	Track (-30.8, -252.5) @ 11/12 22:32:00	End of HOP412 + AR13889
11/13 12:43:00 - 11/13 12:49:54	Fixed (0.0, 0.0)	Post bakeout synoptics
11/13 18:08:00 - 11/13 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
11/14 05:37:00 - 11/14 05:43:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -26.0 min

PROG= 03 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 55 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 98 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 76 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 41 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 86 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1D03: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msCC

Term	Pointing (x, y)	Comment
11/13 12:03:00 - 11/13 12:09:54	Fixed (-528.4, -528.4)	Post bakeout Q1

PROG= 08 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 51 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D04: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 2nd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC

Term	Pointing (x, y)	Comment

11/13 12:13:00 - 11/13 12:19:54 Fixed (528.4, -528.4) Post bakeout Q2

PROG= 19 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 38 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D05: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 3rd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term Pointing (x, y) Comment

11/13 12:23:00 - 11/13 12:29:54 Fixed (528.4, 528.4) Post bakeout Q3

PROG= 15 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 21 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term Pointing (x, y) Comment

11/13 12:33:00 - 11/13 12:39:54 Fixed (-528.4, 528.4) Post bakeout Q4

PROG= 02 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 14 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term Pointing (x, y) Comment

11/13 12:53:00 - 11/13 18:04:54 Track (101.6, -250.9) @ 11/13 12:50:00 AR13889

11/13 18:18:00 - 11/14 03:59:54 Track (151.2, -250.1) @ 11/13 18:15:00 AR13889

11/14 05:47:00 - 11/14 11:33:54 Track (254.8, -247.9) @ 11/14 05:44:00 AR13889

PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 4-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 77 4-time(s) 300.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
11/14 04:03:00 - 11/14 05:33:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -26.0 min

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 55 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 1500.0sec

Seqn= 8 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 74 1-time(s) 2.0sec

med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
11/12 15:30:00 - 11/12 16:54:30	Fixed (-22.0, -288.0)	HOP412 08/20
11/12 17:18:30 - 11/12 18:30:30	Fixed (-22.0, 2.0)	HOP412 11/20
11/12 18:55:00 - 11/12 20:07:30	Fixed (-22.0, 302.0)	HOP412 14/20
11/12 20:31:30 - 11/12 21:44:30	Fixed (-22.0, 602.0)	HOP412 17/20
11/12 22:09:00 - 11/12 23:22:00	Fixed (-22.0, 902.0)	HOP412 20/20
11/12 23:44:00 - 11/13 11:59:54	Track (-30.8, -252.5) @ 11/12 22:32:00	End of HOP412 + AR13889
11/13 12:53:00 - 11/13 18:04:54	Track (101.6, -250.9) @ 11/13 12:50:00	AR13889
11/13 18:18:00 - 11/14 03:59:54	Track (151.2, -250.1) @ 11/13 18:15:00	AR13889
11/14 04:03:00 - 11/14 05:33:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -26.0 min
11/14 05:47:00 - 11/14 11:33:54	Track (254.8, -247.9) @ 11/14 05:44:00	AR13889

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

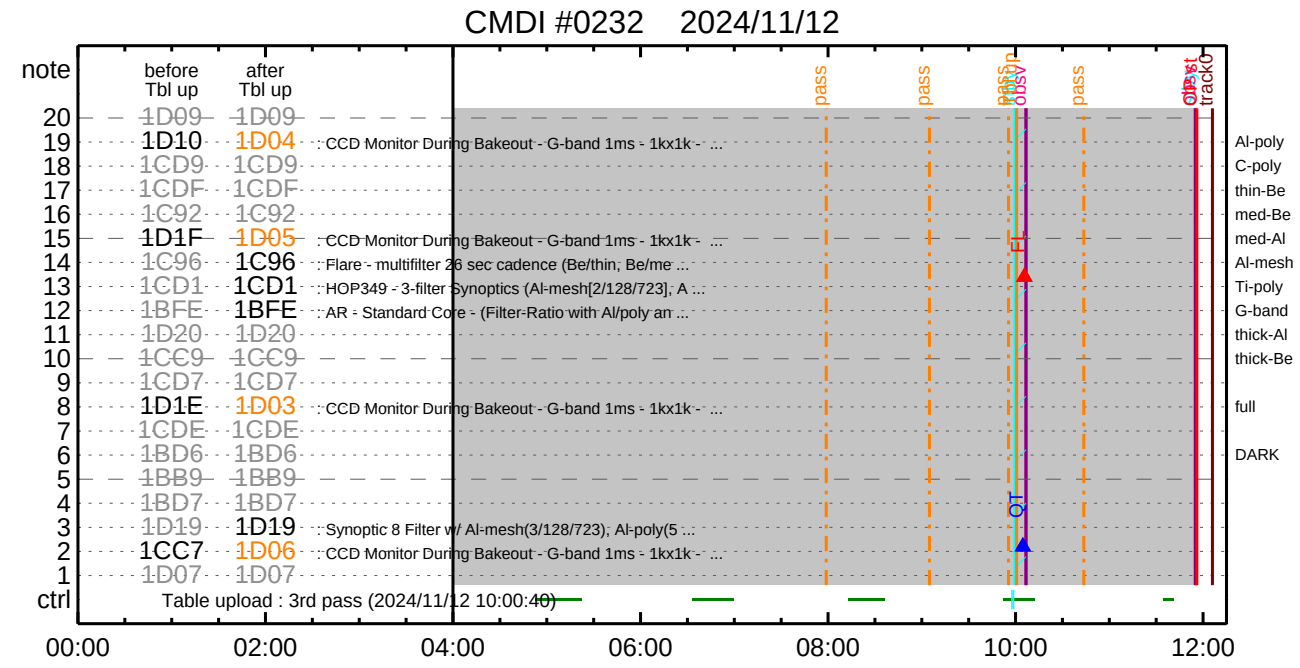
NOT USED

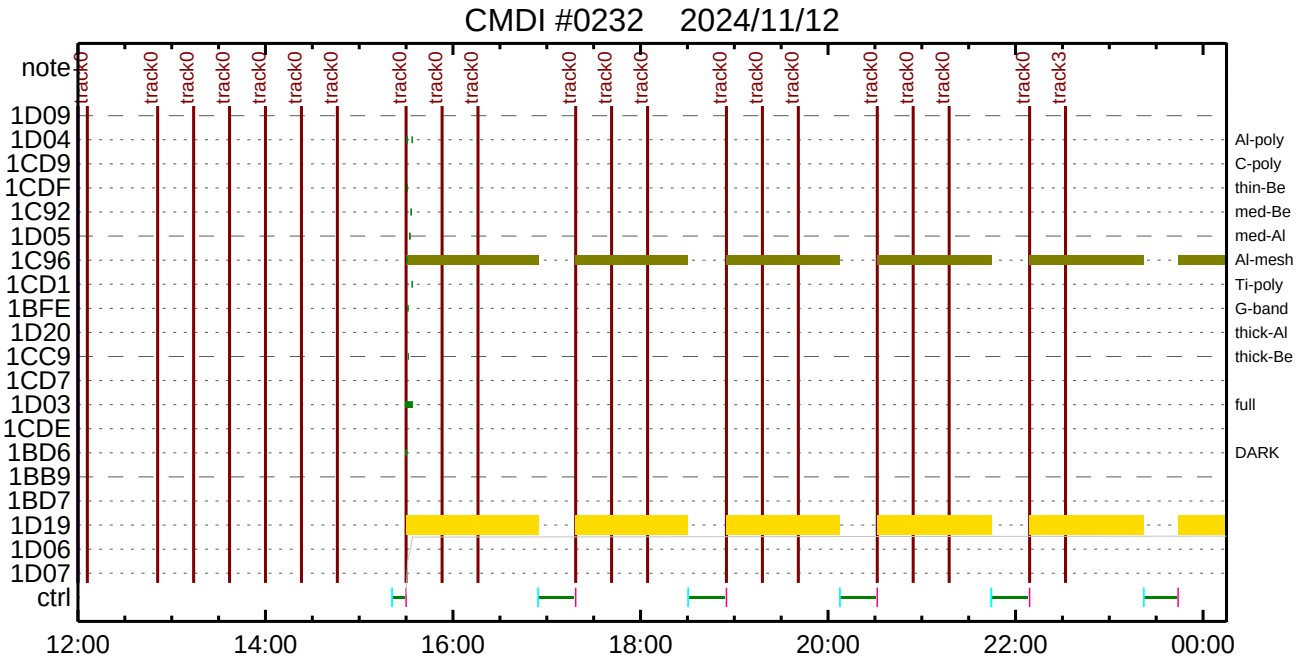
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

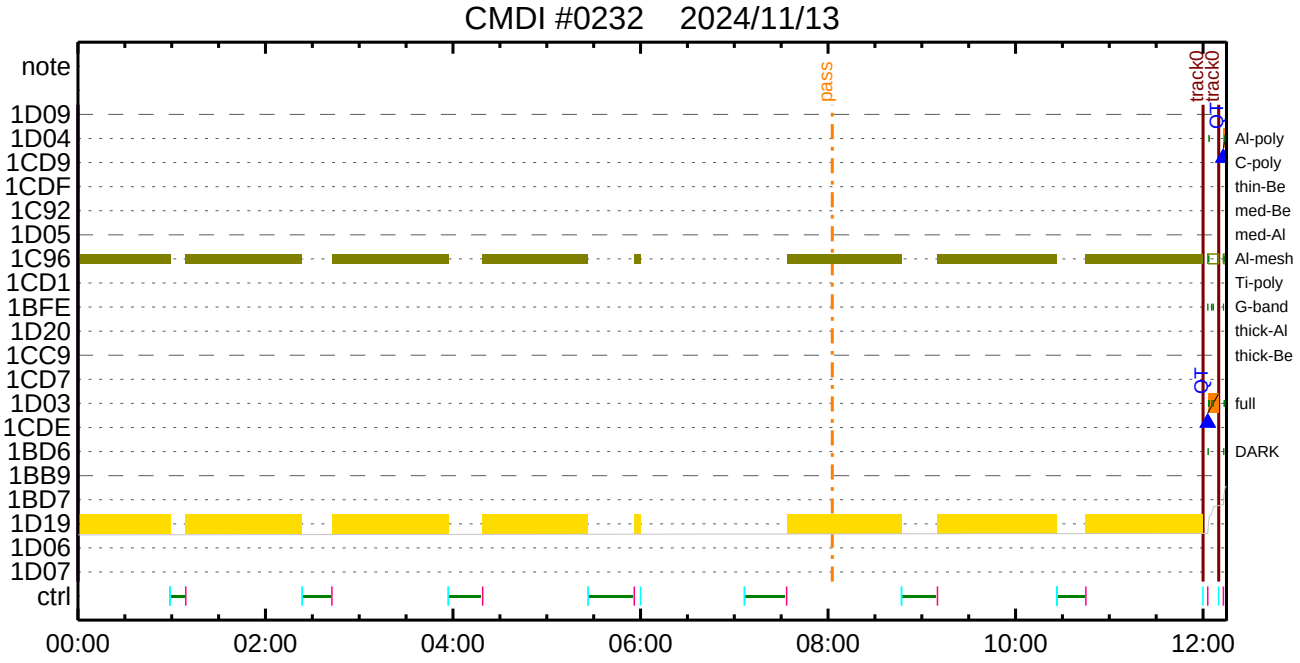
FLD Patrol

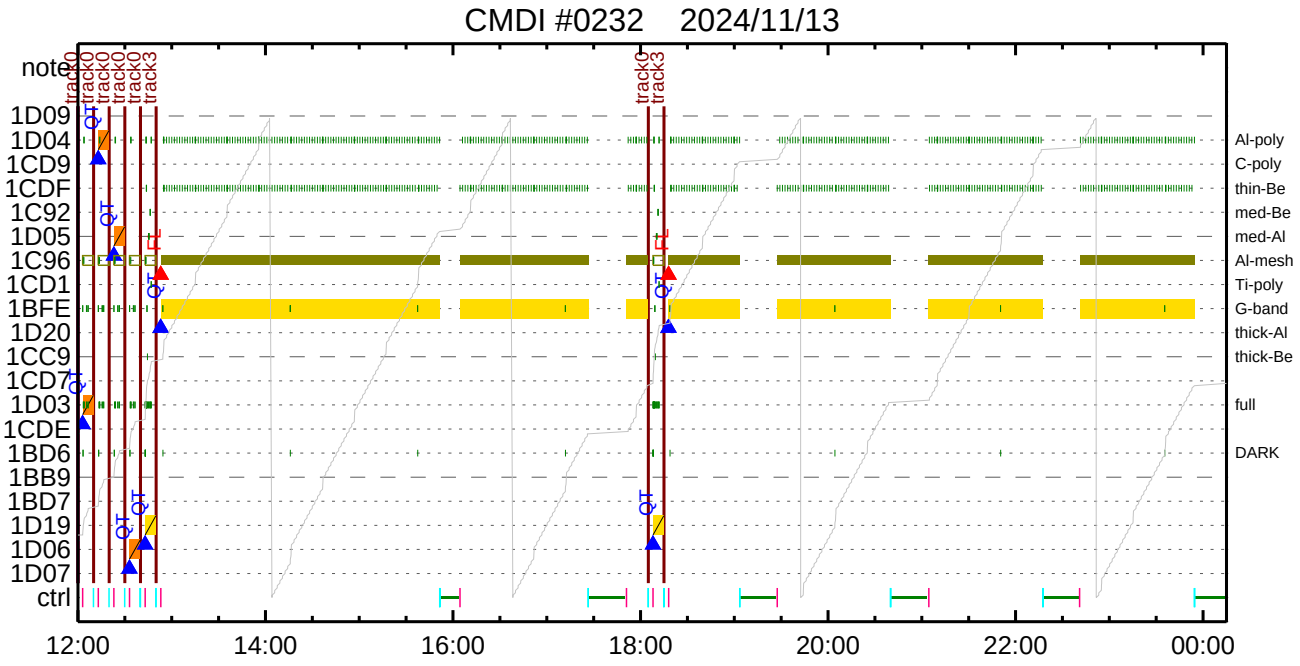
Term	Pointing (x, y)	Comment
11/12 10:01:40 - 11/13 12:02:56	cannot be identified	
11/13 12:50:18 - 11/13 18:05:18	Track (101.6, -250.9) @ 11/13 12:50:00	AR13889
11/13 18:15:18 - 11/14 05:34:18	Track (151.2, -250.1) @ 11/13 18:15:00	AR13889
11/14 05:44:18 - 11/16 11:10:00	Track (254.8, -247.9) @ 11/14 05:44:00	AR13889

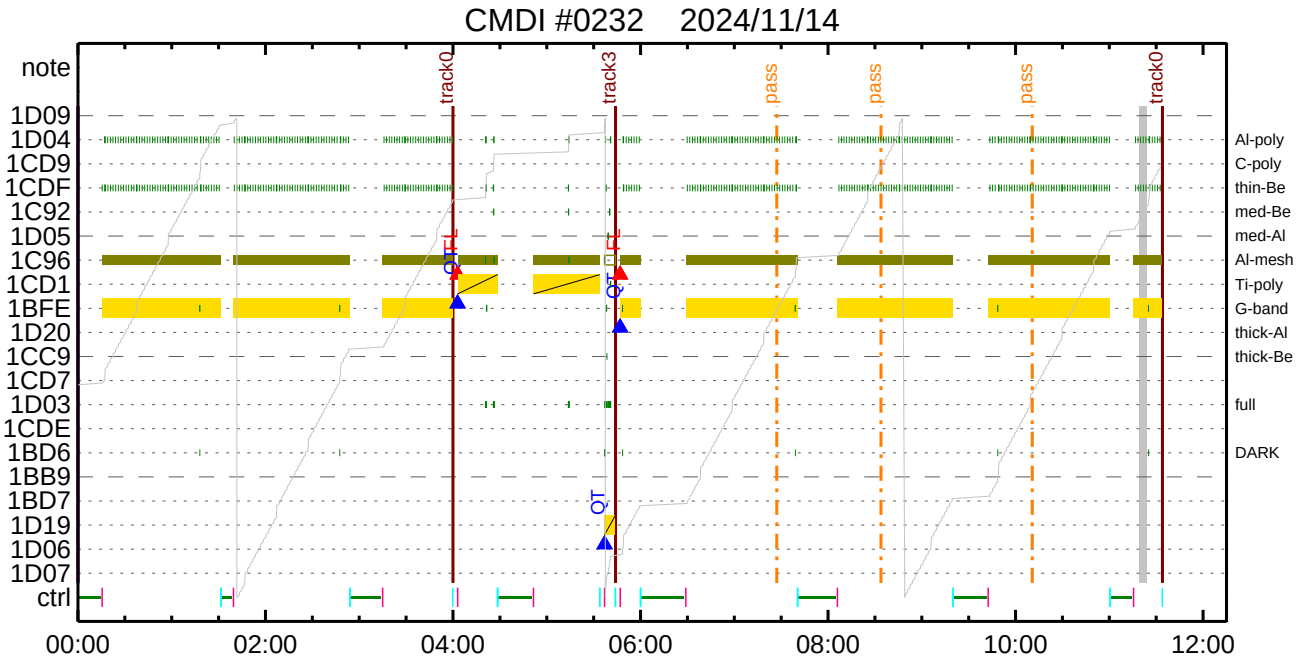
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

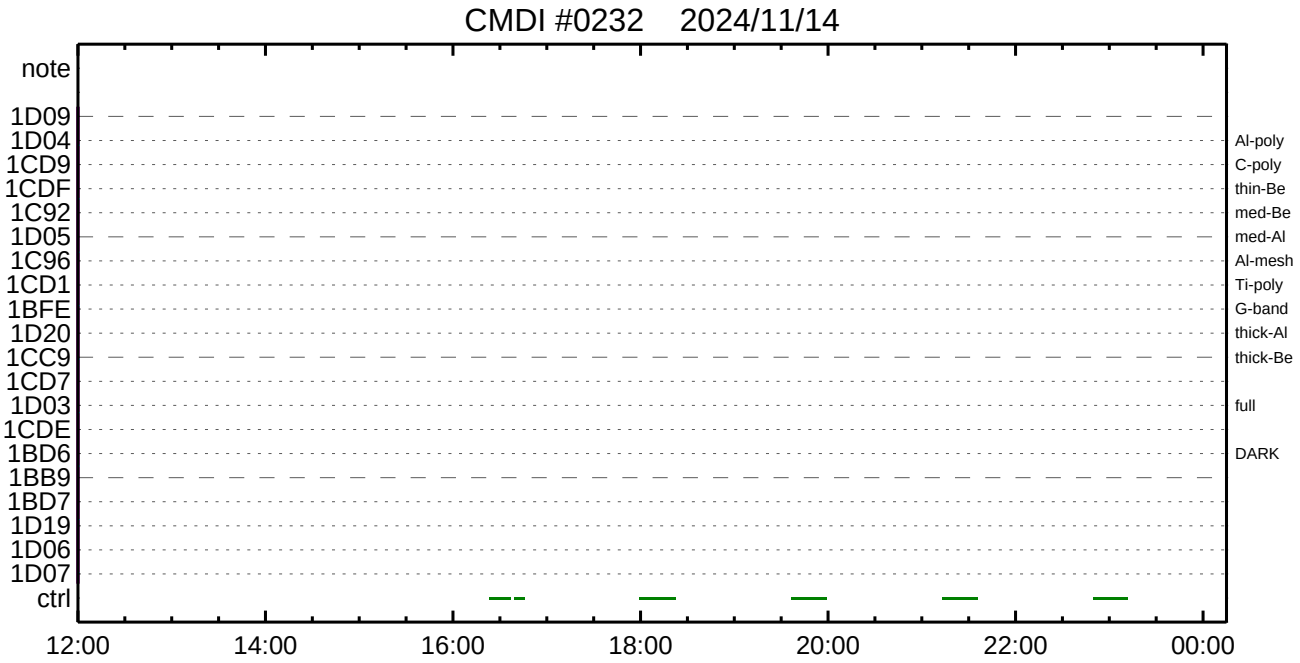

CMDI #0232 2024/11/12
note
before Tbl up
after Tbl up
20 1D09 1D09
19 1D10 1D04 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
18 1CD9 1CD9
17 1CDF 1CDF
16 1C92 1C92
15 1D1F 1D05 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
14 1C96 1C96 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
13 1CD1 1CD1 : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
12 1BFE 1BFE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
11 1D20 1D20
10 1CC9 1CC9
9 1CD7 1CD7
8 1D1E 1D03 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
7 1CDE 1CDE
6 1BD6 1BD6
5 1BB9 1BB9
4 1BD7 1BD7
3 1D19 1D19 : Synoptic 8 Filter w/ Al-mesh(3/128/723); Al-poly(5 ...
2 1CC7 1D06 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
1 1D07 1D07
ctrl Table upload : 3rd pass (2024/11/12 10:00:40)
pass
pass
pass
obsv
pass
dump
track0
FT
OT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

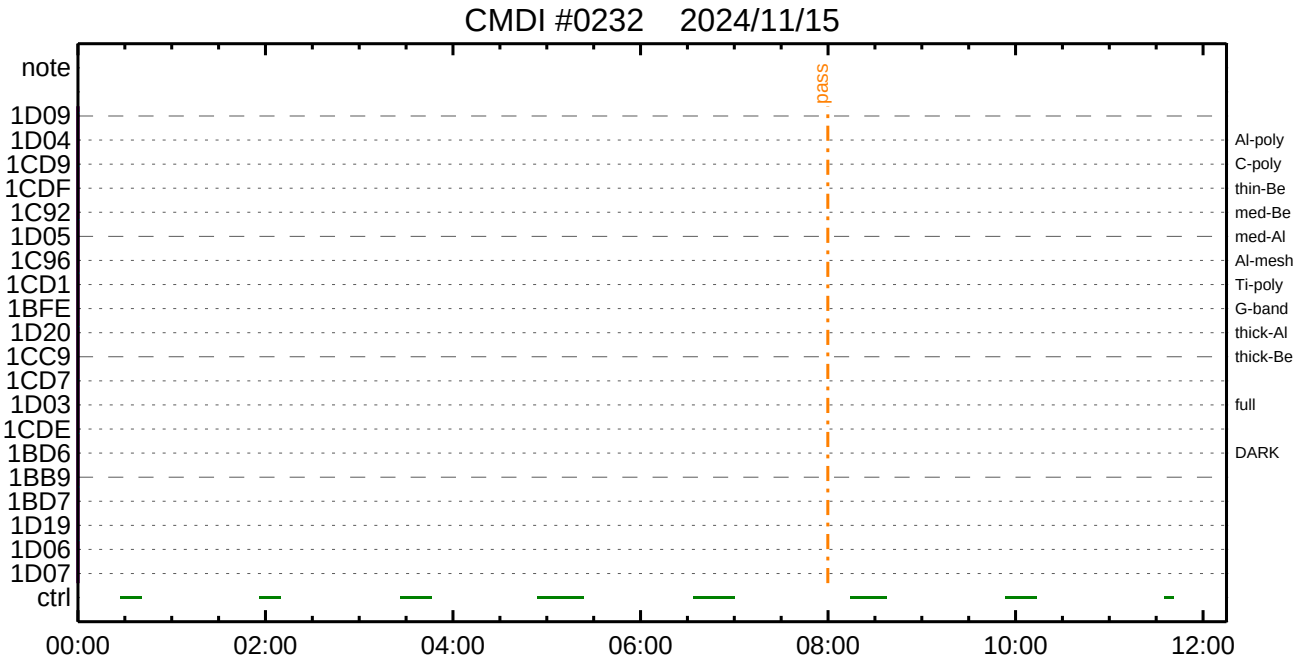

CMDI #0232 2024/11/12
note
track0
track3
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

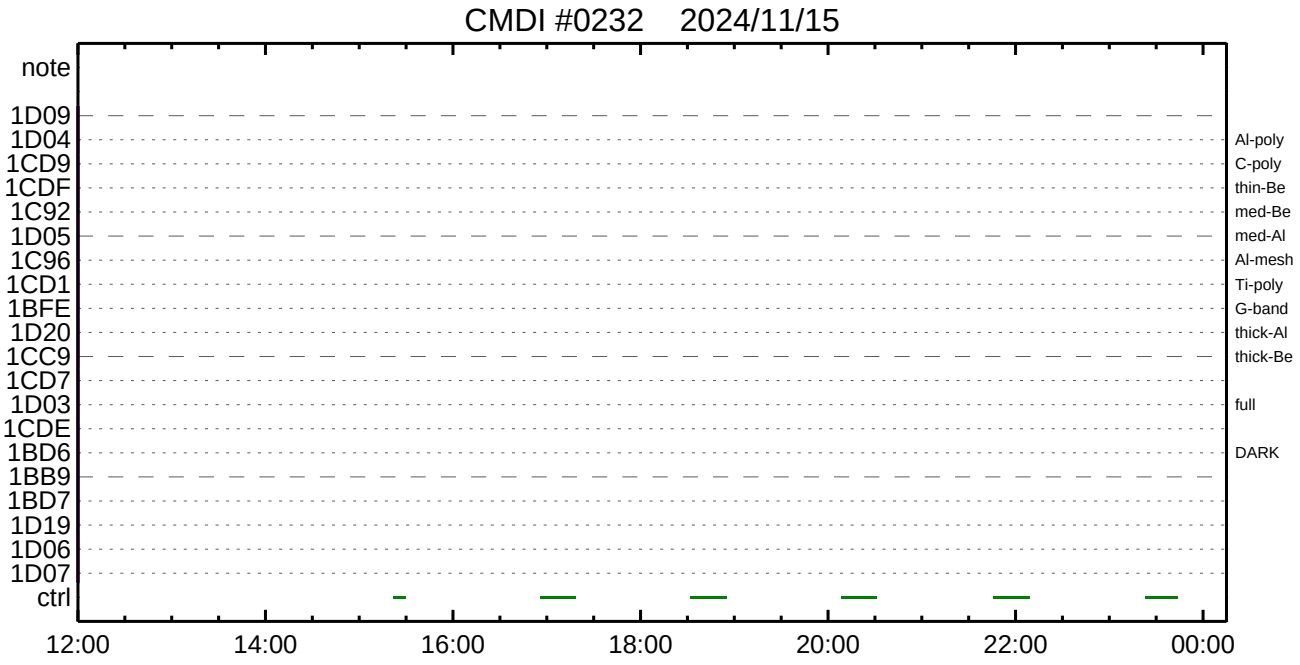

CMDI #0232 2024/11/13
note
pass
track0
track0
OT
OT
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

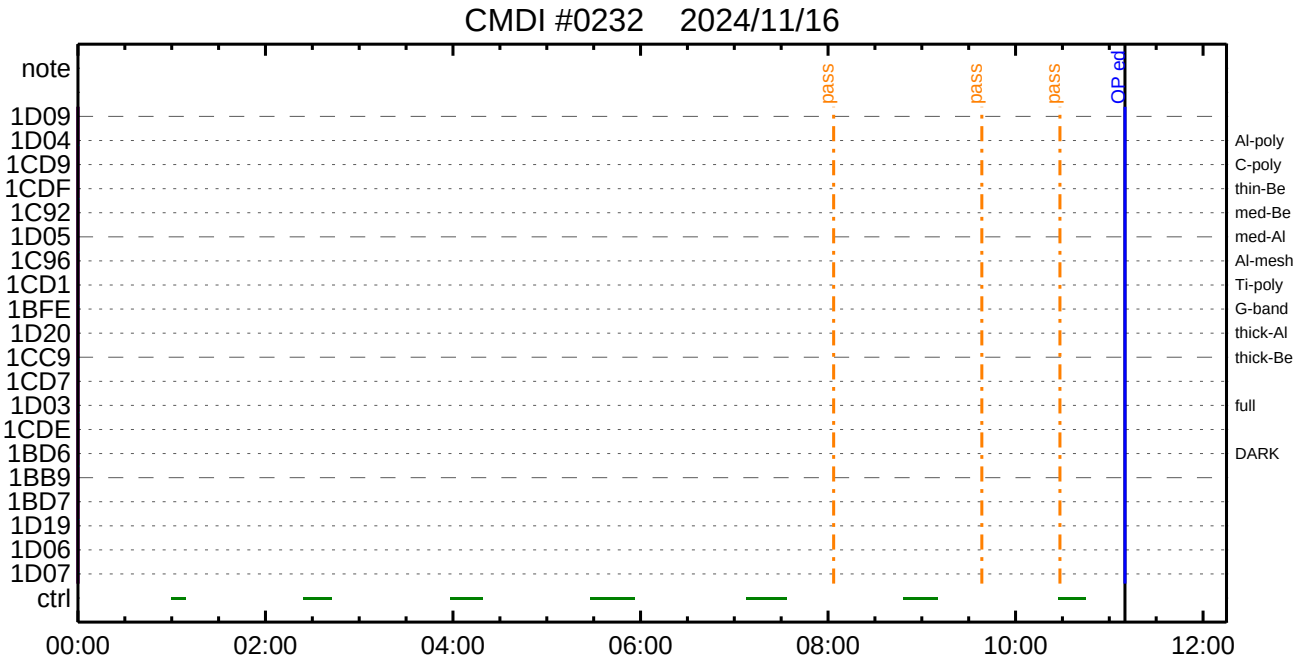

CMDI #0232 2024/11/13
note
track0
track0
track0
track3
track0
track3
QT
FE
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

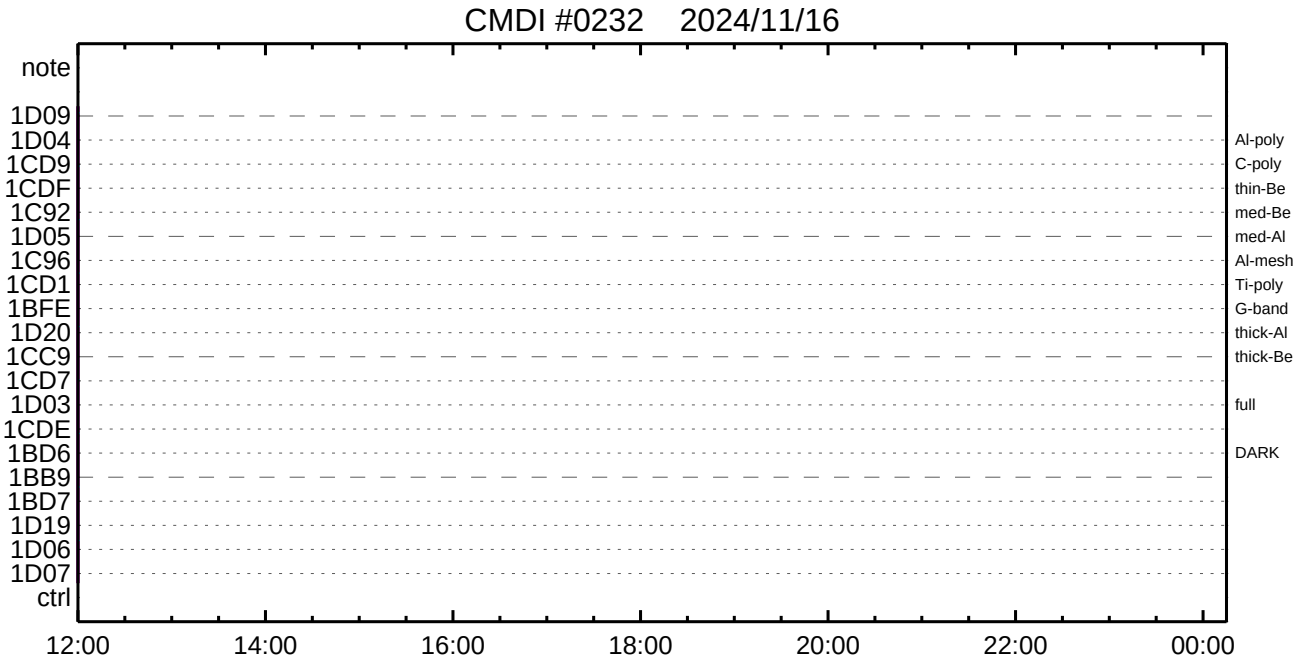

CMDI #0232 2024/11/14
note
track0
track3
pass
pass
pass
track0
QT
FE
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0232 2024/11/14
note
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0232 2024/11/15
note
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0232 2024/11/15
note
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0232 2024/11/16
note
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0232 2024/11/16
note
1D09
1D04
1CD9
1CDF
1C92
1D05
1C96
1CD1
1BFE
1D20
1CC9
1CD7
1D03
1CDE
1BD6
1BB9
1BD7
1D19
1D06
1D07
ctrl
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

(a) Spacecraft Operation Procedure (real-commands)

```
main-925 2024-11-12 11:57:18 194 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP     op-925:OP
0020     ()
0021   . S. OG     og-925:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC        (20 00 7f 01 02)
0028     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC        (07 0b f8)
0035     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC        (20 80 7f 01 02)
0047     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC        (07 0b f8)
0054     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC        (21 00 41 01 02)
0066     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC        (07 0b f8)
0073     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ¬¥À¥ó¥×½ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC        (02 0a f8)
0086     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2024-11-12 11:51:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2024-11-12 11:51:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2024-11-12 11:51:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2024-11-12 11:55:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154   . C. Stop EIS observation and temporarily disable EIS mode changes
0155     C.
0156     C.
0157     C. ********** Start EIS operation (TI set) **********
0158     C. Execute, after the success of OP upload.
0159     C. Set EIS TI-commands
0160   +. TI 2024-11-12 11:55:30.0
0161     DC 07-FC EIS_MODE_MANU
0162     BC       (21 02)
0163   +. TI 2024-11-12 11:55:40.0
0164     DC 07-FC EIS_MODE_CHG_DIS
0165     BC       (22)
0166   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0167     C. ********** End EIS operation (TI set) **********
0168     C.
0169     C.
0170     C.
0171     C. ************ XRT START ***********
0172     C. Execute, after the success of OP upload.
0173   +. TI 2024-11-12 11:55:00.0
0174     DC 07-F0 MDP_XRT_MODE_STBY
0175     BC       (c3)
0176   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0177     C.
0178     C. ************ XRT END ***********
0179     C.
0180   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0181     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0182   . S. DC-BC dcbc-402:DCBC
0183     (MDP_known_event)
0184     C.
0185     C.
0186   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0187   . S. DC-BC dcbc-153:DCBC
0188     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189     C.
0190     C.
0191   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0192     C.
0193   . C. ********** LOS **********
```

0194 C.

```
(a) Spacecraft Operation Procedure (real-commands)

main-926 2024-11-12 11:57:18 178 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 1522316.8 +- 1.0 (s) [ ]
0136     C.
0137    . C.
0138    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_CHG_ENA
0140     BC       (20)
0141    . C. Verify EIS_MODE_CHG_FLG is ENA
0142   +. DC 07-FC EIS_MODE_MANU
0143     BC       (21 02)
0144    . C. Verify EIS in MANUAL mode
0145    . C. Estimated OBSTBL upload time is 53s
0146     C. ********************************
0147     C. EIS START OBSTBL LOAD
0148     C. ********************************
0149    . S. RAM   ram-820:EIS_OBSTBL
0150     ()
0151   +. DC 07-FC EIS_DUMP_OBSTBL
0152     BC       (07 07 07 00 00 70 00)
0153     C.
0154     C. Execute, after the success of OBSTBL upload.
0155     C. Set EIS TI-commands
0156   +. TI 2024-11-12 11:55:50.0
0157     DC 07-FC EIS_MODE_CHG_ENA
0158     BC       (20)
0159    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0160     C. ********************************
0161     C. EIS END OBSTBL LOAD
0162     C. ********************************
0163     C.
0164    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0165     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0166    . S. DC-BC dcbc-402:DCBC
0167     (MDP_known_event)
0168     C.
0169     C.
0170    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0171    . S. DC-BC dcbc-153:DCBC
0172     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173     C.
0174     C.
0175    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0176     C.
0177    . C. ********** LOS **********
0178     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-927 2024-11-12 11:57:18 106 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +. DC 07-F0 MDP_XRT_CTRL_MANU
0020     BC       (c1)
0021   +  DC 07-F0 MDP_XRT_MODE_STBY
0022     BC       (c3)
0023   . C. ----------- Success Verify ?              OK / NG____
0024     C.
0025     C. XRT Obs. Table Upload
0026   . S. RAM   ram-291:MDP_OBS_X
0027     ()
0028     C.
0029   +. DC 07-F0 MDP_DUMP_XRTTBL
0030     BC       (84 07 00 00 00 3a d4)
0031   . C. ----------- Comparison Check ?                        OK / ERR ____
0032     C.
0033     C.
0034   +. DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 01 b1 b1 04 04)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 02 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 03 b1 b1 08 08)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 04 b1 b1 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 06 80 80 20 20)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 07 80 80 20 08)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 08 80 80 08 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 09 c0 c0 10 10)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 0a 40 c0 10 10)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0b 40 40 10 10)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0c c0 40 10 10)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 0d 85 83 06 06)
0058   +  DC 07-F0 MDP_XRT_ROI_SET
0059     BC       (cd 0e 80 80 08 08)
0060   +  DC 07-F0 MDP_XRT_ROI_SET
0061     BC       (cd 0f 80 80 06 06)
0062   +  DC 07-F0 MDP_XRT_ROI_SET
0063     BC       (cd 10 80 80 08 08)
0064   +  DC 07-F0 MDP_XRT_FLD_ENA
0065     BC       (d8)
0066   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067     BC       (c8)
0068   +  DC 07-F0 MDP_XRT_ARS_DIS
0069     BC       (d5)
0070   +  DC 07-F0 MDP_XRT_AEC_RESET
0071     BC       (d0)
0072   +  DC 07-F0 MDP_XRT_FLD_RESET
0073     BC       (da)
0074   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0075     BC       (c4 03)
0076   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0077     BC       (c5 0e)
0078   . C. ---------- Success Verify ?            OK / NG ____
0079     C.
0080     C.
0081   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0082     C.
0083   +. DC 07-F0 MDP_XRT_MODE_OBSV
0084     BC       (c2)
0085   +. TI 2024-11-12 11:55:02.0
0086     DC 07-F0 MDP_XRT_MODE_OBSV
0087     BC       (c2)
0088   . C. ---------- Success Verify ?            OK / NG ____
0089     C.
0090     C. ************ XRT END ***********
0091     C.
0092   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0093     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0094   . S. DC-BC dcbc-402:DCBC
0095     (MDP_known_event)
```

```
0096     C.
0097     C.
0098   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0099   . S. DC-BC dcbc-153:DCBC
0100     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0101     C.
0102     C.
0103   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0104     C.
0105   . C. *********** LOS ***********
0106     C.
```

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(a) Spacecraft Operation Procedure (real-commands)

main-928 2024-11-12 11:57:18 113 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0100     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0101   . S. DC-BC dcbc-402:DCBC
0102     (MDP_known_event)
0103     C.
0104     C.
0105   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0106   . S. DC-BC dcbc-153:DCBC
0107     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108     C.
0109     C.
0110   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0111     C.
0112   . C. ********** LOS **********
0113     C.
```

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*** OP Sequence for XRT ***

2024/11/12  12:06:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM               5 02-76 00 57 ce 01 f3
2024/11/12  12:51:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM               5 02-76 00 4e f1 01 f3
2024/11/12  13:14:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM               5 02-76 00 46 0c 01 f3
2024/11/12  13:37:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM               5 02-76 00 3d 26 01 f3
2024/11/12  14:00:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM               5 02-76 00 34 41 01 f3
2024/11/12  14:23:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM               5 02-76 00 2b 64 01 f3
2024/11/12  14:46:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM               5 02-76 00 22 7e 01 f3
2024/11/12  15:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  15:21:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  15:21:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2024/11/12  15:21:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/11/12  15:24:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/11/12  15:29:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/12  15:30:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM               5 02-76 00 19 99 01 f3
2024/11/12  15:30:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/11/12  15:53:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM               5 02-76 00 10 b4 01 f3
2024/11/12  16:16:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM               5 02-76 00 07 ce 01 f3
2024/11/12  16:54:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  16:54:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  16:54:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2024/11/12  16:54:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/11/12  16:57:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/11/12  17:17:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/12  17:18:30.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM               5 02-76 00 ff cf 01 f3
2024/11/12  17:18:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/11/12  17:41:30.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM               5 02-76 00 f6 f2 01 f3
2024/11/12  18:04:30.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM               5 02-76 00 ee 0d 01 f3
2024/11/12  18:30:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  18:30:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  18:30:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2024/11/12  18:30:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/11/12  18:33:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/11/12  18:54:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/12  18:55:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM               5 02-76 00 e5 27 01 f3
2024/11/12  18:55:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/11/12  19:18:00.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM               5 02-76 00 dc 42 01 f3
2024/11/12  19:41:00.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM               5 02-76 00 d3 65 01 f3
2024/11/12  20:07:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  20:07:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU     1 07-F0 c1
2024/11/12  20:07:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET     1 07-F0 da
2024/11/12  20:07:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT   1 07-F0 e8
2024/11/12  20:10:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP   1 07-F0 e9
2024/11/12  20:30:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/12  20:31:30.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM               5 02-76 00 ca 7f 01 f3
2024/11/12  20:31:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO     1 07-F0 c0
2024/11/12  20:54:30.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM               5 02-76 00 c1 9a 01 f3
2024/11/12  21:17:30.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM               5 02-76 00 b8 b5 01 f3
2024/11/12  21:44:30.0  XRT_CTRL_MANU_400_OG [0x190]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/12  21:44:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/12  21:44:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/12  21:44:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/12  21:47:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/12  22:08:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/12  22:09:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                        AOCU_NM              5 02-76 00 af cf 01 f3
2024/11/12  22:09:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/12  22:32:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                        AOCU_NM              5 02-76 03 03 ce 01 f3
2024/11/12  23:22:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/12  23:22:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/12  23:22:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/12  23:22:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/12  23:25:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/12  23:43:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/12  23:44:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  00:59:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  00:59:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  00:59:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  00:59:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  01:02:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  01:08:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  01:09:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  02:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  02:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  02:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  02:23:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  02:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  02:41:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  02:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  03:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  03:57:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  03:57:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  03:57:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  04:00:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  04:18:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  04:19:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  05:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  05:26:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  05:26:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  05:26:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  05:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  05:55:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  05:56:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  06:00:01.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  06:00:03.0  XRT_TCIB_XRT_S_HTR_A_DIS_445_OG [0x1bd]
                                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2024/11/13  07:06:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  07:06:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  07:06:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  07:06:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
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2024/11/13  07:09:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  07:32:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  07:33:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  08:47:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  08:47:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  08:47:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  08:47:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  08:50:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  09:09:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  09:10:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  10:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  10:26:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  10:26:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  10:26:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  10:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  10:44:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  10:45:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  11:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  11:59:56.0  XRT_FOCUS_POSITION_446_OG [0x1be]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/13  12:00:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2024/11/13  12:02:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  12:02:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/13  12:02:56.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/13  12:02:58.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2024/11/13  12:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  12:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:09:56.0  XRT_FOCUS_POSITION_446_OG [0x1be]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/13  12:10:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2024/11/13  12:12:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  12:12:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/13  12:12:56.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/13  12:12:58.0  XRT_QT_PROG_SET_431_OG [0x1af]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2024/11/13  12:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  12:19:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:19:56.0  XRT_FOCUS_POSITION_446_OG [0x1be]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/13  12:20:00.0  AOCS_ORe-point_Start_24_OG [0x0ae]
                                AOCU_NM              5 02-76 00 d1 07 d1 07
2024/11/13  12:22:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  12:22:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/13  12:22:56.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/13  12:22:58.0  XRT_QT_PROG_SET_429_OG [0x1ad]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2024/11/13  12:23:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  12:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:29:56.0  XRT_FOCUS_POSITION_446_OG [0x1be]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/13  12:30:00.0  AOCS_ORe-point_Start_25_OG [0x0af]
                                AOCU_NM              5 02-76 00 d1 07 2e f9
2024/11/13  12:32:52.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  12:32:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/13  12:32:56.0  XRT_FLD_DIS_404_OG [0x194]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/13  12:32:58.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
```

```
2024/11/13  12:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  12:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:39:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/13  12:40:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/11/13  12:40:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/13  12:40:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/13  12:40:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  12:42:58.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2024/11/13  12:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  12:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:49:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  12:49:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/11/13  12:50:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                AOCU_NM              5 02-76 03 03 ce 01 f3
2024/11/13  12:50:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2024/11/13  12:50:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/11/13  12:50:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2024/11/13  12:50:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  12:50:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  12:52:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2024/11/13  12:52:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/11/13  12:53:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  15:51:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  15:51:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  15:51:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  15:51:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  15:54:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  16:03:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  16:04:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  17:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  17:26:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  17:26:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  17:26:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  17:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  17:50:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  17:51:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  18:04:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  18:04:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  18:04:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/13  18:05:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                AOCU_NM              5 02-76 00 00 00 00 00
2024/11/13  18:05:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/13  18:05:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/13  18:05:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  18:07:58.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2024/11/13  18:08:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  18:14:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  18:14:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  18:14:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
```

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                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/11/13  18:15:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                        AOCU_NM              5 02-76 03 03 ce 01 f3
2024/11/13  18:15:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2024/11/13  18:15:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/11/13  18:15:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2024/11/13  18:15:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/13  18:15:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  18:17:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2024/11/13  18:17:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/11/13  18:18:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  19:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  19:03:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  19:03:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  19:03:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  19:06:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  19:26:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  19:27:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  20:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  20:40:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  20:40:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  20:40:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  20:43:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  21:03:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  21:04:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  22:17:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  22:17:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  22:17:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  22:17:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  22:20:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/13  22:40:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/13  22:41:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/13  23:54:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  23:54:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/13  23:54:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/13  23:54:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/13  23:57:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  00:14:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  00:15:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  01:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  01:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  01:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  01:31:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  01:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  01:38:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  01:39:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  02:54:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  02:54:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  02:54:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  02:54:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  02:57:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
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                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  03:14:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  03:15:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  03:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/14  04:00:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2024/11/14  04:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2024/11/14  04:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/11/14  04:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2024/11/14  04:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/14  04:00:24.0  XRT_FLD_RESET_428_OG [0x1ac]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  04:02:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2024/11/14  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/11/14  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  04:28:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  04:28:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  04:28:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  04:28:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  04:31:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  04:50:30.5  XRT_Custom_430_OG [0x1ae]
2024/11/14  04:51:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  05:33:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  05:33:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  05:33:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2024/11/14  05:34:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2024/11/14  05:34:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2024/11/14  05:34:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/14  05:36:58.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2024/11/14  05:37:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  05:43:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  05:43:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  05:43:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2024/11/14  05:44:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                        AOCU_NM              5 02-76 03 03 ce 01 f3
2024/11/14  05:44:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2024/11/14  05:44:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2024/11/14  05:44:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2024/11/14  05:44:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2024/11/14  05:44:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  05:46:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2024/11/14  05:46:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2024/11/14  05:47:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  06:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  06:00:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  06:00:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  06:00:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  06:03:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  06:28:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  06:29:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  07:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  07:40:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  07:40:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  07:40:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  07:43:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  08:05:00.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  08:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  09:20:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  09:20:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  09:20:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  09:20:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  09:23:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  09:41:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  09:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  11:00:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  11:00:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  11:00:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2024/11/14  11:00:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2024/11/14  11:03:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2024/11/14  11:14:30.0  XRT_Custom_430_OG [0x1ae]
2024/11/14  11:15:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2024/11/14  11:33:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2024/11/14  11:34:00.0  AOCS_ORe-point_Start_26_OG [0x0b0]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```