

XRT Timeline to be uploaded on 2024/11/14

Period: 2024/11/14 11:24:00 - 2024/11/19 10:53:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/14 11:37:00 - 11/14 17:59:00	Track (306.1, -246.6) @ 11/14 11:34:00	# OP start + 10min + AR13889
11/14 18:35:30 - 11/15 03:59:54	Track (366.1, -244.9) @ 11/14 18:32:30	AR13889
11/15 06:18:30 - 11/15 17:59:54	Track (462.8, -241.6) @ 11/15 06:15:30	AR13889
11/15 18:13:00 - 11/15 23:59:54	Track (554.7, -237.8) @ 11/15 18:10:00	AR13889
11/16 03:03:00 - 11/16 03:58:00	Track (617.9, -234.7) @ 11/16 03:00:00	AR13889
11/16 06:18:00 - 11/16 10:27:30	Track (640.0, -233.6) @ 11/16 06:15:00	AR13889 (HOP 495 08:00-09:30)

PROG= 10 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 92	1-time(s)	2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Subr= 2	5-time(s)	2.0sec
Seqn= 47	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 96	4-time(s)	120.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec
Al-poly/Open	thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
11/14 18:25:30 - 11/14 18:32:24	Fixed (0.0, 0.0)	synoptic, shifted 22.5 min
11/15 06:08:30 - 11/15 06:15:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 5.5 min
11/15 18:03:00 - 11/15 18:09:54	Fixed (0.0, 0.0)	synoptic
11/16 06:08:00 - 11/16 06:14:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 5.0 min

PROG= 03 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 55	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 98	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 76	1-time(s)	2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 23	1-time(s)	4.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2	1-time(s)	2.0sec
Seqn= 41	1-time(s)	2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 18	1-time(s)	2.0sec
med-Al/Open	med-Al/thick-Al close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Al/Open	med-Al/Open close	Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 86	1-time(s)	2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 54	1-time(s)	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
11/15 04:03:00 - 11/15 06:05:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 5.5 min
11/16 04:20:00 - 11/16 06:04:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 5.0 min
PROG= 13 Inf.-time(s)		
Subr= 1 1-time(s) 300.0sec		
Seqn= 55 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 15 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 79 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 15-time(s) 1500.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 74 1-time(s) 2.0sec		
med-Be/Open	med-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
med-Be/Open	med-Be/Open close	Safe Norm 2.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1CD9: HOP393 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s

Term	Pointing (x, y)	Comment
11/16 00:11:30 - 11/16 02:59:54	Track (-22.1, -88.9) @ 11/16 00:00:00	HOP393
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 52 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Subr= 2 30-time(s) 360.0sec		
Seqn= 97 2-time(s) 2.0sec		
Al-poly/Open	med-Be/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open	med-Be/Open close	Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
11/14 11:37:00 - 11/14 17:59:00	Track (306.1, -246.6) @ 11/14 11:34:00	# OP start + 10min + AR13889
11/14 18:35:30 - 11/15 03:59:54	Track (366.1, -244.9) @ 11/14 18:32:30	AR13889
11/15 04:03:00 - 11/15 06:05:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 5.5 min
11/15 06:18:30 - 11/15 17:59:54	Track (462.8, -241.6) @ 11/15 06:15:30	AR13889
11/15 18:13:00 - 11/15 23:59:54	Track (554.7, -237.8) @ 11/15 18:10:00	AR13889
11/16 00:11:30 - 11/16 02:59:54	Track (-22.1, -88.9) @ 11/16 00:00:00	HOP393
11/16 03:03:00 - 11/16 03:58:00	Track (617.9, -234.7) @ 11/16 03:00:00	AR13889
11/16 04:20:00 - 11/16 06:04:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 5.0 min
11/16 06:18:00 - 11/16 10:27:30	Track (640.0, -233.6) @ 11/16 06:15:00	AR13889 (HOP 495 08:00-09:30)

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 73 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al close	Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11			1-time(s)			2.0sec						
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87			1-time(s)			2.0sec						
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

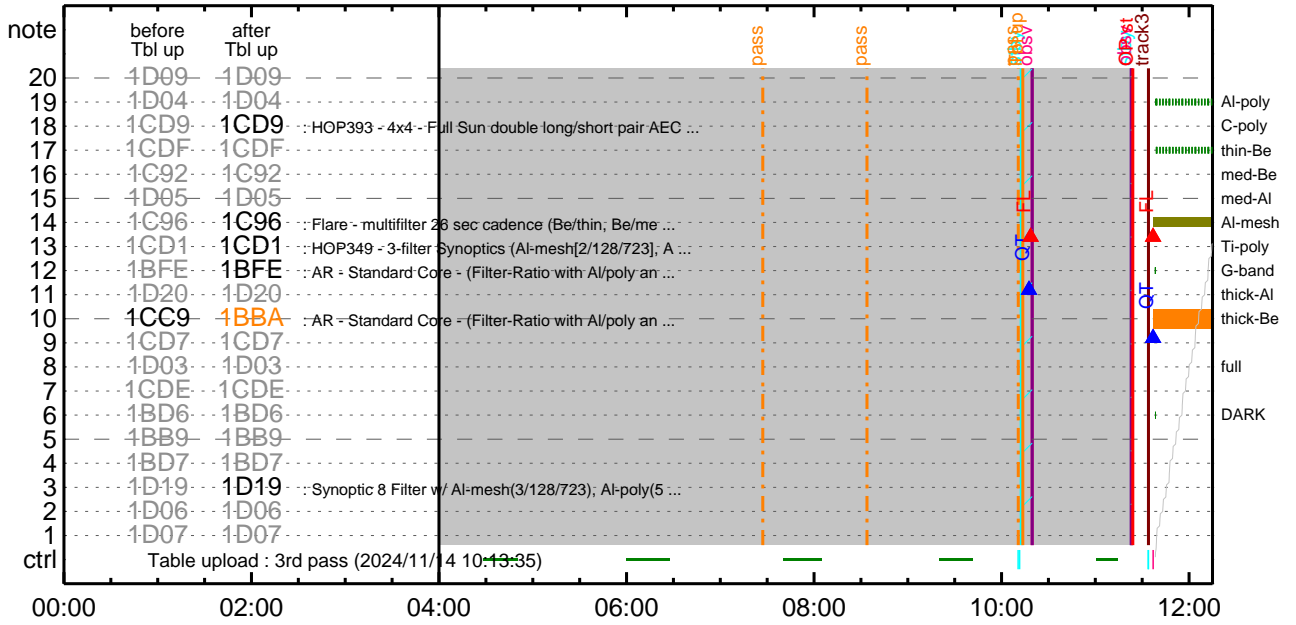
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Flare Detection

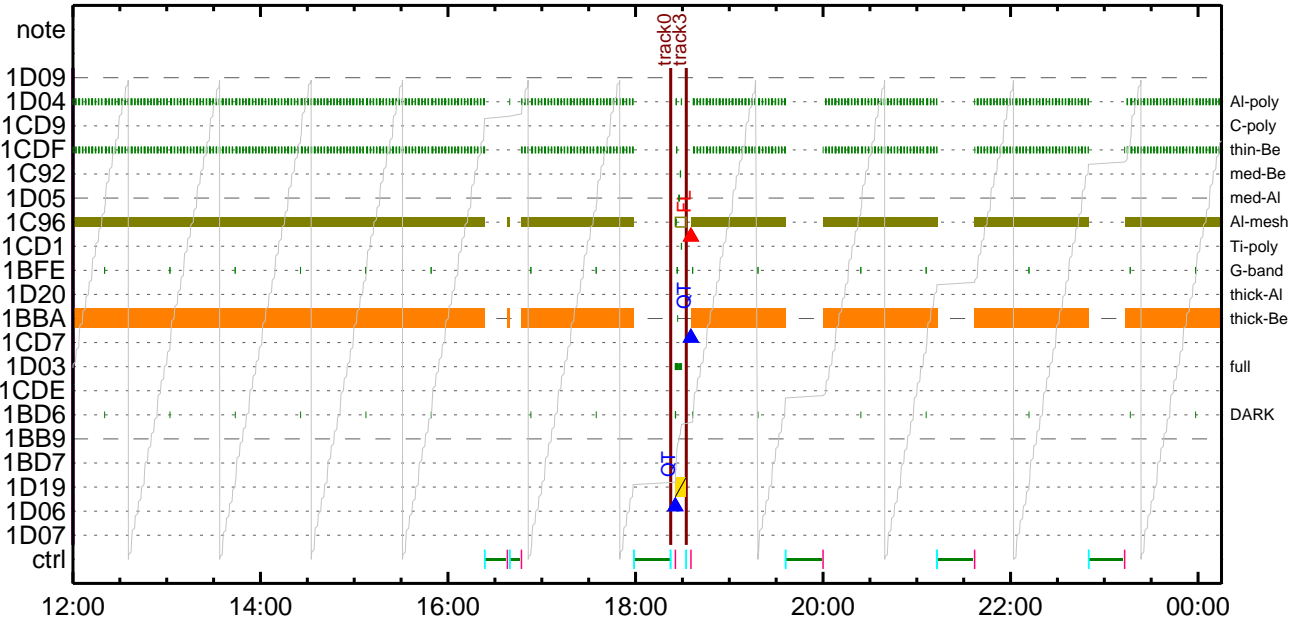
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FLD Patrol												
Term		Pointing (x, y)						Comment				
11/14 10:14:35 - 11/14 18:22:48		cannot be identified										
11/14 18:32:48 - 11/15 06:05:48		Track (366.1, -244.9)		① 11/14 18:32:30		AR13889						
11/15 06:15:48 - 11/15 18:00:18		Track (462.8, -241.6)		② 11/15 06:15:30		AR13889						
11/15 18:10:18 - 11/16 06:05:18		Track (554.7, -237.8)		③ 11/15 18:10:00		AR13889						
11/16 06:15:18 - 11/19 10:53:00		Track (640.0, -233.6)		④ 11/16 06:15:00		AR13889 (HOP 495 08:00-09:30)						
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

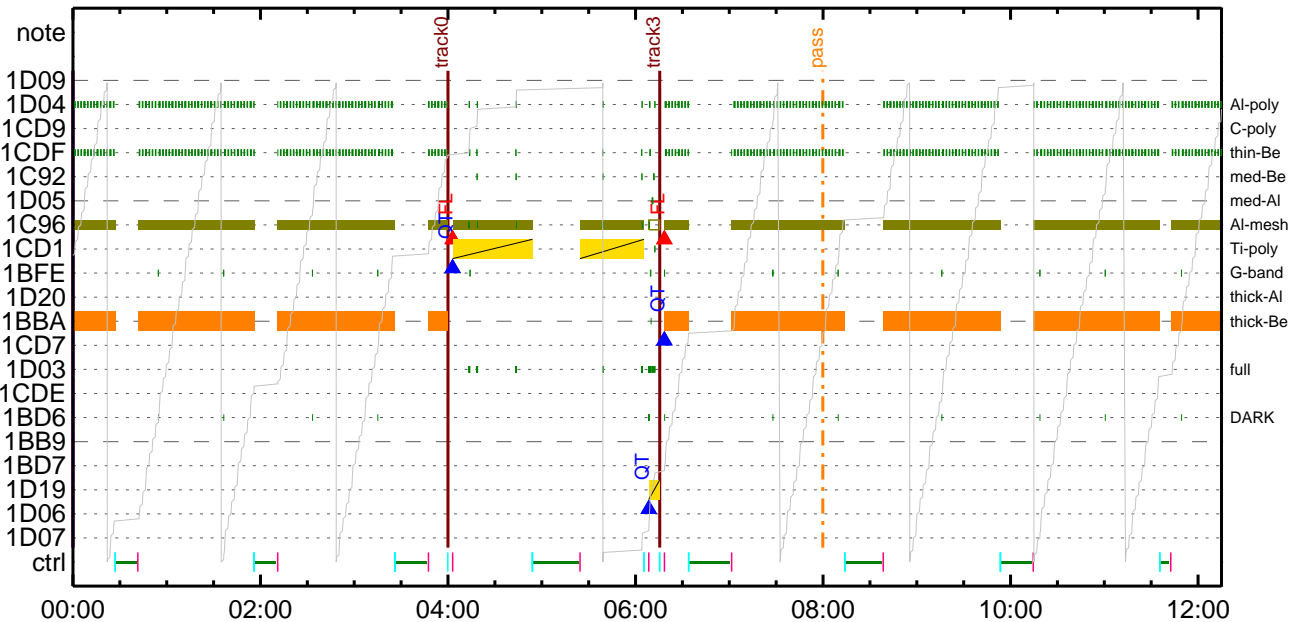
CMDI #0236 2024/11/14



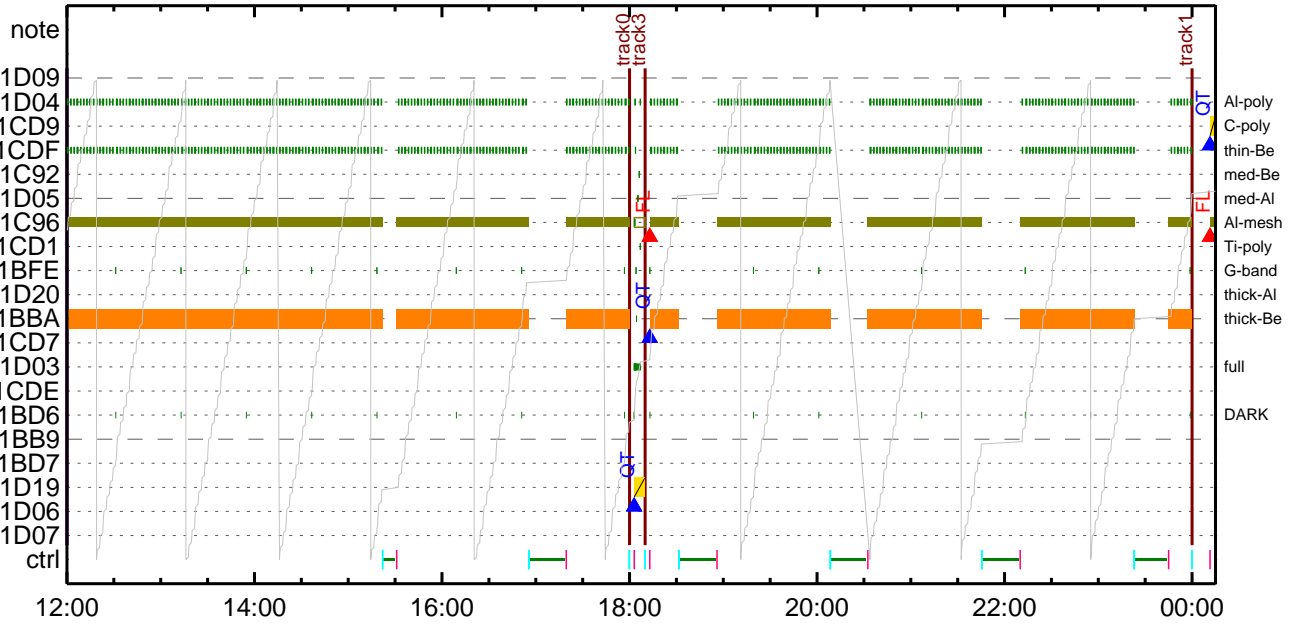
CMDI #0236 2024/11/14



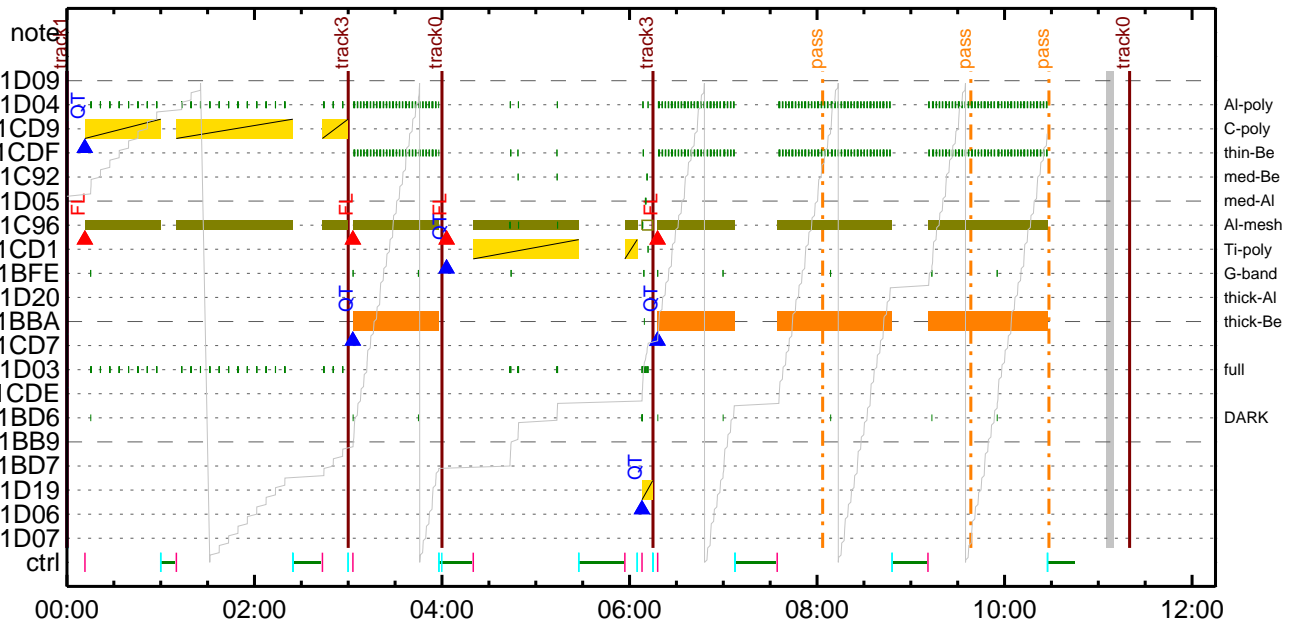
CMDI #0236 2024/11/15



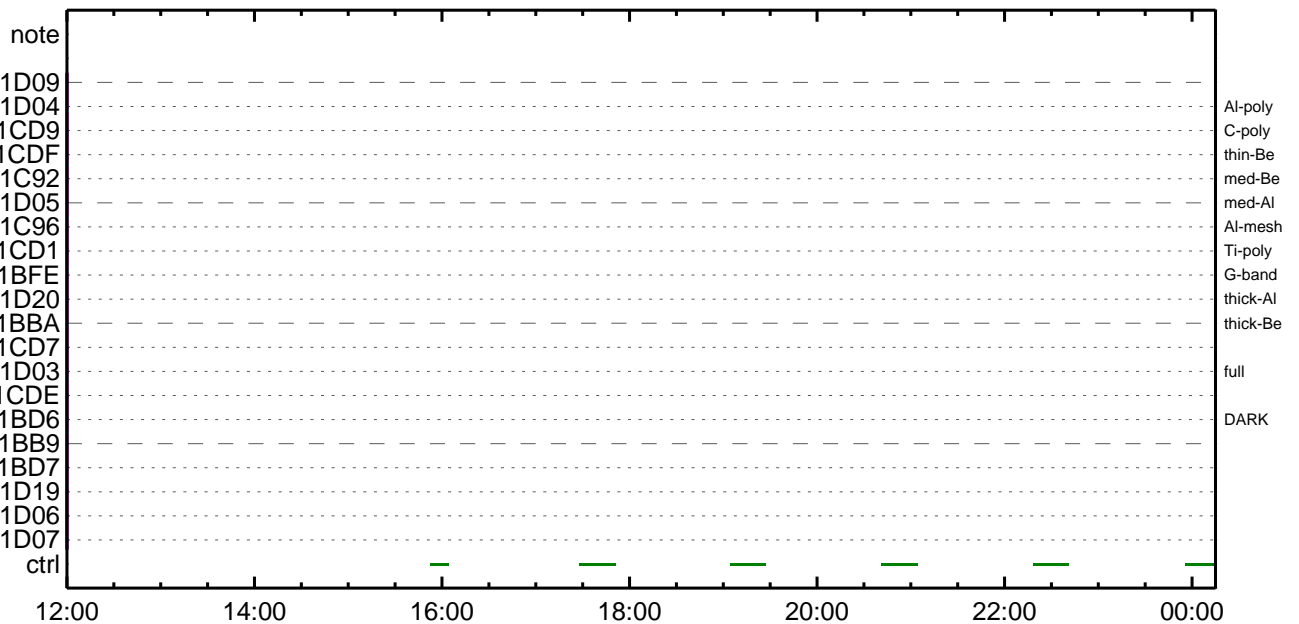
CMDI #0236 2024/11/15



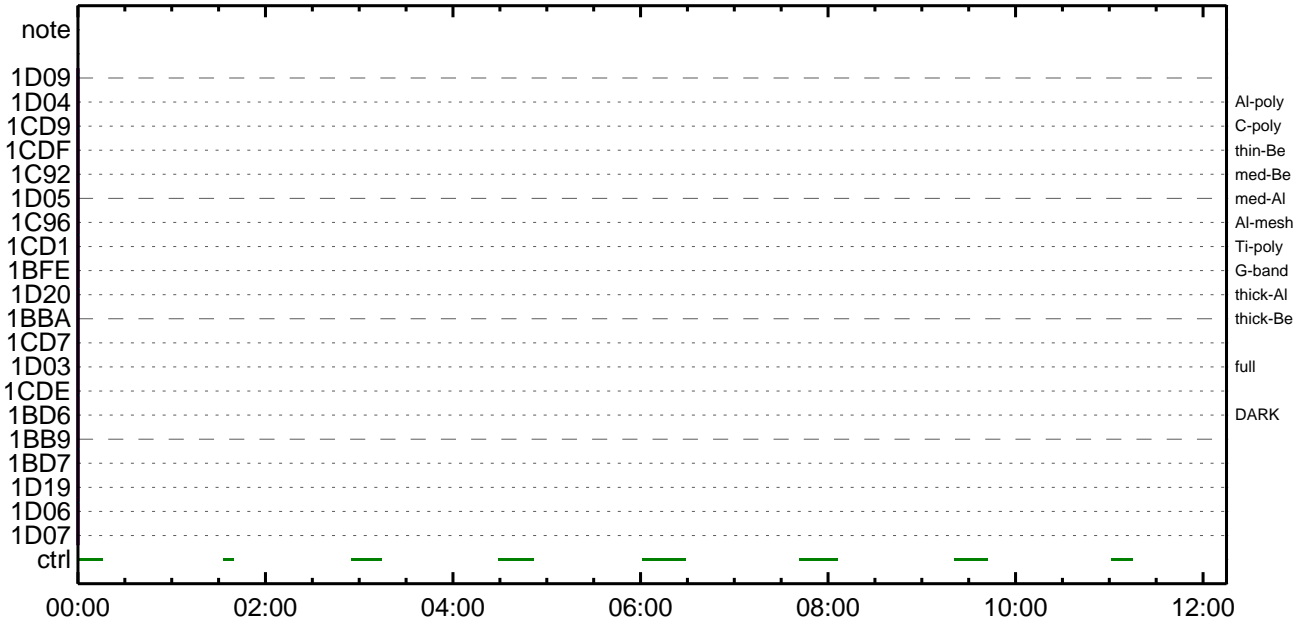
CMDI #0236 2024/11/16



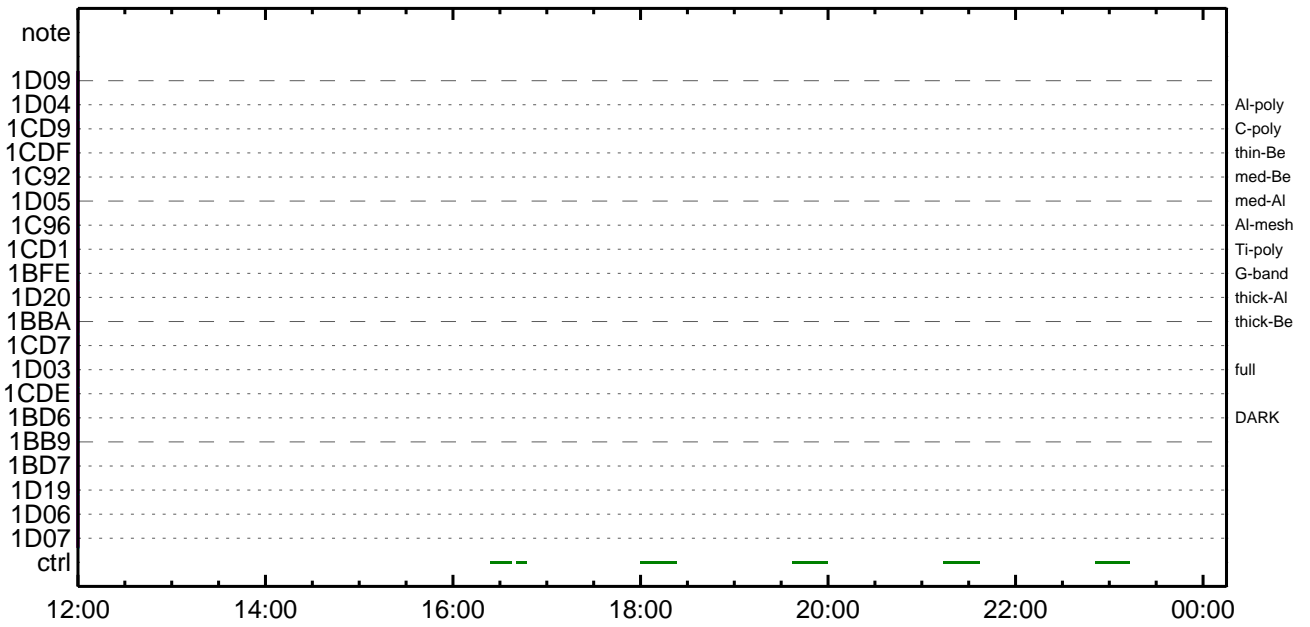
CMDI #0236 2024/11/16



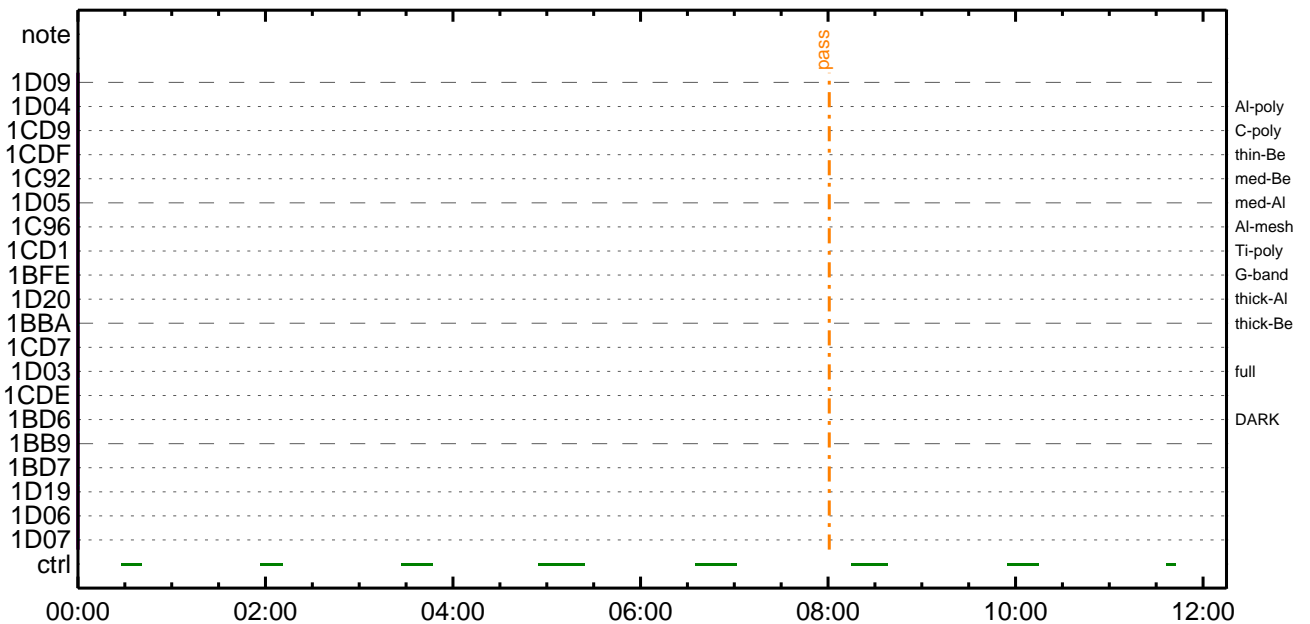
CMDI #0236 2024/11/17



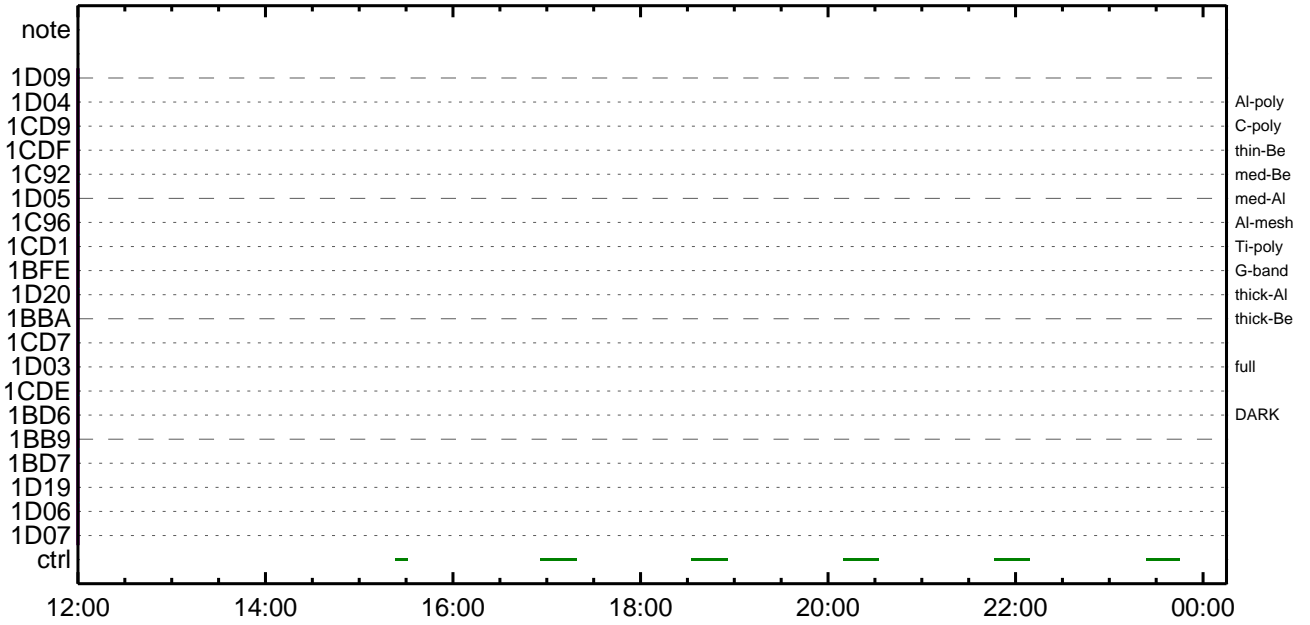
CMDI #0236 2024/11/17



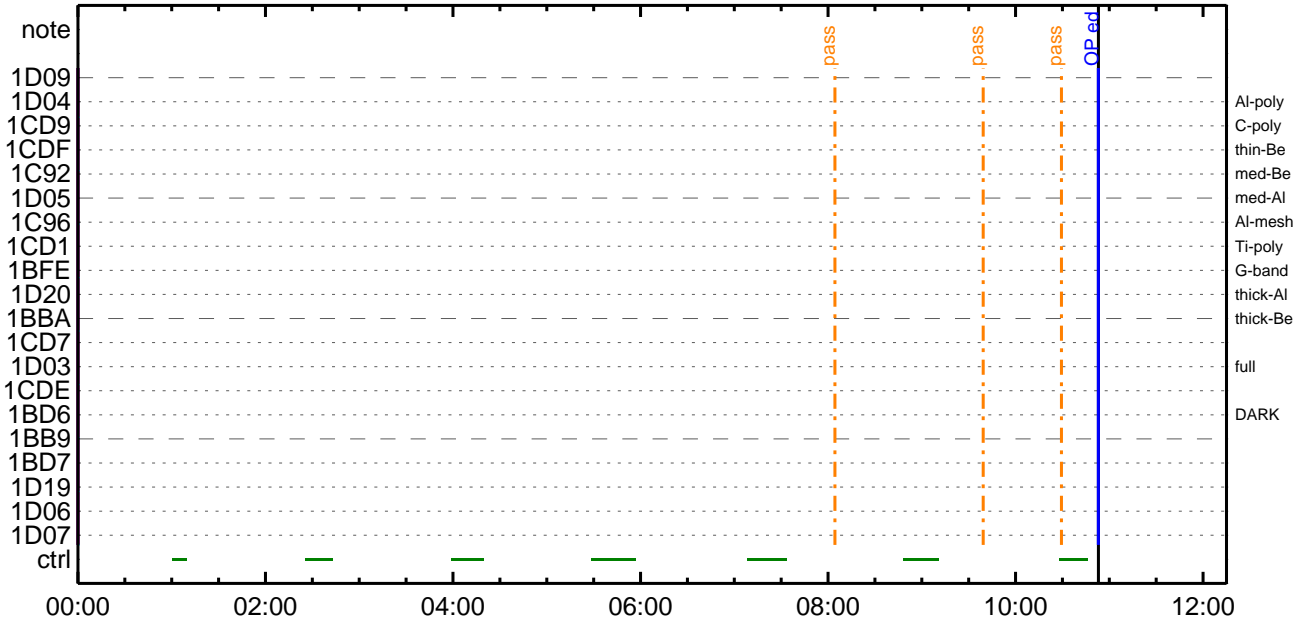
CMDI #0236 2024/11/18



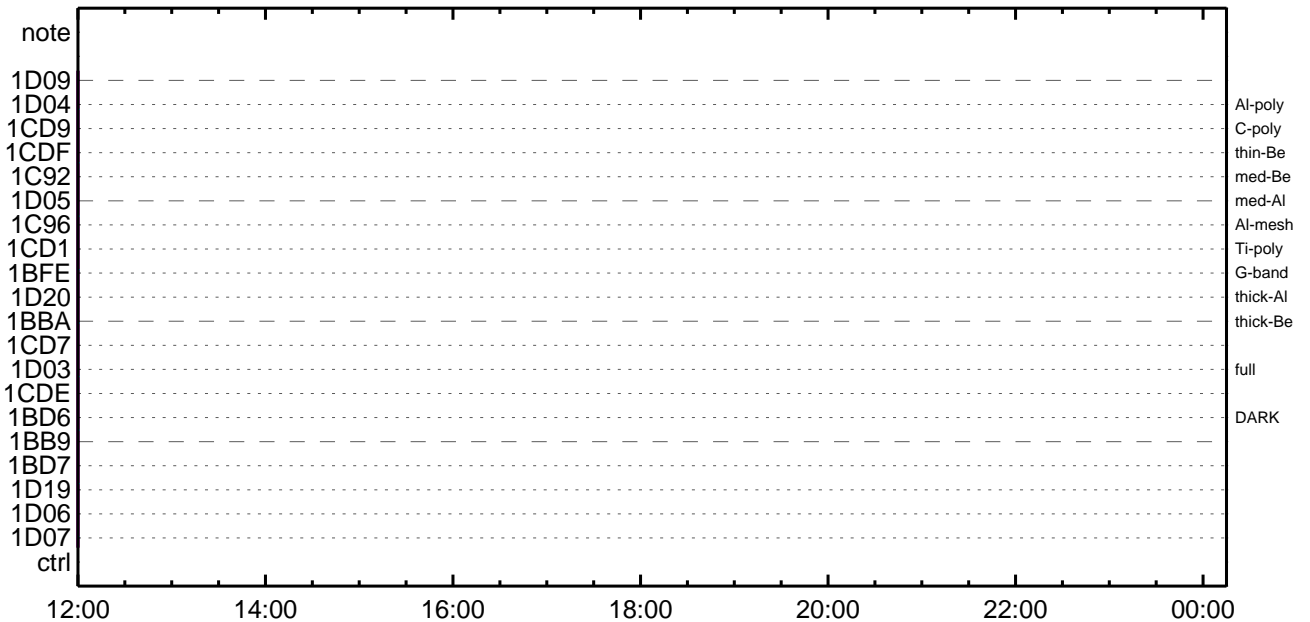
CMDI #0236 2024/11/18



CMDI #0236 2024/11/19



CMDI #0236 2024/11/19




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0096 C.          SET EDUMP  E±°iŸŸ¹qÇ¹Ôn|n³mÈ;E
0097 C.
0098 C. TI 2024-11-14 11:19:00.0
0099 +. TI 2024-11-14 11:19:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0102 C.
0103 +. TI 2024-11-14 11:19:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0106 C.
0107 +. TI 2024-11-14 11:19:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0110 C.
0111 +. TI 2024-11-14 11:23:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0114 C.
0115 C. °E²¼mĪÄê¾iĪŃmĪŸÁŸSŸÄŸ⁻¹àĪÜ
0116 C.          [ ] [HK1_TI_CMD_ENA/DIS]    EQ      ENA
0117 C.          [ ] [HK1_TI_CMD_NUM]      EQ      4
0118 C.          [ ] [HK1_NEXT_EXEC_PIM]    EQ      DHU
0119 C.          [ ] [HK1_NEXT_EXEC_DC]    EQ      0xB3
0120 C.
0121 C. *****
0122 C. TIĪĪ°èŸÄŸÓŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          [ ] [HK1_DMP_TOP_ADRS_1]    EQ      07
0129 C.          [ ] [HK1_DMP_TOP_ADRS_0]    EQ      2B
0130 C.          [ ] [HK1_DMP_BLOCK_NUM]     EQ      3
0131 C.          [ ] [HK1_DMP_REPEAT_NUM]    EQ      0
0132 C.          [ ] [HK1_DMA_DMP_PIM]      EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          [ ] [HK1_PKT_FORM_NO]       EQ      7
0136 C.          [ ] [HK1_PKT_GEN_TIME]      EQ      0.25 s
0137 C.          [ ] [HK1_S_TLM_BIT_RATE]    EQ      32k
0138 C.          [ ] [HK1_X_TLM_BIT_RATE]    EQ      4M
0139 C.          [ ] [HK1_DMP_CHK_FLG]      EQ      EXEC
0140 C.
0141 C. ŸÄŸÓŸ×½ªĪ»mò³ĪÇS
0142 C.          [ ] [HK1_DMP_CHK_FLG]      EQ      NON
0143 C.
0144 C. RAM ID=TI_TBLmĪ¾È¹Ç•è²ĪOKmò³ĪÇS
0145 C.
0146 C. DHUŸâ;¼ŸÉ;È¼Ÿ½;Ÿi;¼ŸÈ;ĪmòĪá¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          [ ] [HK1_PKT_FORM_NO]       EQ      2
0150 C.          [ ] [HK1_PKT_GEN_TIME]      EQ      0.5S
0151 C.          [ ] [HK1_S_TLM_BIT_RATE]    EQ      32K
0152 C.          [ ] [HK1_X_TLM_BIT_RATE]    EQ      4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2024-11-14 11:23:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2024-11-14 11:23:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2024-11-14 11:23:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´ũĪĪ»ö¾ŸmĪÄm¹mĪDCBC•x²è *****
0181 C. (¾ā°ĪŸÓŸÄŸÉŸDŸĪŸÄŸŸèmĪÈ¾¼m¼Ī»Ūm¹mĪè)
0182 S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** ŸDŸ¹•Ī Daily±çĪŃmĪÈ´Øm¹mĪDCBC•x²è *****
0187 S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;āLOSŸÄŸSŸÄŸ⁻¼Ī»Ü;ä
0192 C.
0193 C. ***** LOS *****

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(a) Spacecraft Operation Procedure (real-commands)

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main-931 2024-11-14 12:11:25 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYSYÁÿ-¼Á»Û;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝóYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëµ¿µÁµ•µ°Ê»Í×ÁÇµÍÿ¿YÁ×Yí;¼YÉ;ËËèµ•ÍË;ËËÈ¼°ÇÖµ•µ¿¼¹¿µÍ; ÇÀ®, ùµ¹µèµÐµÇÁ÷¿®µ•µËµµµ³µÈ; £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿µ;ON
0016 C. *****
0017 C. °EÀ, Í×ËÝµÄLOSµÐµÇµÍ»p´Öµò¹ÍË, µ•; ÇÉÖÍ×µËXÁÓONµÍ¹ÖµËµÍµËµµµ³µÈ; £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.          ÇÇ [HK1_XPA_ON/OFF]          EQ      ON
0025 C.          ÇÇ [HK1_XPA_PWR_HI/LO]       EQ      HI
0026 C.          ÇÇ [HK1_XMOD_ON/OFF]         EQ      ON
0027 C.          ÇÇ [HK1_XMOD_QPSK/PM]        EQ      QPSK
0028 C.
0029 . C. XYDÝóYÉYíYÁÿ-¾ÖÀÖµ¬°ÄÄèµ•µ¿µé; Ç°Ê²¼µÍ°EÀ, ¼è½Çµò¼Á¹Öµ¹µé; £
0030 C.
0031 . C. *****
0032 C. DR PT1 ÁÌ¼Í°EÀ,
0033 C. *****
0034 C. °° RESTART;ËPT1;Ëµ•µ¿µ¾¼¹¿µÍ; Ç°Ê²¼µÍ¼Á¹Öµµ°; ÇDCBC-150µØ¿Ëµà; £
0035 C.
0036 . C. ;ãPT1°EÀ, ¾«»Ë;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.          ÇÇ [HK1_REP_PT_1/2]          EQ      PT1 (¼Á¹Ö, ;¾Ú)
0043 C.          ÇÇ [HK1_REP_STA/STP]        EQ      START (¼Á¹Ö, ;¾Ú)
0044 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ      ON (¼Á¹Ö, ;¾Ú)
0045 C.
0046 . C. ;ãYÇYóYËYÉÁÚÁØ;ËÁ•Ä°²óèò;Ë, áµÍ°EÀ, °E³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.          ÇÇ [HK1_REP_PT_1/2]          EQ      PT1 (¼Á¹Ö, ;¾Ú)
0050 C.          ÇÇ [HK1_REP_STA/STP]        EQ      START (¼Á¹Ö, ;¾Ú)
0051 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ      ON (¼Á¹Ö, ;¾Ú)
0052 C.
0053 C.
0054 . C. PT1°EÀ, µ¬¼«°E°Äá»ßµ•µ¿, á; Ç°Ê²¼µò¼Á¹Öµ¹µé; £
0055 C. YÇYóYËYÉÁÚÁØµÄÁ•Ä°²óèòµ¬¾¼µ¾¼¹¿µÍ´°Í´°Íµ¹µèµÐµÇÁÖµÄ; £
0056 C.
0057 . C. *****
0058 C. DR PT2 ÁÌ¼Í°EÀ,
0059 C. *****
0060 C. °° RESTART;ËPT2;Ëµ•µ¿µ¾¼¹¿µÍ; Ç°Ê²¼µÍ¼Á¹Öµµ°; ÇDCBC-151µØ¿Ëµà; £
0061 C.
0062 . C. ;ãPT2°EÀ, ¾«»Ë;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.          ÇÇ [HK1_REP_PT_1/2]          EQ      PT2 (¼Á¹Ö, ;¾Ú)
0069 C.          ÇÇ [HK1_REP_STA/STP]        EQ      START (¼Á¹Ö, ;¾Ú)
0070 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ      ON (¼Á¹Ö, ;¾Ú)
0071 C.
0072 . C. ;ãYÇYóYËYÉÁÚÁØ;ËÁ•Ä°²óèò;Ë, áµÍ°EÀ, °E³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.          ÇÇ [HK1_REP_PT_1/2]          EQ      PT2 (¼Á¹Ö, ;¾Ú)
0076 C.          ÇÇ [HK1_REP_STA/STP]        EQ      START (¼Á¹Ö, ;¾Ú)
0077 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ      ON (¼Á¹Ö, ;¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÀ, Äá»ß; ÇXÁ÷¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÀ, Äá»ß; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.          ÇÇ [HK1_REP_STA/STP]        EQ      STOP
0087 C.          ÇÇ [HK1_S_VC4_ON/OFF]       EQ      OFF
0088 C.          ÇÇ [HK1_X_VC4_ON/OFF]       EQ      OFF
0089 C.
0090 . C. ;ãXÁ÷¿µ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.          ÇÇ [HK1_XMOD_ON/OFF]         EQ      OFF
0095 C.          ÇÇ [HK1_XPA_ON/OFF]         EQ      OFF

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 17s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2024-11-14 11:23:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'uAÎI»ö¼YnEÄDn¹nëDCBC•x²è *****
0156 C. (%ã°I¥ÓYÄYÉYÐYËYÄYÇYÈnÈ¼n¼Ä»Ûn¹në)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•Ï Daily±çÍÑnÈ'Øn¹nëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥S¥Ä¥~¼Ä»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-932 2024-11-14 12:11:25 184 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Ä»Ü;ä
0005 C.
0006 C. YÀYß;¼Y³YDÝóYÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËçµÄµ•µ°Ë>Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ËËèµ•íÍË;ËËÈ¼°ÇÖµ•µç¼i¹çµÍ;çÀ®, ùµ¹µèµDµÇÁ÷ç®µ•µËµµµ³µÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. ç° òÈÀ, Í×ËÝµäLOSµDµÇµÍ»p´òµò¹íÍ, µ•; çÉÔÍ×µËXÁÓONµÍ¹ÔµËµÍµËµµµ³µÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝóYÉYíYÁY~¾òÄÖµ¬°ÄÄèµ•µç;µé; ç°Ë²¼µÍ°ÈÀ, ¼è¼çµò¼Ä¹Ôµ¹µé;£
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÌ¼í°ÈÀ,
0033 C. *****
0034 C. ç° RESTART;ËPT1;Ëµ•µçµ¼¼i¹çµÍ; ç°Ë²¼µÍ¼Ä¹Ôµ»µ°; çDCBC-150µØçÈµà;£
0035 C.
0036 . C. ;ãPT1°ÈÀ, ³«»Í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¼Ú)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÉYËÄÚÄØ;ËÄ•Ä°²óèò;Ë, äµÍ°ÈÀ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ;¼Ú)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÈÀ, µ¬¼«°ÈÄ»ßµ•µç, ä; ç°Ë²¼µò¼Ä¹Ôµ¹µé;£
0055 C. YçYóYÉYËÄÚÄØµäÄ•Ä°²óèòµ¬¼áµ¼¼i¹çµÍ´°Í´°Í»µ¹µèµDµÇÁÖµÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÌ¼í°ÈÀ,
0059 C. *****
0060 C. ç° RESTART;ËPT2;Ëµ•µçµ¼¼i¹çµÍ; ç°Ë²¼µÍ¼Ä¹Ôµ»µ°; çDCBC-151µØçÈµà;£
0061 C.
0062 . C. ;ãPT2°ÈÀ, ³«»Í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¼Ú)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÉYËÄÚÄØ;ËÄ•Ä°²óèò;Ë, äµÍ°ÈÀ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ;¼Ú)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ;¼Ú)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÈÄ, Ää»ß; çXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÈÄ, Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 0c)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0e)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2024-11-14 11:23:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 C.
0170 . C. ***** MDP 'úÃî«î»ö¼Ý«ÊÃÐ«¹«ëDCBC•x²è *****
0171 C. (%ã°î¥Ó¥Ã¥Ê¥Ð¥Ë¥Á¥ç¥è«Ê¼«¼Ã»Û«¹«é)
0172 . S. DC-BC dcbc-402:DCBC
0173 (MDP_known_event)
0174 C.
0175 C.
0176 . C. ***** ¥Ð¥¹•Ï Daily±çÍÑ«Ê'Ø«¹«ëDCBC•x²è *****
0177 . S. DC-BC dcbc-153:DCBC
0178 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0179 C.
0180 C.
0181 . C. ;ãLOS¥Á¥S¥Ã¥~¼Ã»Û;ä
0182 C.
0183 . C. ***** LOS *****
0184 C.
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*** OP Sequence for XRT ***

2024/11/14	11:33:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	11:33:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	11:33:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2024/11/14	11:34:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 03 ce 01 f3		
2024/11/14	11:34:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/14	11:34:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/14	11:34:22.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2024/11/14	11:34:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/11/14	11:34:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/14	11:36:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2024/11/14	11:36:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2024/11/14	11:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/14	16:23:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	16:23:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	16:23:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/14	16:23:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/14	16:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/14	16:37:00.0	XRT_Custom_430_OG [0x1ae]					
2024/11/14	16:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/14	16:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	16:39:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	16:39:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/14	16:39:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/14	16:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/14	16:46:00.0	XRT_Custom_430_OG [0x1ae]					
2024/11/14	16:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/14	17:59:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	17:59:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	17:59:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2024/11/14	17:59:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2024/11/14	18:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2024/11/14	18:22:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	18:22:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	18:22:28.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2024/11/14	18:22:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2024/11/14	18:22:48.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2024/11/14	18:22:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2024/11/14	18:22:52.0	XRT_ARS_DIS_435_OG [0x1b3]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2024/11/14	18:25:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2024/11/14	18:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2024/11/14	18:32:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	18:32:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2024/11/14	18:32:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2024/11/14	18:32:30.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 03 ce 01 f3		
2024/11/14	18:32:48.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2024/11/14	18:32:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2024/11/14	18:32:52.0	XRT_AEC_RESET_448_OG [0x1c0]					

2024/11/14	18:32:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2024/11/14	18:32:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/14	18:35:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2024/11/14	18:35:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/11/14	18:35:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/14	19:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/14	19:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/14	19:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/14	19:36:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/14	19:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/14	19:59:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/14	20:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/14	21:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/14	21:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/14	21:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/14	21:13:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/14	21:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/14	21:36:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/14	21:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/14	22:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/14	22:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/14	22:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/14	22:50:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/14	22:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/14	23:12:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/14	23:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/15	00:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	00:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	00:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/15	00:27:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/15	00:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/15	00:40:30.0	XRT_Custom_430_OG [0x1ae]				
2024/11/15	00:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/15	01:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	01:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	01:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/15	01:56:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/15	01:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/15	02:10:00.0	XRT_Custom_430_OG [0x1ae]				
2024/11/15	02:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/15	03:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	03:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	03:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2024/11/15	03:26:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/11/15	03:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/11/15	03:46:30.0	XRT_Custom_430_OG [0x1ae]				
2024/11/15	03:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/11/15	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/11/15	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

2024/11/15	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2024/11/15	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/15	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/15	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/15	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/15	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2024/11/15	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/11/15	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	04:54:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	04:54:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	04:54:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	04:54:06.0	XRT_PREFLR_STRT_417_OG [0x1a1] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	04:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	05:23:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/15	05:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	06:05:28.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2024/11/15	06:05:48.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/15	06:05:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/15	06:05:52.0	XRT_ARS_DIS_435_OG [0x1b3] MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/15	06:08:28.0	XRT_QT_PROG_SET_436_OG [0x1b4] MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2024/11/15	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	06:15:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	06:15:28.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2024/11/15	06:15:30.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03	03	ce	01	f3
2024/11/15	06:15:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/15	06:15:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/15	06:15:52.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/15	06:15:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/15	06:15:56.0	XRT_FLD_RESET_434_OG [0x1b2] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	06:18:26.0	XRT_QT_PROG_SET_442_OG [0x1ba] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2024/11/15	06:18:28.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2024/11/15	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	06:34:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	06:34:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	06:34:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	06:34:06.0	XRT_PREFLR_STRT_417_OG [0x1a1] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	06:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	07:00:30.0	XRT_Custom_430_OG [0x1ae]							
2024/11/15	07:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	08:14:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	08:14:02.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	08:14:04.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	08:14:06.0	XRT_PREFLR_STRT_417_OG [0x1a1] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	08:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

2024/11/15	08:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	08:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/11/15	09:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	09:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	09:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	09:53:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	09:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	10:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	10:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/11/15	11:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	11:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	11:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	11:35:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	11:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	11:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	11:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/11/15	15:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	15:22:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	15:22:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	15:22:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	15:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	15:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	15:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/11/15	16:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	16:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	16:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	16:55:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/15	16:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2024/11/15	17:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/15	17:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
2024/11/15	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/15	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00				
2024/11/15	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/15	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2024/11/15	18:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/15	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2024/11/15	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/15	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/15	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/11/15	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 ce 01 f3				
2024/11/15	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/15	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/15	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/15	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/15	18:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da				

2024/11/15	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2024/11/15	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/15	18:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/15	18:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	18:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	18:31:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/15	18:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/15	18:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/15	18:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2024/11/15	20:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/15	20:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	20:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	20:08:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/15	20:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/15	20:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/15	20:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2024/11/15	21:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/15	21:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	21:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	21:45:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/15	21:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/15	22:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/15	22:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2024/11/15	23:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/15	23:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	23:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	23:23:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/15	23:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/15	23:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/15	23:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				
2024/11/15	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/15	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/15	23:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	00:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2024/11/16	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00 00 00 00
2024/11/16	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/11/16	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/11/16	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/11/16	00:00:26.0	XRT_FLD_RESET_438_OG [0x1b6]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/16	00:11:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/16	00:11:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2024/11/16	00:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/16	01:00:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/16	01:00:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	01:00:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	01:00:06.5	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/16	01:03:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/16	01:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/16	01:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG				

2024/11/16	02:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	02:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	02:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_START_417_OG [0x1a1]	1	07-F0	e8				
2024/11/16	02:24:36.0	XRT_PREFLR_START_417_OG [0x1a1]	MDP_XRT_PREFLR_START	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9				
2024/11/16	02:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
2024/11/16	02:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/16	02:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	02:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	02:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2024/11/16	03:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 03 ce 01 f3				
			AOCU_NM	5	02-76	03 03 ce 01 f3				
2024/11/16	03:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/16	03:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/16	03:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/16	03:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/16	03:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/16	03:02:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2024/11/16	03:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/11/16	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/16	03:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	03:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	03:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/16	03:58:06.0	XRT_PREFLR_START_417_OG [0x1a1]	MDP_XRT_PREFLR_START	1	07-F0	e8				
			MDP_XRT_PREFLR_START	1	07-F0	e8				
2024/11/16	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/16	04:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
			AOCU_NM	5	02-76	00 00 00 00 00				
2024/11/16	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2024/11/16	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2024/11/16	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2024/11/16	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2024/11/16	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/16	04:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/16	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2024/11/16	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2024/11/16	04:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/16	04:20:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	05:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	05:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	05:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2024/11/16	05:27:36.0	XRT_PREFLR_START_417_OG [0x1a1]	MDP_XRT_PREFLR_START	1	07-F0	e8				
			MDP_XRT_PREFLR_START	1	07-F0	e8				
2024/11/16	05:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2024/11/16	05:56:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2024/11/16	05:57:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	06:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2024/11/16	06:04:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2024/11/16	06:05:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2024/11/16	06:05:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	d9				

2024/11/16	06:05:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/16	06:07:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2024/11/16	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/16	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	06:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	06:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2024/11/16	06:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03	03 ce 01 f3
2024/11/16	06:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2024/11/16	06:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2024/11/16	06:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2024/11/16	06:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2024/11/16	06:15:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/16	06:17:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2024/11/16	06:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2024/11/16	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/16	07:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	07:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	07:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/16	07:07:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/16	07:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/16	07:33:30.0	XRT_Custom_430_OG [0x1ae]					
2024/11/16	07:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/16	08:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	08:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	08:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/16	08:48:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/16	08:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/16	09:10:00.0	XRT_Custom_430_OG [0x1ae]					
2024/11/16	09:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2024/11/16	10:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	10:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2024/11/16	10:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2024/11/16	10:27:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2024/11/16	10:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2024/11/16	11:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00