

XRT Timeline to be uploaded on 2024/12/28

Period: 2024/12/28 11:35:00 - 2025/01/07 11:03:00

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Normal mode

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XOB #1BD7: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak last)

Term	Pointing (x, y)	Comment
12/28 11:48:00 - 12/28 12:59:54	Track (350.2, -64.2) @ 12/28 11:45:00	# OP start + 10min + Spectral Atlas QS obs.
01/01 17:59:30 - 01/02 03:37:30	Track (510.8, 331.3) @ 01/01 17:56:30	AR13938
01/02 06:13:00 - 01/02 18:00:24	Track (594.0, 328.2) @ 01/02 06:10:00	AR13938
01/02 18:13:30 - 01/03 03:59:54	Track (667.5, 324.6) @ 01/02 18:10:30	AR13938
01/03 06:30:00 - 01/03 17:39:00	Track (733.2, 320.2) @ 01/03 06:27:00	AR13938
01/03 18:15:30 - 01/03 23:59:54	Track (786.6, 315.4) @ 01/03 18:12:30	AR13938
01/04 05:59:30 - 01/04 11:46:00	Track (829.6, 310.2) @ 01/04 05:56:30	AR13938

PROG= 07 Inf.-time(s)

Subr= 1	8-time(s)	900.0sec
Seqn= 8	1-time(s)	2.0sec
thin-Be/Open	med-Be/Open	close
Safe	Norm	1.00s
Obs	4x4	2048x2048 (1024, 1024)
Q=98	3	0
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	1.41s
Obs	4x4	2048x2048 (1024, 1024)
DPCM	2	0
2.0sec		
Seqn= 6	1-time(s)	2.0sec
Al-poly/Open	Al-poly/Open	close
Safe	Norm	125ms
Obs	4x4	2048x2048 (1024, 1024)
Q=98	3	0
2.0sec		
Al-poly/Open	Al-poly/Open	close
Safe	Norm	1.00s
Obs	4x4	2048x2048 (1024, 1024)
DPCM	2	0
2.0sec		
Subr= 2	1-time(s)	2.0sec
Seqn= 30	1-time(s)	2.0sec
Open/G-band	Open/G-band	open
Safe	Norm	1ms
Obs	1x1	512x512 (1024, 1024)
Q=90	0	0
2.0sec		
Open/G-band	Open/G-band	close
Safe	Norm	1ms
Obs	1x1	512x512 (1024, 1024)
Q=95	0	0
2.0sec		

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
12/28 13:03:00 - 12/28 17:38:00	Track (-330.3, 330.4) @ 12/28 13:00:00	AR13938
12/28 18:18:00 - 12/29 03:59:54	Track (-287.1, 331.5) @ 12/28 18:11:30	AR13938
01/01 12:57:00 - 01/01 17:46:24	Track (473.9, 332.3) @ 01/01 12:50:00	AR13938

PROG= 12 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 92	1-time(s)	2.0sec
Open/G-band	Open/G-band	open
Safe	Norm	1ms
Obs	1x1	384x384 (1064, 1048)
DPCM	0	0
2.0sec		
Open/G-band	Open/G-band	close
Safe	Norm	1ms
Obs	1x1	384x384 (1064, 1048)
DPCM	0	0
2.0sec		
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	16.0s
Obs	1x1	384x384 (1064, 1048)
Q=98	0	0
2.0sec		
Subr= 2	4-time(s)	2.0sec
Seqn= 47	1-time(s)	2.0sec
Al-poly/Open	thin-Be/Open	close
Safe	Norm	250ms
Obs	1x1	384x384 (1064, 1048)
Q=95	2	0
2.0sec		
Al-poly/Open	thin-Be/Open	close
Safe	Norm	250ms
Obs	1x1	384x384 (1064, 1048)
Q=95	3	0
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	2	0
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	3	0
2.0sec		
Seqn= 77	4-time(s)	300.0sec
Al-poly/Open	thin-Be/Open	close
Safe	Norm	250ms
Obs	1x1	384x384 (1064, 1048)
Q=95	1	0
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	1	0
95.0sec		
Al-poly/Open	thin-Be/Open	close
Safe	Norm	250ms
Obs	1x1	384x384 (1064, 1048)
Q=95	1	1
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	1	1
95.0sec		
Al-poly/Open	thin-Be/Open	close
Safe	Norm	250ms
Obs	1x1	384x384 (1064, 1048)
Q=95	1	2
2.0sec		
thin-Be/Open	med-Be/Open	close
Safe	Norm	500ms
Obs	1x1	384x384 (1064, 1048)
Q=95	1	2
2.0sec		

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
12/28 18:04:30 - 12/28 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
12/29 05:48:30 - 12/29 05:55:00	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -14.5 min
01/01 12:43:00 - 01/01 12:49:54	Fixed (0.0, 0.0)	Post bakeout synoptics
01/01 17:49:30 - 01/01 17:56:24	Fixed (0.0, 0.0)	synoptic, shifted -13.5 min
01/02 06:03:00 - 01/02 06:09:54	Fixed (0.0, 0.0)	HOP349 + synoptic
01/02 18:03:30 - 01/02 18:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min
01/03 06:20:00 - 01/03 06:26:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 17.0 min
01/03 18:05:30 - 01/03 18:12:24	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min
01/04 05:49:30 - 01/04 05:56:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -13.5 min

PROG= 03 1-time(s)

Subr= 1	1-time(s)	2.0sec
Seqn= 5	1-time(s)	2.0sec
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	500ms
Obs	2x2	2048x2048 (1024, 1024)
Q=98	0	0
2.0sec		
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	500ms
Obs	4x4	2048x2048 (1024, 1024)
Q=98	0	0
2.0sec		
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	500ms
Obs	8x8	2048x2048 (1024, 1024)
Q=98	0	0
2.0sec		
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	500ms
Obs	1x1	2048x512 (1024, 1024)
DPCM	0	0
2.0sec		
Open/Ti-poly	Open/thick-Al	close
Safe	Dark	500ms
Obs	1x1	512x2048 (1024, 1024)
DPCM	0	0
2.0sec		
Seqn= 55	1-time(s)	2.0sec
Open/Al-mesh	Open/Al-mesh	close
Safe	Norm	2ms
Obs	2x2	2048x2048 (1024, 1024)
Q=95	0	0
2.0sec		
Open/Al-mesh	Open/Al-mesh	close
Safe	Norm	125ms
Obs	2x2	2048x2048 (1024, 1024)
Q=95	0	0
2.0sec		
Open/Al-mesh	Open/Al-mesh	close
Safe	Norm	707ms
Obs	2x2	2048x2048 (1024, 1024)
Q=95	0	0
2.0sec		

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 76 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 41 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 18 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 86 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w												
Term			Pointing (x, y)				Comment					
12/29 04:07:00 - 12/29 05:42:30			Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -14.5 min					
01/02 04:03:00 - 01/02 05:59:54			Fixed (0.0, 0.0)				HOP349 + synoptic					
01/03 04:31:30 - 01/03 06:16:54			Fixed (0.0, 0.0)				HOP349 + synoptic, shifted 17.0 min					
01/04 04:03:00 - 01/04 05:46:24			Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -13.5 min					

PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 300.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 15-time(s) 1500.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D03: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msCC												
Term			Pointing (x, y)				Comment					
01/01 12:03:00 - 01/01 12:09:54			Fixed (-528.4, -528.4)				Post bakeout Q1					

PROG= 08 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 51 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 120.0sec												
Seqn= 93 2-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 34 1-time(s) 60.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D04: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 2nd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC

Term	Pointing (x, y)	Comment
01/01 12:13:00 - 01/01 12:19:54	Fixed (528.4, -528.4)	Post bakeout Q2
PROG= 19 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 38 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 1ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 120.0sec		
Seqn= 93 2-time(s) 2.0sec		
Open/Al-mesh	Open/thick-Al close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 3 2-time(s) 2.0sec		
Seqn= 34 1-time(s) 60.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D05: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 3rd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC

Term	Pointing (x, y)	Comment
01/01 12:23:00 - 01/01 12:29:54	Fixed (528.4, 528.4)	Post bakeout Q3
PROG= 15 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 21 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 1ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 120.0sec		
Seqn= 93 2-time(s) 2.0sec		
Open/Al-mesh	Open/thick-Al close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 3 2-time(s) 2.0sec		
Seqn= 34 1-time(s) 60.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC

Term	Pointing (x, y)	Comment
01/01 12:33:00 - 01/01 12:39:54	Fixed (-528.4, 528.4)	Post bakeout Q4
PROG= 02 1-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 14 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 120.0sec		
Seqn= 93 2-time(s) 2.0sec		
Open/Al-mesh	Open/thick-Al close	Safe Norm 2.00s Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	med-Be/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 3 2-time(s) 2.0sec		
Seqn= 34 1-time(s) 60.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7

Term	Pointing (x, y)	Comment
01/04 00:25:30 - 01/04 03:59:54	Track (-22.0, 14.6) ^{01/04 00:00:00}	HOP393
PROG= 01 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 52 1-time(s) 2.0sec		

Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 2 30-time(s) 720.0sec												
Seqn= 97 2-time(s) 2.0sec												
Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GB

Term	Pointing (x, y)	Comment
12/28 11:48:00 - 12/28 12:59:54	Track (350.2, -64.2) @ 12/28 11:45:00	# OP start + 10min + Spectral Atlas QS obs.
12/28 13:03:00 - 12/28 17:38:00	Track (-330.3, 330.4) @ 12/28 13:00:00	AR13938
12/28 18:18:00 - 12/29 03:59:54	Track (-287.1, 331.5) @ 12/28 18:11:30	AR13938
12/29 04:07:00 - 12/29 05:42:30	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -14.5 min
01/01 12:57:30 - 01/01 17:46:24	Track (473.9, 332.3) @ 01/01 12:50:00	AR13938
01/01 17:59:30 - 01/02 03:37:30	Track (510.8, 331.3) @ 01/01 17:56:30	AR13938
01/02 04:03:00 - 01/02 05:59:54	Fixed (0.0, 0.0)	HOP349 + synoptic
01/02 06:13:00 - 01/02 18:00:24	Track (594.0, 328.2) @ 01/02 06:10:00	AR13938
01/02 18:13:30 - 01/03 03:59:54	Track (667.5, 324.6) @ 01/02 18:10:30	AR13938
01/03 04:31:30 - 01/03 06:16:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 17.0 min
01/03 06:30:00 - 01/03 17:39:00	Track (733.2, 320.2) @ 01/03 06:27:00	AR13938
01/03 18:15:30 - 01/03 23:59:54	Track (786.6, 315.4) @ 01/03 18:12:30	AR13938
01/04 00:25:30 - 01/04 03:59:54	Track (-22.0, 14.6) @ 01/04 00:00:00	HOP393
01/04 04:03:00 - 01/04 05:46:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -13.5 min
01/04 05:59:30 - 01/04 11:46:00	Track (829.6, 310.2) @ 01/04 05:56:30	AR13938

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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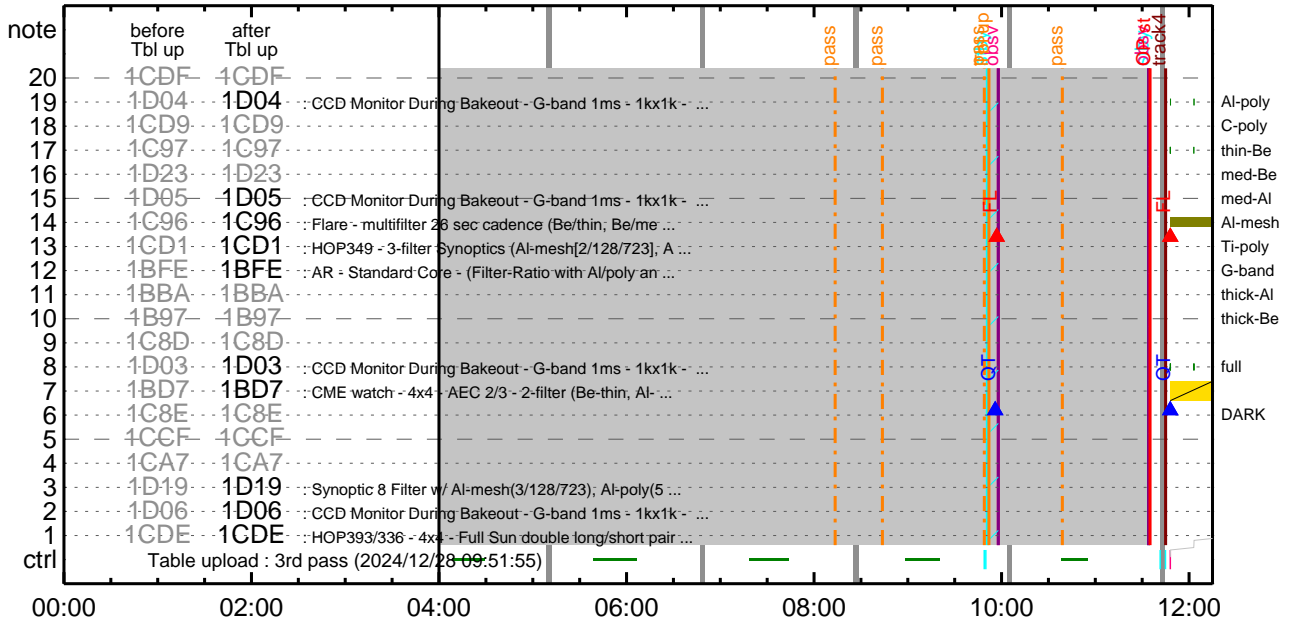
Flare Detection

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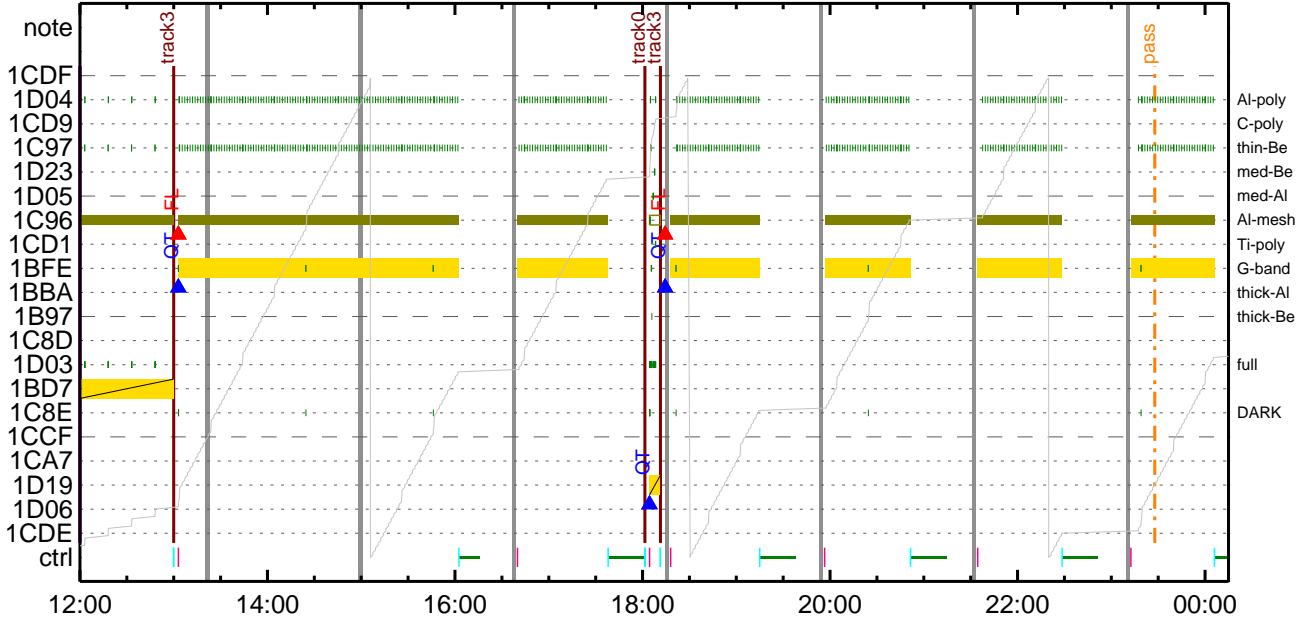
FLD Patrol

Term	Pointing (x, y)	Comment									
12/28 09:52:55 - 12/28 18:01:48	cannot be identified										
12/28 18:11:48 - 12/29 05:45:48	Track (-287.1, 331.5) @ 12/28 18:11:30	AR13938									
01/01 12:54:18 - 01/01 17:46:48	Track (473.9, 332.3) @ 01/01 12:50:00	AR13938									
01/01 17:56:46 - 01/02 06:00:18	Track (510.8, 331.3) @ 01/01 17:56:30	AR13938									
01/02 06:10:16 - 01/02 18:00:48	Track (594.0, 328.2) @ 01/02 06:10:00	AR13938									
01/02 18:10:46 - 01/03 06:17:18	Track (667.5, 324.6) @ 01/02 18:10:30	AR13938									
01/03 06:27:16 - 01/03 18:02:48	Track (733.2, 320.2) @ 01/03 06:27:00	AR13938									
01/03 18:12:46 - 01/04 05:46:48	Track (786.6, 315.4) @ 01/03 18:12:30	AR13938									
01/04 05:56:46 - 01/07 11:03:00	Track (829.6, 310.2) @ 01/04 05:56:30	AR13938									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

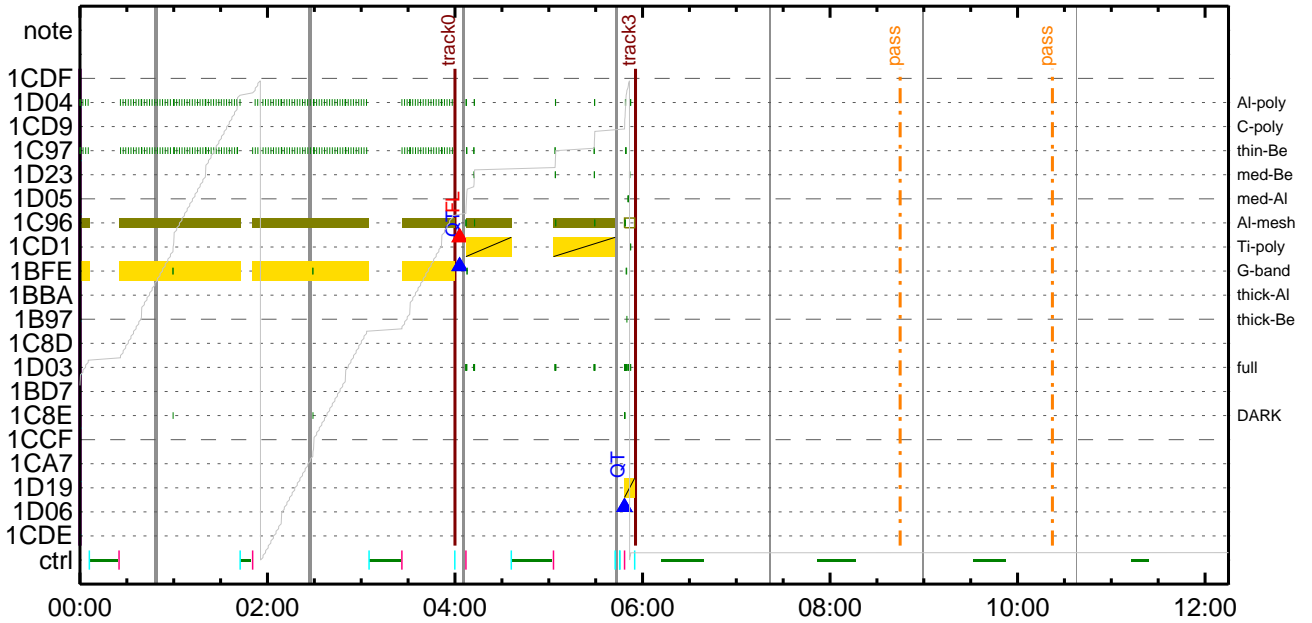
CMDI #0310 2024/12/28



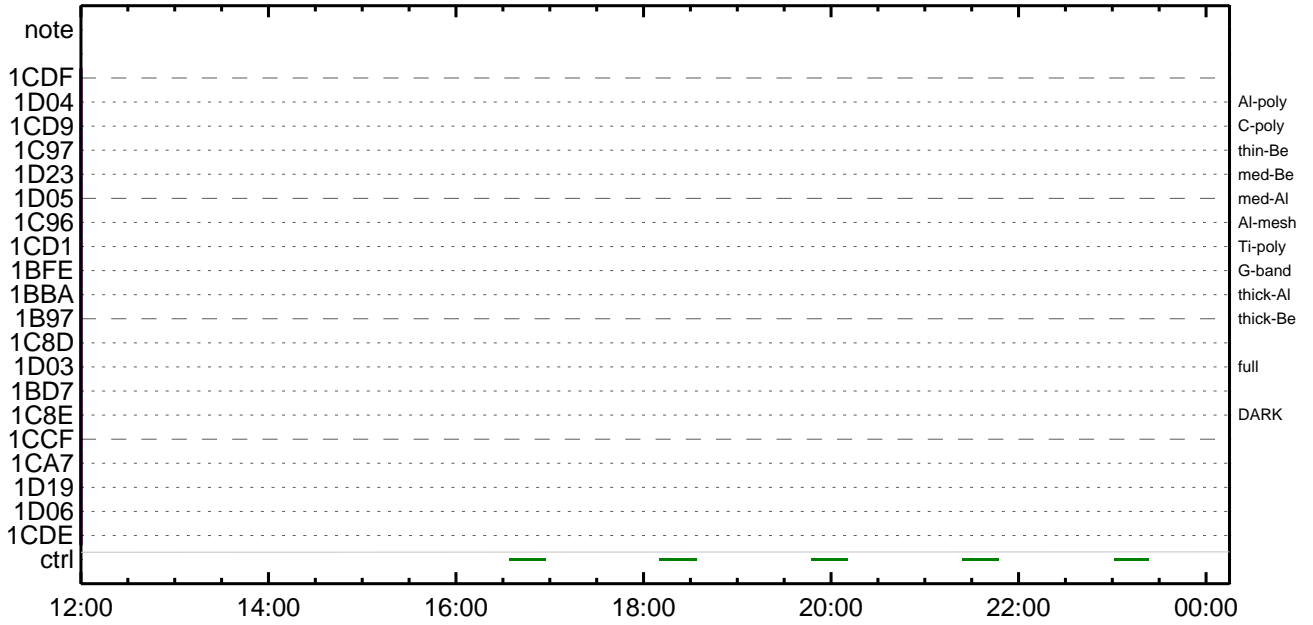
CMDI #0310 2024/12/28



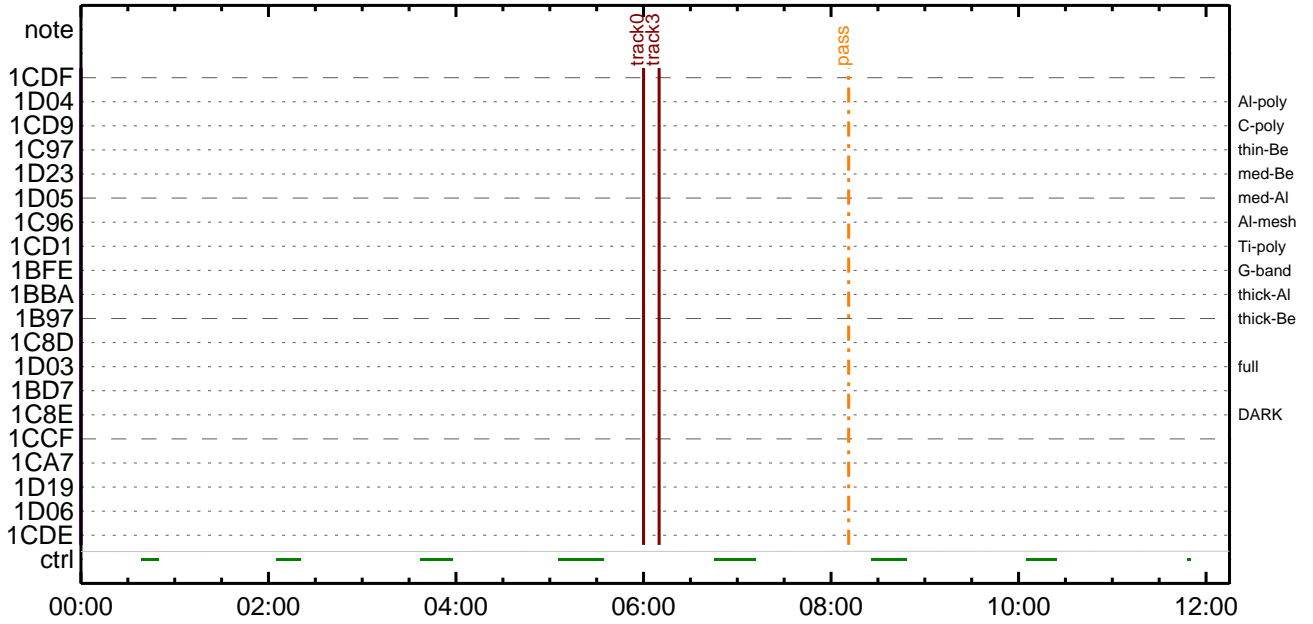
CMDI #0310 2024/12/29



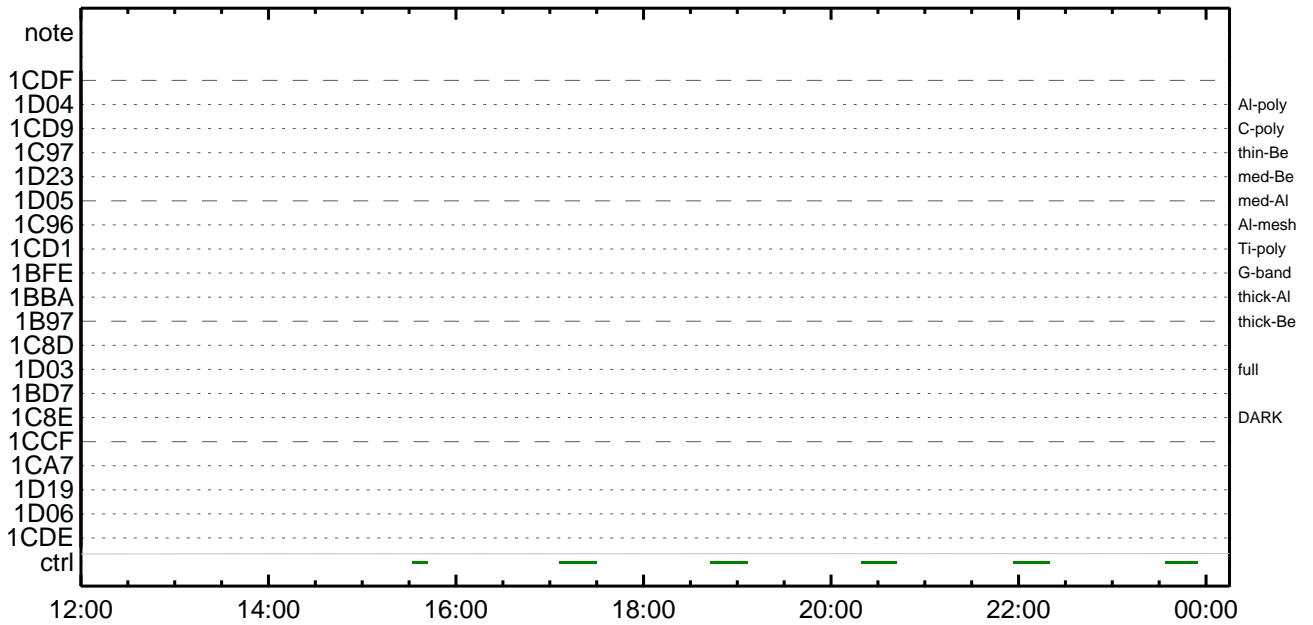
CMDI #0310 2024/12/29



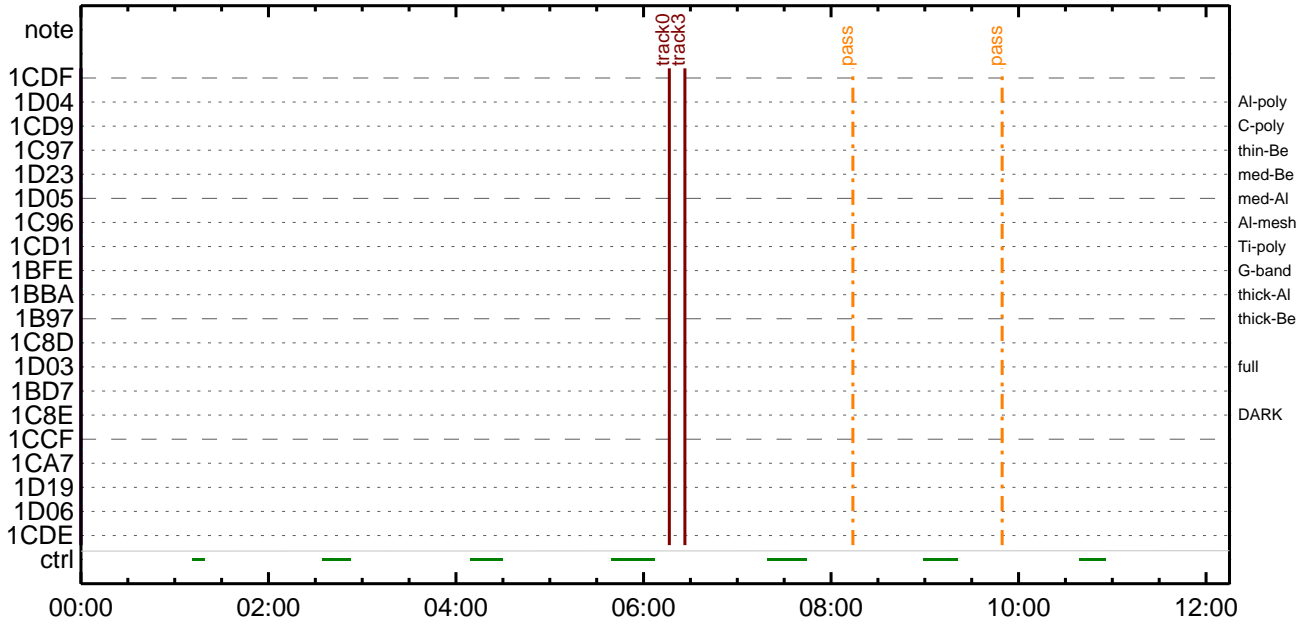
CMDI #0310 2024/12/30



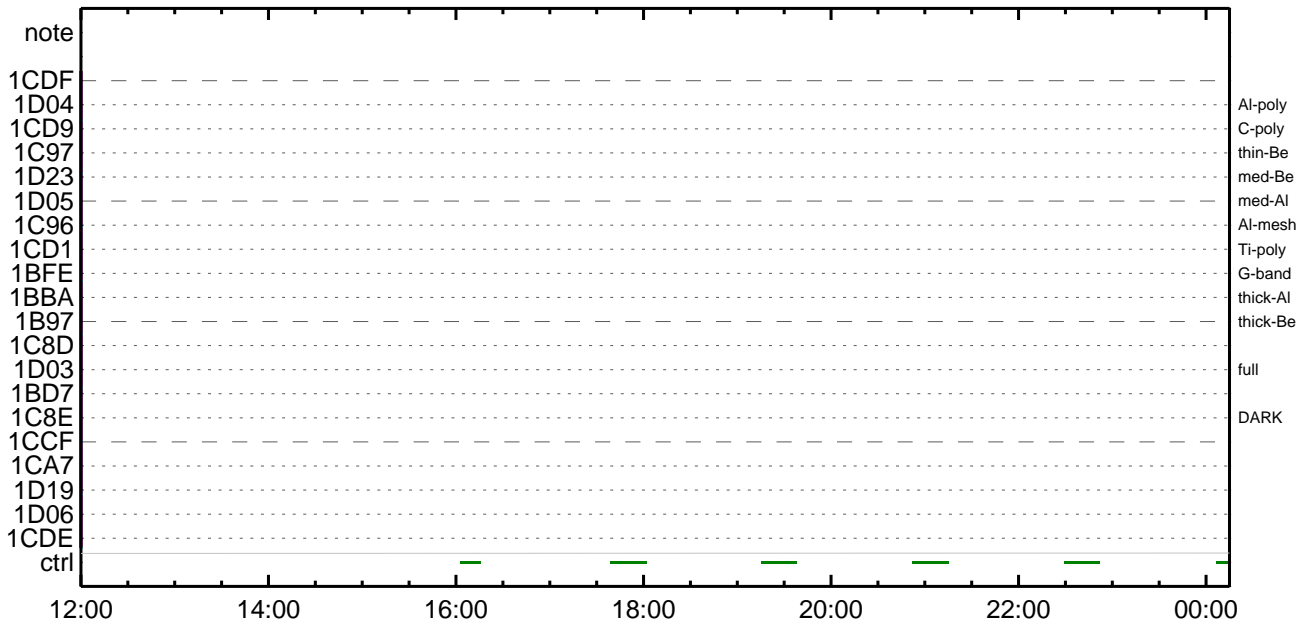
CMDI #0310 2024/12/30



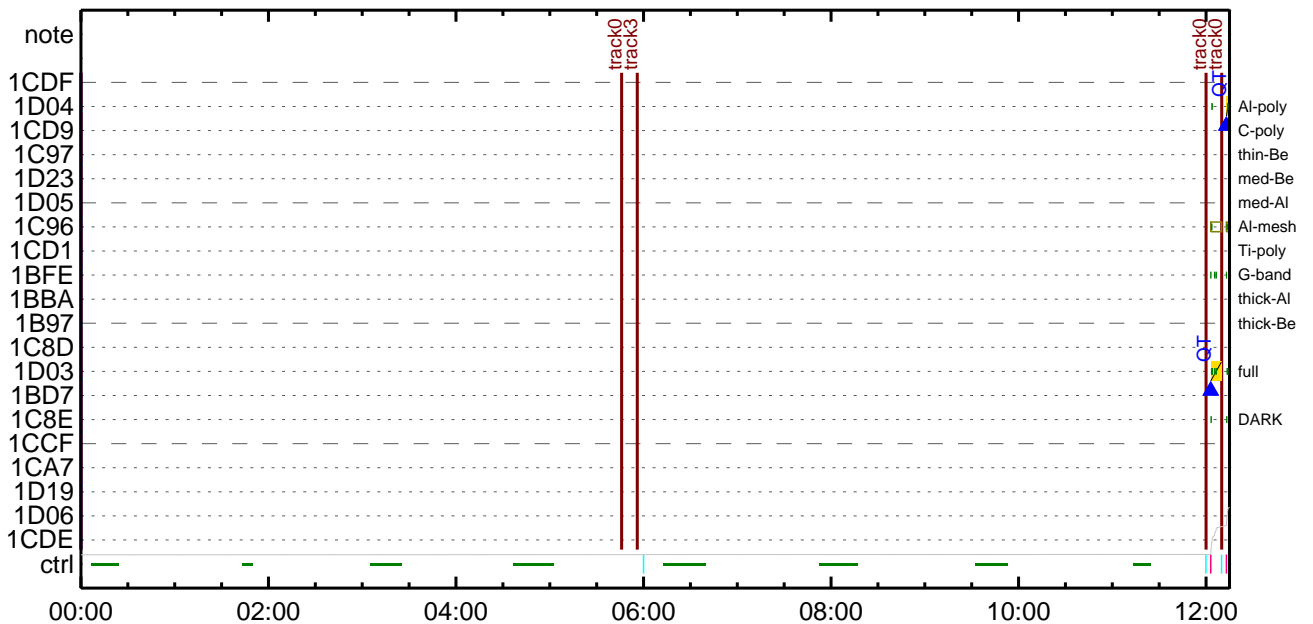
CMDI #0310 2024/12/31



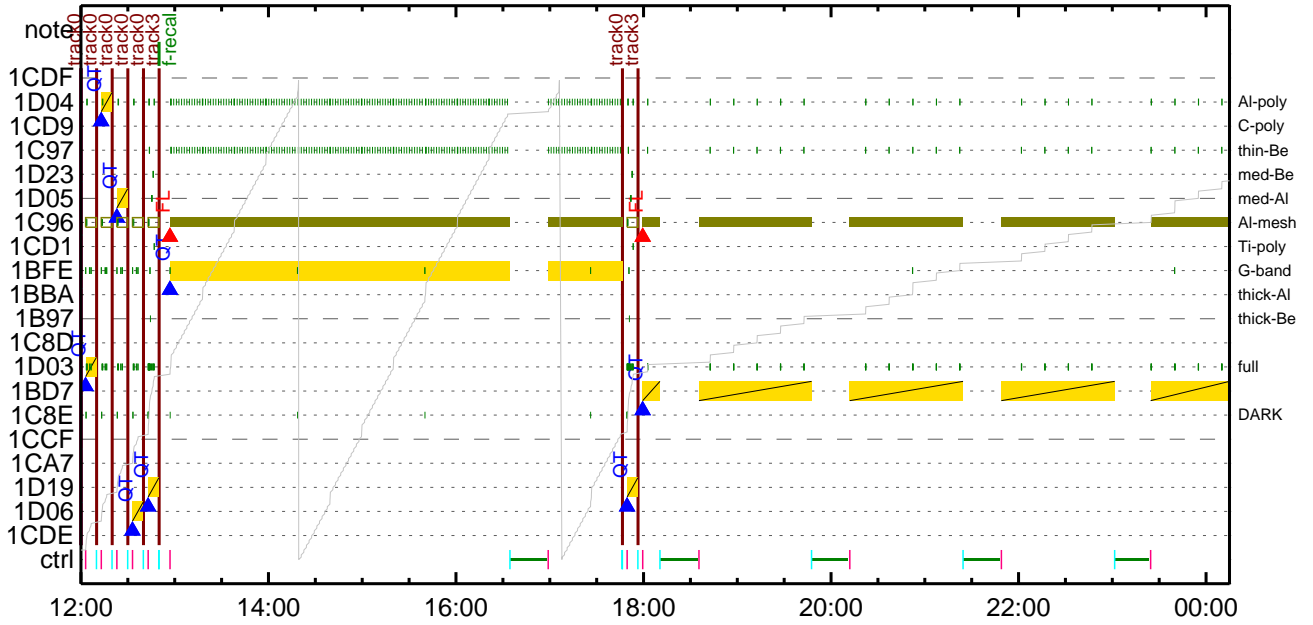
CMDI #0310 2024/12/31



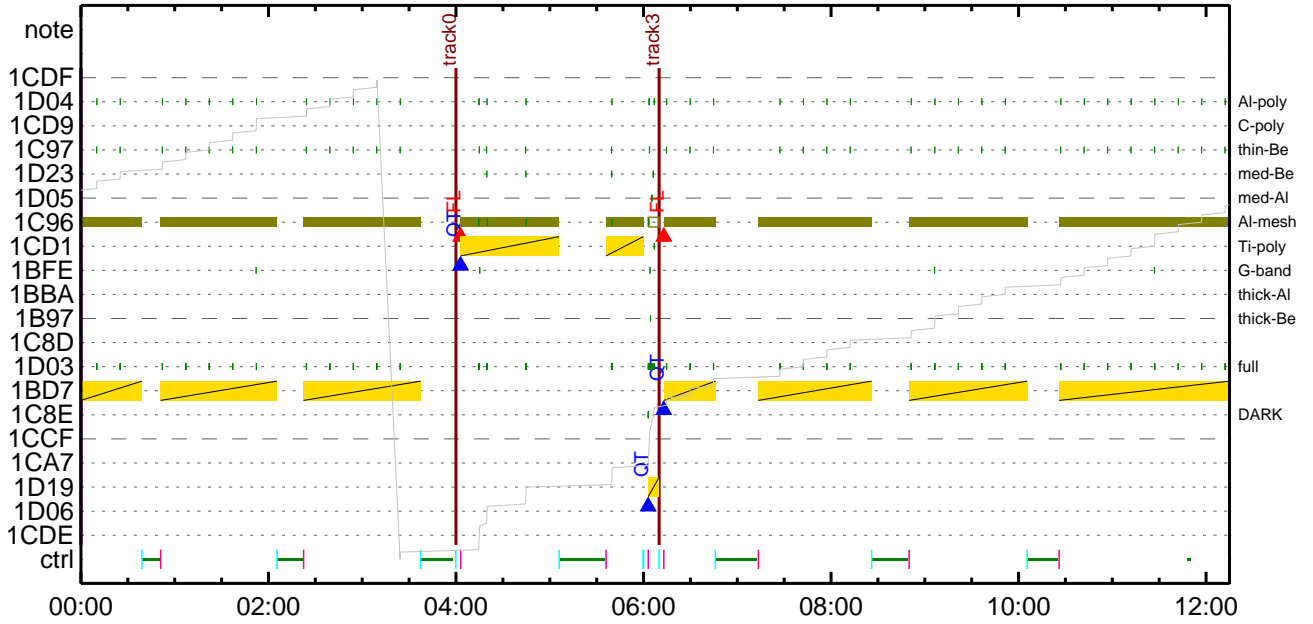
CMDI #0310 2025/01/01



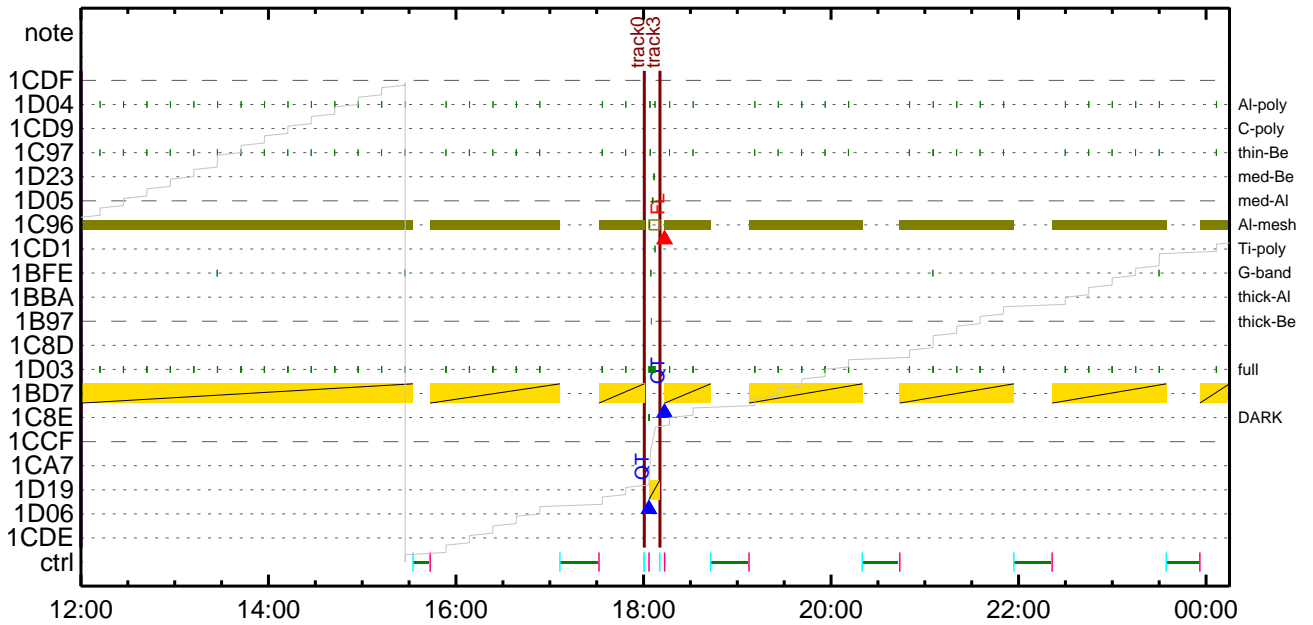
CMDI #0310 2025/01/01



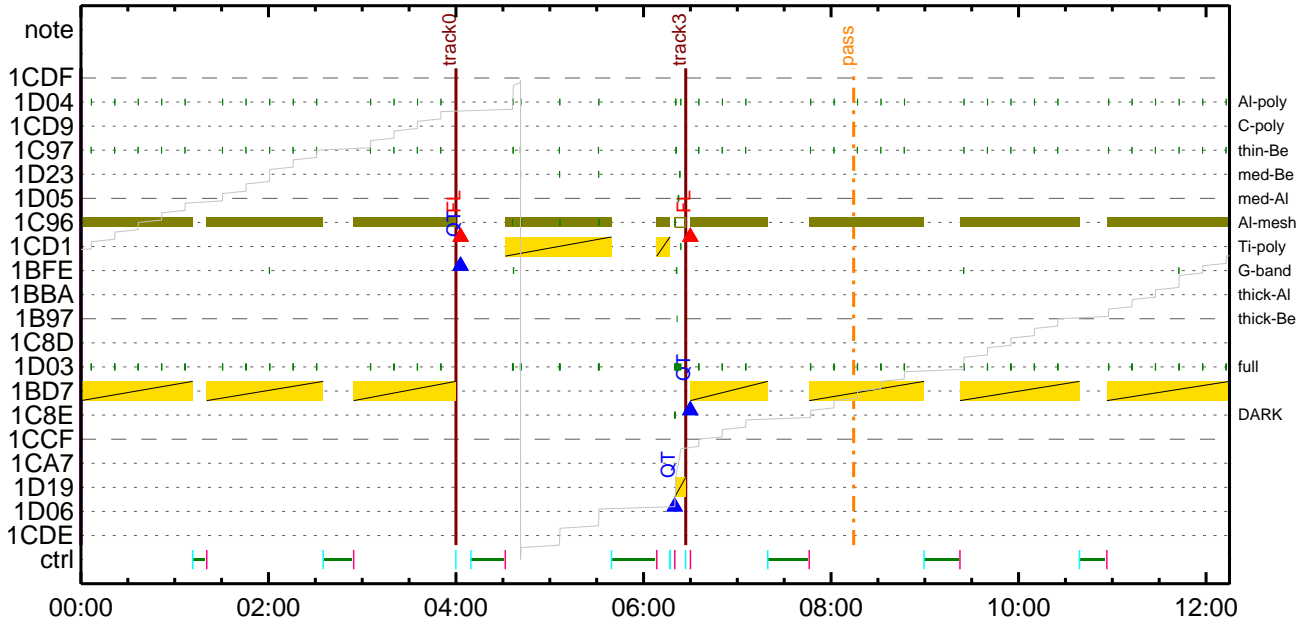
CMDI #0310 2025/01/02



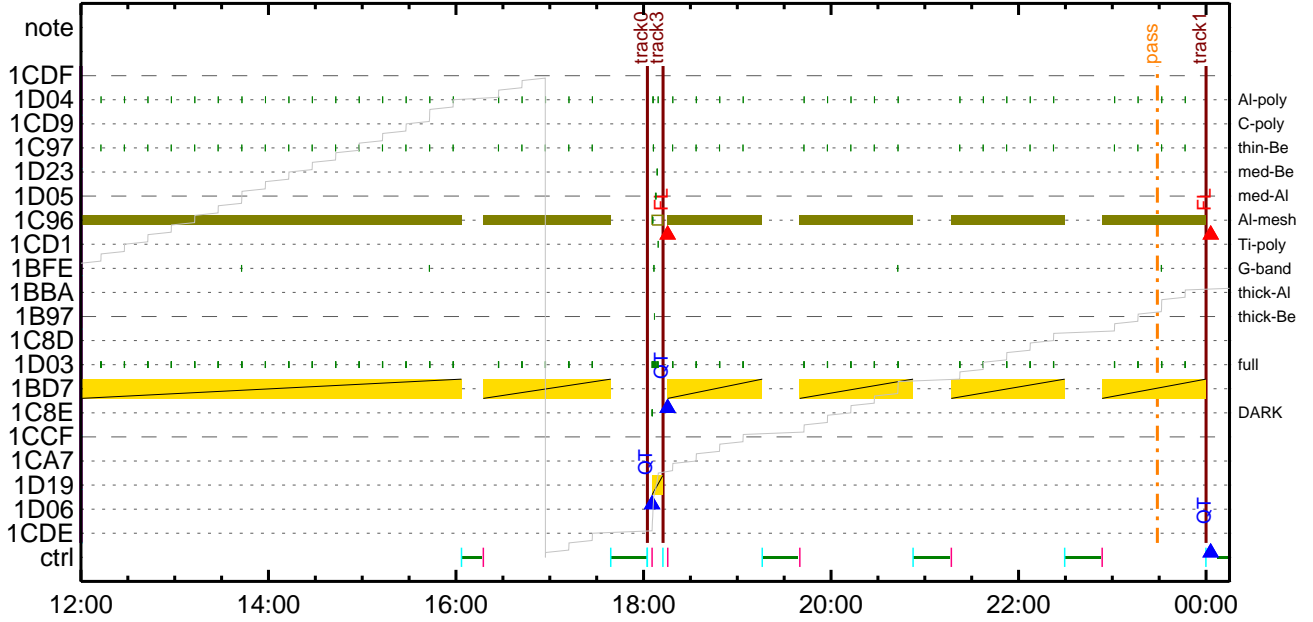
CMDI #0310 2025/01/02



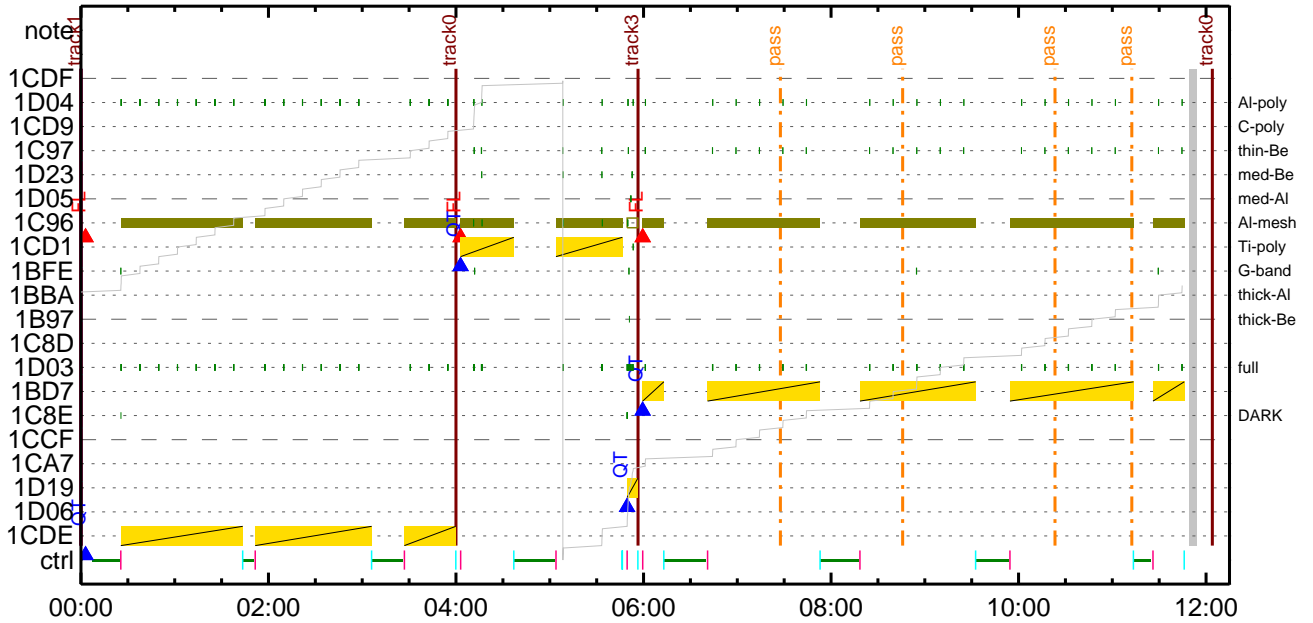
CMDI #0310 2025/01/03



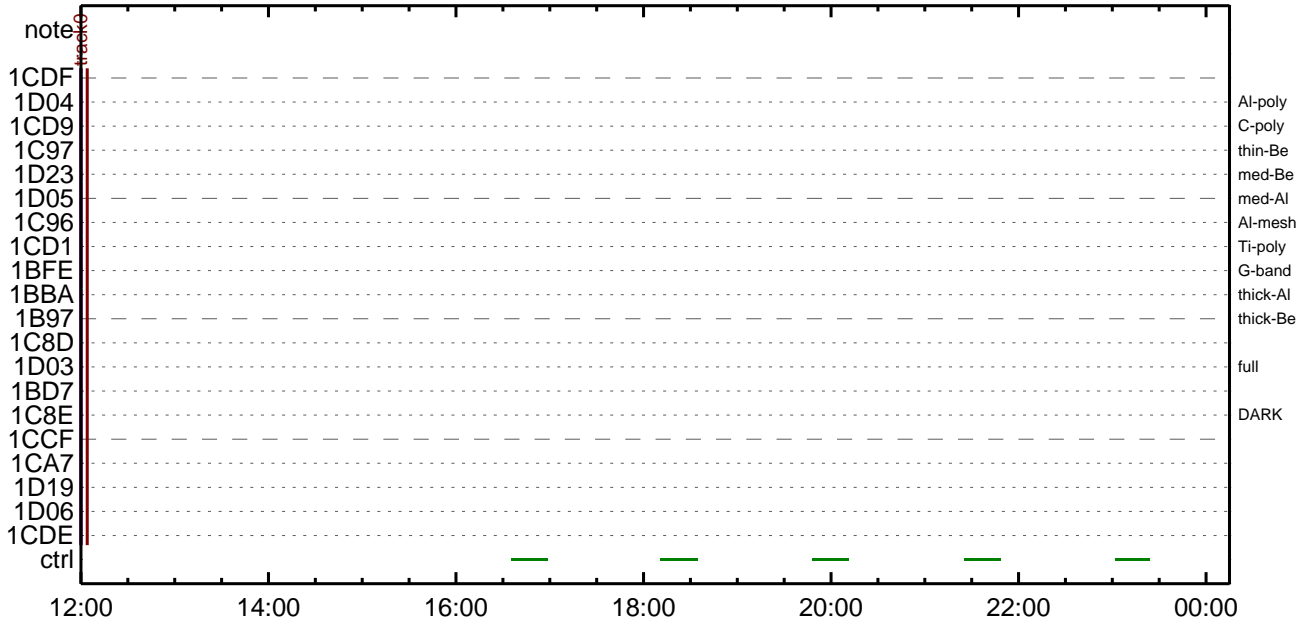
CMDI #0310 2025/01/03



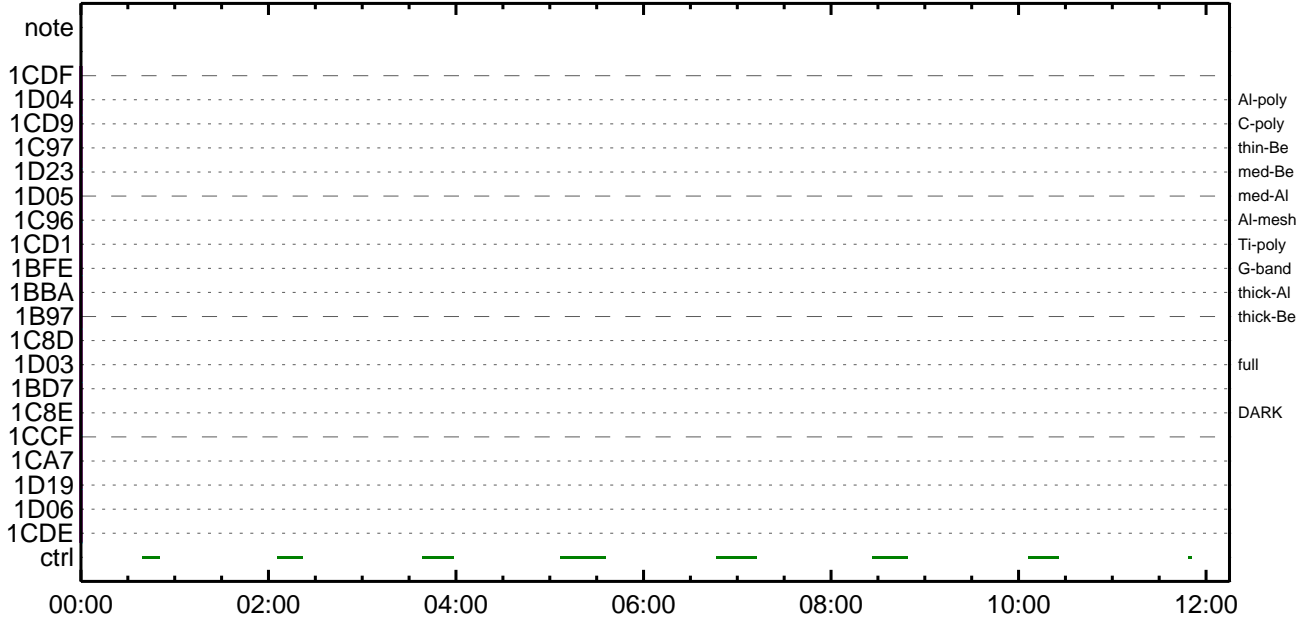
CMDI #0310 2025/01/04



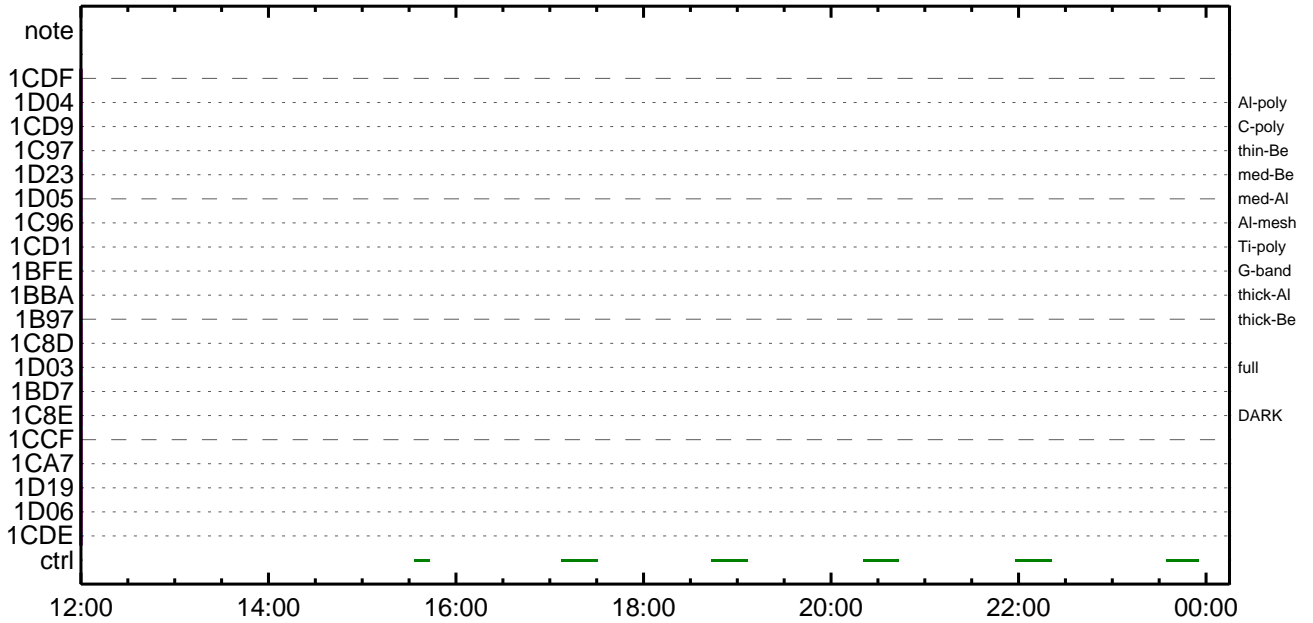
CMDI #0310 2025/01/04



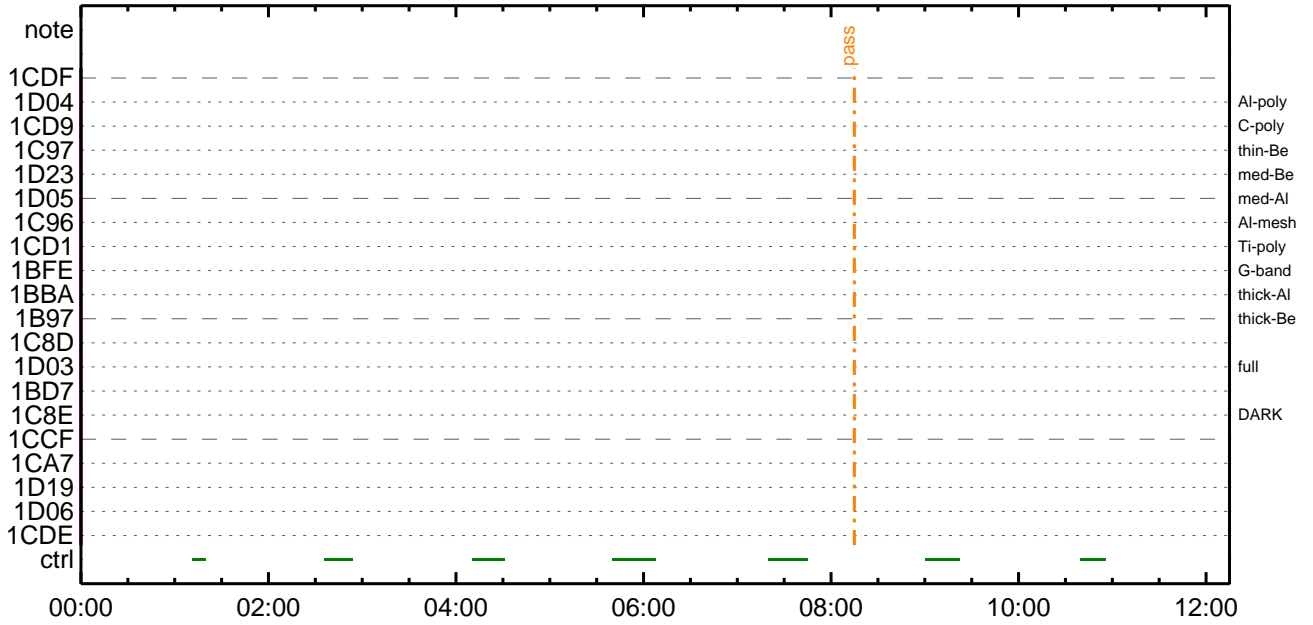
CMDI #0310 2025/01/05



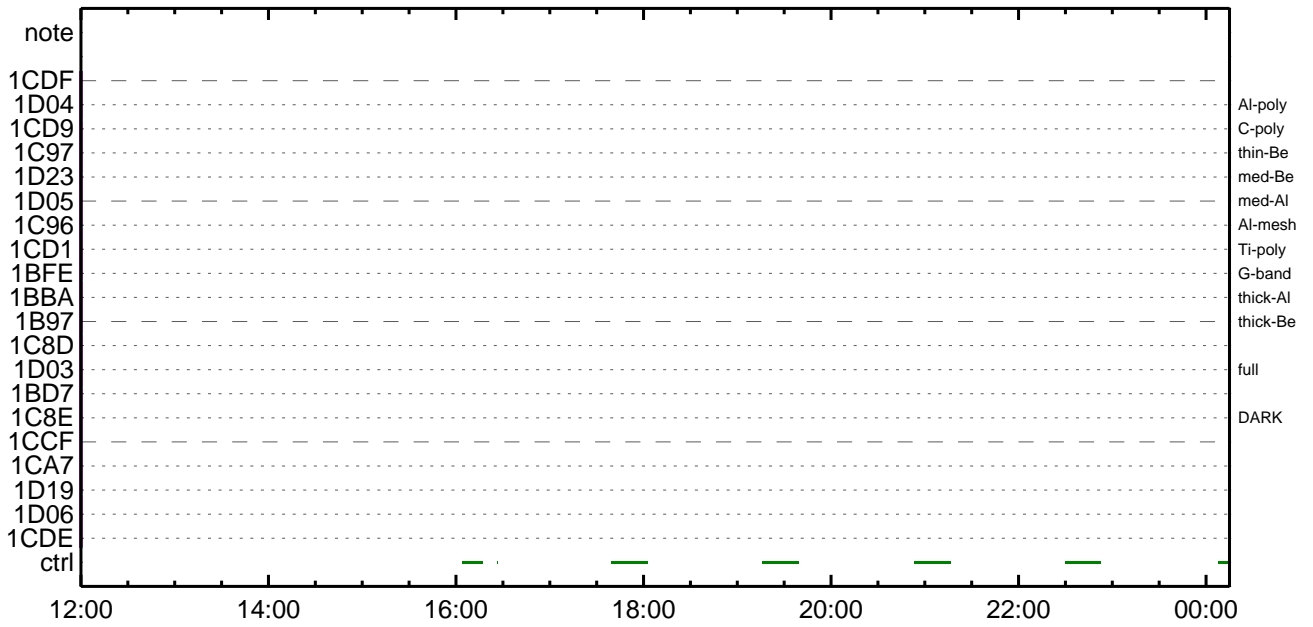
CMDI #0310 2025/01/05



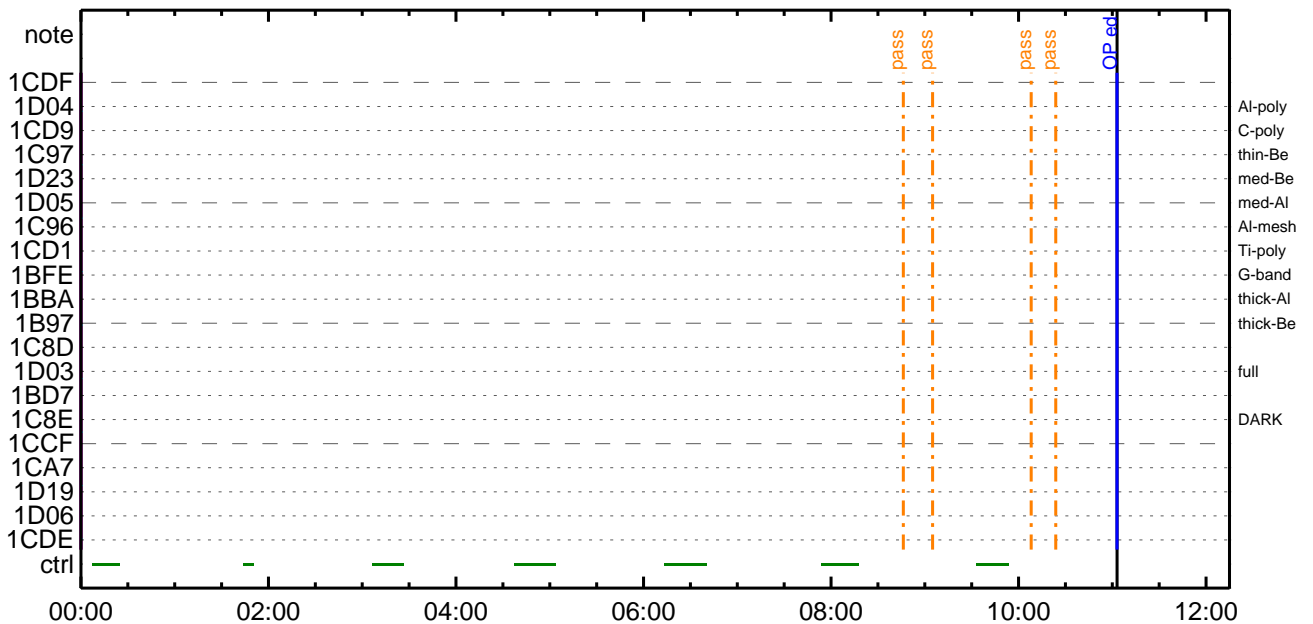
CMDI #0310 2025/01/06

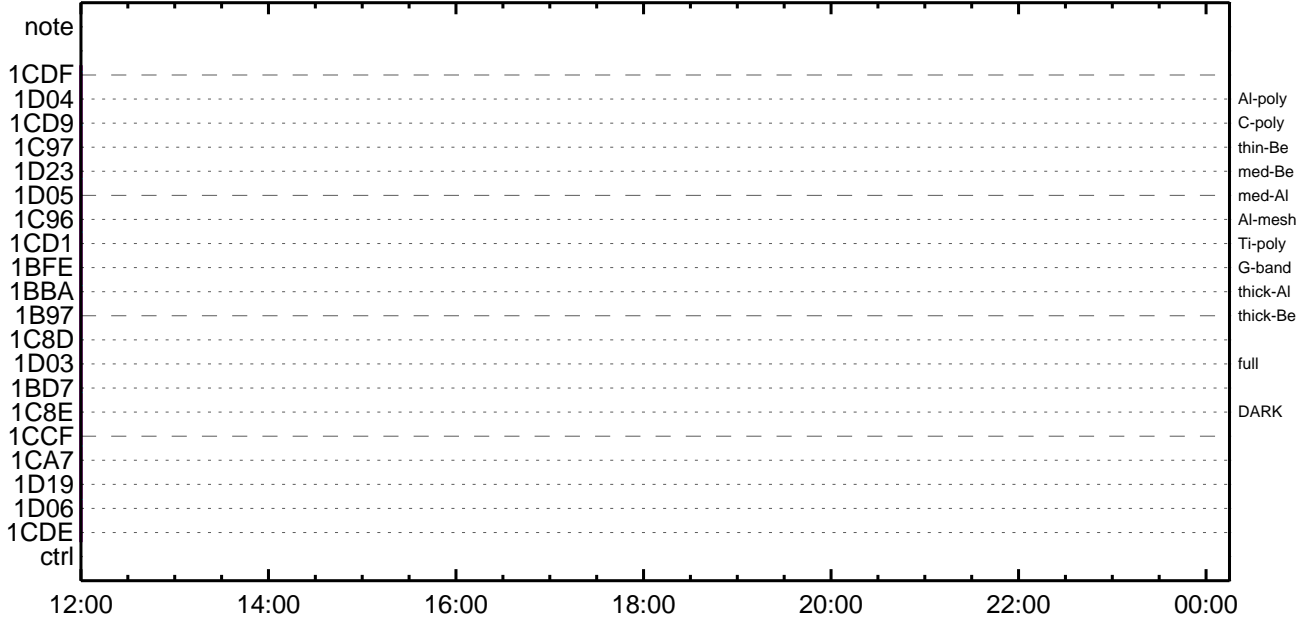


CMDI #0310 2025/01/06



CMDI #0310 2025/01/07






```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGYí;¼YÉ;¼YÁYóYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGYí;¼YÉ;ä
0103 S. OP op-020:OP
0104 ( )
0105 S. OG og-020:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPÍî°eYÁYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÁYóYx¼aî»«ð³ÎÇS
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG«î¼E¹ç•ë²ÏOK«ð³ÎÇS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÁYóYx¼aî»«ð³ÎÇS
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG«î¼E¹ç•ë²ÏOK«ð³ÎÇS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÁYóYx¼aî»«ð³ÎÇS
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP«î¼E¹ç•ë²ÏOK«ð³ÎÇS
0165 C.
0166 C. ***** °Ê²¼«î¼Ä'¶Á°«ÊÉ¬«°Á÷¿® (¾Åµ-YÁYóYx¼e½ç«ðÁÓÆ«Ç¼a¬"«ë¼i¹ç«Ç«â) *****
0167 C. DHUYâ;¼YÉ;Ê¼Y½;Yi;¼YÉ;Ë«ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;S OPOG UPLOAD«¬Á÷¿®NG«î¼i¹ç;ç°Ê²¼«îTI-CMDÁ÷¿®«î¼Ä¹Ô«««Ê«««³««;£
0180 C. «««ç;çSET«EDUMP«î¼E¹ç•ë²ÏN¼¹ç¹Ô«««³««;£
0181 C.
0182 C. TIY³YD¼YóYÉ«ðÁDî¿(UT)
0183 +. TI 2024-12-28 11:30:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2024-12-28 11:30:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2024-12-28 11:30:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

```

0194 C.
0195 +. TI 2024-12-28 11:34:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼ñîÄë¾íññîîŷÄŷ$ŷÄŷ~¹àü
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]        EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 . C. ŷÄŷÖŷ×½ªî»ñò³îÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLñî¾È¹Ç•è²ìOKñò³îÇ$
0229 C.
0230 . C. DHUŷâ;¼ŷÉ;Ê¼ŷ½.ŷi;¼ŷÈ;Ëñòîáñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]        EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2024-12-28 11:34:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 . C. -----
0246 C.      HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C.      -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2024-12-28 11:34:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2024-12-28 11:34:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 . C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2024-12-28 11:34:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ûÃîñî»ö¾ŷñÈÄñ¹ñèDCBC•x²è *****
0276 C. (¾å°îŷÖŷÄŷÈŷĐŷÈŷÄŷÇŷÈñÈ¾¾ññ¼Ä»Ûñ¹ñè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷĐŷ¹•î Daily±çîññÈ´Øñ¹ñèDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÄŷ$ŷÄŷ~¼Ä»Û;ä
0287 C.
0288 . C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-021 2024-12-28 12:16:48 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÄŸ~¼Ä»Û;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èñ¿ñÄñ•µ°È»Í×ÁÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ÈÈè¼µ•ííÈ;ÈñÈ¼°ÇÖñ•ñ¿¼í¹ÇñÍ;çÄ®, ùñ¹ñèñDñÇÄ÷¿®ñ•ñÈñññ³ñÈ;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ()
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 . C. Verify EIS in MANUAL mode
0052 C. Estimated OBSTBL upload time is 47s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ()
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2024-12-28 11:34:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP ´ûÄÎñÍ»ó¼ŸñÈÄDñ¹ñèDCBC•×²è *****
0072 C. (¾å°íŸÓŸÄŸÈŸDŸŸÈŸáŸçŸèñÈ¼¼ññ¼Ä»Ûñ¹ñè)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** ŸDŸ¹!•Ï Daily±¿ÍññÈ´Øñ¹ñèDCBC•×²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSŸÁŸSŸÄŸ~¼Ä»Û;ä
0083 C.
0084 . C. ***** LOS *****
0085 C.
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG ____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b c0 c0 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c 40 c0 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 40 40 10 10)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e c0 40 10 10)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_AEC_RESET
0155 BC (d0)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 07)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 0e)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2024-12-28 11:34:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 . C. *****
0176 C. SOT table upload
0177 C. *****
0178 . C. < Stop SP table >
0179 +. DC 07-F0 MDP_SP_CTRL_MANU
0180 BC (61)
0181 C. -----
0182 C. MDP_SP_CTRL_MODE = MANU [ ]
0183 C. -----
0184 C.
0185 . C. <Upload SP Observation Table>
0186 . S. RAM ram-289:MDP_OBS_S
0187 ()
0188 C.
0189 . C. < Dump RAMID=MDP_OBS_S >
0190 +. DC 07-F0 MDP_DUMP_SPTBL
0191 BC (83 07 00 00 00 38 b8)
0192 C. -----
0193 C. MDP_OBS_S verify = OK/NG [ ]

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0194 C. -----
0195 C.
0196 C. *****
0197 C. SOT TI command set
0198 C. *****
0199 C. Execute, after the success of TBL upload.
0200 +. TI 2024-12-28 11:34:18.0
0201 DC 07-F0 MDP_SOT_MODE_OBSV
0202 BC (40)
0203 . C. -----
0204 C. HK1_TI_CMD_NUM = 1 CNTUP []
0205 C. -----
0206 C.
0207 C.
0208 . C. ***** MDP 'úÁîñÎ»ò¼ÝñÊÂÐñ¹ñëDCBC•x²è *****
0209 C. (¼á°î¿ÓŸÁŸÊŸÐŸÊŸÁŸÇŸÈñÊ¼ñ¼Á»Ûñ¹ñë)
0210 . S. DC-BC dcbc-402:DCBC
0211 (MDP_known_event)
0212 C.
0213 C.
0214 . C. ***** ŸÐŸ¹•Ï Daily±çÍÑñÈ´Øñ¹ñëDCBC•x²è *****
0215 . S. DC-BC dcbc-153:DCBC
0216 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0217 C.
0218 C.
0219 . C. ;ãLOSŸÁŸSŸÁŸ¬¼Á»Û;ä
0220 C.
0221 . C. ***** LOS *****
0222 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-024 2024-12-28 12:16:48 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÃŸ^-¼Ã»Û;ä
0005 C.
0006 C. ŸÀŸß;¼Ÿ³ŸDŸóŸÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. ̂í;Ēñ¿ñĀñ•μ°Ē»Í×ĀÇñĪŸçŸĀŸ×Ÿí;¼ŸÉ;ĒĒè½μ•īĪĒ;ĒñĒ¼°ÇŌñ•ñ¿¼ī¹ÇñĪ;çĀ®, ùñ¹ñĒñDñÇĀ÷¿®ñ•ñĒñññ³ñĒ;Ē
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP 'úĀĪñĪ»ó¼ŸñĒĒñ¹ñĒDCBC•×²è *****
0016 C. (¾ā°īŸŌŸĀŸĒŸDŸĒŸĀŸçŸĒñĒĒ¾ññ¼Ā»Ûñ¹ñĒ)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** ŸDŸ¹•Ī Daily÷¿ĪññĒ'Ōñ¹ñĒDCBC•×²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSŸÁŸSŸÃŸ^-¼Ã»Û;ä
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

*** OP Sequence for XRT ***

```

2024/12/28 11:41:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 11:41:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET       1 07-F0 da
2024/12/28 11:41:34.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT    1 07-F0 e8
2024/12/28 11:44:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP    1 07-F0 e9
2024/12/28 11:44:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 11:44:56.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION     4 07-F8 22 ff aa 00
2024/12/28 11:45:00.0 AOCs_Orе-point_Start_1_OG [0x097]
                        AOCU_NM                 5 02-76 04 03 ce 01 f3
2024/12/28 11:45:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA        1 07-F0 d8
2024/12/28 11:45:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA    1 07-F0 c8
2024/12/28 11:45:20.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET      1 07-F0 d0
2024/12/28 11:45:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS        1 07-F0 d5
2024/12/28 11:45:24.0 XRT_FLD_RESET_428_OG [0x1ac]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2024/12/28 11:47:56.0 XRT_QT_PROG_SET_421_OG [0x1a5]
                        MDP_XRT_QT_PROG_SET    2 07-F0 c4 07
2024/12/28 11:47:58.0 XRT_FL_PROG_SET_439_OG [0x1b7]
                        MDP_XRT_FL_PROG_SET    2 07-F0 c5 0e
2024/12/28 11:48:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2024/12/28 12:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 12:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 12:59:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION     4 07-F8 22 fe 97 00
2024/12/28 13:00:00.0 AOCs_Orе-point_Start_2_OG [0x098]
                        AOCU_NM                 5 02-76 03 03 ce 01 f3
2024/12/28 13:00:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA        1 07-F0 d8
2024/12/28 13:00:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA    1 07-F0 c8
2024/12/28 13:00:22.0 XRT_AEC_RESET_448_OG [0x1c0]
                        MDP_XRT_AEC_RESET      1 07-F0 d0
2024/12/28 13:00:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS        1 07-F0 d5
2024/12/28 13:00:26.0 XRT_FLD_RESET_434_OG [0x1b2]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2024/12/28 13:02:56.0 XRT_QT_PROG_SET_447_OG [0x1bf]
                        MDP_XRT_QT_PROG_SET    2 07-F0 c4 0c
2024/12/28 13:02:58.0 XRT_FL_PROG_SET_439_OG [0x1b7]
                        MDP_XRT_FL_PROG_SET    2 07-F0 c5 0e
2024/12/28 13:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2024/12/28 16:02:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 16:02:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET       1 07-F0 da
2024/12/28 16:02:34.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT    1 07-F0 e8
2024/12/28 16:05:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP    1 07-F0 e9
2024/12/28 16:39:00.0 XRT_Custom_430_OG [0x1ae]
2024/12/28 16:40:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2024/12/28 17:38:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 17:38:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET       1 07-F0 da
2024/12/28 17:38:04.0 XRT_PREFLR_STRT_425_OG [0x1a9]
                        MDP_XRT_PREFLR_STRT    1 07-F0 e8
2024/12/28 17:41:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP    1 07-F0 e9
2024/12/28 18:01:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 18:01:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2024/12/28 18:01:28.0 XRT_FOCUS_POSITION_406_OG [0x196]
                        XRT_FOCUS_POSITION     4 07-F8 22 ff aa 00
2024/12/28 18:01:30.0 AOCs_Orе-point_Start_3_OG [0x099]
                        AOCU_NM                 5 02-76 00 00 00 00 00
2024/12/28 18:01:48.0 XRT_FLD_DIS_409_OG [0x199]
                        MDP_XRT_FLD_DIS        1 07-F0 d9
2024/12/28 18:01:50.0 XRT_FLRCTRL_DIS_413_OG [0x19d]
                        MDP_XRT_FLRCTRL_DIS    1 07-F0 c9
2024/12/28 18:01:52.0 XRT_ARS_DIS_435_OG [0x1b3]
                        MDP_XRT_ARS_DIS        1 07-F0 d5
2024/12/28 18:04:28.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET    2 07-F0 c4 03
2024/12/28 18:04:30.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0

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2024/12/28	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/28	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/28	18:11:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2024/12/28	18:11:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 03 ce 01 f3
2024/12/28	18:11:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2024/12/28	18:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/12/28	18:11:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2024/12/28	18:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2024/12/28	18:11:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/28	18:14:26.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2024/12/28	18:14:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/12/28	18:17:00.0	XRT_Custom_430_OG [0x1ae]			
2024/12/28	18:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/28	19:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/28	19:15:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/28	19:15:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/28	19:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/28	19:55:30.0	XRT_Custom_430_OG [0x1ae]			
2024/12/28	19:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/28	20:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/28	20:51:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/28	20:51:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/28	20:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/28	21:33:30.0	XRT_Custom_430_OG [0x1ae]			
2024/12/28	21:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/28	22:28:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/28	22:28:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/28	22:28:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/28	22:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/28	23:11:30.0	XRT_Custom_430_OG [0x1ae]			
2024/12/28	23:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	00:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	00:06:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/29	00:06:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/29	00:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/29	00:24:00.0	XRT_Custom_430_OG [0x1ae]			
2024/12/29	00:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	01:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	01:42:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/29	01:42:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/29	01:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/29	01:49:30.0	XRT_Custom_430_OG [0x1ae]			
2024/12/29	01:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	03:05:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	03:05:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/29	03:05:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/29	03:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/29	03:25:00.0	XRT_Custom_430_OG [0x1ae]			
2024/12/29	03:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

2024/12/29	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/12/29	04:00:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2024/12/29	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2024/12/29	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2024/12/29	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0] MDP_XRT_AEC_RESET	1	07-F0	d0
2024/12/29	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/12/29	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac] MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/29	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2024/12/29	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2024/12/29	04:06:00.0	XRT_Custom_430_OG [0x1ae]			
2024/12/29	04:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	04:36:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	04:36:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/29	04:36:04.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/29	04:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/29	05:02:00.0	XRT_Custom_430_OG [0x1ae]			
2024/12/29	05:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	05:42:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	05:42:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2024/12/29	05:42:34.0	XRT_PREFLR_STRT_425_OG [0x1a9] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2024/12/29	05:45:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	05:45:26.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	05:45:28.0	XRT_FOCUS_POSITION_406_OG [0x196] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2024/12/29	05:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2024/12/29	05:45:48.0	XRT_FLD_DIS_409_OG [0x199] MDP_XRT_FLD_DIS	1	07-F0	d9
2024/12/29	05:45:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2024/12/29	05:45:52.0	XRT_ARS_DIS_435_OG [0x1b3] MDP_XRT_ARS_DIS	1	07-F0	d5
2024/12/29	05:48:28.0	XRT_QT_PROG_SET_436_OG [0x1b4] MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2024/12/29	05:48:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2024/12/29	05:55:00.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2024/12/29	05:55:30.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03 03 ce 01 f3
2024/12/29	06:00:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_403_OG [0x193] TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2024/12/30	06:00:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2024/12/30	06:10:00.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03 03 ce 01 f3
2024/12/31	06:16:30.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2024/12/31	06:26:30.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03 03 ce 01 f3
2025/01/01	05:46:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2025/01/01	05:56:00.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	03 03 ce 01 f3
2025/01/01	06:00:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	06:00:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_445_OG [0x1bd] TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2025/01/01	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	11:59:56.0	XRT_FOCUS_POSITION_446_OG [0x1be] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/01/01	12:00:00.0	AOCs_OrE-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00 2e f9 2e f9
2025/01/01	12:02:52.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5
2025/01/01	12:02:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/01/01	12:02:56.0	XRT_FLD_DIS_404_OG [0x194] MDP_XRT_FLD_DIS	1	07-F0	d9
2025/01/01	12:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/01/01	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			

2025/01/01	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/01	12:09:56.0	XRT_FOCUS_POSITION_446_OG [0x1be]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/01/01	12:10:00.0	AOCS_Orе-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	2e	f9	d1	07
2025/01/01	12:12:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/01	12:12:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/01/01	12:12:56.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/01/01	12:12:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2025/01/01	12:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/01	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/01	12:19:56.0	XRT_FOCUS_POSITION_446_OG [0x1be]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/01/01	12:20:00.0	AOCS_Orе-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	d1	07	d1	07
2025/01/01	12:22:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/01	12:22:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/01/01	12:22:56.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/01/01	12:22:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2025/01/01	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/01	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/01	12:29:56.0	XRT_FOCUS_POSITION_446_OG [0x1be]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/01/01	12:30:00.0	AOCS_Orе-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	d1	07	2e	f9
2025/01/01	12:32:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/01	12:32:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/01/01	12:32:56.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/01/01	12:32:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2025/01/01	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/01	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/01	12:39:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/01/01	12:40:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00	00
2025/01/01	12:40:16.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/01/01	12:40:18.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/01/01	12:40:20.0	XRT_ARS_DIS_433_OG [0x1b1]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/01	12:42:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2025/01/01	12:43:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/01	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/01	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/01	12:49:58.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]	XRT_FOCUS_RECAL	2	07-F8	78	00			
2025/01/01	12:50:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	03	ce	01	f3
2025/01/01	12:53:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/01/01	12:54:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/01/01	12:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/01/01	12:54:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/01/01	12:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/01	12:54:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/01	12:56:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2025/01/01	12:56:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/01/01	12:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/01	16:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				

2025/01/01	16:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/01	16:34:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/01	16:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/01	16:58:00.0	XRT_Custom_430_OG [0x1ae]				
2025/01/01	16:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/01	17:46:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	17:46:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	17:46:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/01/01	17:46:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2025/01/01	17:46:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/01/01	17:46:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/01/01	17:46:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/01/01	17:49:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/01/01	17:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/01	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	17:56:26.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/01/01	17:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 ce 01 f3
2025/01/01	17:56:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/01/01	17:56:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/01/01	17:56:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/01/01	17:56:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/01/01	17:56:54.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/01	17:59:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2025/01/01	17:59:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/01/01	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/01	18:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	18:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/01	18:10:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/01	18:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/01	18:34:30.0	XRT_Custom_430_OG [0x1ae]				
2025/01/01	18:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/01	19:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	19:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/01	19:47:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/01	19:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/01	20:11:00.0	XRT_Custom_430_OG [0x1ae]				
2025/01/01	20:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/01	21:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	21:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/01	21:24:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/01	21:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/01	21:48:00.0	XRT_Custom_430_OG [0x1ae]				
2025/01/01	21:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/01	23:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/01	23:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/01	23:01:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/01	23:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/01	23:23:30.0	XRT_Custom_430_OG [0x1ae]				
2025/01/01	23:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

2025/01/02	00:39:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	00:39:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2025/01/02	00:39:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/01/02	00:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/01/02	00:50:00.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	00:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/01/02	02:05:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	02:05:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2025/01/02	02:05:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/01/02	02:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/01/02	02:21:30.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	02:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/01/02	03:37:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	03:37:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2025/01/02	03:37:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/01/02	03:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/01/02	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2025/01/02	04:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2025/01/02	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/01/02	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/01/02	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/01/02	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/01/02	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2025/01/02	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2025/01/02	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2025/01/02	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/01/02	05:06:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	05:06:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2025/01/02	05:06:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/01/02	05:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/01/02	05:35:00.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	05:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/01/02	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2025/01/02	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2025/01/02	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2025/01/02	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/01/02	06:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2025/01/02	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/01/02	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/01/02	06:09:56.0	XRT_FOCUS_POSITION_406_OG [0x196]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2025/01/02	06:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	03 03 ce 01 f3			
2025/01/02	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/01/02	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/01/02	06:10:20.0	XRT_AEC_RESET_448_OG [0x1c0]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/01/02	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			

2025/01/02	06:10:24.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/01/02	06:12:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2025/01/02	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e	
2025/01/02	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	06:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	06:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/01/02	06:46:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/01/02	06:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/01/02	07:12:30.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	07:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	08:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	08:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/01/02	08:26:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/01/02	08:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/01/02	08:49:00.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	08:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	10:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	10:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/01/02	10:05:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/01/02	10:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/01/02	10:25:00.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	10:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	15:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/01/02	15:32:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/01/02	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/01/02	15:42:30.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	15:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	17:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/01/02	17:06:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/01/02	17:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/01/02	17:30:30.0	XRT_Custom_430_OG [0x1ae]						
2025/01/02	17:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	18:00:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2025/01/02	18:00:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2025/01/02	18:00:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2025/01/02	18:00:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2025/01/02	18:00:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/01/02	18:03:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2025/01/02	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/01/02	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/01/02	18:10:26.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2025/01/02	18:10:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 ce 01 f3		
2025/01/02	18:10:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/01/02	18:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/01/02	18:10:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		

2025/01/02	18:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/02	18:10:54.0	XRT_FLD_RESET_428_OG [0x1ac]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/02	18:13:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2025/01/02	18:13:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/01/02	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/02	18:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/02	18:43:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/02	18:43:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/02	18:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/02	19:06:30.0	XRT_Custom_430_OG [0x1ae]							
2025/01/02	19:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/02	20:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/02	20:20:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/02	20:20:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/02	20:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/02	20:43:00.0	XRT_Custom_430_OG [0x1ae]							
2025/01/02	20:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/02	21:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/02	21:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/02	21:57:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/02	22:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/02	22:20:30.0	XRT_Custom_430_OG [0x1ae]							
2025/01/02	22:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/02	23:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/02	23:34:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/02	23:34:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/02	23:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/02	23:55:00.0	XRT_Custom_430_OG [0x1ae]							
2025/01/02	23:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/03	01:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/03	01:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/03	01:11:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/03	01:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/03	01:19:30.0	XRT_Custom_430_OG [0x1ae]							
2025/01/03	01:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/03	02:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/03	02:35:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/03	02:35:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/03	02:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/03	02:53:30.0	XRT_Custom_430_OG [0x1ae]							
2025/01/03	02:54:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/03	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/03	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/01/03	04:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/01/03	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/01/03	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/01/03	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/01/03	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/03	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/03	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							

2025/01/03	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/01/03	04:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	04:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/03	04:09:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/03	04:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/03	04:30:30.0	XRT_Custom_430_OG [0x1ae]					
2025/01/03	04:31:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/03	05:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	05:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/03	05:39:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/03	05:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/03	06:07:30.5	XRT_Custom_430_OG [0x1ae]					
2025/01/03	06:08:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/03	06:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	06:16:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	06:16:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2025/01/03	06:17:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/01/03	06:17:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/01/03	06:17:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/01/03	06:19:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2025/01/03	06:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/03	06:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	06:26:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2025/01/03	06:27:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 ce 01	f3
2025/01/03	06:27:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/01/03	06:27:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/01/03	06:27:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/01/03	06:27:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/01/03	06:27:24.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/03	06:29:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2025/01/03	06:29:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/01/03	06:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/03	07:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	07:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/03	07:19:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/03	07:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/03	07:45:00.0	XRT_Custom_430_OG [0x1ae]					
2025/01/03	07:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/03	08:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	08:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/03	08:59:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/03	09:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/03	09:21:30.0	XRT_Custom_430_OG [0x1ae]					
2025/01/03	09:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/03	10:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/03	10:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/03	10:39:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/03	10:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

2025/01/03	10:55:30.0	XRT_Custom_430_OG [0x1ae]			
2025/01/03	10:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/03	16:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	16:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/03	16:03:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/03	16:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/03	16:16:30.0	XRT_Custom_430_OG [0x1ae]			
2025/01/03	16:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/03	17:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	17:39:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/03	17:39:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/03	17:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/03	18:02:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	18:02:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	18:02:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/01/03	18:02:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/01/03	18:02:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/01/03	18:02:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/01/03	18:02:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/01/03	18:05:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/01/03	18:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/03	18:12:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	18:12:26.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/01/03	18:12:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 03 ce 01 f3
2025/01/03	18:12:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/01/03	18:12:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/01/03	18:12:50.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/01/03	18:12:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/01/03	18:12:54.0	XRT_FLD_RESET_428_OG [0x1ac]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/03	18:15:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2025/01/03	18:15:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/01/03	18:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/03	19:16:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	19:16:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/03	19:16:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/03	19:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/03	19:39:00.0	XRT_Custom_430_OG [0x1ae]			
2025/01/03	19:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/03	20:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	20:52:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/03	20:52:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/03	20:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/03	21:16:00.0	XRT_Custom_430_OG [0x1ae]			
2025/01/03	21:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/01/03	22:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/01/03	22:29:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/01/03	22:29:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/01/03	22:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/01/03	22:52:30.0	XRT_Custom_430_OG [0x1ae]			

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2025/01/03	22:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/03	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/03	23:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/01/04	00:00:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	01 03 ce 01 f3				
2025/01/04	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/01/04	00:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/01/04	00:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/01/04	00:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/04	00:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/04	00:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2025/01/04	00:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/01/04	00:24:30.0	XRT_Custom_430_OG [0x1ae]							
2025/01/04	00:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/04	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/04	01:43:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/04	01:43:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/04	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/04	01:50:30.0	XRT_Custom_430_OG [0x1ae]							
2025/01/04	01:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/04	03:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/04	03:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/04	03:06:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/04	03:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/04	03:26:00.0	XRT_Custom_430_OG [0x1ae]							
2025/01/04	03:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/04	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/04	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/01/04	04:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/01/04	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/01/04	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/01/04	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/01/04	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/04	04:00:24.0	XRT_FLD_RESET_428_OG [0x1ac]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/04	04:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2025/01/04	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/01/04	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/04	04:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/04	04:37:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/01/04	04:37:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/01/04	04:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/01/04	05:03:00.0	XRT_Custom_430_OG [0x1ae]							
2025/01/04	05:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/01/04	05:46:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/04	05:46:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/01/04	05:46:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/01/04	05:46:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/01/04	05:46:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/01/04	05:46:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/01/04	05:49:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							

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2025/01/04	05:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_OT_PROG_SET	2	07-F0	c4	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/04	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/04	05:56:26.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2025/01/04	05:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 ce 01 f3	
2025/01/04	05:56:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/01/04	05:56:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/01/04	05:56:50.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/01/04	05:56:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/01/04	05:56:54.0	XRT_FLD_RESET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/04	05:59:26.0	XRT_OT_PROG_SET_421_OG [0x1a5]	MDP_XRT_OT_PROG_SET	2	07-F0	c4	07
2025/01/04	05:59:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/01/04	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/04	06:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/04	06:13:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/04	06:13:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/04	06:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/04	06:40:00.0	XRT_Custom_430_OG [0x1ae]					
2025/01/04	06:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/04	07:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/04	07:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/04	07:53:04.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/04	07:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/04	08:17:30.0	XRT_Custom_430_OG [0x1ae]					
2025/01/04	08:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/04	09:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/04	09:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/04	09:32:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/04	09:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/04	09:53:30.0	XRT_Custom_430_OG [0x1ae]					
2025/01/04	09:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/04	11:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/04	11:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/01/04	11:13:34.0	XRT_PREFLR_STRT_425_OG [0x1a9]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/01/04	11:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/01/04	11:25:00.0	XRT_Custom_430_OG [0x1ae]					
2025/01/04	11:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/01/04	11:46:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/01/04	12:04:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	