

Period: 2025/02/27 11:39:00 - 2025/03/04 11:06:00

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XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/32768)																
Term		Pointing (x, y)					Comment									
02/27 17:52:30 - 02/27 17:59:24		Fixed (0.0, 0.0)					synoptic, shifted -10.5 min									
02/28 06:03:32 - 02/28 06:09:54		Fixed (0.0, 0.0)					HOP349 + synoptic									
02/28 18:06:00 - 02/28 18:12:54		Fixed (0.0, 0.0)					synoptic, shifted 3.0 min									
03/01 06:22:32 - 03/01 06:29:24		Fixed (0.0, 0.0)					HOP349 + synoptic, shifted 19.5 min									
PROG= 03 1-time(s)																
Subr= 1 1-time(s) 2.0sec																
Seqn= 5 1-time(s) 2.0sec																
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec																
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec																
Seqn= 55 1-time(s) 2.0sec																
└ Open/Al-mesh Open/Al-mesh close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Seqn= 98 1-time(s) 2.0sec																
└ Al-poly/Open Al-poly/Open close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Seqn= 76 1-time(s) 2.0sec																
└ thin-Be/Open thin-Be/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Seqn= 23 1-time(s) 4.0sec																
└ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec																
└ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Subr= 2 1-time(s) 2.0sec																
Seqn= 41 1-time(s) 2.0sec																
└ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
Seqn= 18 1-time(s) 2.0sec																
└ med-Al/Open med-Al/thick-Al close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ med-Al/Open med-Al/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Seqn= 86 1-time(s) 2.0sec																
└ med-Be/Open Open/thick-Al close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ med-Be/Open med-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
Seqn= 54 1-time(s) 2.0sec																
└ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└ Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval																

XOB #1D1F: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with			
Term	Pointing (x, y)		Comment
02/27 18:02:30 - 02/28 00:42:00	Track (896.3, -235.0) @ 02/27 17:59:30	AR13998	

02/28 00:58:38 - 02/28 03:40:00 Track (896.3, -235.0) @ 02/27 17:59:30 AR13998
02/28 06:13:00 - 02/28 18:02:54 Track (915.2, -248.5) @ 02/28 06:10:00 AR13998
02/28 18:16:00 - 02/28 23:37:00 Track (-15.0, 385.1) @ 02/28 18:13:00 AR14006
03/01 06:32:30 - 03/01 10:42:00 Track (92.5, 384.3) @ 03/01 06:29:30 AR14006

PROG= 11 Inf.-time(s)												
Subr= 1		1-time(s)	2.0sec									
Seqn= 92		1-time(s)	2.0sec									
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0 2.0sec
Subr= 2		4-time(s)	2.0sec									
Seqn= 47		1-time(s)	2.0sec									
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0 2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0 2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
Seqn= 77		4-time(s)	600.0sec									
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 95.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 95.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec
Default Filter				Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp. AEC Buffer Interval

XOB #1BBE: CME watch - 4x4 - AEC 1/2/3 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 30s cad (G-band/Leak first)

Term		Pointing (x, y)							Comment						
02/28 00:53:30 - 02/28 00:58:00		Track (896.3, -235.0) @ 02/27 17:59:30							AR13998						
PROG= 06 Inf.-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 30		1-time(s)		2.0sec											
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec	
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2		20-time(s)		30.0sec											
Seqn= 64		1-time(s)		2.0sec											
		Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	1	0	2.0sec	
		Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
		Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

XOB #1D24: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 120 mi

Term			Pointing (x, y)					Comment							
02/28 04:03:00 - 02/28 06:03:00			Fixed (0.0, 0.0)					HOP349 + synoptic							
PROG= 02 Inf.-time(s)															
Subr= 1		1-time(s)		300.0sec											
Seqn=100		1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 84		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 57		1-time(s)		2.0sec											
thin-Be/Open		thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 81		1-time(s)		2.0sec											
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=90	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=95	0	0	2.0sec	
Subr= 2		80-time(s)		90.0sec											
Seqn= 91		1-time(s)		2.0sec											
thin-Be/Open		med-Be/Open	close	Safe	Norm	16ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec	
Seqn= 25		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/Open	close	Safe	Norm	4ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec	
Seqn= 43		1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec	
Default Filter				Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7

Term			Pointing (x, y)										Comment			
03/01 00:03:00 - 03/01 03:59:54			Track (23.2, 115.4) @ 03/01 00:00:00										HOP393			
PROG= 01 Inf.-time(s)																
Subr= 1		1-time(s)		2.0sec												
Seqn= 30		1-time(s)		2.0sec												
└		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec		
└		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec		

XOB #1C9B: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+L)															
Term			Pointing (x, y)					Comment							
03/01 04:03:00 - 03/01 06:22:00			Fixed (0.0, 0.0)					HOP349 + synoptic, shifted 19.5 min							
PROG= 18 Inf.-time(s)															
Subr= 1		1-time(s)		300.0sec											
Seqn=100		1-time(s)		2.0sec											
└─		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 84		1-time(s)		2.0sec											
└─		Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─		Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 57		1-time(s)		2.0sec											
└─		thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─		thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─		thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 81		1-time(s)		2.0sec											
└─		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=90	0	0	2.0sec
└─		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=95	0	0	2.0sec
Subr= 2		9-time(s)		360.0sec											
Seqn= 8		1-time(s)		2.0sec											
└─		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└─		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec											
└─		Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└─		Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 29		1-time(s)		2.0sec											
└─		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└─		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC	Buffer	Interval

NOT USED

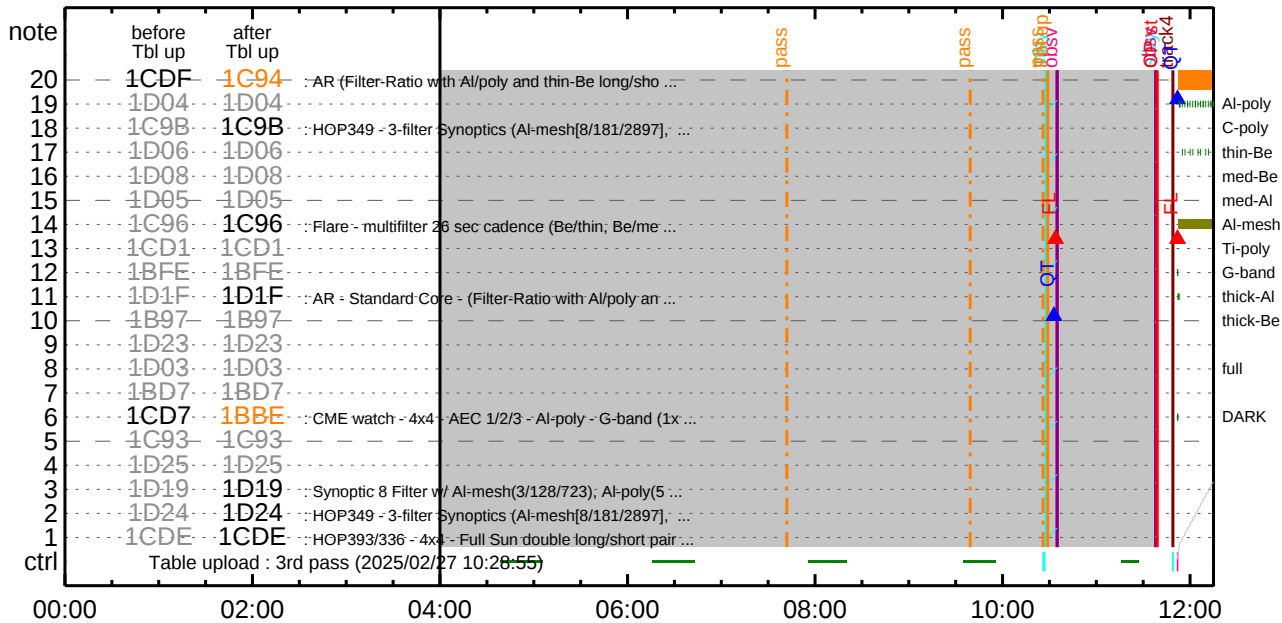
* * * * *

Flare Detection

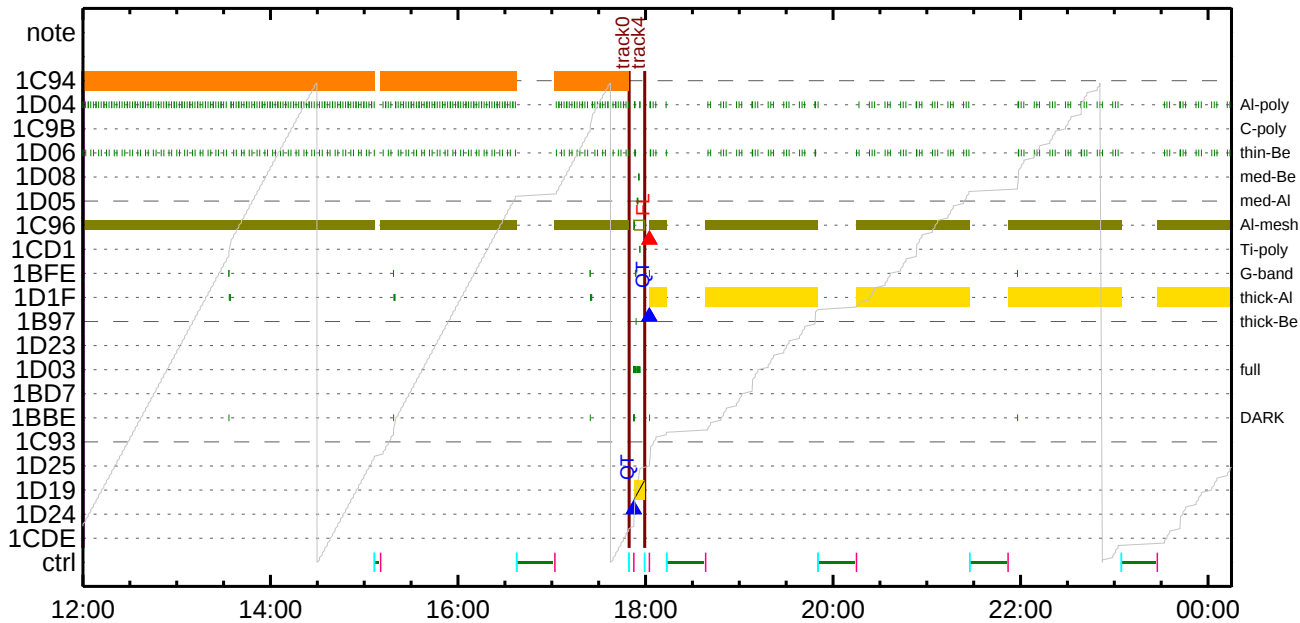
* * * * *

FLD Patrol												
Term			Pointing (x, y)						Comment			
02/27 10:29:55 - 02/27 17:49:48			cannot be identified									
02/27 17:59:48 - 02/28 06:03:24			Track (	896.3,	-235.0)	@ 02/27 17:59:30	AR13998					
02/28 06:10:18 - 02/28 18:03:18			Track (	915.2,	-248.5)	@ 02/28 06:10:00	AR13998					
02/28 18:13:18 - 03/01 06:22:24			Track (	-15.0,	385.1)	@ 02/28 18:13:00	AR14006					
03/01 06:29:48 - 03/04 11:06:00			Track (	92.5,	384.3)	@ 03/01 06:29:30	AR14006					
Al-poly/Open		Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

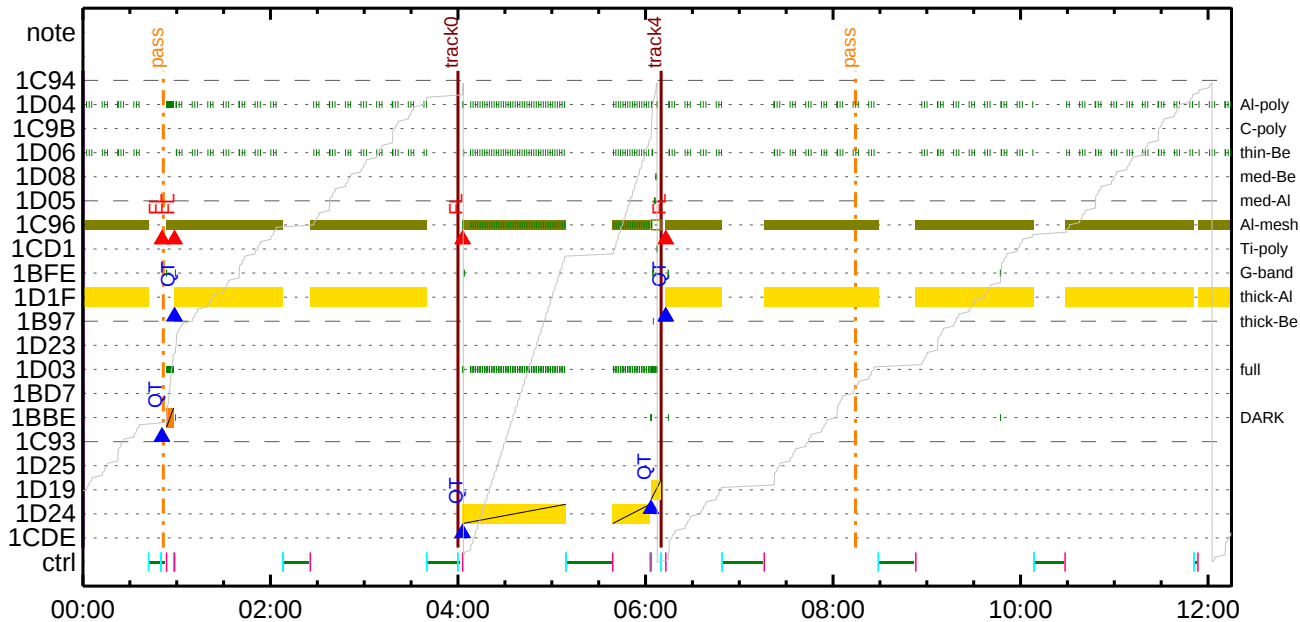
CMDI #0412 2025/02/27



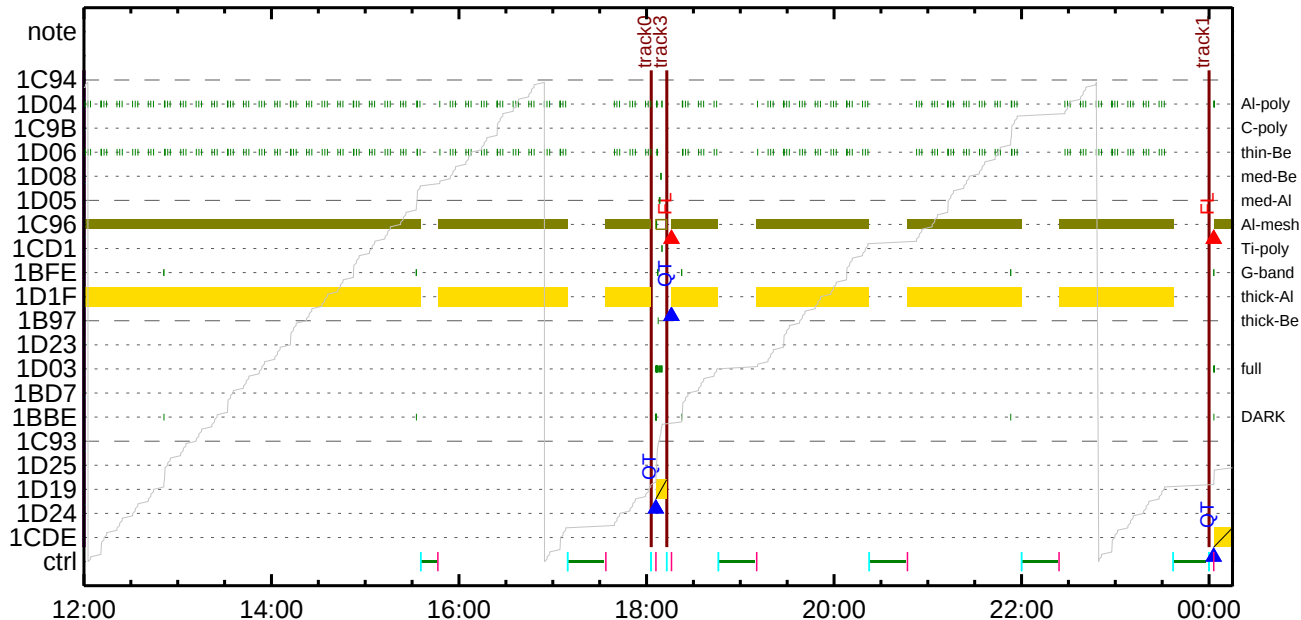
CMDI #0412 2025/02/27



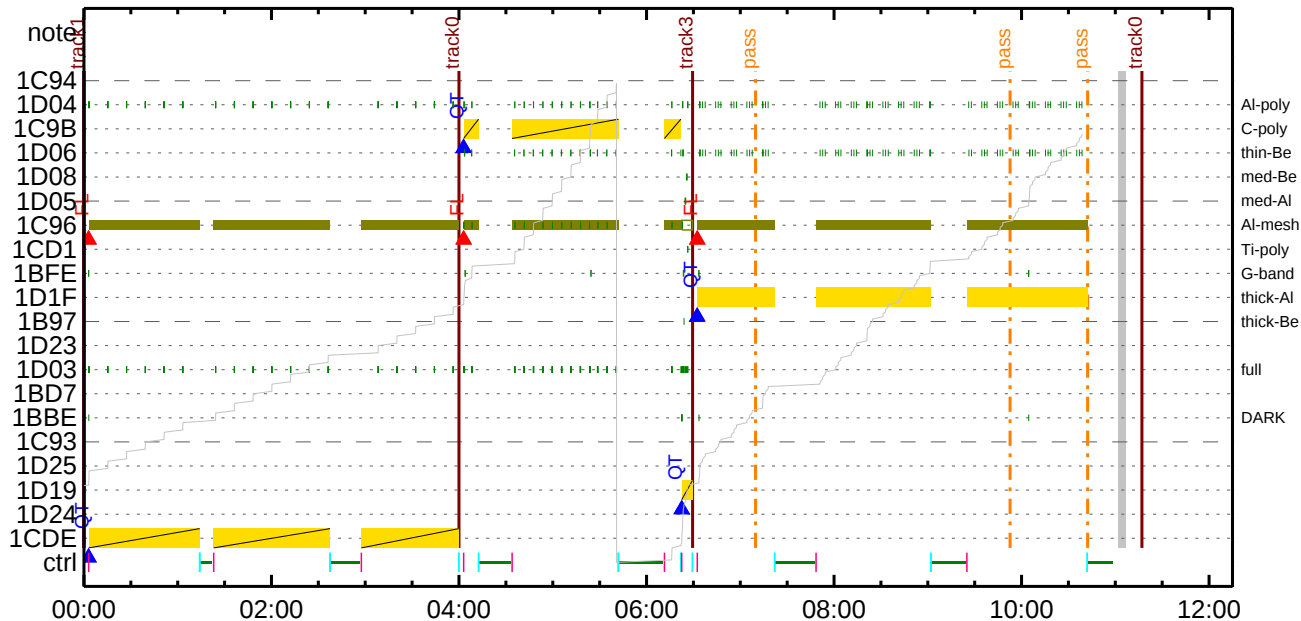
CMDI #0412 2025/02/28



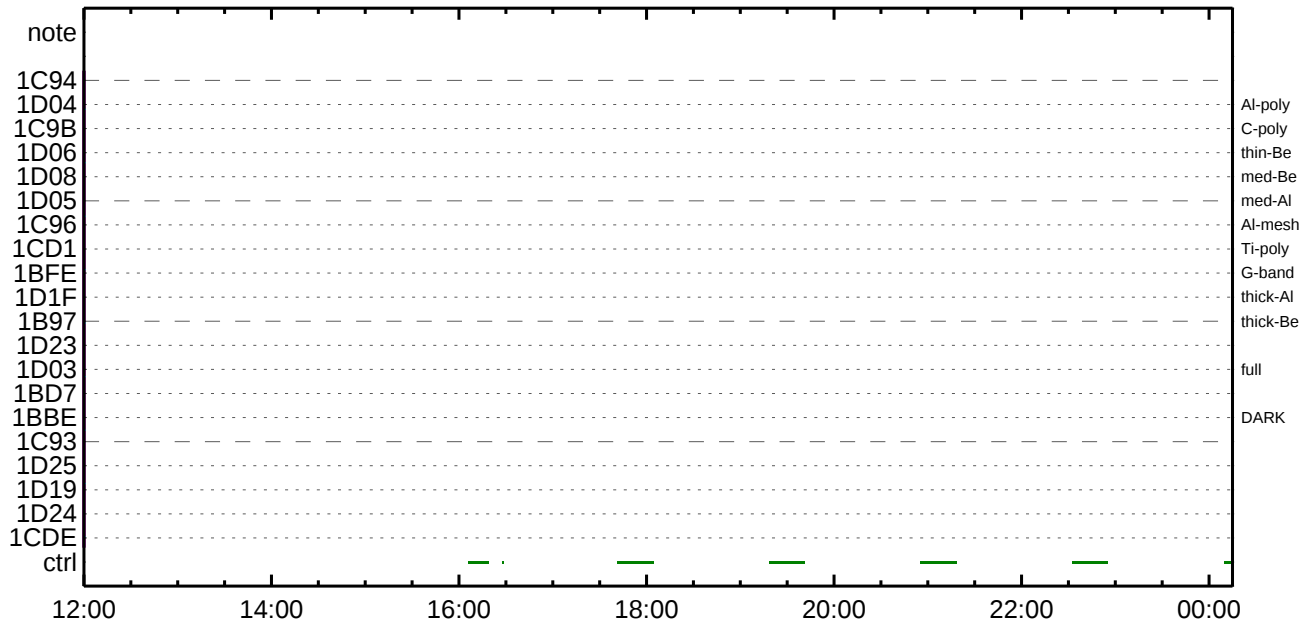
CMDI #0412 2025/02/28



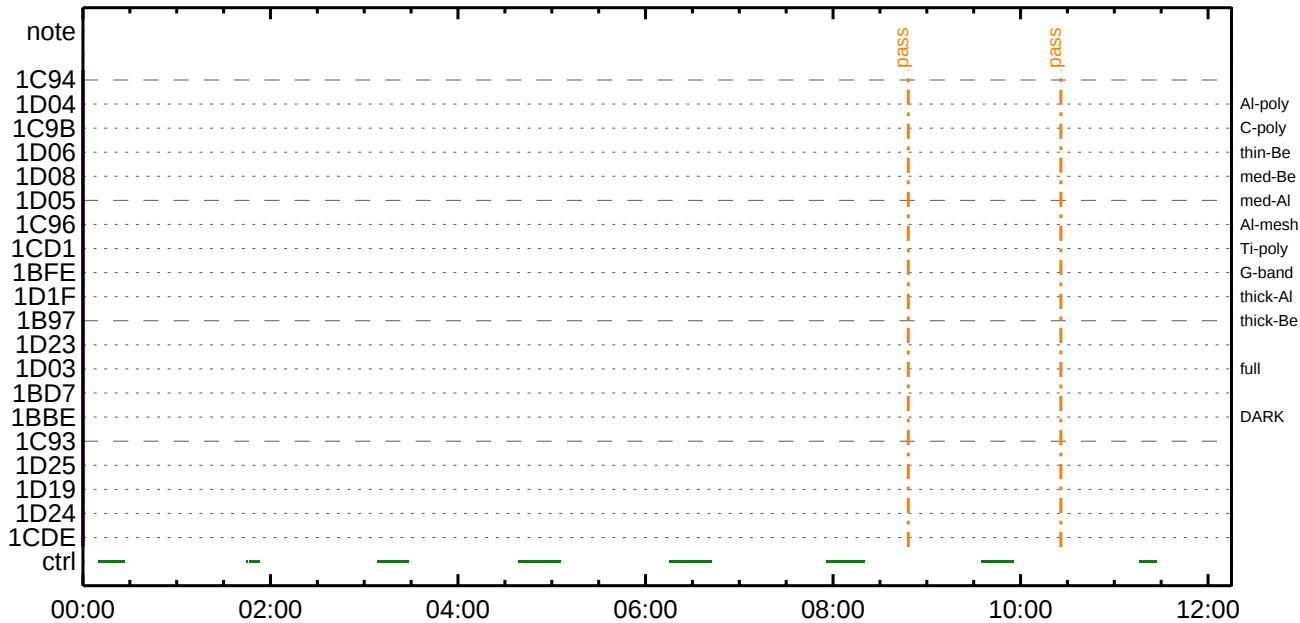
CMDI #0412 2025/03/01



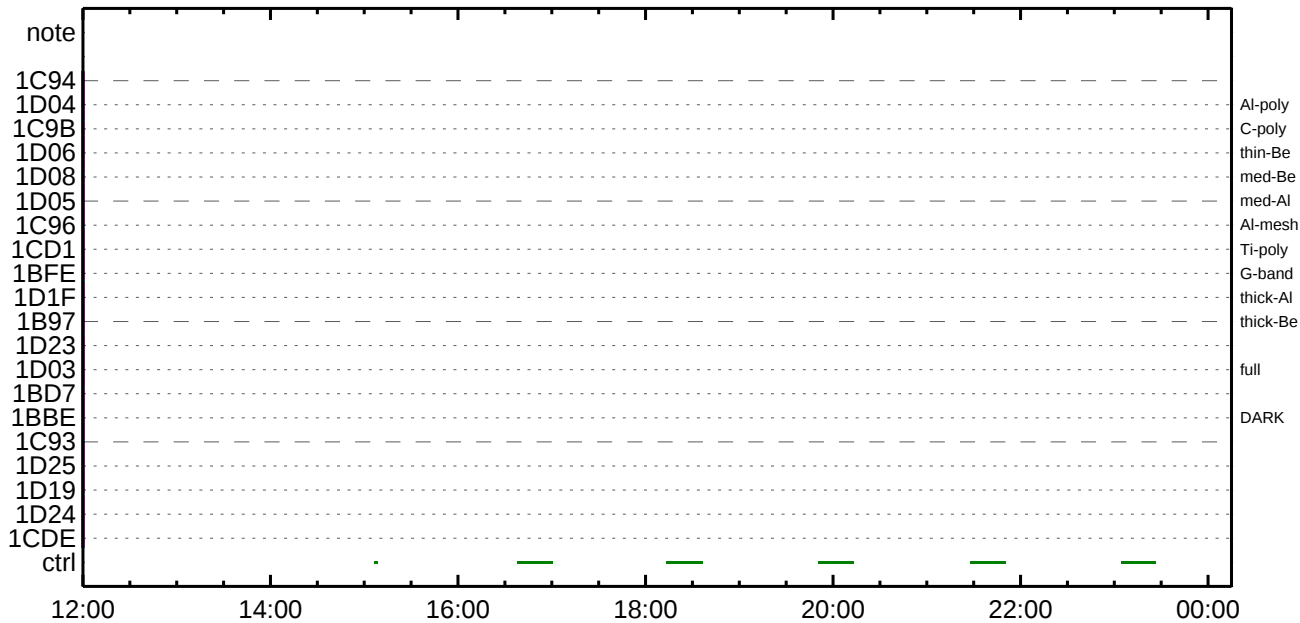
CMDI #0412 2025/03/01



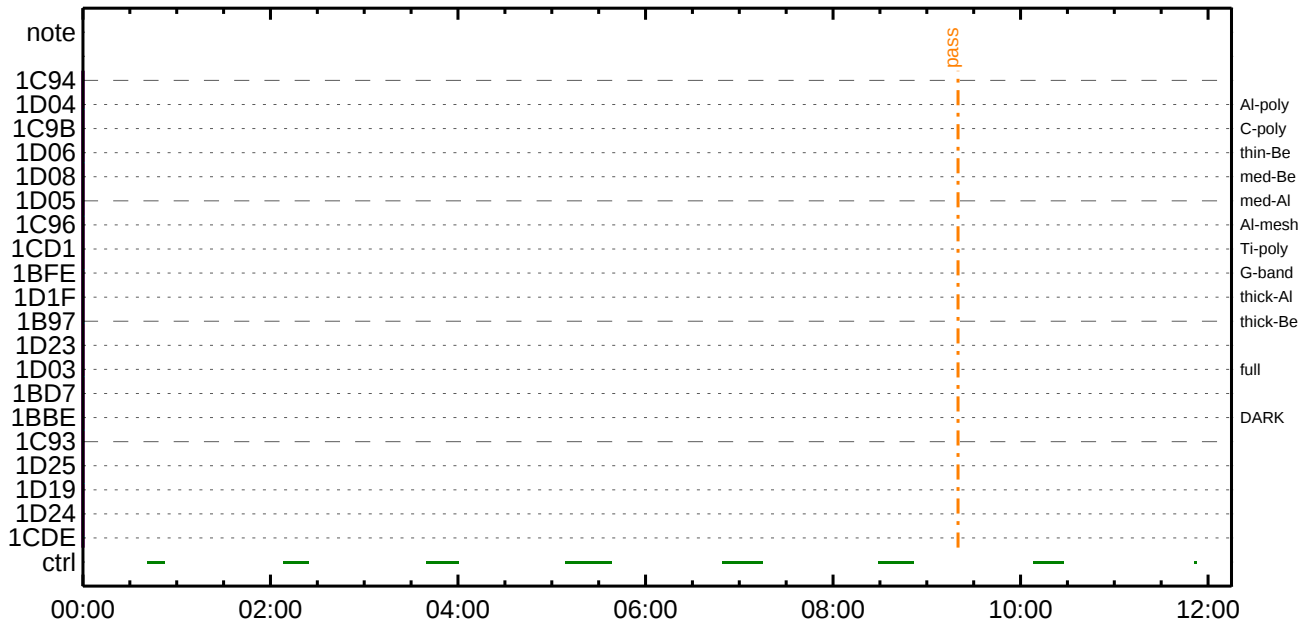
CMDI #0412 2025/03/02



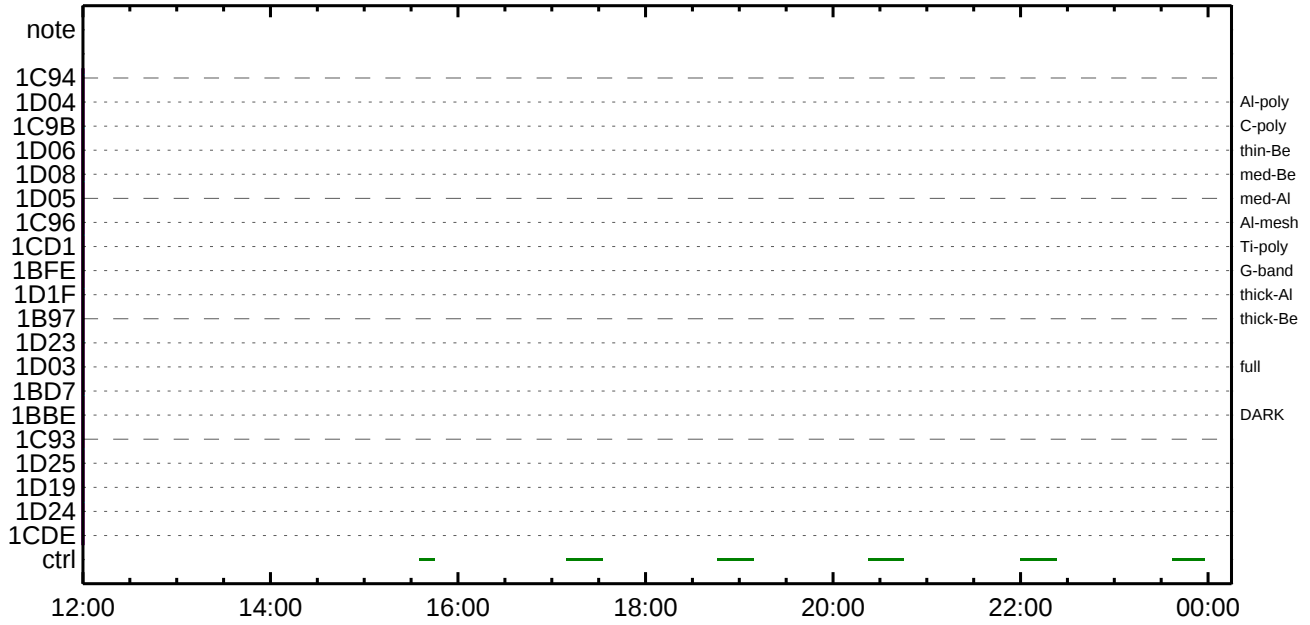
CMDI #0412 2025/03/02



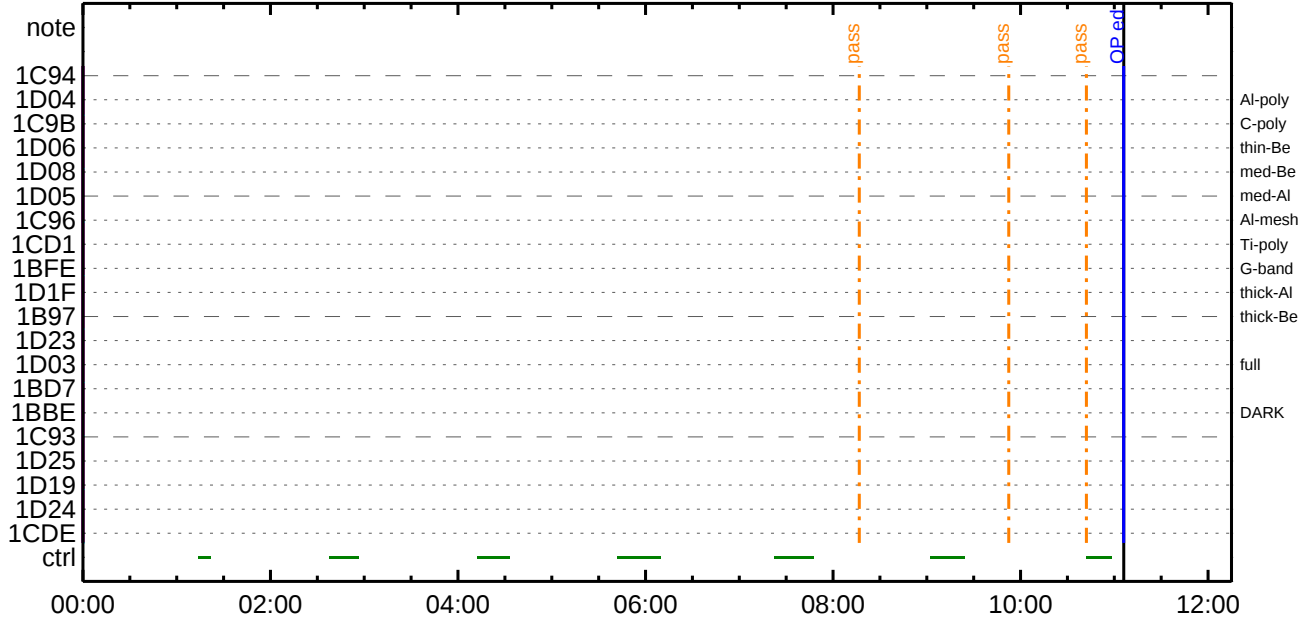
CMDI #0412 2025/03/03



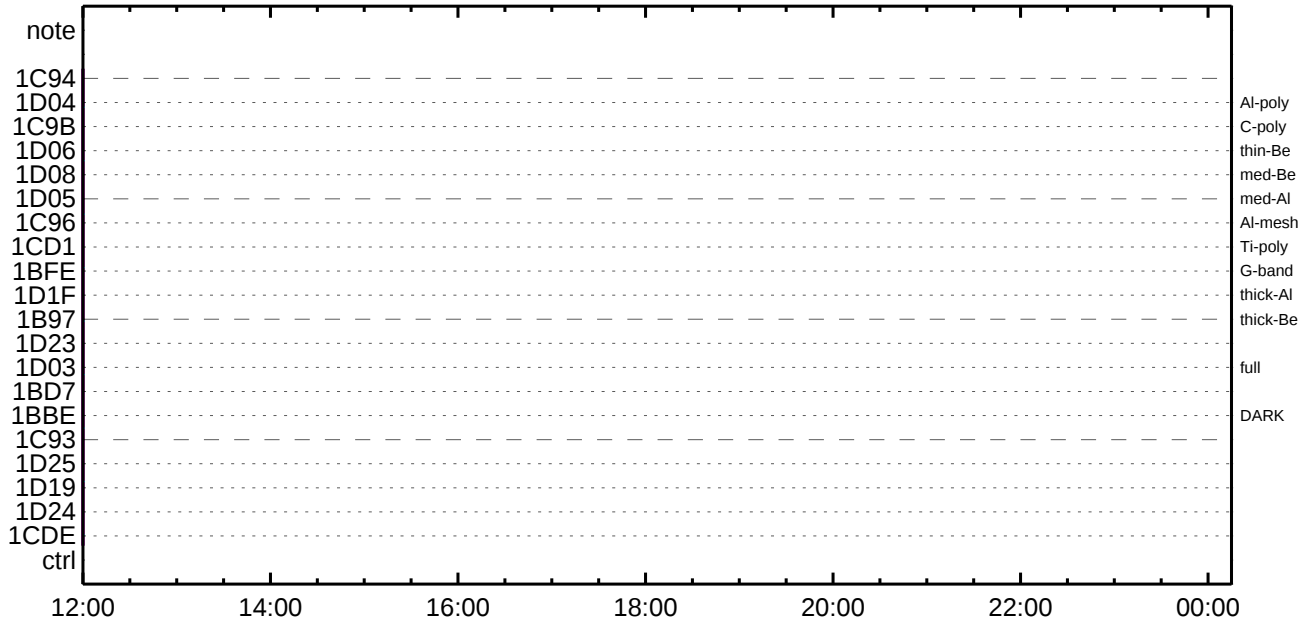
CMDI #0412 2025/03/03



CMDI #0412 2025/03/04



CMDI #0412 2025/03/04



```
main-119 2025-02-27 12:22:48 194 33 SOLAR-B MAIN //
```

```
main-11: 2025-02-27 12:22:48 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸPŸóŸÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçñÄñ•µ°E»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèµ•íÍË;ËñÉ¼°ÇÖñ•ñç¼í¹ÇñÍ;çÄ®,ùñ¹ñËñPñÇÁ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÄŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-119:OP
0020 ()
0021 . S. OG og-119:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÎî°èŸÄŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÄŸóŸ×½ªî»ñò³ÎÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñÎ¼Ë¹Ç•ë²ÎOKñò³ÎÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÄŸóŸ×½ªî»ñò³ÎÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñÎ¼Ë¹Ç•ë²ÎOKñò³ÎÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÄŸóŸ×½ªî»ñò³ÎÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñÎ¼Ë¹Ç•ë²ÎOKñò³ÎÇ$
0081 C.
0082 . C. ***** °Ë²¼ñÎ¼Ä´¶°ñËË¬ñ°Ä÷ç® (¼âµ-ŸÄŸóŸ×¼è½ÇñòÄÓÄæñÇ½ªñ``ñë¼í¹ÇñÇñâ) *****
0083 C. DHUŸä;¼ŸÉ;Ë¼ý½,Ÿí;¼ŸÉ;ËñòÍäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñÎ¼í¹Ç;ç°Ë²¼ñÎTI-CMDÁ÷ç®ñÎ¼Ä¹Ôñ•ñËñññ³ñË;£
```

0096	C.	SET EDUMP			
0097	C.				
0098	C.	TI 2025-02-27 11:34:00.0			
0099	+	TI 2025-02-27 11:34:00.0			
0100	DC	01-B3 DHU_OP_STOP			
0101	C.		[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.				
0103	+	TI 2025-02-27 11:34:01.0			
0104	DC	01-B4 DHU_OP_COPY			
0105	C.		[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.				
0107	+	TI 2025-02-27 11:34:01.0			
0108	DC	01-B5 DHU_OPOG_COPY			
0109	C.		[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.				
0111	+	TI 2025-02-27 11:38:59.5			
0112	DC	01-B2 DHU_OP_START			
0113	C.		[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.				
0115	C.				
0116	C.		[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.		[HK1_TI_CMD_NUM]	EQ	4
0118	C.		[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.		[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.				
0121	C.	*****			
0122	C.	TI 2025-02-27 11:38:59.5			
0123	C.	*****			
0124	C.				
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)			
0126	+	DC 01-23 DHU_DMA_DMP_PRM_SET			
0127	BC	(03 ab 03 01 02)			
0128	C.		[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.		[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.		[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.		[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.		[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG			
0134	BC	(07 0b f8)			
0135	C.		[HK1_PKT_FORM_NO]	EQ	7
0136	C.		[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.		[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.		[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.		[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.				
0141	C.				
0142	C.		[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.				
0144	C.	RAM ID=TI_TBL			
0145	C.				
0146	C.	DHU			
0147	+	DC 01-22 DHU_MODE_CHNG			
0148	BC	(02 0a f8)			
0149	C.		[HK1_PKT_FORM_NO]	EQ	2
0150	C.		[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.		[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.		[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.				
0154	C.	Stop EIS observation and temporarily disable EIS mode changes			
0155	C.				
0156	C.				
0157	C.	***** Start EIS operation (TI set) *****			
0158	C.	Execute, after the success of OP upload.			
0159	C.	Set EIS TI-commands			
0160	+	TI 2025-02-27 11:38:30.0			
0161	DC	07-FC EIS_MODE_MANU			
0162	BC	(21 02)			
0163	+	TI 2025-02-27 11:38:40.0			
0164	DC	07-FC EIS_MODE_CHG_DIS			
0165	BC	(22)			
0166	C.		[HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****			
0168	C.				
0169	C.				
0170	C.				
0171	C.	***** XRT START *****			
0172	C.	Execute, after the success of OP upload.			
0173	+	TI 2025-02-27 11:38:00.0			
0174	DC	07-F0 MDP_XRT_MODE_STBY			
0175	BC	(c3)			
0176	C.		[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.				
0178	C.	***** XRT END *****			
0179	C.				
0180	C.	***** MDP 'gAifI>ôY EADn1 eDCBCx2è *****			
0181	C.	(%â°iYôYAYEYDYEYAYCYEèE½¼¼¼)Ün1 è)			
0182	S.	DC-BC dcbc-402:DCBC			
0183		(MDP_known_event)			
0184	C.				
0185	C.				
0186	C.	***** YDY1•i Daily±iN E'Øn1 eDCBCx2è *****			
0187</					

(a) Spacecraft Operation Procedure (real-commands)

```
main-120 2025-02-27 12:22:48 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¿¿Ä»•µ°Æ»Í×ÄÇ»ÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËè½µ•ííË;Ë»Ë¼°ÇÖ»•¿¿¼í¹Ç»Í;çÄ®,ù»¹»è»Ð»ÇÄ÷¿®»•»Ë»»»³»È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ()
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 C. Verify EIS in MANUAL mode
0052 C. Estimated OBSTBL upload time is 13s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ()
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2025-02-27 11:38:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP ´ûÄî»Í»ó¼Ÿ»ËÄÐ»¹»èDCBC•×²è *****
0072 C. (¾ä°íŸóŸÄŸËŸŸŸŸÉŸäŸçŸè»Ë½¾»¼Ä»Ü»¹»è)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** ŸÐŸ¹•Ï Daily±¿íÑ»Ë´Ø»¹»èDCBC•×²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0083 C.
0084 . C. ***** LOS *****
0085 C.
```

```
main-121 2025-02-27 12:22:48 186 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÄñ•µ°E»Í×ÄÇñÍŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•ííË;ËñÉ¼°ÇÖñ•ñç¼í¹ÇñÏ;çÄ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ËÝñäLOSñÐñÇñÏ»p´Öðòíí,ñ•;çÉÖÍ×ñËXÄÓONñÏ¹ÖñËñíñËñññ³ñË;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÖÄÖñ¬°ÄÄèñ•ñç,ç°Ë²¼ñí°EÄ,¼è½Çñðò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ËPT1;Ëñ•ñç¼ñ¼í¹ÇñÏ;ç°Ë²¼ñí¼Ä¹Öñññ°;çDCBC-150ñØçËñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲËËÄÚÄØ;ËÄ•Ä°²óÈò;Ë,ãñí°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ë²¼ñðò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲËËÄÚÄØñäÄ•Ä°²óÈòñ¬¼áñ¼í¹ÇñÏ´°í»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ËPT2;Ëñ•ñç¼ñ¼í¹ÇñÏ;ç°Ë²¼ñí¼Ä¹Öñññ°;çDCBC-151ñØçËñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲËËÄÚÄØ;ËÄ•Ä°²óÈò;Ë,ãñí°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EQ OFF
```

```
0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 85 83 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 + DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 0b)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 0e)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2025-02-27 11:38:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîñÎ»ö¼ÝñÊÃÐñ¹ñÐDCBC•×²è *****
0173 C. (¾å°î¥Ó¥Ã¥Ê¥Ð¥Ê¥â¥ç¥Ëñ½¾¾ñ¼Ã»Ûñ¹ñË)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** ¥Ð¥¹•Ï Daily±¿îññÊ´Øñ¹ñÐDCBC•×²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ;ãLOS¥Á¥$¥Ã¥¬¼Ã»Û;ã
0184 C.
0185 . C. ***** LOS *****
0186 C.
```

Feb 27, 25 12:22

XRT_OGLIST_0412.chk

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*** OP Sequence for XRT ***

2025/02/27	11:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	11:48:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	11:48:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/02/27	11:49:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 64 01 ca				
2025/02/27	11:49:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/02/27	11:49:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/02/27	11:49:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/02/27	11:49:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/02/27	11:49:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/02/27	11:51:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2025/02/27	11:51:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/02/27	11:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/02/27	15:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	15:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	15:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/02/27	15:06:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/02/27	15:09:30.0	XRT_Custom_430_OG [0x1ae]							
2025/02/27	15:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/02/27	15:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/02/27	16:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	16:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	16:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/02/27	16:37:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/02/27	16:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/02/27	17:01:00.0	XRT_Custom_430_OG [0x1ae]							
2025/02/27	17:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/02/27	17:49:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	17:49:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	17:49:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/02/27	17:49:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/02/27	17:49:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/02/27	17:49:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/02/27	17:49:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/02/27	17:52:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2025/02/27	17:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/02/27	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/02/27	17:59:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/02/27	17:59:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 64 01 ca				
2025/02/27	17:59:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/02/27	17:59:50.5	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/02/27	17:59:52.5	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/02/27	17:59:54.5	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/02/27	17:59:56.5	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/02/27	18:02:26.5	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2025/02/27	18:02:28.5	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/02/27	18:02:30.5	XRT_CTRL_AUTO_408_OG [0x198]							

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2025/02/27	18:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/27	18:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	18:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	18:13:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/27	18:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/27	18:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/27	18:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/02/27	19:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/27	19:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	19:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	19:50:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/27	19:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/27	20:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/27	20:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/02/27	21:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/27	21:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	21:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	21:27:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/27	21:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/27	21:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/27	21:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/02/27	23:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/27	23:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	23:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/27	23:04:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/27	23:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/27	23:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/27	23:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/02/28	00:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	00:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	00:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	00:42:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	00:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	00:50:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	00:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	00:50:04.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	00:50:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/02/28	00:50:24.0	XRT_FLD_ENA_411_OG [0x19b]				
2025/02/28	00:50:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/02/28	00:50:28.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/02/28	00:50:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/02/28	00:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	00:50:34.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	00:50:36.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/02/28	00:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/02/28	00:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/02/28	00:58:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	00:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	00:58:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	00:58:24.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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2025/02/28	00:58:26.0	MDP_XRT_FLD_ENA	1	07-F0	d8
		XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/02/28	00:58:28.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/02/28	00:58:30.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	00:58:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	00:58:34.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/02/28	00:58:36.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/02/28	00:58:38.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	02:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	02:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	02:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	02:08:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	02:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	02:24:30.0	XRT_Custom_430_OG [0x1ae]			
2025/02/28	02:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	03:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	03:40:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	03:40:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	03:40:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	03:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/02/28	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/02/28	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/02/28	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/02/28	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/02/28	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	04:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/02/28	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/02/28	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	05:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	05:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	05:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	05:09:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	05:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	05:38:00.0	XRT_Custom_430_OG [0x1ae]			
2025/02/28	05:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	06:03:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	06:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	06:03:04.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/02/28	06:03:24.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/02/28	06:03:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/02/28	06:03:28.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	06:03:30.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/02/28	06:03:32.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/02/28	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/02/28	06:10:00.0	AOCs_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 03 64 01 ca
2025/02/28	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/02/28	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/02/28	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/02/28	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	06:12:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/02/28	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/02/28	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	06:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	06:49:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	06:49:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	06:49:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	06:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	07:15:00.0	XRT_Custom_430_OG [0x1ae]				
2025/02/28	07:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	08:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	08:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	08:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	08:29:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	08:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	08:52:00.0	XRT_Custom_430_OG [0x1ae]				
2025/02/28	08:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	10:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	10:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	10:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	10:08:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	10:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	10:27:30.0	XRT_Custom_430_OG [0x1ae]				
2025/02/28	10:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	11:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	11:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	11:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	11:51:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	11:52:30.0	XRT_Custom_430_OG [0x1ae]				
2025/02/28	11:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	11:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	15:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	15:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	15:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	15:35:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	15:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	15:45:30.0	XRT_Custom_430_OG [0x1ae]				
2025/02/28	15:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	17:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	17:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	17:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da

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2025/02/28	17:09:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	17:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	17:33:00.0	XRT_Custom_430_OG [0x1ae]			
2025/02/28	17:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	18:02:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	18:02:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/02/28	18:03:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/02/28	18:03:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/02/28	18:03:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/02/28	18:03:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	18:05:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/02/28	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	18:12:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	18:12:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/02/28	18:13:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 03 64 01 ca
2025/02/28	18:13:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/02/28	18:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/02/28	18:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/02/28	18:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/02/28	18:13:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	18:15:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/02/28	18:15:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/02/28	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	18:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	18:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	18:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	18:46:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	18:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	19:09:30.0	XRT_Custom_430_OG [0x1ae]			
2025/02/28	19:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	20:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	20:22:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	20:22:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	20:22:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	20:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	20:46:00.0	XRT_Custom_430_OG [0x1ae]			
2025/02/28	20:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	22:00:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	22:00:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/02/28	22:00:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	22:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	22:23:00.0	XRT_Custom_430_OG [0x1ae]			
2025/02/28	22:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/02/28	23:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	23:37:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	23:37:04.0	XRT_FLD_RESET_415_OG [0x19f]			

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2025/02/28	23:37:06.0	MDP_XRT_FLD_RESET	1	07-F0	da
		XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/02/28	23:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/02/28	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/02/28	23:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/01	00:00:00.0	AOCs_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	01 00 00 00 00
2025/03/01	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/01	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/01	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/01	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/01	00:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	00:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/03/01	00:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/01	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	01:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	01:14:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	01:14:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	01:14:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	01:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	01:22:00.0	XRT_Custom_430_OG [0x1ae]			
2025/03/01	01:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	02:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	02:37:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	02:37:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	02:37:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	02:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	02:56:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/01	02:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/01	04:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/03/01	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/01	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/01	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/01	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/01	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	04:02:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/03/01	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/01	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	04:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	04:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	04:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	04:12:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	04:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	04:33:00.5	XRT_Custom_430_OG [0x1ae]			
2025/03/01	04:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	05:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			

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2025/03/01	05:42:02.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	05:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	05:42:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	05:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	06:10:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/01	06:11:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	06:22:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	06:22:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	06:22:04.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/01	06:22:24.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/01	06:22:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/01	06:22:28.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/01	06:22:30.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/03/01	06:22:32.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	06:29:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	06:29:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	06:29:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/03/01	06:29:30.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 03 64 01 ca
2025/03/01	06:29:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/01	06:29:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/01	06:29:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/01	06:29:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/01	06:29:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	06:32:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/03/01	06:32:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/01	06:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	07:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	07:22:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	07:22:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	07:22:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	07:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	07:47:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/01	07:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	09:02:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	09:02:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	09:02:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	09:02:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	09:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	09:24:00.0	XRT_Custom_430_OG [0x1ae]			
2025/03/01	09:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/01	10:42:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	10:42:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/01	10:42:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/01	10:42:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/01	10:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/01	11:17:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00