

Period: 2025/03/04 11:06:00 - 2025/03/08 11:04:00

XOB #1CF0: Synoptic 8 Filter w/ Al-mesh(5128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(128/576/2304)			
Term	Pointing (x, y)	Comment	
03/04 18:08:00 - 03/04 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min	
03/05 17:52:30 - 03/05 17:59:24	Fixed (0.0, 0.0)	synoptic, shifted -10.5 min	

XOB #1D23: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 1800s cad (G-band/Leak last)				
Term	Pointing (x, y)			Comment
03/04 18:18:00 - 03/05 03:59:54	Track (-569.9, -166.9)	@ 03/04 18:15:00	AR 14012 tracking	
03/05 06:01:30 - 03/05 17:49:24	Track (-480.2, -159.7)	@ 03/05 05:58:30	AR cont.	

PROG= 09		Inf.-time(s)											
Subr= 1	8-time(s)	1800.0sec											
Seqn= 8	1-time(s)	2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec	
Seqn= 6	1-time(s)	2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec	
Subr= 2	1-time(s)	2.0sec											
Seqn= 30	1-time(s)	2.0sec											

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
03/05 04:03:00 - 03/05 05:48:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -11.5 min
03/06 04:03:00 - 03/06 05:59:54	Fixed (0.0, 0.0)	HOP349 + synoptic

PROG= 05 Inf.-time(s)

Subr= 1	1-time(s)	300.0sec										
Seqn=100	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 57	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2	6-time(s)	1200.0sec										
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CCC: Synoptic 7 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(64/1024/5795) - Thick-Be(32768), Al-poly+Ti-poly(128/2048), Med-Al(289

Term	Pointing (x, y)	Comment
03/05 05:51:30 - 03/05 05:58:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -11.5 min
03/06 06:03:00 - 03/06 10:09:54	Fixed (0.0, 0.0)	HOP349 + synoptic

PROG= 04 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 45	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23	1-time(s)	4.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 41	1-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 85	1-time(s)	2.0sec										
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 4	1-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C8D: Alignment with North Pole Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 5min cad

Term	Pointing (x, y)	Comment
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03/05 18:14:30 - 03/05 19:59:54			Fixed (0.0, 930.0)			Coalignment North									
PROG= 13 1-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 23		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		24-time(s)		300.0sec											
Seqn= 69		1-time(s)		2.0sec											
Al-poly/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	2x2	2048x1536 (1024, 768)		Q=95	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1C8E: Alignment with East limb Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 8 min cad															
Term			Pointing (x, y)					Comment							
03/05 20:15:00 - 03/06 03:59:54			Fixed (-970.0, 0.0)					Coalignment East							
PROG= 07 1-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 23		1-time(s)		2.0sec											
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		15-time(s)		480.0sec											
Seqn= 70		1-time(s)		2.0sec											
		Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)		Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-														
Term		Pointing (x, y)				Comment								
03/04 11:19:00 - 03/04 17:41:00		Fixed (-20.0, -958.0)				# OP start + 10min + HOP206 South Pole								
03/04 18:18:00 - 03/05 03:59:54		Track (-569.9, -166.9) @ 03/04 18:15:00				AR 14012 tracking								
03/05 04:03:00 - 03/05 05:48:24		Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -11.5 min								
03/05 06:01:30 - 03/05 17:49:24		Track (-480.2, -159.7) @ 03/05 05:58:30				AR cont.								
03/06 04:03:00 - 03/06 05:59:54		Fixed (0.0, 0.0)				HOP349 + synoptic								
PROG= 14 30-time(s)														
Subr= 1		20-time(s)		2.0sec										
Seqn= 11		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 73		1-time(s)		10.0sec										
thin-Be/Open		med-Be/Open close		Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
med-Be/Open		Open/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 10		1-time(s)		2.0sec										
med-Al/Open		med-Al/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 87		1-time(s)		2.0sec										
Open/G-band		Open/G-band open		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/G-band		Open/G-band close		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al close		Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al close		Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

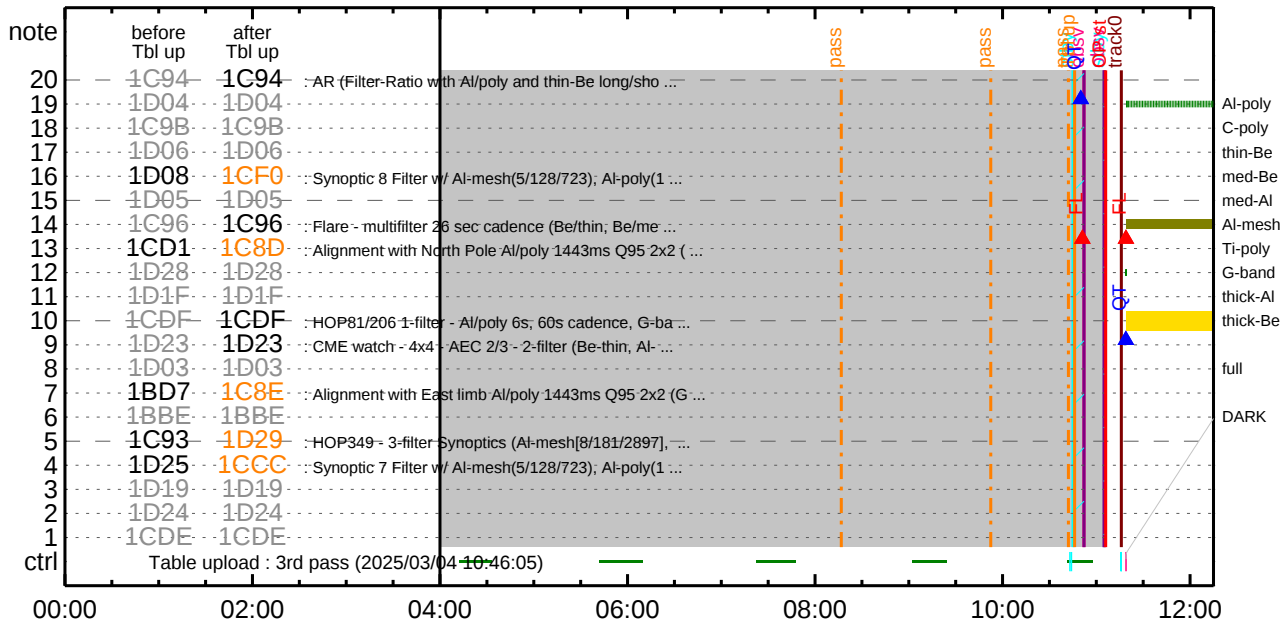
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Flare Detection

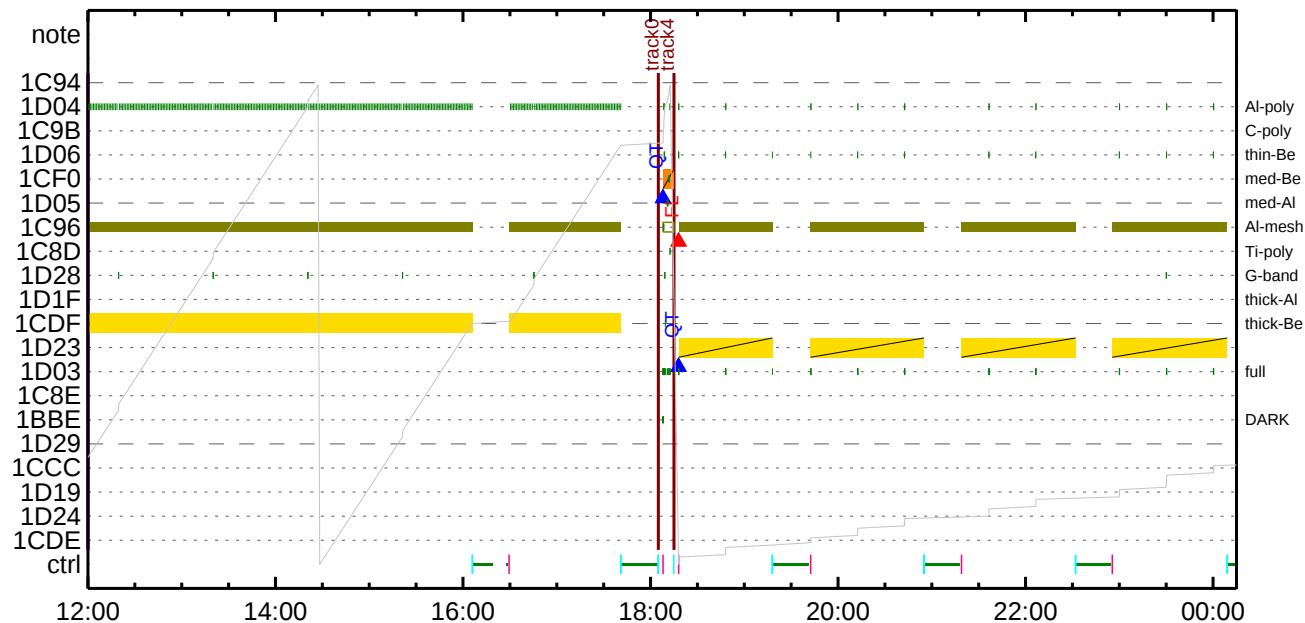
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FLD Patrol												
Term			Pointing (x, y)					Comment				
03/04 10:47:05 - 03/04 18:05:18			cannot be identified									
03/04 18:15:16 - 03/05 05:48:48			Track (-569.9, -166.9)		@ 03/04 18:15:00			AR 14012 tracking				
03/05 05:58:46 - 03/05 17:49:48			Track (-480.2, -159.7)		@ 03/05 05:58:30			AR cont.				
03/06 04:00:18 - 03/06 06:00:18			Fixed (0.0, 0.0)					HOP349 + synoptic				
Al-poly/Open		Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8			Q=50	30sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

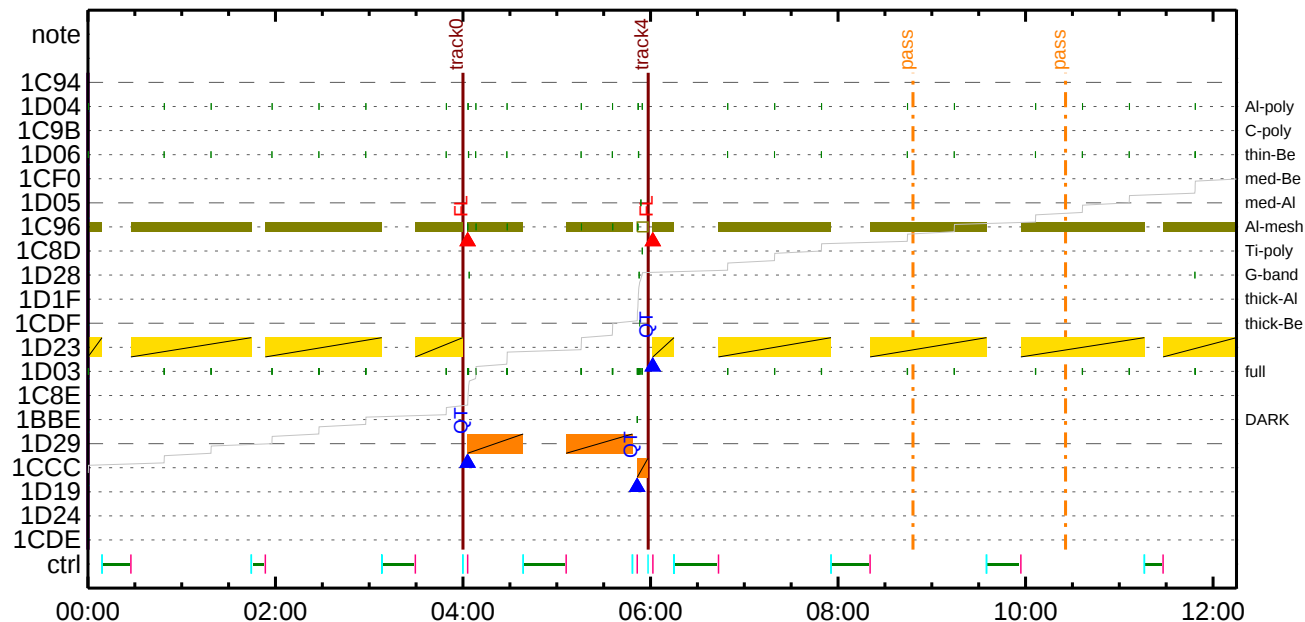
CMDI #0423 2025/03/04



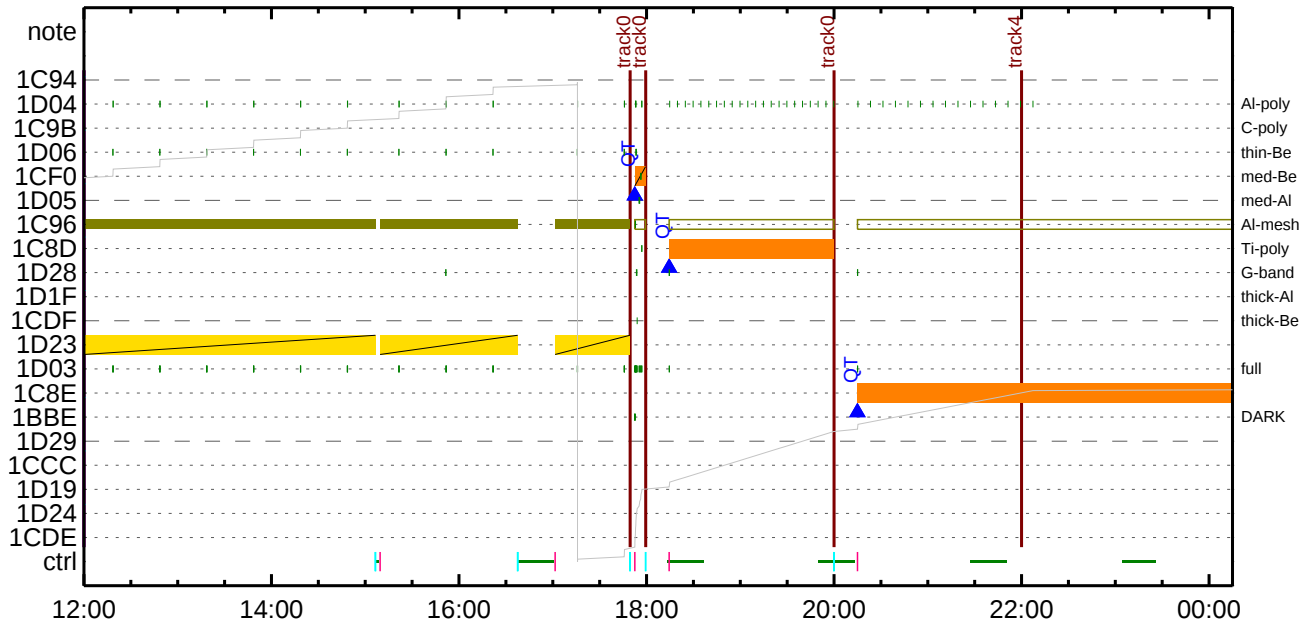
CMDI #0423 2025/03/04



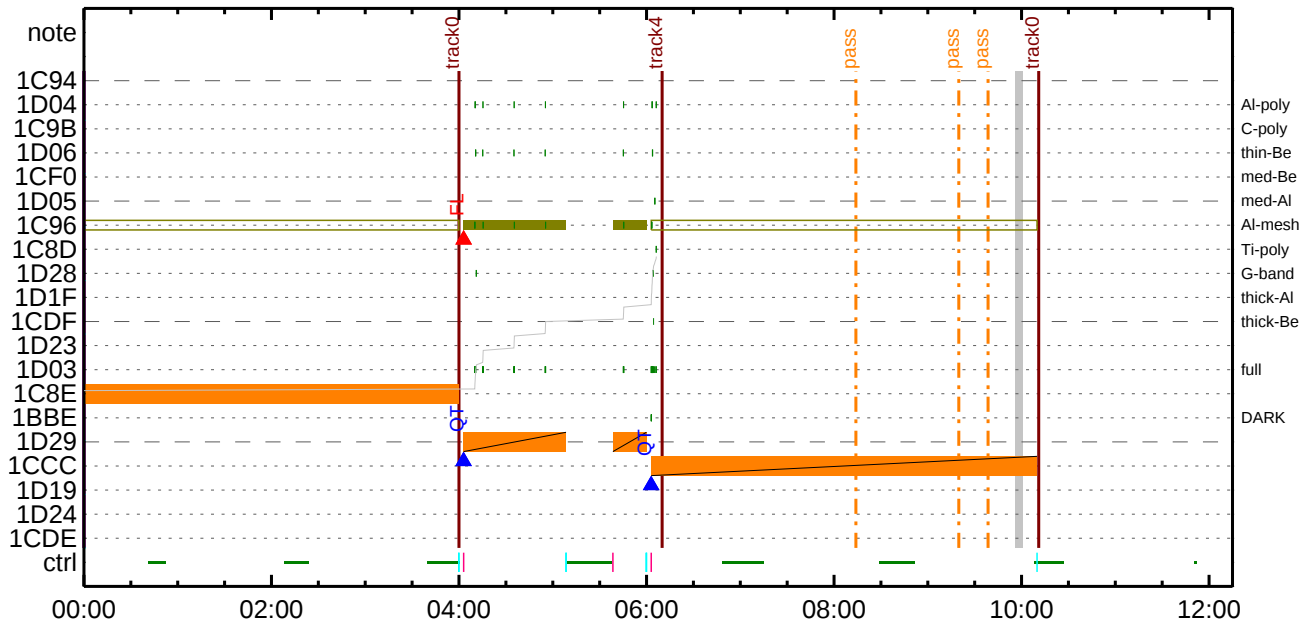
CMDI #0423 2025/03/05



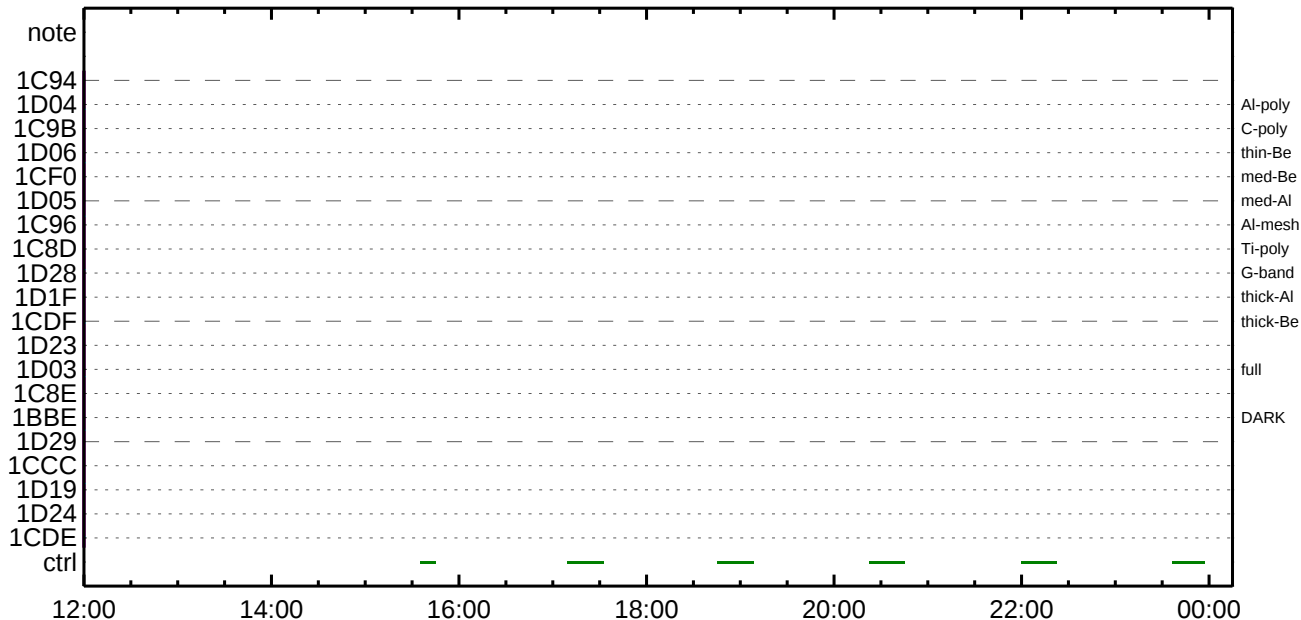
CMDI #0423 2025/03/05



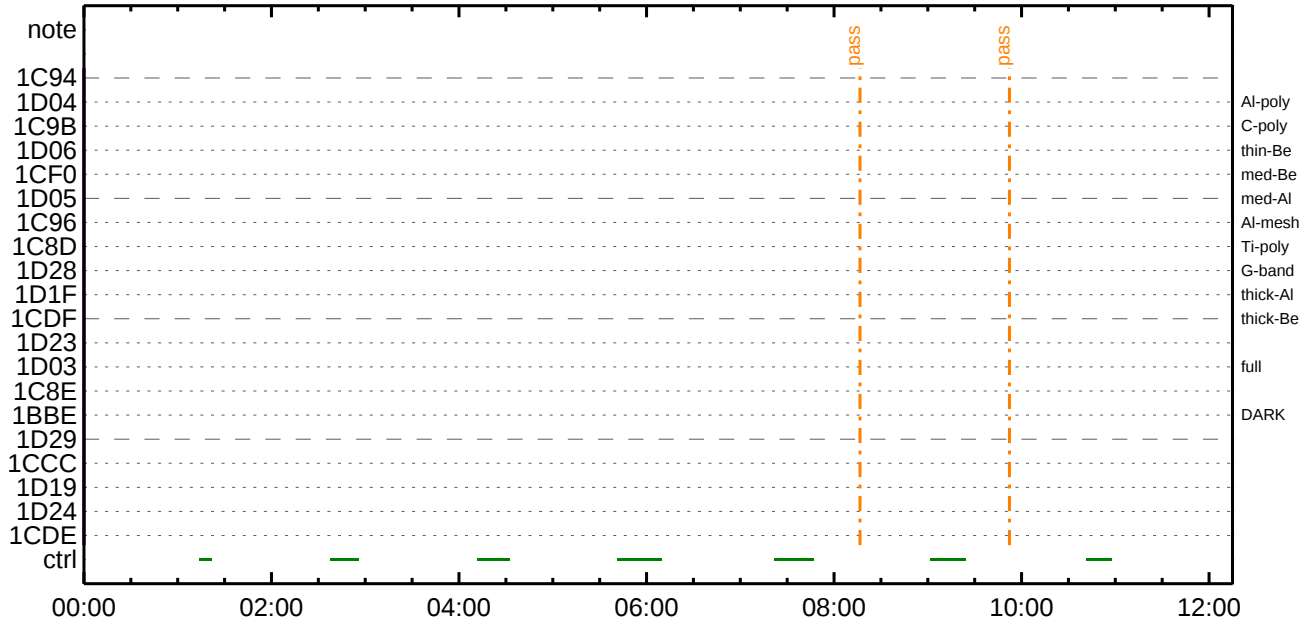
CMDI #0423 2025/03/06



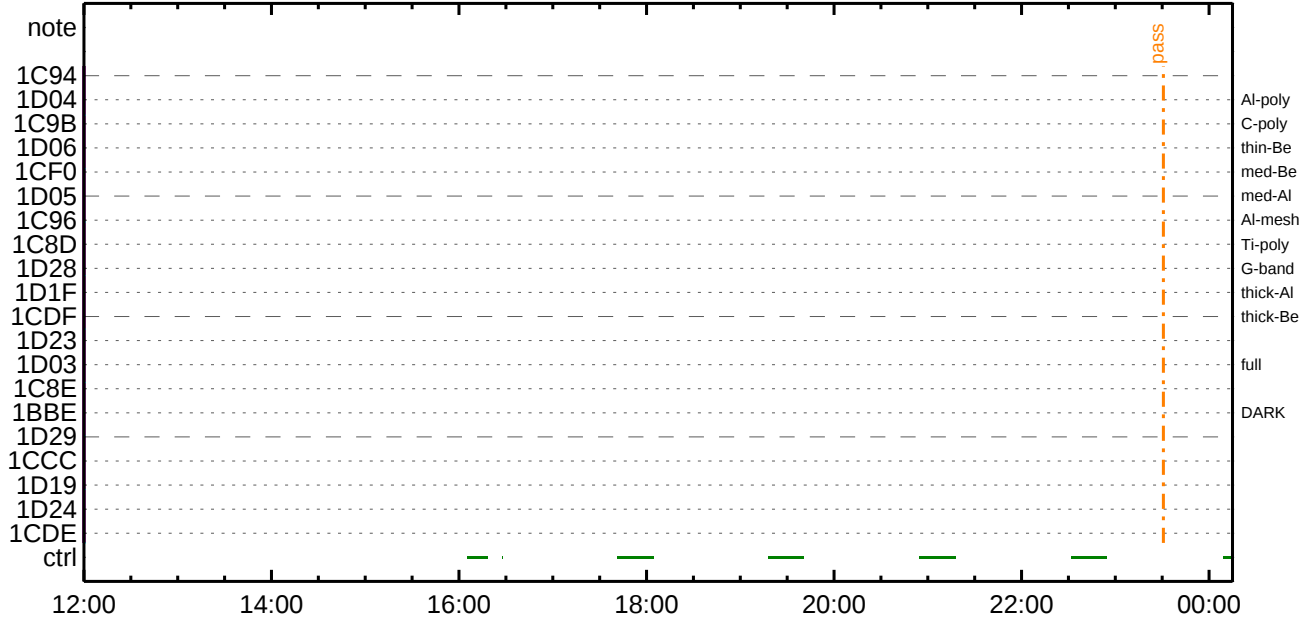
CMDI #0423 2025/03/06



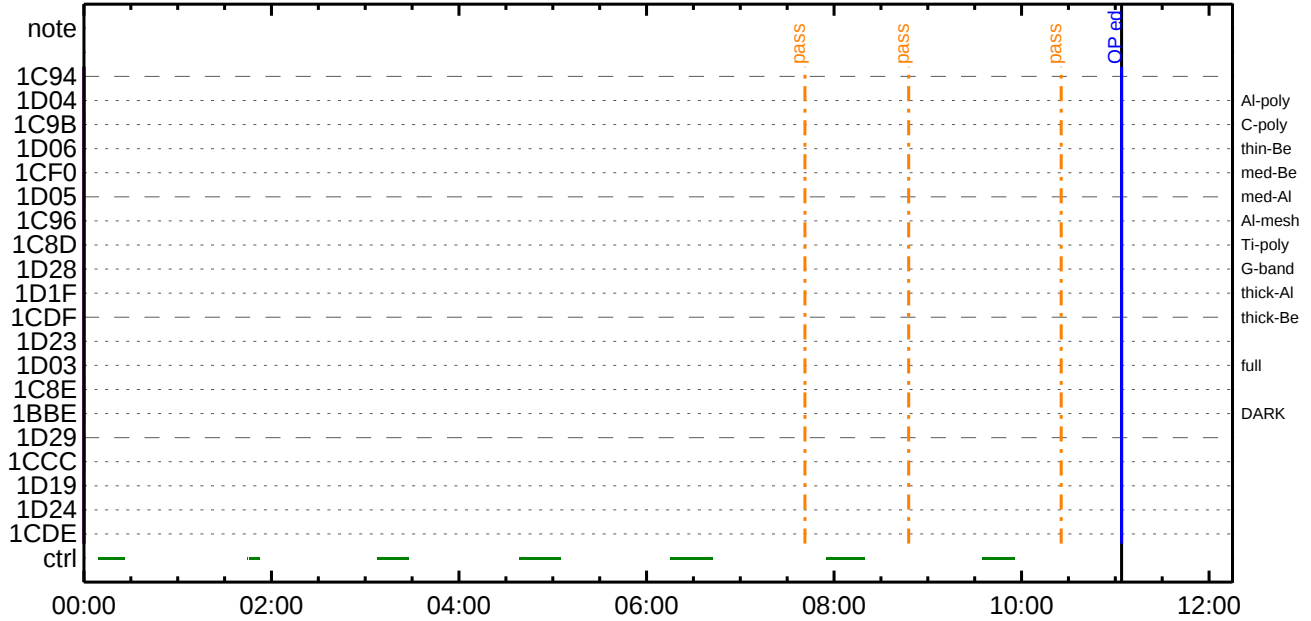
CMDI #0423 2025/03/07

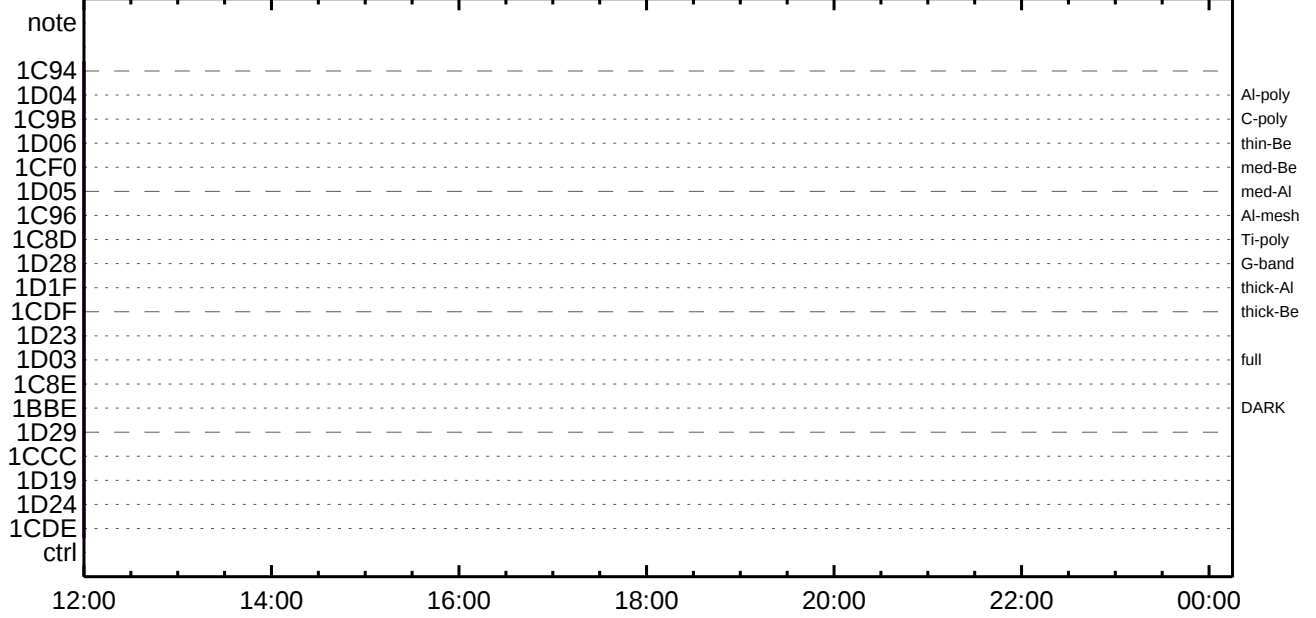


CMDI #0423 2025/03/07



CMDI #0423 2025/03/08





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main-128 2025-03-04 13:24:30 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŸçŸÃŸ×Ÿí;¼ŸÉ;ËËè²µ•íîË;ËñÊ¼°ÇÖñ•ñç¼¹¹çñİ;çÀ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñÊñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,í×ÈÝñäLOSñÐñÇñÎ»p´Öòò¹íî,ñ•;çÉÖÍ×ñÊXÁÓONñİ¹ÖñÊñíñÊñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÄÖñ¬°ÄÄêñ•ñç,ç°Ê²¼ñî°EÄ,¼ê½çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñç¼ñ¼¹¹çñİ;ç°Ê²¼ñî¼Ä¹Öññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0045 C.
0046 . C. ;ãŸçŸóŸEŸËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŸçŸóŸEŸËÄÚÂØñÄ•Ä°²öÈòñ¬¼áñ¼¹¹çñİ´°î»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñç¼ñ¼¹¹çñİ;ç°Ê²¼ñî¼Ä¹Öññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0071 C.
0072 . C. ;ãŸçŸóŸEŸËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OG¥1;¼¥É;|¥À¥ó¥×
0100 C. *****
0101 C.
0102 C. ;ãOP/OG¥1;¼¥É;ä
0103 S. OP op-128:OP
0104 ( )
0105 S. OG og-128:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°è¥À¥ó¥×;ä
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥À¥ó¥×½ªî»¤ð³îÇ$
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î¾È¹Ç•ë²îOK¤ð³îÇ$
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥À¥ó¥×½ªî»¤ð³îÇ$
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î¾È¹Ç•ë²îOK¤ð³îÇ$
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥À¥ó¥×½ªî»¤ð³îÇ$
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP¤î¾È¹Ç•ë²îOK¤ð³îÇ$
0165 C.
0166 C. ***** °È²¼¤î¾Ä´¶Á¤¤ÈÉ¬¤°Á÷÷@ (¾âµ-¥À¥ó¥×¼È½Ç¤ðÄÓÃæ¤Ç½ª¤¤¬¤ë¾¼¹Ç¤Ç¤â) *****
0167 C. DHU¥â;¼¥É;È¼ý½,¥ì;¼¥É;È¤ððîÄ¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;$ OPOG UPLOAD¤¬Á÷÷@NG¤î¾¼¹Ç;¢°È²¼¤î¾TI-CMDÁ÷÷@¤î¾Ä¹Ô¤¤¤¤È¤¤¤¤¤¤;£
0180 C. ¤¤¤¤;¢SET¤ÈDUMP¤î¾±°î¾¥¹¹Ç¹Ô¤¤¤¤¤¤;£
0181 C.
0182 C. TI¥³¥¤¥ó¥É¤ðÄBîÇ (UT)
0183 +. TI 2025-03-04 11:01:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2025-03-04 11:01:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2025-03-04 11:01:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2025-03-04 11:05:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼¤İÄê¼İİÑ¤İ¥Á¥$¥Ä¥~¹àİÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]          EQ          DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]          EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ          07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ          2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]          EQ          3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]          EQ          0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ          7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ          0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]          EQ          32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ          EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ          NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¾È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHU¥â;¼¥É;Ê¼Ý½,¥İ;¼¥È;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ          2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ          0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]          EQ          32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2025-03-04 11:05:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2025-03-04 11:05:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2025-03-04 11:05:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2025-03-04 11:05:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃİ¤İ»ö¼Ý¤ÈÂ¤¤¹¤èDCBC•×²è *****
0276 C. (¼å°İ¥Ö¥Ä¥È¥Ð¥È¥Ä¥Ç¥è¤È¼¤¤¼Ä»Ü¤¹¤è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•İ Daily±çİÑ¤È´Ø¤¹¤èDCBC•×²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;äLOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0287 C.
0288 C. ***** LOS *****
0289 C.
```

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main-129 2025-03-04 13:24:30 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÐñÇñÎ»p´Öòò¹íî,ñ•;ÇÉÖÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲŲ~¾ÔÄÖñ¬°ÄÄêñ•ñç,Ç°Ê²¼ñî°EÄ,¼ê½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçñ¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲËŲÊÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;Ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲÇŲóŲÉŲËŲÊÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñ¼¹¹Çñİ´°î»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçñ¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲÇŲóŲÉŲËŲÊÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 2896979.8 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 15s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-03-04 11:05:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÃÐñ¹ñëDCBC•×²è *****
0165 C. (¾å°îÝÓÝÃÝÊÝÐÝÊÝÁÝÇÝÊñÊ¼¼ñ¼Ã»Ûñ¹ñë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•Ï Daily±¿ÍÑñÊ'Øñ¹ñëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÁÝ$ÝÃÝ¹¼Ã»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.
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(a) Spacecraft Operation Procedure (real-commands)

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main-130 2025-03-04 13:24:30 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÀŸß;¼Ÿ³ŸÐŸóŸÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ēñ¿ñÄñ•µ°ñ»í×ÁÇñİŸçŸŸ×Ÿí;¼ŸÉ;ĒĒè½µ•ííĒ;ĒñĒ¼°ÇŒñ•ñ¿¼¹Çñİ;çÄ®,ùñ¹ñĒñÐñÇÁ÷¿®ñ•ñĒñññ³ñĒ;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 08 08)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 85 83 06 06)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 20 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 20)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b 80 80 08 08)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0c 80 60 20 18)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0d a0 80 18 20)
0060 +. DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 +. DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 +. DC 07-F0 MDP_XRT_FLD_ENA
0065 BC (d8)
0066 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067 BC (c8)
0068 +. DC 07-F0 MDP_XRT_ARS_DIS
0069 BC (d5)
0070 +. DC 07-F0 MDP_XRT_AEC_RESET
0071 BC (d0)
0072 +. DC 07-F0 MDP_XRT_FLD_RESET
0073 BC (da)
0074 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0075 BC (c4 14)
0076 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0077 BC (c5 0e)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C.
0081 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0082 C.
0083 +. DC 07-F0 MDP_XRT_MODE_OBSV
0084 BC (c2)
0085 +. TI 2025-03-04 11:05:02.0
0086 DC 07-F0 MDP_XRT_MODE_OBSV
0087 BC (c2)
0088 . C. ----- Success Verify ? OK / NG ____
0089 C.
0090 C. ***** XRT END *****
0091 . C. *****
0092 C. SOT table upload
0093 C. *****
0094 . C. < Stop SP table >
0095 +. DC 07-F0 MDP_SP_CTRL_MANU
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0096      BC      (61)
0097      C. -----
0098      C.   MDP_SP_CTRL_MODE      = MANU      [ ]
0099      C. -----
0100      C.
0101      . C. <Upload SP Observation Table>
0102      . S. RAM      ram-285:MDP_OBS_S
0103      ( )
0104      C.
0105      . C. < Dump RAMID=MDP_OBS_S >
0106 +. DC 07-F0 MDP_DUMP_SPTBL
0107      BC      (83 07 00 00 00 38 b8)
0108      C. -----
0109      C.   MDP_OBS_S verify = OK/NG      [ ]
0110      C. -----
0111      C.
0112      C. *****
0113      C.   SOT TI command set
0114      C. *****
0115      C. Execute, after the success of TBL upload.
0116 +. TI 2025-03-04 11:05:18.0
0117      DC 07-F0 MDP_SOT_MODE_OBSV
0118      BC      (40)
0119      C. -----
0120      C.   HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0121      C. -----
0122      C.
0123      C.
0124      . C. ***** MDP 'ûÃÎñî»ö¼ÝñĚÂĐñ¹ñĚDCBC•x²è *****
0125      C. (%ã°Ĭ¥Ō¥Ā¥Ě¥Đ¥Ě¥Ā¥Ĉ¥ĚñĚ¼ñ¼Ā»Ūñ¹ñĚ)
0126      . S. DC-BC dcbc-402:DCBC
0127      (MDP_known_event)
0128      C.
0129      C.
0130      . C. ***** ¥Đ¥¹•Ĭ Daily±¿ĬññĚ'Œñ¹ñĚDCBC•x²è *****
0131      . S. DC-BC dcbc-153:DCBC
0132      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133      C.
0134      C.
0135      . C. ;ãLOS¥Ā¥$¥Ā¥¹¼Ā»Ū;ä
0136      C.
0137      . C. ***** LOS *****
0138      C.

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*** OP Sequence for XRT ***

2025/03/04	11:15:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	11:15:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	11:15:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/03/04	11:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 55 26 01 ca
2025/03/04	11:16:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/04	11:16:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/04	11:16:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/04	11:16:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/04	11:16:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	11:18:56.0	XRT_QT_PROG_SET_414_OG [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/03/04	11:18:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/04	11:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/04	16:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	16:06:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	16:06:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	16:06:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/04	16:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/04	16:28:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/04	16:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/04	17:41:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	17:41:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	17:41:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	17:41:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/04	17:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/04	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	18:04:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/04	18:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/03/04	18:05:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/04	18:05:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/04	18:05:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/04	18:07:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2025/03/04	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/04	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	18:14:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/04	18:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 03 64 01 ca
2025/03/04	18:15:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/04	18:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/04	18:15:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/04	18:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/04	18:15:24.0	XRT_FLD_RESET_428_OG [0x1ac]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	18:17:56.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2025/03/04	18:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/04	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/04	19:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	19:18:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/03/04	19:18:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	19:18:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/04	19:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/04	19:41:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/04	19:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/04	20:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	20:55:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	20:55:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	20:55:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/04	20:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/04	21:18:00.0	XRT_Custom_430_OG [0x1ae]			
2025/03/04	21:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/04	22:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	22:32:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/04	22:32:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/04	22:32:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/04	22:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/04	22:54:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/04	22:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/05	00:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	00:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	00:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/05	00:09:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/05	00:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/05	00:26:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/05	00:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/05	01:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	01:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	01:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/05	01:44:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/05	01:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/05	01:52:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/05	01:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/05	03:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	03:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	03:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/05	03:08:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/05	03:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/05	03:28:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/05	03:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/05	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/05	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/05	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/03/05	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/05	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/05	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/05	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/05	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/05	04:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]			

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2025/03/05	04:02:58.0	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
		XRT_FL_PROG_SET_439_OG [0x1b7]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/03/05	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/05	04:38:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	04:38:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	04:38:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/05	04:38:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/05	04:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/05	05:05:00.0	XRT_Custom_430_OG [0x1ae]				
2025/03/05	05:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/05	05:48:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	05:48:26.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	05:48:28.0	XRT_FOCUS_POSITION_406_OG [0x196]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2025/03/05	05:48:48.0	XRT_FLD_DIS_409_OG [0x199]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/03/05	05:48:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/03/05	05:48:52.0	XRT_ARS_DIS_435_OG [0x1b3]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/05	05:51:28.0	XRT_QT_PROG_SET_441_OG [0x1b9]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2025/03/05	05:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/05	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	05:58:26.0	XRT_FOCUS_POSITION_406_OG [0x196]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2025/03/05	05:58:30.0	AOCs_Ore-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	04 03 64 01	ca
2025/03/05	05:58:46.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/03/05	05:58:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/03/05	05:58:50.0	XRT_AEC_RESET_448_OG [0x1c0]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/03/05	05:58:52.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/05	05:58:54.0	XRT_FLD_RESET_428_OG [0x1ac]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/05	06:01:26.0	XRT_QT_PROG_SET_405_OG [0x195]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2025/03/05	06:01:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/03/05	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/05	06:15:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	06:15:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	06:15:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/05	06:15:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/05	06:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/05	06:42:30.0	XRT_Custom_430_OG [0x1ae]				
2025/03/05	06:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/05	07:55:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	07:55:32.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	07:55:34.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/05	07:55:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/05	07:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/05	08:19:30.0	XRT_Custom_430_OG [0x1ae]				
2025/03/05	08:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/05	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	09:35:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/05	09:35:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/05	09:35:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/05	09:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/05	09:56:00.0	XRT_Custom_430_OG [0x1ae]				

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2025/03/05	09:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/05	11:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	11:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	11:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/05	11:16:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/03/05	11:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/03/05	11:27:00.0	XRT_Custom_430_OG [0x1ae]							
2025/03/05	11:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/05	15:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	15:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	15:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/05	15:06:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/03/05	15:08:30.0	XRT_Custom_430_OG [0x1ae]							
2025/03/05	15:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/05	15:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/03/05	16:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	16:37:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	16:37:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/05	16:37:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/03/05	16:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/03/05	17:00:30.0	XRT_Custom_430_OG [0x1ae]							
2025/03/05	17:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/05	17:49:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	17:49:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	17:49:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/03/05	17:49:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/03/05	17:49:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/03/05	17:49:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/03/05	17:49:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/03/05	17:52:28.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2025/03/05	17:52:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/05	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	17:59:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/03/05	17:59:30.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ad 59 00 00				
2025/03/05	17:59:48.0	XRT_FLD_DIS_433_OG [0x1b1]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/03/05	18:14:24.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/03/05	18:14:26.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/03/05	18:14:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2025/03/05	18:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/05	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/05	19:59:58.0	XRT_FOCUS_POSITION_446_OG [0x1be]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/03/05	20:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 56 35				
2025/03/05	20:00:18.0	XRT_FLD_DIS_433_OG [0x1b1]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/03/05	20:14:54.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/03/05	20:14:56.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/03/05	20:14:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				

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2025/03/05	20:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/03/05	22:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 04 03 64 01 ca
2025/03/06	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2025/03/06	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2025/03/06	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2025/03/06	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2025/03/06	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2025/03/06	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/03/06	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/03/06	04:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2025/03/06	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0e
2025/03/06	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/03/06	05:08:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	05:08:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	05:08:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/03/06	05:08:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/03/06	05:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/03/06	05:37:30.0	XRT_Custom_430_OG [0x1ae]		
2025/03/06	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/03/06	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2025/03/06	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2025/03/06	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2025/03/06	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/03/06	06:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2025/03/06	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/03/06	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 04 03 64 01 ca
2025/03/06	10:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/03/06	10:11:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00