

XRT Timeline to be uploaded on 2025/03/27

Period: 2025/03/27 10:31:00 - 2025/04/01 11:02:00

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Normal mode

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XOB #1D08: Synoptic for HOP448 w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096)														
Term		Pointing (x, y)			Comment									
03/27 18:03:30 - 03/27 18:10:24		Fixed (0.0, 0.0)			synoptic, shifted 0.5 min									
03/28 06:19:30 - 03/28 06:26:24		Fixed (0.0, 0.0)			synoptic, shifted 16.5 min									
03/28 18:05:00 - 03/28 18:11:54		Fixed (0.0, 0.0)			synoptic, shifted 2.0 min									
03/29 05:49:00 - 03/29 05:55:54		Fixed (0.0, 0.0)			synoptic, shifted -14.0 min									
03/29 09:59:00 - 03/29 10:38:24		Fixed (0.0, 0.0)			Eclipse observations									
03/29 12:33:00 - 03/29 12:39:54		Fixed (0.0, 0.0)			Eclipse observations									
PROG= 16 1-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 5		1-time(s)		2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 55		1-time(s)		2.0sec										
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 98		1-time(s)		2.0sec										
Al-poly/Open		Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 76		1-time(s)		2.0sec										
thin-Be/Open		thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23		1-time(s)		4.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2		1-time(s)		2.0sec										
Seqn= 41		1-time(s)		2.0sec										
Open/thick-Be		Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 18		1-time(s)		2.0sec										
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Al/Open		med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 86		1-time(s)		2.0sec										
med-Be/Open		Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Be/Open		med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Be/Open		med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with														
Term		Pointing (x, y)					Comment							
03/27 18:13:30 - 03/27 20:19:54		Track (130.3, 236.1) @ 03/27 18:10:30					# AR 14039							
03/28 17:03:00 - 03/28 17:38:30		Track (331.7, 228.8) @ 03/28 17:00:00					# AR 14039							
03/28 18:15:00 - 03/28 23:59:54		Track (341.9, 228.2) @ 03/28 18:12:00					# AR 14039							
03/29 05:59:00 - 03/29 09:11:14		Track (438.8, 222.4) @ 03/29 05:56:00					# AR 14039							
03/29 12:43:00 - 03/29 14:24:54		Track (491.8, 218.5) @ 03/29 12:40:00					Eclipse obs. end AR 14039							
PROG= 11 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
└ Seqn= 92		1-time(s)		2.0sec										
└┐ Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└┐ Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└┐ Open/Ti-poly		Open/thick-Al		close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		4-time(s)		2.0sec										
└ Seqn= 47		1-time(s)		2.0sec										
└┐ Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└┐ Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└┐ thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└┐ thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Seqn= 77		4-time(s)		300.0sec										
└┐ Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
└┐ thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
└┐ Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
└┐ thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
└┐ Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
└┐ thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w														
Term			Pointing (x, y)				Comment							
03/28 03:48:00 - 03/28 06:16:24			Fixed (0.0, 0.0)				HOP 349							
03/29 03:33:00 - 03/29 05:45:54			Fixed (0.0, 0.0)				HOP 349							
PROG= 02 Inf.-time(s)														
Subr= 1		1-time(s)		300.0sec										
Seqn= 55		1-time(s)		2.0sec										
└ Open/Al-mesh		Open/Al-mesh close		Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh close		Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh close		Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 15		1-time(s)		2.0sec										
└ Al-poly/Open		Al-poly/Open close		Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open close		Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 79		1-time(s)		2.0sec										
└ thin-Be/Open		thin-Be/Open close		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open close		Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open close		Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 30		1-time(s)		2.0sec										
└ Open/G-band		Open/G-band open		Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band close		Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		15-time(s)		1500.0sec										
Seqn= 8		1-time(s)		2.0sec										
└ thin-Be/Open		med-Be/Open close		Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ thin-Be/Open		med-Be/Open close		Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 74		1-time(s)		2.0sec										
└ med-Be/Open		med-Be/Open close		Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ med-Be/Open		med-Be/Open close		Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec										
└ Al-poly/Open		Al-poly/Open close		Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ Al-poly/Open		Al-poly/Open close		Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 29		1-time(s)		2.0sec										
└ Open/Al-mesh		Open/Al-mesh close		Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh close		Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

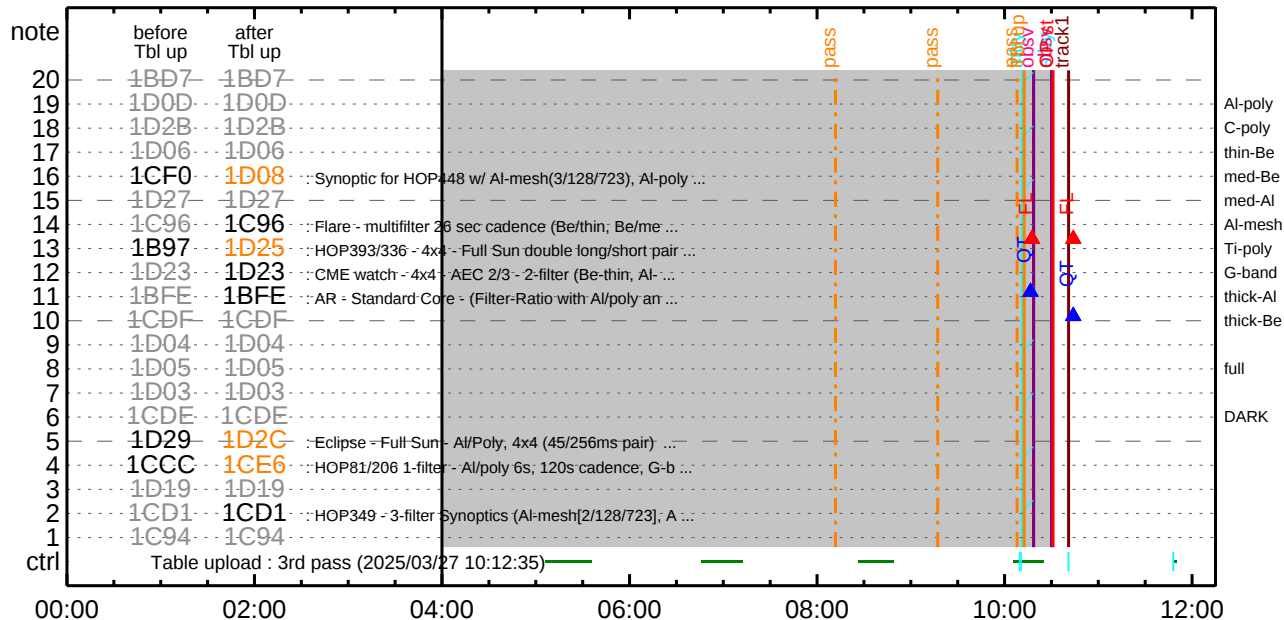
XOB #1D25: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 12														
Term					Pointing (x, y)					Comment				
03/29 00:03:00 - 03/29 03:29:54					Track (-20.0, 74.6) @ 03/29 00:00:00					HOP 393				
PROG= 13 Inf-time(s)														
└ Subr= 1		1-time(s)			2.0sec									
└ Seqn= 30		1-time(s)			2.0sec									
└ ┌		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
└ └		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D2C: Eclipse - Full Sun - Al/Poly, 4x4 (45/256ms pair) - occasional Al/poly, 2x2 (double 1.4s) - AEC=0														
Term			Pointing (x, y)					Comment						
03/29 09:14:20 - 03/29 09:55:54			Fixed (0.0, 0.0)					Eclipse observations						
03/29 10:41:30 - 03/29 12:29:54			Fixed (0.0, 0.0)					Eclipse observations						
PROG= 05 1-time(s) 2.0sec														
Subr= 1 1-time(s) 30.0sec														
Seqn= 72 1-time(s) 30.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
Seqn= 42 33-time(s) 20.0sec														
		Al-poly/Open	Open/thick-Al	close	Safe	Norm	44ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Al-poly/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 62 1-time(s) 2.0sec														
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 42 27-time(s) 20.0sec														
		Al-poly/Open	Open/thick-Al	close	Safe	Norm	44ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Al-poly/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

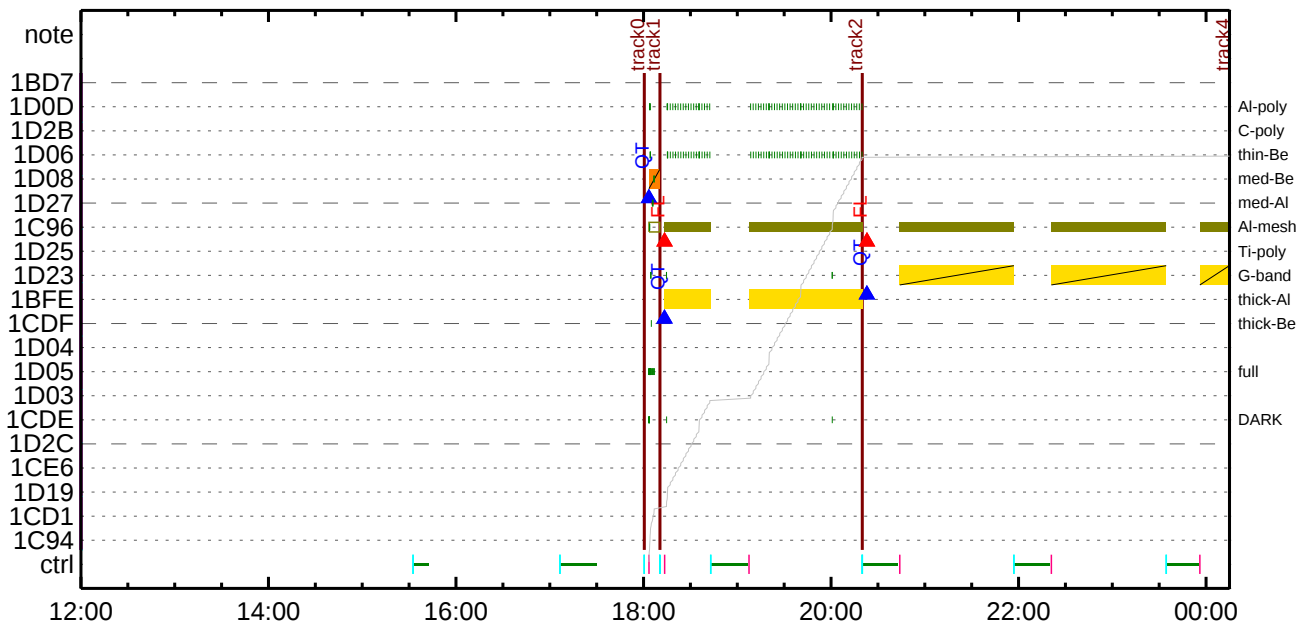
FLD Patrol			
Term	Pointing (x, y)		Comment
03/27 10:13:35 - 03/27 18:00:48	cannot be identified		
03/27 18:10:48 - 03/28 06:16:48	Track (130.3, 236.1)	@ 03/27 18:10:30	# AR 14039
03/28 06:26:48 - 03/28 18:02:18	Track (240.1, 232.8)	@ 03/28 06:26:30	# AR 14039

03/28 18:12:18 - 03/29 05:46:18	Track (341.9, 228.2)	@ 03/28 18:12:00	# AR 14039									
03/29 05:56:18 - 03/29 09:11:38	Track (438.8, 222.4)	@ 03/29 05:56:00	# AR 14039									
03/29 12:40:18 - 04/01 11:02:00	Track (491.8, 218.5)	@ 03/29 12:40:00	Eclipse obs. end AR 14039									
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8			Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

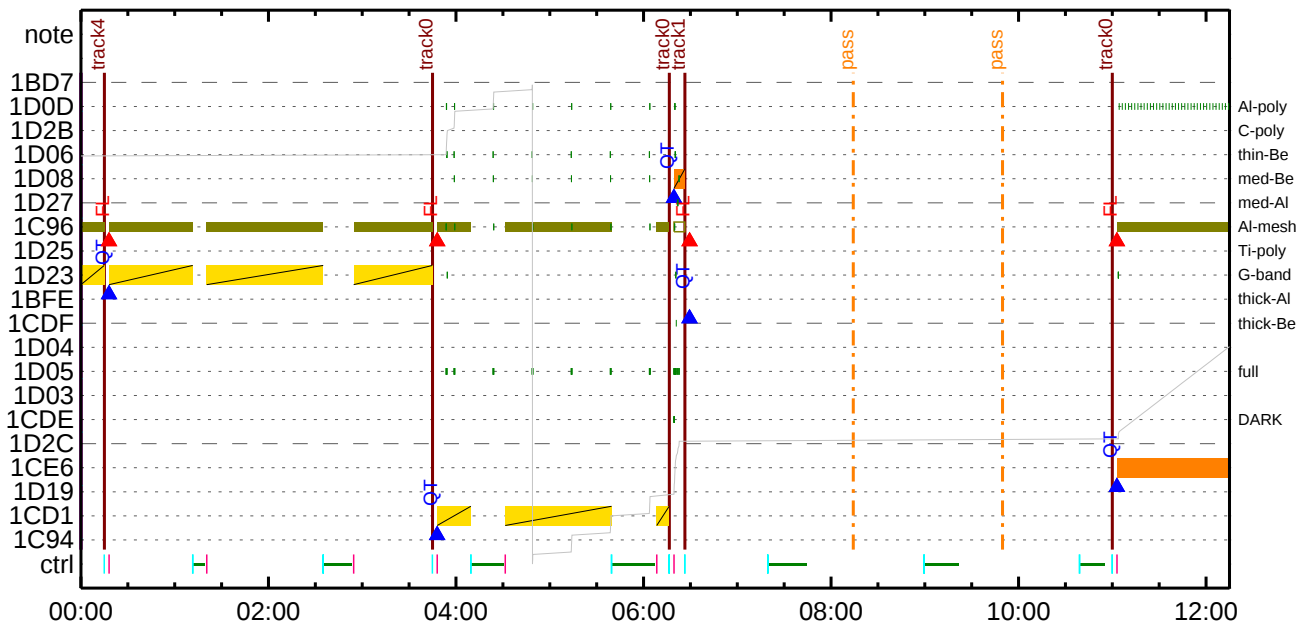
CMDI #0480 2025/03/27



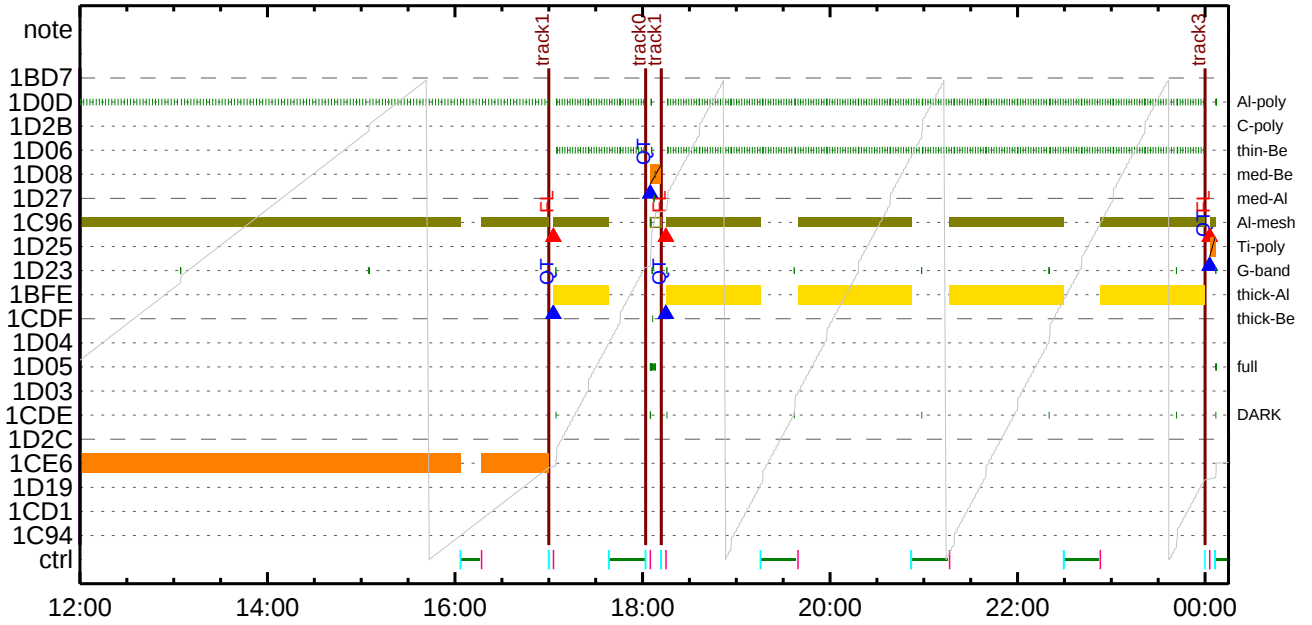
CMDI #0480 2025/03/27



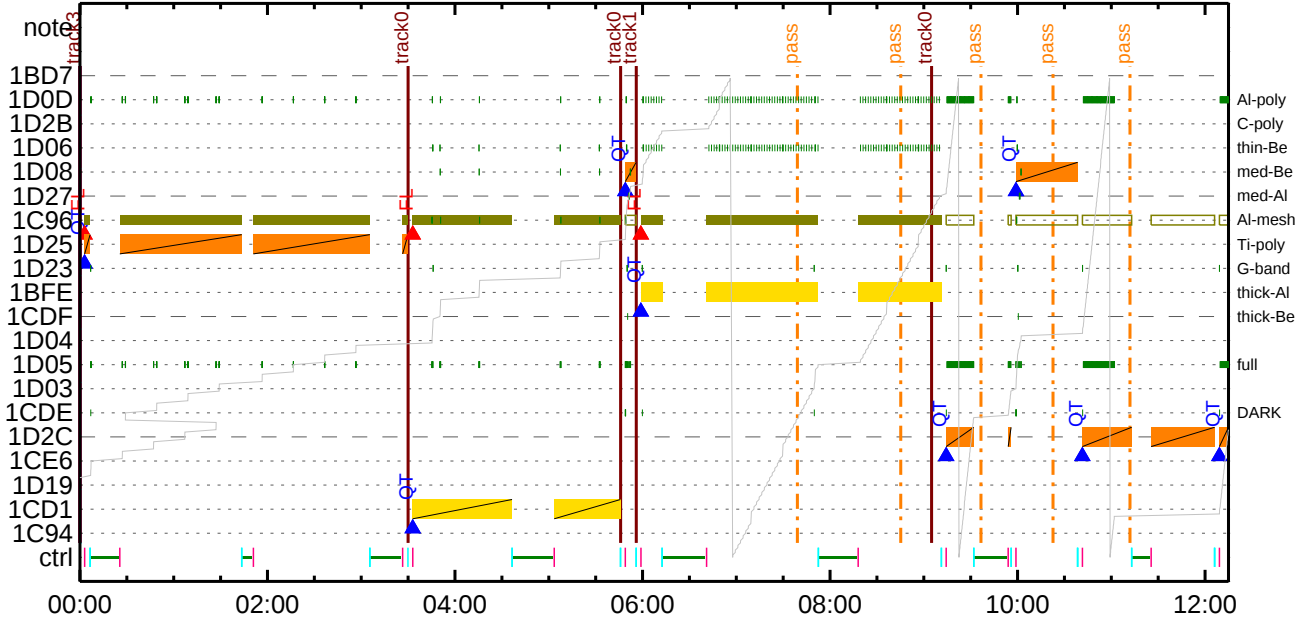
CMDI #0480 2025/03/28



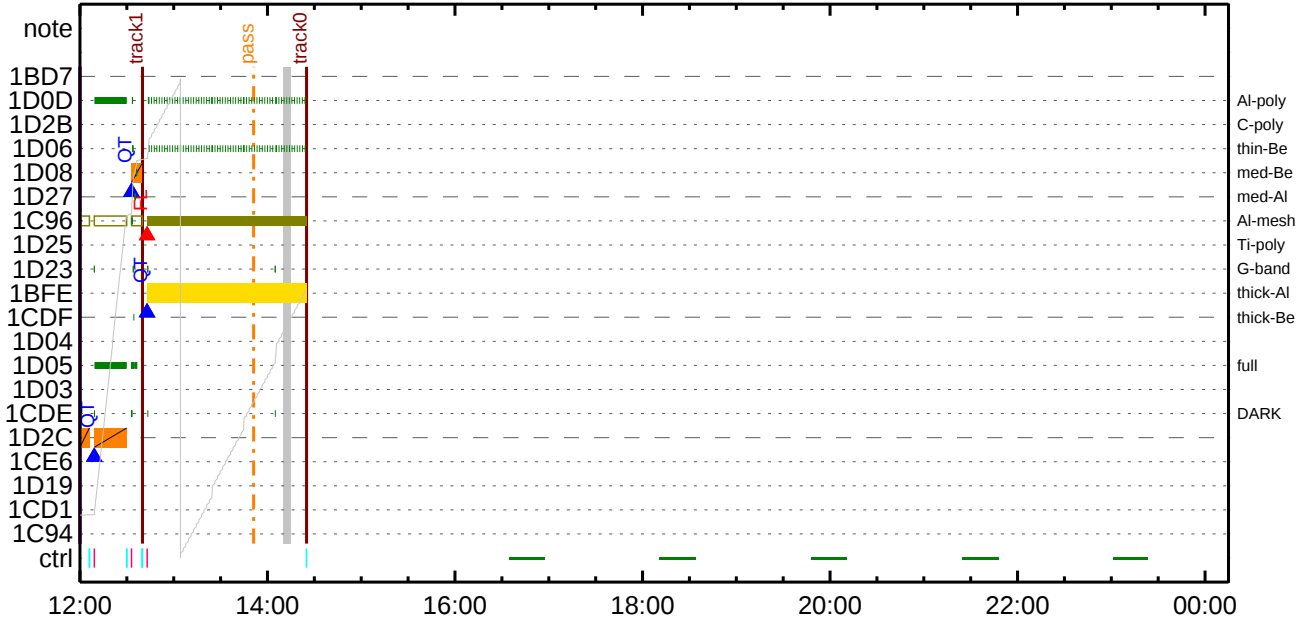
CMDI #0480 2025/03/28



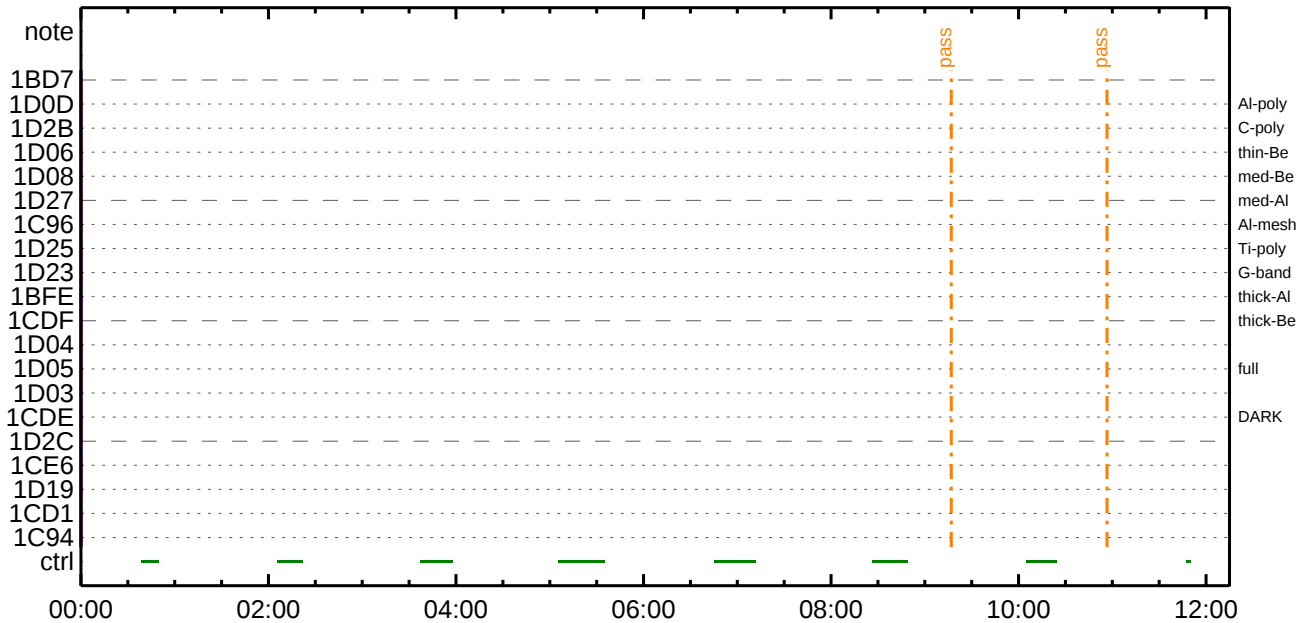
CMDI #0480 2025/03/29



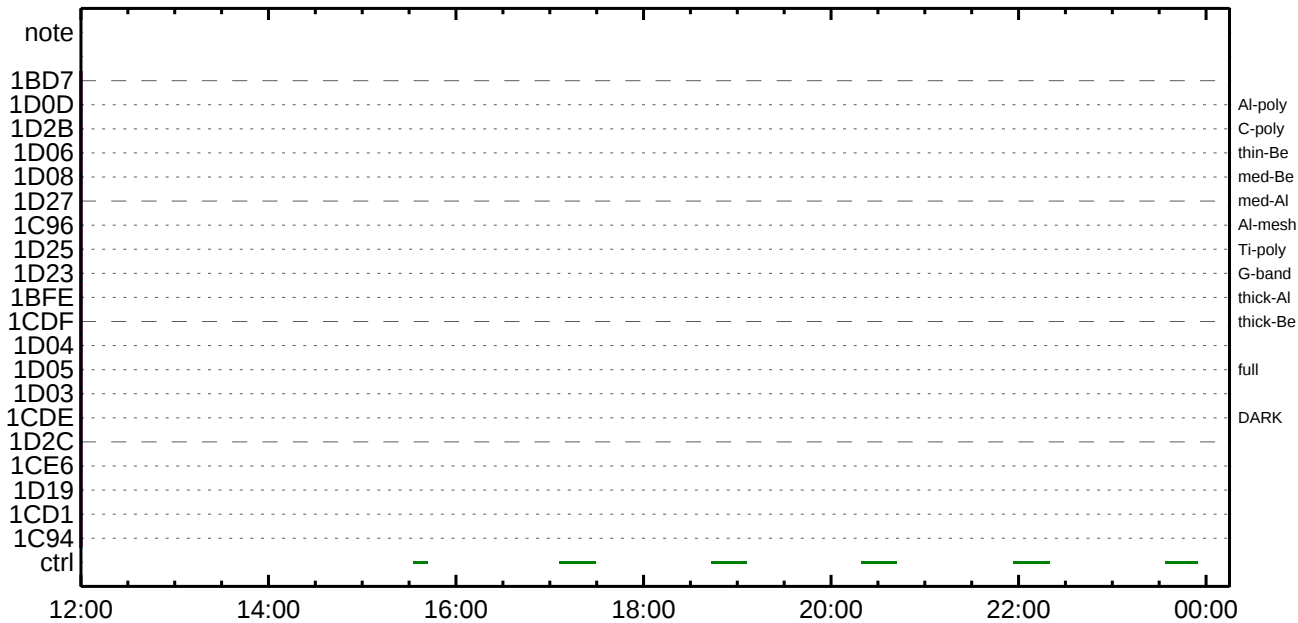
CMDI #0480 2025/03/29



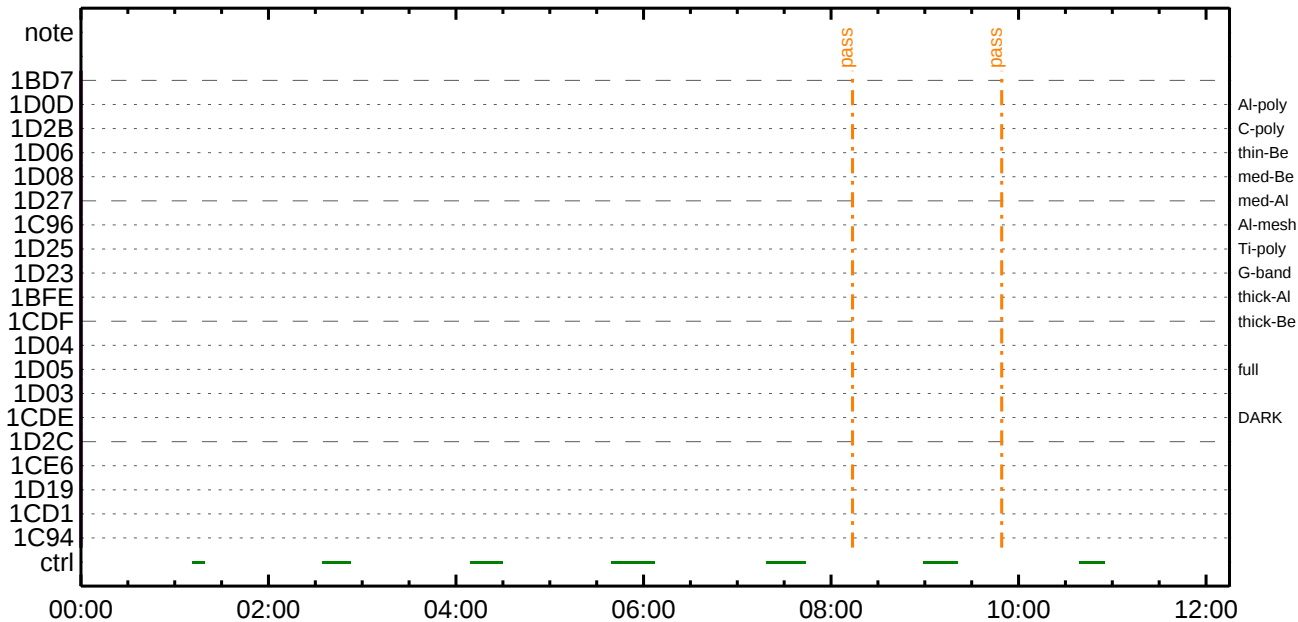
CMDI #0480 2025/03/30



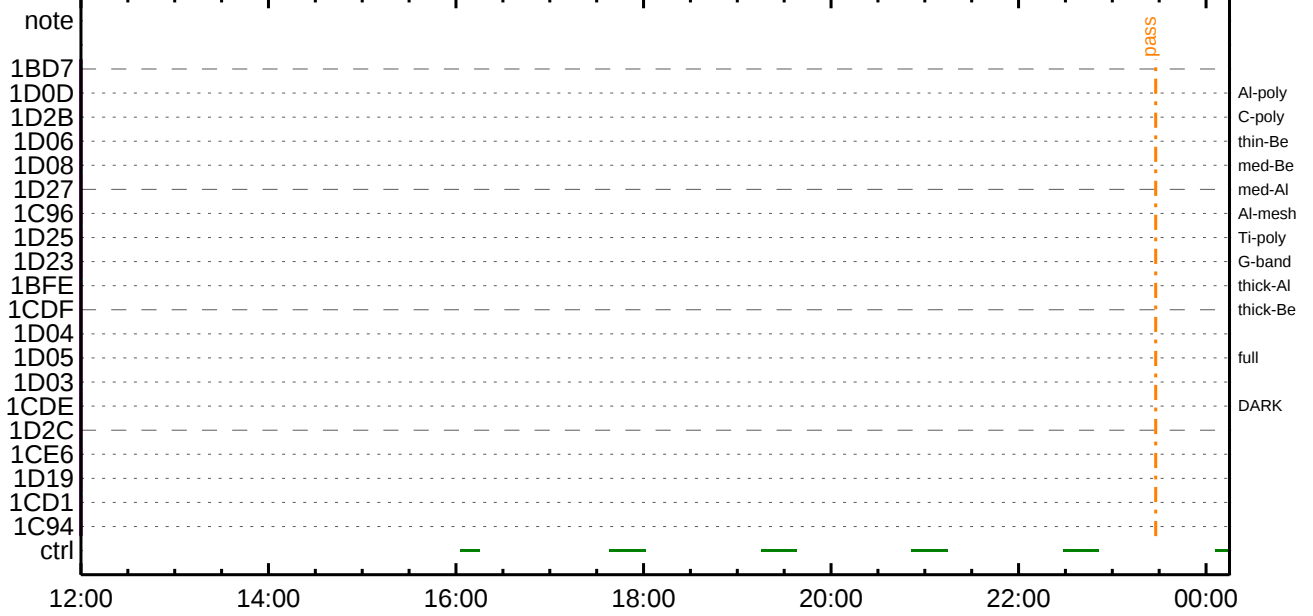
CMDI #0480 2025/03/30



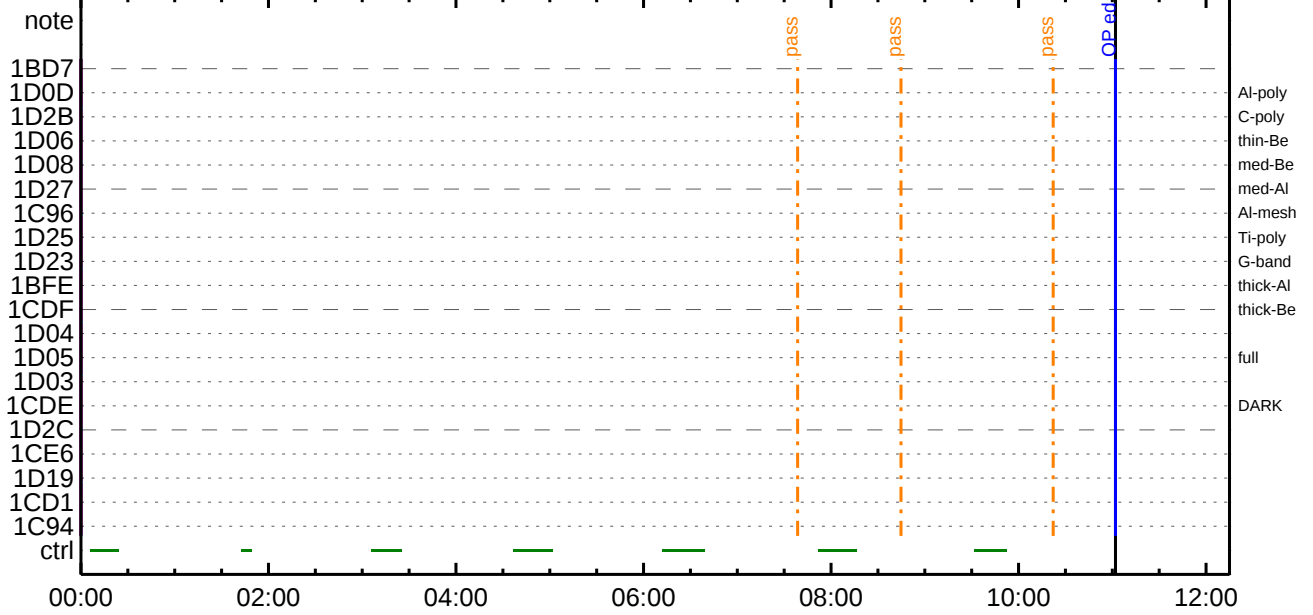
CMDI #0480 2025/03/31



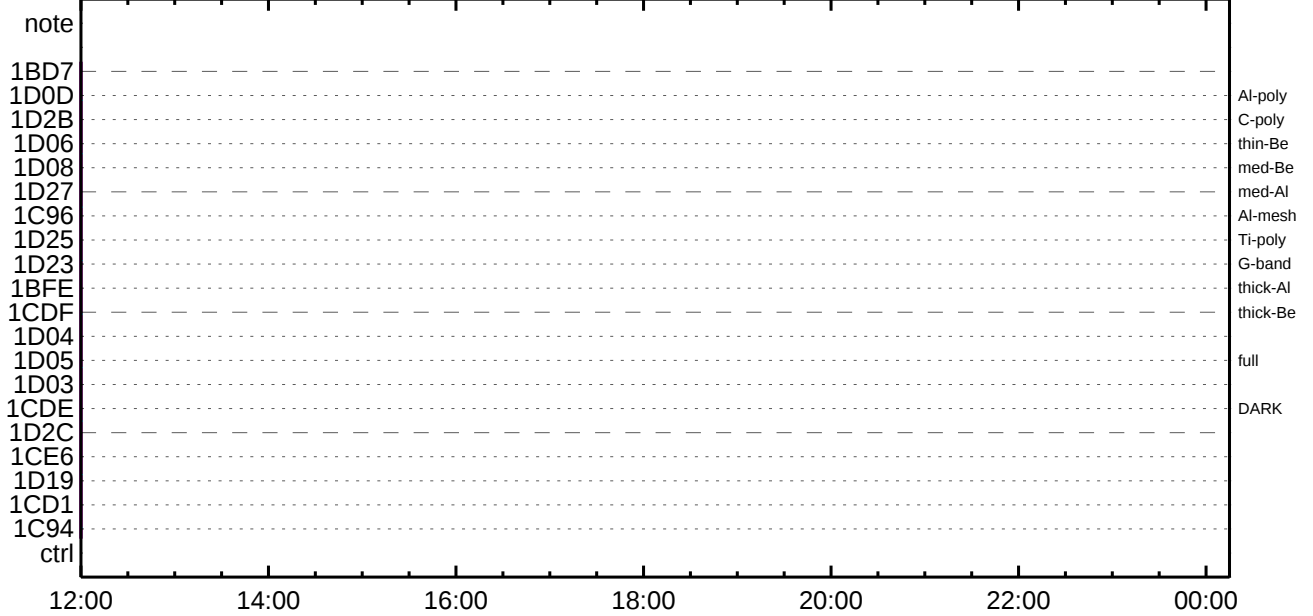
CMDI #0480 2025/03/31



CMDI #0480 2025/04/01



CMDI #0480 2025/04/01



```
main-179 2025-03-27 12:20:30 205 33 SOLAR-B MAIN //
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main-179 2025-03-27 12:20:30 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸPŸóŸÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ëñç²Äñ•µ°E»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËè²µ•íÍË;ËñÉ¼°ÇÖñ•ñç¼í¹çñÍ;çÄ®,ùñ¹ñèñPñÇÁ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÄŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-179:OP
0020 ()
0021 . S. OG og-179:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÎî°èŸÄŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¼Ë¹ç•è²îOKñò³îÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¼Ë¹ç•è²îOKñò³îÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñî¼Ë¹ç•è²îOKñò³îÇ$
0081 C.
0082 . C. ***** °Ë²¼ñî¼Ä´¶Ä°ñËË¬ñ°Ä÷ç® (¼âµ-ŸÄŸóŸ×¼è½çñòÄÓæñÇªñ"ñè¼¹¹çñÇñâ) *****
0083 C. DHUŸä;¼ŸÉ;Ë¼ý½,Ÿí;¼ŸÉ;ËñòÍäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñî¼¹¹ç;ç°Ë²¼ñîTI-CMDÁ÷ç®ñî¼Ä¹Ôñ•ñËñññ³ñË;£
```

0096	C.			
0097	C.			
0098	. C.	TIY³ÝPÝÓÝÉ¤òÄDİ¿ (UT)		
0099	+ . TI	2025-03-27 10:26:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+ . TI	2025-03-27 10:26:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+ . TI	2025-03-27 10:26:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+ . TI	2025-03-27 10:30:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼¤İÄê¾İÍÑ¤İ¥Á¥\$¥V¥~¹àİÜ		
0116	C.	¢¢[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	¢¢[HK1_TI_CMD_NUM]	EQ	4
0118	C.	¢¢[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	¢¢[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	. C.	*****		
0122	C.	TIIÎ°è¥VÖ¥×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+ . DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+ . DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7
0136	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	. C.	¥À¥Ö¥×½ª¹İ»¤ò³İÇ\$		
0142	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	. C.	RAM ID=TI_TBL¤İ¾È¹Ç•ê²ÌOK¤ò³İÇ\$		
0145	C.			
0146	. C.	DHUYâ;¼¥É;Ê¼ý½.¥ì;¼¥È;Ë¤òİá¤¹		
0147	+ . DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	¢¢[HK1_PKT_FORM_NO]	EQ	2
0150	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	*****		
0155	C.	SOT TI command set		
0156	C.	*****		
0157	C.	Execute, after the success of OP upload.		
0158	+ . TI	2025-03-27 10:30:16.0		
0159	DC	07-F0 MDP_SOT_MODE_STBY		
0160	BC	(41)		
0161	. C.	-----		
0162	C.	HK1_TI_CMD_NUM = 1 CNTUP []		
0163	C.	-----		
0164	C.	***** SOT END *****		
0165	C.			
0166	C.	***** XRT START *****		
0167	C.	Execute, after the success of OP upload.		
0168	+ . TI	2025-03-27 10:30:00.0		
0169	DC	07-F0 MDP_XRT_MODE_STBY		
0170	BC	(c3)		
0171	. C.	[] [HK1_TI_CMD_NUM] EQ 1COUNTUP		
0172	C.			
0173	C.	***** XRT END *****		
0174	. C.	Stop EIS observation and temporarily disable EIS mode changes		
0175	C.			
0176	C.			
0177	C.	***** Start EIS operation (TI set) *****		
0178	C.	Execute, after the success of OP upload.		
0179	C.	Set EIS TI-commands		
0180	+ . TI	2025-03-27 10:30:30.0		
0181	DC	07-FC EIS_MODE_MANU		
0182	BC	(21 02)		
0183	+ . TI	2025-03-27 10:30:40.0		
0184	DC	07-FC EIS_MODE_CHG_DIS		
0185	BC	(22)		
0186	. C.	[] [HK1_TI_CMD_NUM] EQ 2 COUNTUP		
0187	C.	***** End EIS operation (TI set) *****		
0188	C.			
0189	C.			
0190	C.			
0191	. C.	**** MDP ´ûÄİ¤İ»ö¾Ý¤EÄD¤¹¤èDCBC•×²è ****		
0192	C.	(¾å°İ¥Ó¥Ä¥E¥P¥E¥ä¥Ç¥è¤E½¾¤¤¼Ä»Ü¤¹¤è)		
0193	. S.	DC-BC dcbc-402:DCBC		

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0194      (MDP_known_event)
0195      C.
0196      C.
0197      . C. ***** 𐄂𐄂𐄂!•İ Daily±ĳİÑ𐄂Ē'Ø𐄂¹𐄂ĕDCBC•x²è *****
0198      . S. DC-BC dcbc-153:DCBC
0199      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200      C.
0201      C.
0202      . C. ĳāLOS𐄂Á𐄂$𐄂Ā𐄂⁻¼Ā»Üĳä
0203      C.
0204      . C. ***** LOS *****
0205      C.
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main-180 2025-03-27 12:20:30 201 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËðçðÄð•µ°E»Í×ÅÇñÎŸçŸÄŸ×Ÿí;¼ŸÉ;ËÈè²µ•íîÊ;ËÈÈ¼°ÇÖð•ðç¼¹¹Çðİ;çÀ®,ùð¹ðèðððÇÄ÷ç®ð•ðÈððð³ðÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝðäLOSððÇçñÎ»p´Öðò¹íî,ð•;çÉÖÍ×ðÈXÄÓONðİ¹ÖðÈðíðÈððð³ðÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÄÖð¬°ÄÄêð•ðççé;ç°Ê²¼ðî°EÄ,¼ê½Çðò¼Ä¹Öð¹ðè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëð•ðçðð¼¹¹Çðİ;ç°Ê²¼ðî¼Ä¹Öð»ð°;çDCBC-150ðØçÊðà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŸçŸóŸEŸEÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãðî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ð¬¼«E°Ää»ßð•ðçç,ä;ç°Ê²¼ðò¼Ä¹Öð¹ðè;£
0055 C. ŸçŸóŸEŸEÄÚÂØðäÄ•Ä°²öÈòð¬¼áðð¼¹¹Çðİ´°î»ð¹ðèðððÇÄÖðÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëð•ðçðð¼¹¹Çðİ;ç°Ê²¼ðî¼Ä¹Öð»ð°;çDCBC-151ðØçÊðà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŸçŸóŸEŸEÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãðî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
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0096 C.  
0097 C.  
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0099 C. Upload the Orbit Element and the Target Attitude  
0100 C. RAM-ID:TARGET_ATT  
0101 . S. RAM ram-150:TARGET_ATT  
0102 ()  
0103 C.  
0104 C.  
0105 C. Set the dump memory area of TARGET_ATT  
0106 +. DC 02-48 AOCU_DUMP_SET  
0107 BC (07 00 00 00 18 00)  
0108 C.  
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0110 C.  
0111 C.  
0112 C. Change the TLMFormatNo for the AOCs Dump Format  
0113 +. DC 01-22 DHU_MODE_CHNG  
0114 BC (04 0b f8)  
0115 C.  
0116 C. Wait for AOCS DUMP to end  
0117 C.  
0118 . C. Check the dump memory  
0119 C.  
0120 C. Result = OK [ ]  
0121 C.  
0122 +. DC 01-22 DHU_MODE_CHNG  
0123 BC (02 0a f8)  
0124 C.  
0125 C. <A_***>[TLM STS] FMT = 2 [ ]  
0126 C.  
0127 +. DC 02-8E AOCU_ORB_UPD  
0128 C.  
0129 . C. *****  
0130 C. SOT table upload  
0131 C. *****  
0132 . C. < Stop SP table >  
0133 +. DC 07-F0 MDP_SP_CTRL_MANU  
0134 BC (61)  
0135 C. -----  
0136 C. MDP_SP_CTRL_MODE = MANU [ ]  
0137 C. -----  
0138 C.  
0139 . C. <Upload SP Observation Table>  
0140 . S. RAM ram-287:MDP_OBS_S  
0141 ()  
0142 C.  
0143 . C. < Dump RAMID=MDP_OBS_S >  
0144 +. DC 07-F0 MDP_DUMP_SPTBL  
0145 BC (83 07 00 00 00 38 b8)  
0146 C. -----  
0147 C. MDP_OBS_S verify = OK/NG [ ]  
0148 C. -----  
0149 C.  
0150 C. *****  
0151 C. SOT TI command set  
0152 C. *****  
0153 C. Execute, after the success of TBL upload.  
0154 +. TI 2025-03-27 10:30:18.0  
0155 DC 07-F0 MDP_SOT_MODE_OBSV  
0156 BC (40)  
0157 C. -----  
0158 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]  
0159 C. -----  
0160 C.  
0161 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0162 +. DC 07-FC EIS_MODE_CHG_ENA  
0163 BC (20)  
0164 . C. Verify EIS_MODE_CHG_FLG is ENA  
0165 +. DC 07-FC EIS_MODE_MANU  
0166 BC (21 02)  
0167 . C. Verify EIS in MANUAL mode  
0168 . C. Estimated OBSTBL upload time is 7s  
0169 C. *****  
0170 C. EIS START OBSTBL LOAD  
0171 C. *****  
0172 . S. RAM ram-820:EIS_OBSTBL  
0173 ()  
0174 +. DC 07-FC EIS_DUMP_OBSTBL  
0175 BC (07 07 07 00 00 70 00)  
0176 C.  
0177 C. Execute, after the success of OBSTBL upload.  
0178 C. Set EIS TI-commands  
0179 +. TI 2025-03-27 10:30:50.0  
0180 DC 07-FC EIS_MODE_CHG_ENA  
0181 BC (20)  
0182 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0183 C. *****  
0184 C. EIS END OBSTBL LOAD  
0185 C. *****  
0186 C.  
0187 . C. ***** MDP 'ũÃîñİ»ö¼Ý¤ÊÂÐ¤¹¤ÈDCBC•×²è *****  
0188 C. (¾å°ì¥Ó¥Ä¥Æ¥Ð¥Ë¥Å¥Ç¥§¤Ê½¤¼¤¼¤¼»Ű¤¹¤ë)  
0189 . S. DC-BC dcbc-402:DCBC  
0190 (MDP_known_event)  
0191 C.  
0192 C.  
0193 . C. ***** ¥Ð¥¹•İ Daily+:İÑ¤Ê'Ø¤¹¤ÈDCBC•×²è *****
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0194 . S. DC-BC dcbc-153:DCBC
0195 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0196 C.
0197 C.
0198 . C. ;ãLOS¥Á¥\$¥Ã¥´¼Â»Ü;ä
0199 C.
0200 . C. ***** LOS *****
0201 C.

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main-181 2025-03-27 12:20:30 100 33 SOLAR-B MAIN //
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main-181 2025-03-27 12:20:32 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0005 C.
0006 C. ¥Ä¥B;¼¥³¥P¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë±¿±Ä±•µ°E»Í×ÄÇ±Í¥ç¥Ä¥×¥í;¼¥É;ËËè½µ•îÍË;Ë±E¼°ÇÔ±•±¿¼i¹¿±İ;çÀ®,ù±¹±è±P±ÇÁ÷¿®±•±Ë±±±³±Ë;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG_____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR _____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 08 08)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 85 83 06 06)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 08 08)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 +. DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 +. DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 +. DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 +. DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 0c)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 0e)
0072 . C. ----- Success Verify ? OK / NG _____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2025-03-27 10:30:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG _____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP ´ûÃÎ±Í»ö¼Ý±ËÂÐ±¹±èDCBC•×²è *****
0087 C. (¼å°i¥Ó¥Ä¥Ë¥P¥P¥P¥Ä¥ç¥É±ËË¼¼±¼Ä»Ü±¹±è)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** ¥P¥¹•İ Daily±¿íN±Ë´Ø±¹±èDCBC•×²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.

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0096 C.
0097 . C. ; ãLOS¥Á¥\$¥Ã¥~¼Ã»Ü; ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

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*** OP Sequence for XRT ***

2025/03/27	10:40:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	10:40:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	10:40:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/03/27	10:41:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 03 64 01 ca				
2025/03/27	10:41:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/03/27	10:41:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/03/27	10:41:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/03/27	10:41:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/03/27	10:41:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/27	10:43:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2025/03/27	10:43:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/03/27	11:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	11:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	11:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/27	11:48:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/03/27	11:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/03/27	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	15:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	15:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/27	15:32:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/03/27	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/03/27	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	17:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	17:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/27	17:06:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/03/27	17:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/03/27	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	18:00:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/03/27	18:00:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/03/27	18:00:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/03/27	18:00:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/03/27	18:00:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/03/27	18:03:28.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2025/03/27	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/03/27	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	18:10:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/03/27	18:10:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/03/27	18:10:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 03 64 01 ca				
2025/03/27	18:10:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/03/27	18:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/03/27	18:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/03/27	18:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/03/27	18:10:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/03/27	18:13:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2025/03/27	18:13:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							

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2025/03/27	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/03/27	18:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	18:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	18:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/03/27	18:43:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/03/27	18:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/03/27	19:06:30.0	XRT_Custom_430_OG [0x1ae]						
2025/03/27	19:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/03/27	20:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	20:19:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2025/03/27	20:20:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 03 64 01	ca	
2025/03/27	20:20:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	20:20:02.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	20:20:04.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/03/27	20:20:06.5	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/03/27	20:20:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/03/27	20:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/03/27	20:20:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2025/03/27	20:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/03/27	20:20:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/03/27	20:22:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2025/03/27	20:22:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e	
2025/03/27	20:23:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/03/27	20:43:00.0	XRT_Custom_430_OG [0x1ae]						
2025/03/27	20:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/03/27	21:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	21:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	21:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/03/27	21:57:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/03/27	22:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/03/27	22:20:00.0	XRT_Custom_430_OG [0x1ae]						
2025/03/27	22:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/03/27	23:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	23:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/27	23:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/03/27	23:34:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/03/27	23:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/03/27	23:55:00.0	XRT_Custom_430_OG [0x1ae]						
2025/03/27	23:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/03/28	00:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/03/28	00:14:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2025/03/28	00:15:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 03 64 01	ca	
2025/03/28	00:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/03/28	00:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/03/28	00:15:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2025/03/28	00:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/03/28	00:15:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/03/28	00:17:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	

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2025/03/28	00:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0e	
2025/03/28	00:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/03/28	01:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	01:11:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	01:11:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/03/28	01:11:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/03/28	01:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/03/28	01:19:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/28	01:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/03/28	02:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	02:35:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	02:35:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/03/28	02:35:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/03/28	02:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/03/28	02:53:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/28	02:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/03/28	03:44:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	03:44:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/03/28	03:45:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2025/03/28	03:45:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2025/03/28	03:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2025/03/28	03:45:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0 d0	
2025/03/28	03:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2025/03/28	03:45:24.0	XRT_FLD_RESET_446_OG [0x1be]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/03/28	03:47:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 02	
2025/03/28	03:47:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0e	
2025/03/28	03:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/03/28	04:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	04:09:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	04:09:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/03/28	04:09:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/03/28	04:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/03/28	04:30:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/28	04:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/03/28	05:39:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	05:39:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	05:39:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/03/28	05:39:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/03/28	05:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/03/28	06:07:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/28	06:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/03/28	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	06:16:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/03/28	06:16:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/03/28	06:16:30.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2025/03/28	06:16:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2025/03/28	06:16:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2025/03/28	06:16:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2025/03/28	06:19:28.0	XRT_QT_PROG_SET_420_OG [0x1a4]			

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2025/03/28	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	06:26:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	06:26:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/03/28	06:26:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 03 64 01 ca	
2025/03/28	06:26:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/03/28	06:26:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/03/28	06:26:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/03/28	06:26:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/28	06:26:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	06:29:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2025/03/28	06:29:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/03/28	07:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	07:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	07:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	07:19:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/28	07:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/28	08:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	08:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	08:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	08:59:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/28	09:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/28	10:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	10:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	10:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	10:39:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/28	10:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/28	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/03/28	11:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 55 26 01 ca	
2025/03/28	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/03/28	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/03/28	11:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/03/28	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/28	11:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	11:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2025/03/28	11:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/03/28	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	16:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	16:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	16:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	16:03:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/28	16:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/28	16:16:00.0	XRT_Custom_430_OG [0x1ae]					
2025/03/28	16:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					

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2025/03/28	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	16:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	17:00:00.0	AOCs_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/03/28	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 64 01 ca	
2025/03/28	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/03/28	17:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/03/28	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/03/28	17:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/28	17:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	17:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2025/03/28	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/03/28	17:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	17:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	17:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	17:38:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	17:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/28	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/28	18:01:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	18:01:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	18:02:00.0	AOCs_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/03/28	18:02:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/03/28	18:02:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/03/28	18:02:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/03/28	18:04:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/28	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2025/03/28	18:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	18:11:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	18:11:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	18:12:00.0	AOCs_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/03/28	18:12:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 64 01 ca	
2025/03/28	18:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/03/28	18:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/03/28	18:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/03/28	18:12:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/03/28	18:14:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	18:14:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2025/03/28	18:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/03/28	19:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	19:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	19:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	19:15:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/03/28	19:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/03/28	19:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/03/28	19:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_Custom_430_OG	1	07-F0	c0	
2025/03/28	20:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/03/28	20:52:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/03/28	20:52:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2025/03/28	20:52:06.0	MDP_XRT_FLD_RESET	1	07-F0	da
		XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/28	20:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/28	21:15:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/28	21:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/28	22:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/28	22:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/28	22:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/28	22:29:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/28	22:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/28	22:52:00.0	XRT_Custom_430_OG [0x1ae]			
2025/03/28	22:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/28	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/28	23:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	00:00:00.0	AOCs_ORe-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76	03 03 64 01 ca
2025/03/29	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/29	00:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/29	00:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/29	00:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	00:00:24.0	XRT_FLD_RESET_446_OG [0x1be]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	00:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2025/03/29	00:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/29	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	00:06:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	00:06:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	00:06:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	00:06:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	00:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	00:24:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/29	00:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	01:43:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	01:43:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	01:43:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	01:50:00.5	XRT_Custom_430_OG [0x1ae]			
2025/03/29	01:51:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	03:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	03:05:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	03:05:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	03:05:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	03:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	03:25:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/29	03:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	03:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	03:29:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	03:30:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/03/29	03:30:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/29	03:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/29	03:30:20.0	XRT_AEC_RESET_448_OG [0x1c0]			

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2025/03/29	03:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/29	03:30:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	03:32:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	03:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/03/29	03:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/29	04:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	04:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	04:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	04:36:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	04:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	05:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	05:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/03/29	05:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	05:45:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	05:45:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	05:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	05:46:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2025/03/29	05:46:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/29	05:46:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/29	05:48:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	05:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2025/03/29	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	05:55:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	05:55:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	05:56:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/03/29	05:56:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 64 01 ca
2025/03/29	05:56:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/29	05:56:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/29	05:56:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/29	05:56:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	05:58:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	05:58:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/03/29	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/29	06:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	06:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	06:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	06:12:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	06:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	06:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	06:41:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/03/29	07:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	07:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	07:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	07:52:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	07:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	08:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	08:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/03/29	09:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/03/29	09:11:14.0	XRT_CTRL_MANU_402_OG [0x192]	5	02-F6	00 00 00 00 00
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	09:11:16.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	09:11:18.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	09:11:38.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/29	09:11:40.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/29	09:11:42.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	09:14:18.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2025/03/29	09:14:20.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	09:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	09:32:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	09:32:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	09:32:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	09:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	09:53:00.0	XRT_Custom_430_OG [0x1ae]			
2025/03/29	09:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	09:55:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	09:55:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	09:55:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	09:56:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/29	09:56:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/29	09:56:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	09:58:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2025/03/29	09:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	10:38:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	10:38:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	10:38:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	10:38:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/29	10:38:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/29	10:38:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	10:41:28.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2025/03/29	10:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	11:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	11:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	11:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	11:13:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/03/29	11:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/03/29	11:24:30.0	XRT_Custom_430_OG [0x1ae]			
2025/03/29	11:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	12:06:04.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	12:06:06.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	12:06:08.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	12:06:28.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/29	12:06:30.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/29	12:06:32.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	12:09:08.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2025/03/29	12:09:10.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/03/29	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	12:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/03/29	12:30:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/03/29	12:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/03/29	12:30:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	12:32:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2025/03/29	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	12:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/03/29	12:40:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 64 01 ca
2025/03/29	12:40:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/03/29	12:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/03/29	12:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/03/29	12:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/03/29	12:40:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/03/29	12:42:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/03/29	12:42:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/03/29	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/03/29	14:24:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/03/29	14:25:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00