XRT Timeline to be uploaded on 2025/04/01

Period: 2025/04/01 11:02:00 - 2025/04/05 12:06:00

* * * * * **Normal mode** * * * * *

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/01 11:25:00 - 04/01 17:45:54	Track (-685.9, -226.3) @ 04/01 11:12:00	# OP start + 10min, AR 14048
04/01 17:59:00 - 04/01 18:55:00	Track (426.7, 291.8) @ 04/01 17:56:00	HOP497 RS AR burst
04/02 11:03:00 - 04/02 17:59:54	Track (-523.4, -211.9) @ 04/02 11:00:00	AR 14048

PROG= 11 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 4-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 77 4-time(s) 300.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1D08: Synoptic for HOP448 w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4

Term	Pointing (x, y)	Comment
04/01 17:49:00 - 04/01 17:55:54	Fixed (0.0, 0.0)	synoptic, shifted -14.0 min
04/02 04:22:06 - 04/02 04:29:54	Fixed (0.0, 0.0)	synoptic+HOP349
04/02 18:03:00 - 04/02 18:09:54	Fixed (0.0, 0.0)	synoptic
04/03 06:18:06 - 04/03 06:25:24	Fixed (0.0, 0.0)	synoptic + HOP349

PROG= 16 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 55 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 98 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 76 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 41 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 86 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1BA9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/01 18:58:06 - 04/01 19:47:00	Track (426.7, 291.8) @ 04/01 17:56:00	HOP497 RS AR burst

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 8-time(s) 30.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

XOB #1D23: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 1800s cad (G-band/Leak last)

Term	Pointing (x, y)	Comment
04/01 20:11:00 - 04/02 02:29:54	Fixed (900.0, 227.0)	HOP504 pole-to-pole
04/02 18:13:00 - 04/02 23:59:54	Track (-468.9, -208.5) @ 04/02 18:10:00	AR 14048
04/03 06:28:30 - 04/03 11:28:00	Track (-370.9, -203.7) @ 04/03 06:25:30	AR 14048

PROG= 12 Inf.-time(s)

Subr= 1 8-time(s) 1800.0sec

Seqn= 8 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 30 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

XOB #1CD1: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[12/181/1443], thin-Be[24/512/3897] with 512x512 G-band+Leak - 375min cad) + CME w

Term	Pointing (x, y)	Comment
04/02 02:33:00 - 04/02 04:19:00	Fixed (0.0, 0.0)	synoptic+HOP349
04/03 04:30:00 - 04/03 06:15:00	Fixed (0.0, 0.0)	synoptic + HOP349

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 55 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 79 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 1500.0sec

Seqn= 8 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 74 1-time(s) 2.0sec

med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval

XOB #1D2D: HOP476_SOOP_AR_Be_Thin - (Med-Be) - Med-Be (Al-Thick) - AEC3 - PFB - Be-Thick- Be-Med 384X384 - AEC3 - 90s cad

Term	Pointing (x, y)	Comment

04/02 04:33:00 - 04/02 10:59:54 Track (563.0, 278.7) @ 04/02 04:30:00 HOP500 flare watch

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 180-time(s) 90.0sec

Seqn= 3 1-time(s) 2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2	2.0sec

Seqn= 39 1-time(s) 2.0sec

med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1	2.0sec

Seqn= 80 1-time(s) 2.0sec

med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1C8D: Alignment with North Pole Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 5min cad

Term	Pointing (x, y)	Comment
04/03 00:15:00 - 04/03 01:59:54	Fixed (0.0, 930.0)	coalignment Nlimb

PROG= 06 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 23 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 24-time(s) 300.0sec

Seqn= 69 1-time(s) 2.0sec

Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x1536 (1024, 768)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1C8E: Alignment with East limb Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 8 min cad

Term	Pointing (x, y)	Comment
04/03 02:15:00 - 04/03 03:59:54	Fixed (-970.0, 0.0)	coalignment Elimb

PROG= 07 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 23 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 480.0sec

Seqn= 70 1-time(s) 2.0sec

Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1536x2048 (1280, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
04/01 11:25:00 - 04/01 17:45:54	Track (-685.9, -226.3) @ 04/01 11:12:00	# OP start + 10min, AR 14048
04/01 17:59:00 - 04/01 18:55:00	Track (426.7, 291.8) @ 04/01 17:56:00	HOP497 RS AR burst
04/01 18:58:06 - 04/01 19:47:00	Track (426.7, 291.8) @ 04/01 17:56:00	HOP497 RS AR burst
04/01 20:11:00 - 04/02 02:29:54	Fixed (900.0, 227.0)	HOP504 pole-to-pole
04/02 02:33:00 - 04/02 04:19:00	Fixed (0.0, 0.0)	synoptic+HOP349
04/02 11:03:00 - 04/02 17:59:54	Track (-523.4, -211.9) @ 04/02 11:00:00	AR 14048
04/02 18:13:00 - 04/02 23:59:54	Track (-468.9, -208.5) @ 04/02 18:10:00	AR 14048
04/03 04:30:00 - 04/03 06:15:00	Fixed (0.0, 0.0)	synoptic + HOP349
04/03 06:28:30 - 04/03 11:28:00	Track (-370.9, -203.7) @ 04/03 06:25:30	AR 14048

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D26: HOP500_SOOP_FLARE_MODE_Be_Thick_6s_Fixed_Exposure

Term	Pointing (x, y)	Comment
04/02 04:33:00 - 04/02 10:59:54	Track (563.0, 278.7) @ 04/02 04:30:00	HOP500 flare watch

PROG= 17 30-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 87 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Subr= 2 120-time(s) 2.0sec

Seqn= 44 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec

Seqn= 32 10-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec

Active Region Search

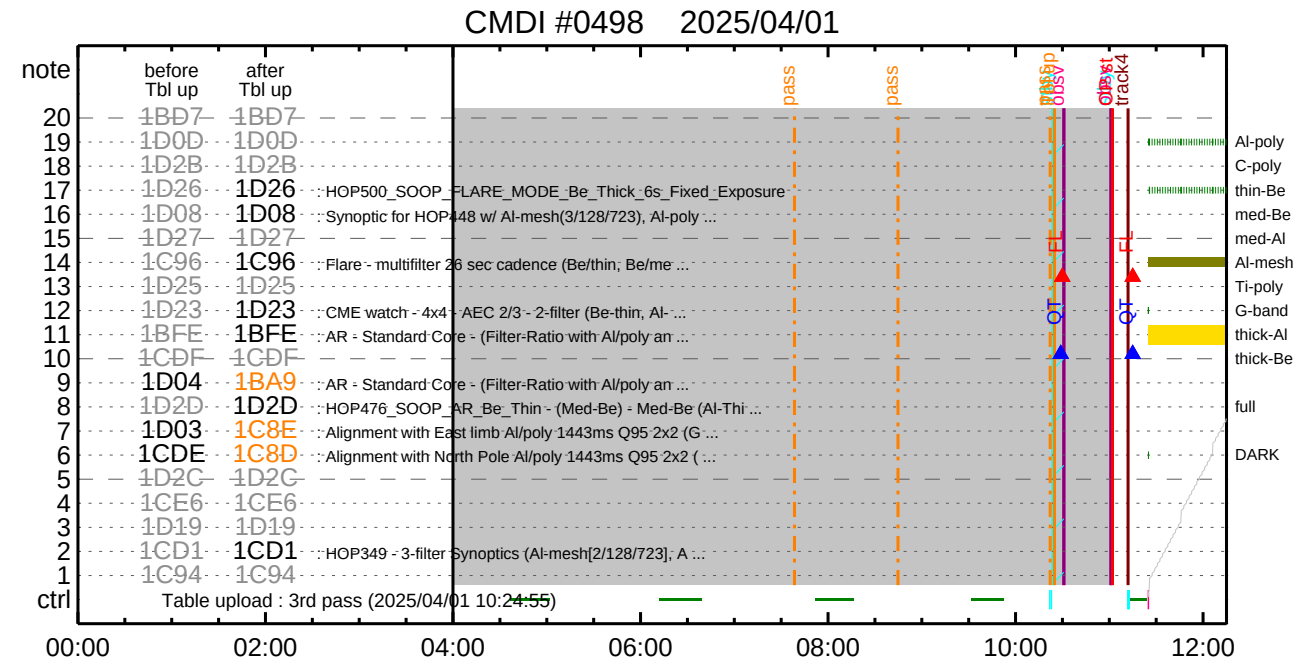
NOT USED

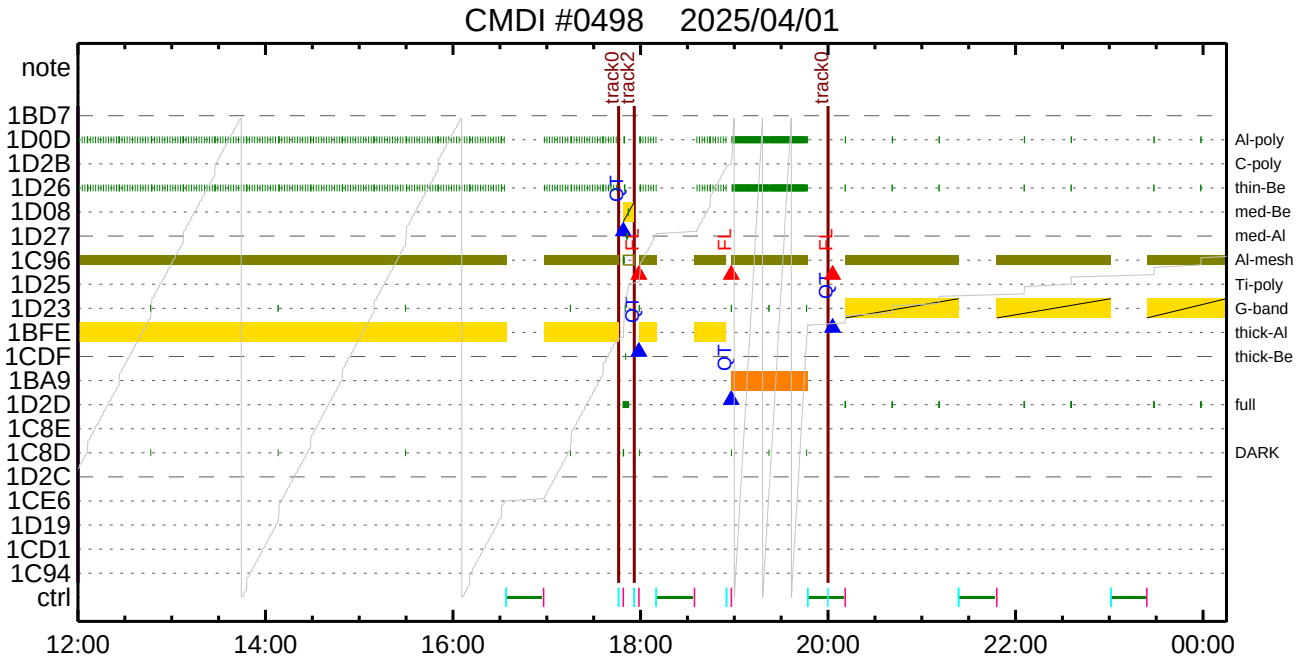
Flare Detection

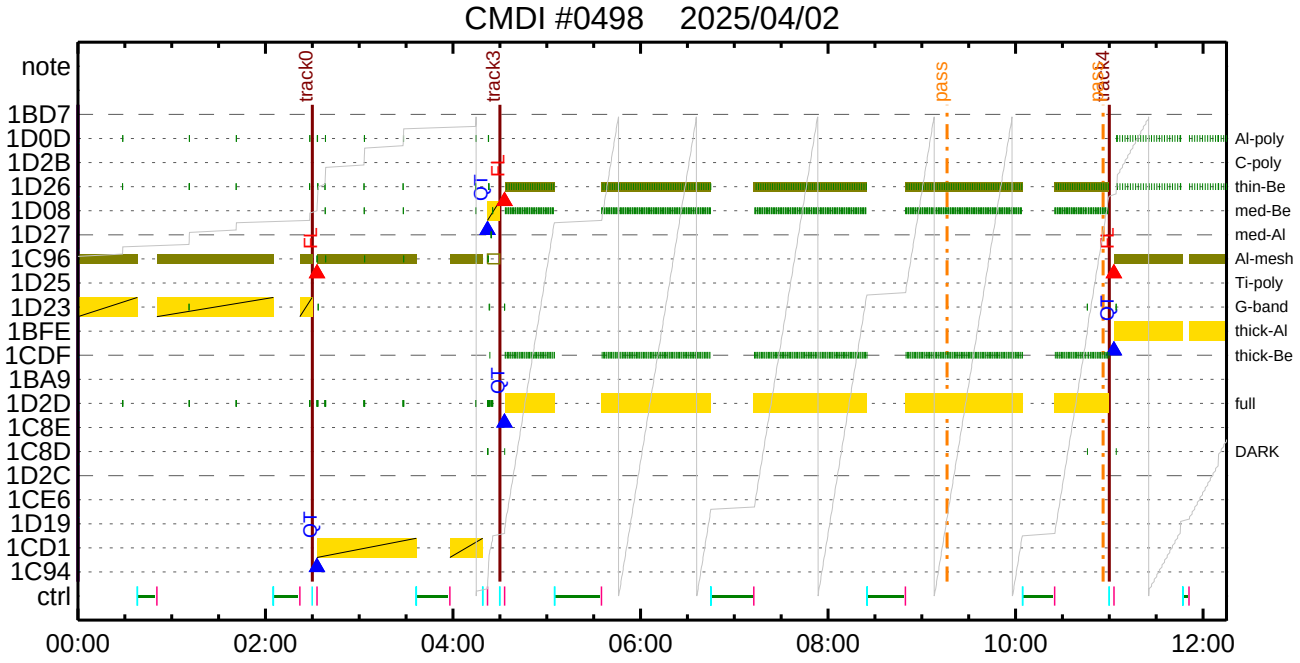
FLD Patrol

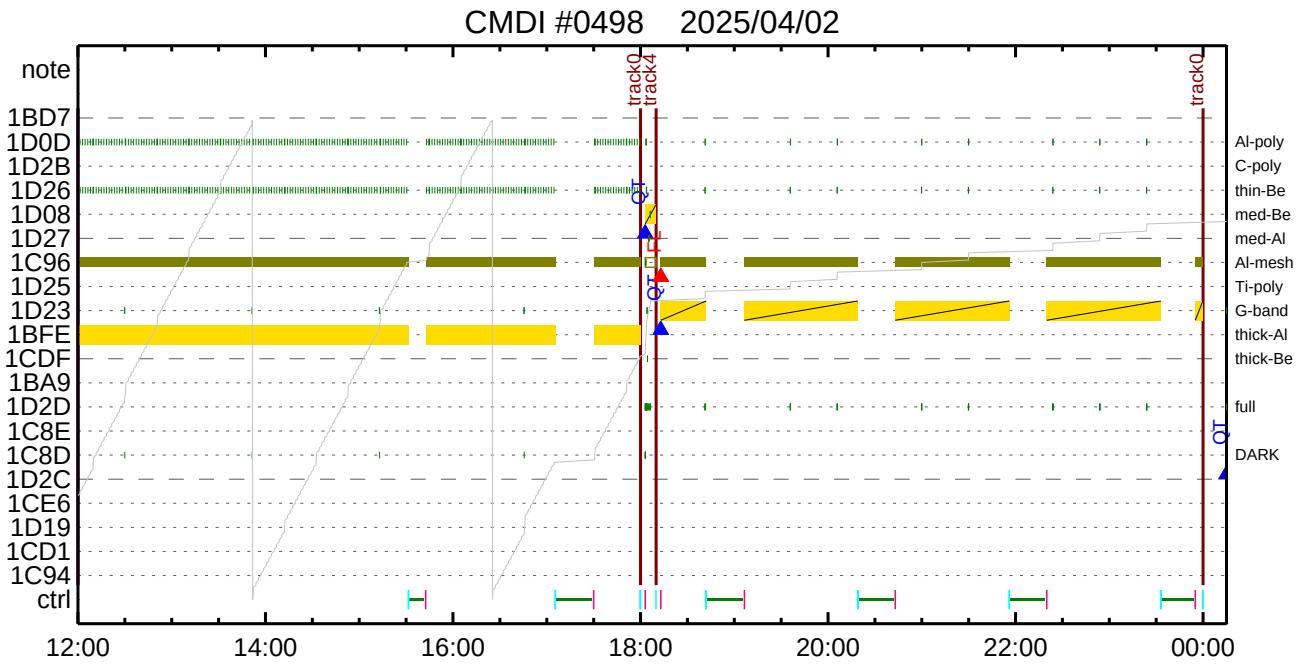
Term	Pointing (x, y)	Comment
04/01 10:25:55 - 04/01 17:46:18	cannot be identified	
04/01 17:56:18 - 04/02 04:19:24	Track (426.7, 291.8) @ 04/01 17:56:00	HOP497 RS AR burst
04/02 04:30:18 - 04/02 18:00:18	Track (563.0, 278.7) @ 04/02 04:30:00	HOP500 flare watch
04/02 18:10:16 - 04/03 00:00:18	Track (-468.9, -208.5) @ 04/02 18:10:00	AR 14048
04/03 04:00:16 - 04/03 06:15:24	Fixed (0.0, 0.0)	synoptic + HOP349
04/03 06:25:46 - 04/05 12:06:00	Track (-370.9, -203.7) @ 04/03 06:25:30	AR 14048

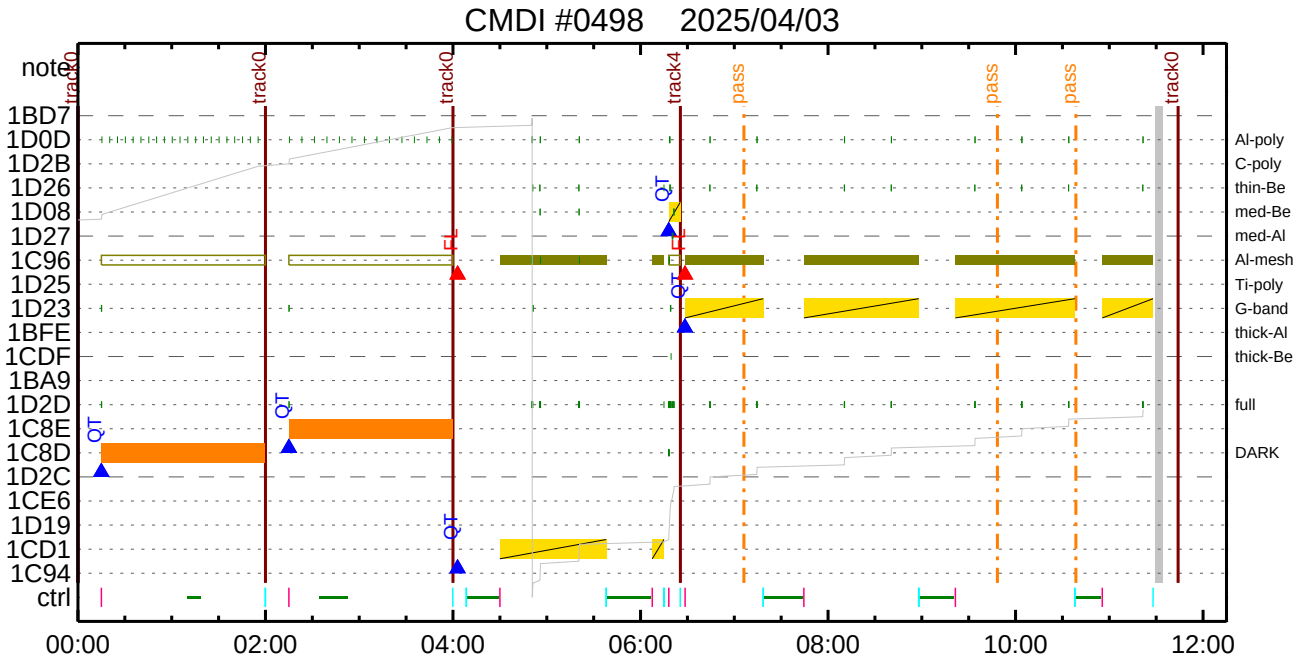
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec

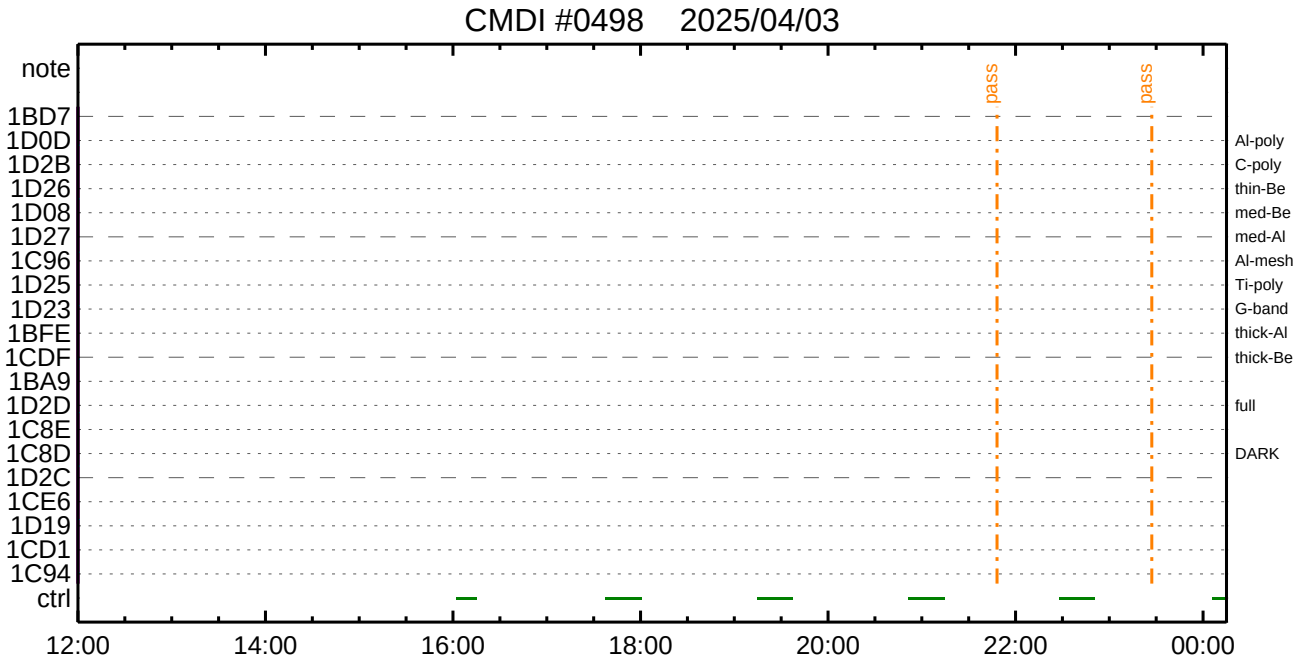

CMDI #0498 2025/04/01
note
before Tbl up
after Tbl up
20 1BD7 1BD7
19 1D0D 1D0D
18 1D2B 1D2B
17 1D26 1D26 : HOP500_SOOP_FLARE_MODE_Be_Thick_6s_Fixed_Exposure
16 1D08 1D08 : Synoptic for HOP448 w/ Al-mesh(3/128/723), Al-poly ...
15 1D27 1D27
14 1C96 1C96 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
13 1D25 1D25
12 1D23 1D23 : CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al- ...
11 1BFE 1BFE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
10 1CDF 1CDF
9 1D04 1BA9 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
8 1D2D 1D2D : HOP476_SOOP_AR_Be_Thin - (Med-Be) - Med-Be (Al-Thi ...
7 1D03 1C8E : Alignment with East limb Al/poly 1443ms Q95 2x2 (G ...
6 1CDE 1C8D : Alignment with North Pole Al/poly 1443ms Q95 2x2 (...
5 1D2C 1D2C
4 1CE6 1CE6
3 1D19 1D19
2 1CD1 1CD1 : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
1 1C94 1C94
ctrl Table upload : 3rd pass (2025/04/01 10:24:55)
pass
pass
track4
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

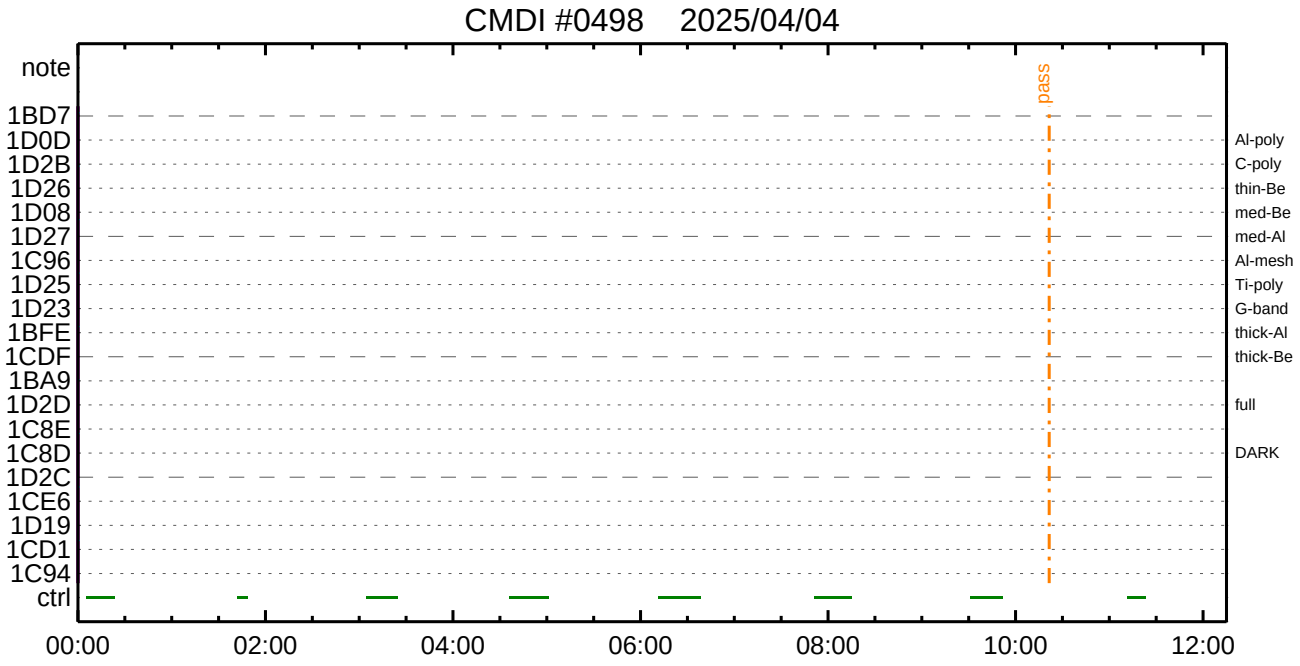

CMDI #0498 2025/04/01
note
track0
track2
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

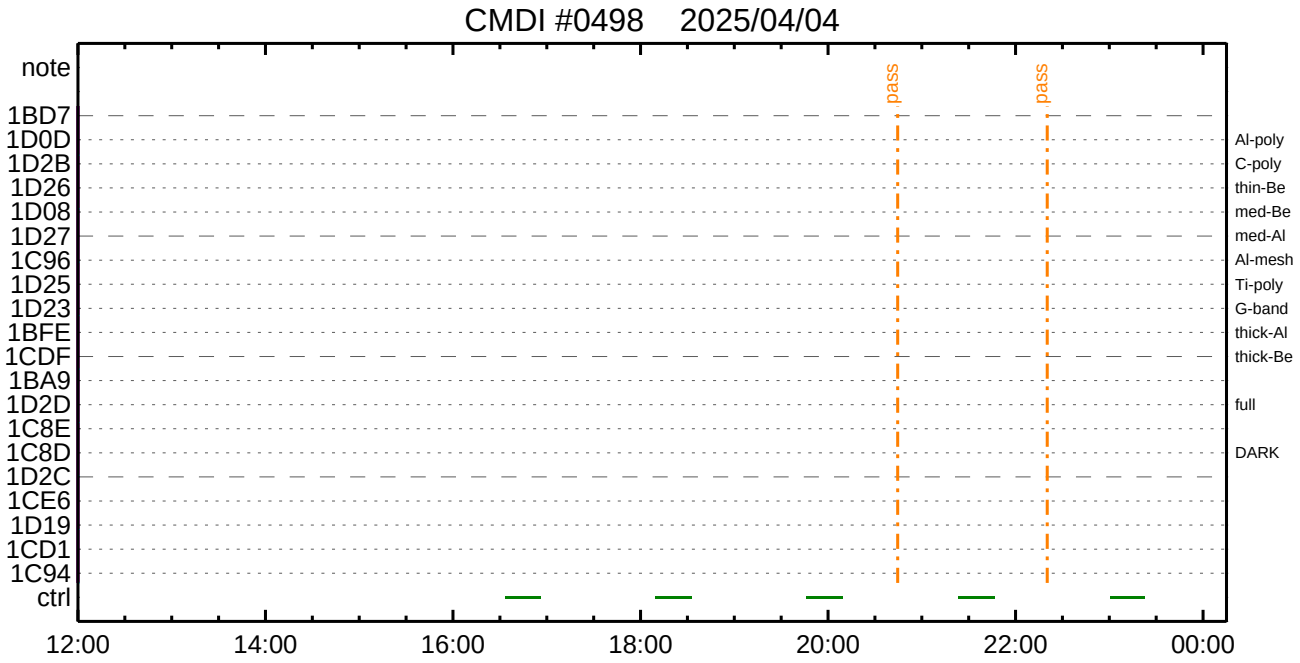

CMDI #0498 2025/04/02
note
track0
track3
pass
track4
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

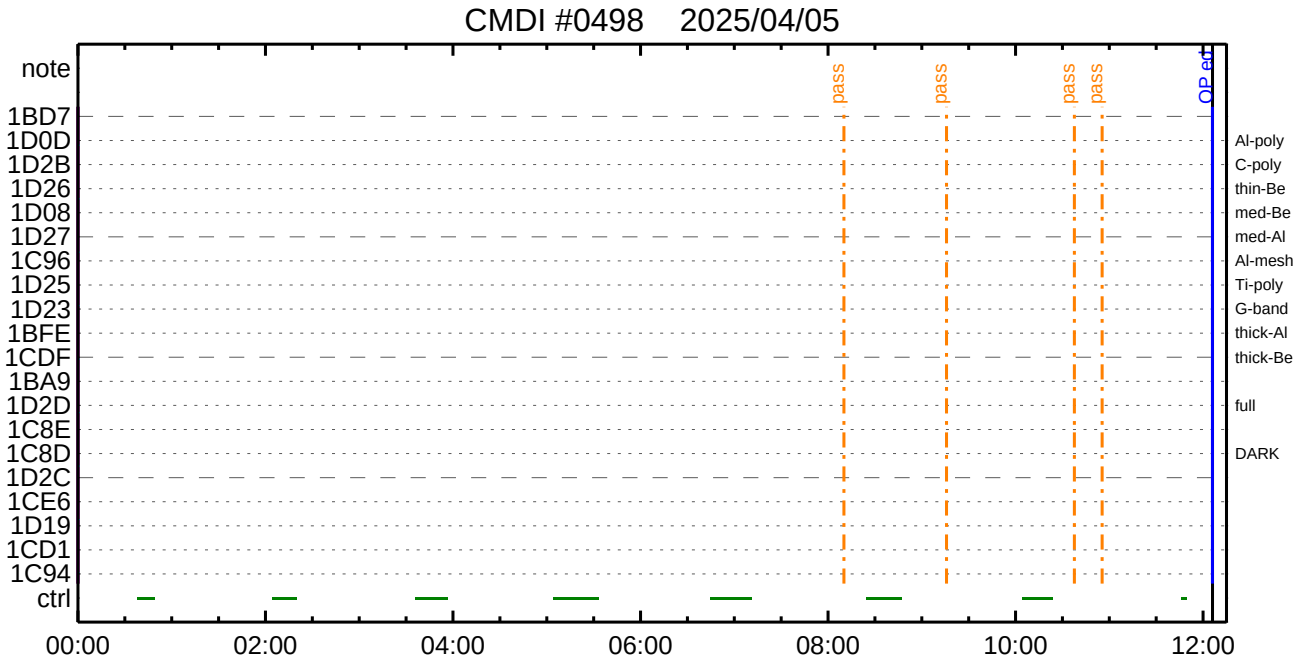

CMDI #0498 2025/04/02
note
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
track0
track4
track0
QT
FL
QT
QT
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK

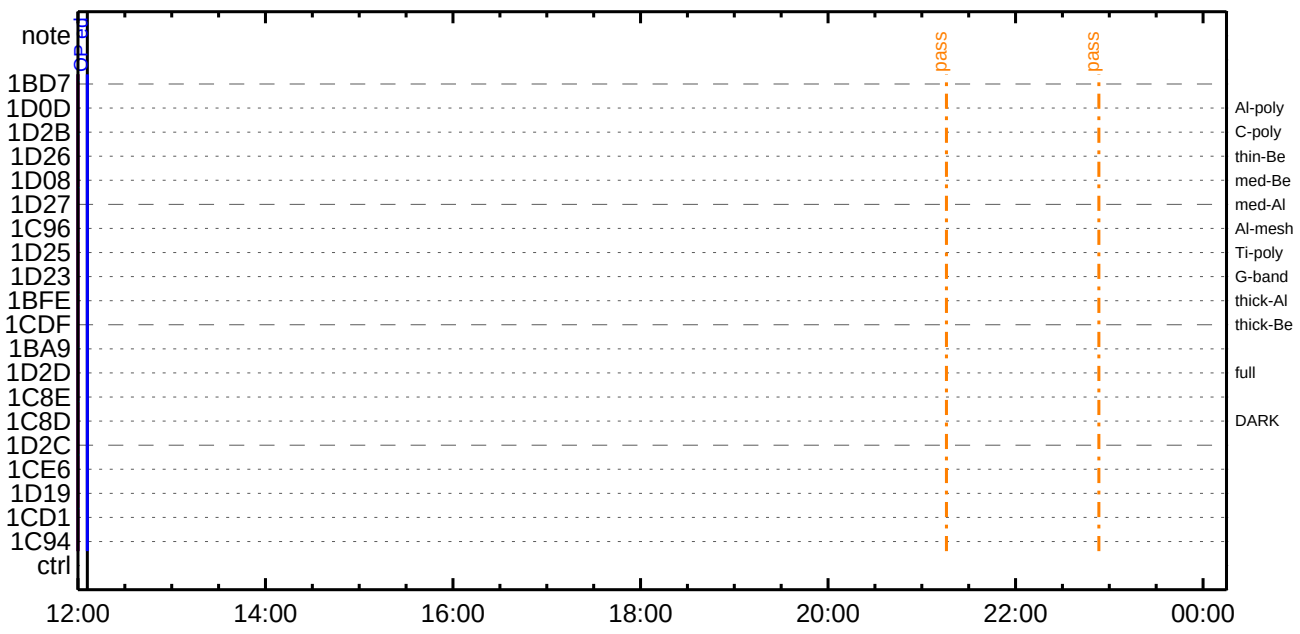

CMDI #0498 2025/04/03
note
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
track0
track0
track0
track4
pass
pass
pass
track0
QT
QT
FL
QT
FL
QT
QT
00:00
02:00
04:00
06:00
08:00
10:00
12:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0498 2025/04/03
note
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
pass
12:00
14:00
16:00
18:00
20:00
22:00
00:00
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK


CMDI #0498 2025/04/04
note
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0498 2025/04/04
note
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0498 2025/04/05
note
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0498 2025/04/05
note
OP on
pass
pass
1BD7
1D0D
1D2B
1D26
1D08
1D27
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

(a) Spacecraft Operation Procedure (real-commands)

```
main-191 2025-04-01 14:21:19 194 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP     op-191:OP
0020     ()
0021   . S. OG     og-191:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC        (20 00 7f 01 02)
0028     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC        (07 0b f8)
0035     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC        (20 80 7f 01 02)
0047     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC        (07 0b f8)
0054     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC        (21 00 41 01 02)
0066     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC        (07 0b f8)
0073     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ¬¥À¥ó¥×½ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC        (02 0a f8)
0086     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2025-04-01 10:57:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2025-04-01 10:57:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2025-04-01 10:57:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2025-04-01 11:01:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154   . C. Stop EIS observation and temporarily disable EIS mode changes
0155     C.
0156     C.
0157     C. ********** Start EIS operation (TI set) **********
0158     C. Execute, after the success of OP upload.
0159     C. Set EIS TI-commands
0160   +. TI 2025-04-01 11:01:30.0
0161     DC 07-FC EIS_MODE_MANU
0162     BC       (21 02)
0163   +. TI 2025-04-01 11:01:40.0
0164     DC 07-FC EIS_MODE_CHG_DIS
0165     BC       (22)
0166   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0167     C. *********** End EIS operation (TI set) ***********
0168     C.
0169     C.
0170     C.
0171     C. ************ XRT START ***********
0172     C. Execute, after the success of OP upload.
0173   +. TI 2025-04-01 11:01:00.0
0174     DC 07-F0 MDP_XRT_MODE_STBY
0175     BC       (c3)
0176   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0177     C.
0178     C. ************ XRT END ***********
0179     C.
0180   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0181     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0182   . S. DC-BC dcbc-402:DCBC
0183     (MDP_known_event)
0184     C.
0185     C.
0186   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0187   . S. DC-BC dcbc-153:DCBC
0188     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189     C.
0190     C.
0191   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0192     C.
0193   . C. ********** LOS **********
```

0194 C.

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(a) Spacecraft Operation Procedure (real-commands)

main-192 2025-04-01 14:21:19 178 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. ***** AOCS Commands (Orbital Element Update) *****
0130     C. Update the orbital element
0131   +. DC 02-50 AOCU_ORB_PRPGT_START
0132     BC       (16)
0133   +  DC 02-8E AOCU_ORB_UPD
0134     C.
0135     C.       <A_ORB>[ORBIT] EPC = 5316198.1 +- 1.0 (s) [ ]
0136     C.
0137    . C.
0138    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139   +. DC 07-FC EIS_MODE_CHG_ENA
0140     BC       (20)
0141    . C. Verify EIS_MODE_CHG_FLG is ENA
0142   +. DC 07-FC EIS_MODE_MANU
0143     BC       (21 02)
0144    . C. Verify EIS in MANUAL mode
0145    . C. Estimated OBSTBL upload time is 32s
0146     C. ********************************
0147     C. EIS START OBSTBL LOAD
0148     C. ********************************
0149    . S. RAM   ram-820:EIS_OBSTBL
0150     ()
0151   +. DC 07-FC EIS_DUMP_OBSTBL
0152     BC       (07 07 07 00 00 70 00)
0153     C.
0154     C. Execute, after the success of OBSTBL upload.
0155     C. Set EIS TI-commands
0156   +. TI 2025-04-01 11:01:50.0
0157     DC 07-FC EIS_MODE_CHG_ENA
0158     BC       (20)
0159    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0160     C. ********************************
0161     C. EIS END OBSTBL LOAD
0162     C. ********************************
0163     C.
0164    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0165     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0166    . S. DC-BC dcbc-402:DCBC
0167     (MDP_known_event)
0168     C.
0169     C.
0170    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0171    . S. DC-BC dcbc-153:DCBC
0172     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173     C.
0174     C.
0175    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0176     C.
0177    . C. ********** LOS **********
0178     C.
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(a) Spacecraft Operation Procedure (real-commands)

main-193 2025-04-01 14:21:19 190 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
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0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +. DC 07-F0 MDP_XRT_CTRL_MANU
0104     BC       (c1)
0105  +  DC 07-F0 MDP_XRT_MODE_STBY
0106     BC       (c3)
0107   . C. ------------ Success Verify ?              OK / NG____
0108     C.
0109     C. XRT Obs. Table Upload
0110   . S. RAM   ram-291:MDP_OBS_X
0111     ()
0112     C.
0113  +. DC 07-F0 MDP_DUMP_XRTTBL
0114     BC       (84 07 00 00 00 3a d4)
0115   . C. ------------ Comparison Check ?                        OK / ERR ____
0116     C.
0117     C.
0118  +. DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 01 b1 b1 04 04)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 02 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 03 b1 b1 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 04 b1 b1 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 05 85 83 06 06)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 06 85 83 06 06)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 07 80 80 20 20)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 08 80 80 20 08)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 09 80 80 08 20)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0a 80 80 08 08)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0b 80 80 06 06)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 0c 80 60 20 18)
0142  +  DC 07-F0 MDP_XRT_ROI_SET
0143     BC       (cd 0d a0 80 18 20)
0144  +  DC 07-F0 MDP_XRT_ROI_SET
0145     BC       (cd 0f 80 80 06 06)
0146  +  DC 07-F0 MDP_XRT_ROI_SET
0147     BC       (cd 10 80 80 08 08)
0148  +  DC 07-F0 MDP_XRT_FLD_ENA
0149     BC       (d8)
0150  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151     BC       (c8)
0152  +  DC 07-F0 MDP_XRT_ARS_DIS
0153     BC       (d5)
0154  +  DC 07-F0 MDP_XRT_AEC_RESET
0155     BC       (d0)
0156  +  DC 07-F0 MDP_XRT_FLD_RESET
0157     BC       (da)
0158  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159     BC       (c4 0b)
0160  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161     BC       (c5 0e)
0162   . C. ---------- Success Verify ?            OK / NG ____
0163     C.
0164     C.
0165   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0166     C.
0167  +. DC 07-F0 MDP_XRT_MODE_OBSV
0168     BC       (c2)
0169  +. TI 2025-04-01 11:01:02.0
0170     DC 07-F0 MDP_XRT_MODE_OBSV
0171     BC       (c2)
0172   . C. ---------- Success Verify ?            OK / NG ____
0173     C.
0174     C. ************ XRT END ***********
0175     C.
0176   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0177     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0178   . S. DC-BC dcbc-402:DCBC
0179     (MDP_known_event)
0180     C.
0181     C.
0182   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0183   . S. DC-BC dcbc-153:DCBC
0184     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0185     C.
0186     C.
0187   . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
0188     C.
0189   . C. *********** LOS ***********
0190     C.
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*** OP Sequence for XRT ***

2025/04/01  11:11:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  11:11:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  11:11:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/01  11:12:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/01  11:12:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/01  11:12:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/01  11:12:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/01  11:12:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/01  11:12:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  11:12:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  11:12:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  11:12:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  11:12:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/01  11:14:56.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2025/04/01  11:14:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/01  11:15:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/01  11:24:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/01  11:25:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  16:34:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  16:34:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  16:34:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  16:34:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/01  16:37:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/01  16:57:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/01  16:58:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  17:45:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  17:45:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  17:45:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/01  17:46:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/01  17:46:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/01  17:46:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/01  17:46:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/01  17:48:58.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2025/04/01  17:49:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  17:55:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  17:55:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  17:55:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/01  17:56:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 03 64 01 ca
2025/04/01  17:56:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/01  17:56:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/01  17:56:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/01  17:56:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/01  17:56:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  17:58:56.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2025/04/01  17:58:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/01  17:59:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  18:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  18:10:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  18:10:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  18:10:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/01  18:13:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/01  18:33:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/01  18:34:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  18:55:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  18:55:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  18:55:04.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/01  18:55:24.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/01  18:55:26.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/01  18:55:28.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/01  18:55:30.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/01  18:55:32.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  18:58:02.0  XRT_QT_PROG_SET_405_OG [0x195]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2025/04/01  18:58:04.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/01  18:58:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  19:47:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  19:47:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  19:47:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  19:47:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/01  19:50:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/01  19:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  19:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/01  20:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 eb cf b0 00
2025/04/01  20:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/01  20:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/01  20:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/01  20:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/01  20:00:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  20:02:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2025/04/01  20:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/01  20:10:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/01  20:11:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  21:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  21:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  21:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  21:23:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/01  21:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/01  21:47:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/01  21:48:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/01  23:01:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  23:01:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/01  23:01:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/01  23:01:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/01  23:04:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/01  23:23:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/01  23:24:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
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2025/04/02  00:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  00:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  00:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  00:38:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  00:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  00:49:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  00:50:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  02:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  02:05:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  02:05:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  02:05:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  02:08:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  02:21:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  02:22:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  02:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  02:29:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/02  02:30:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/02  02:30:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/02  02:30:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/02  02:30:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/02  02:30:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/02  02:30:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  02:32:56.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2025/04/02  02:32:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/02  02:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  03:36:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  03:36:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  03:36:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  03:36:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  03:39:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  03:57:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  03:58:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  04:19:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  04:19:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  04:19:04.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/02  04:19:24.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/02  04:19:26.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/02  04:19:28.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/02  04:22:04.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2025/04/02  04:22:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  04:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  04:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  04:29:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/02  04:30:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 03 03 64 01 ca
2025/04/02  04:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/02  04:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/02  04:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/02  04:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
```

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2025/04/02  04:30:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  04:32:56.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2025/04/02  04:32:58.0  XRT_FL_PROG_SET_443_OG [0x1bb]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 11
2025/04/02  04:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  05:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  05:05:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  05:05:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  05:05:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  05:08:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  05:34:00.5  XRT_Custom_430_OG [0x1ae]
2025/04/02  05:35:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  06:45:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  06:45:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  06:45:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  06:45:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  06:48:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  07:11:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  07:12:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  08:25:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  08:25:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  08:25:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  08:25:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  08:28:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  08:48:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  08:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  10:04:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  10:04:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  10:04:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  10:04:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  10:07:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  10:24:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  10:25:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  10:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  10:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  10:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/02  11:00:00.5  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/02  11:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/02  11:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/02  11:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/02  11:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/02  11:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  11:02:56.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2025/04/02  11:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/02  11:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  11:47:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  11:47:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  11:47:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  11:47:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  11:50:00.0  XRT_Custom_430_OG [0x1ae]
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2025/04/02  11:50:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  11:51:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  15:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  15:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  15:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  15:31:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  15:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  15:41:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  15:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  17:05:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  17:05:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  17:05:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  17:05:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  17:08:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  17:29:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  17:30:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  17:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  17:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/02  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/02  18:00:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/02  18:00:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/02  18:00:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/02  18:02:58.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2025/04/02  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  18:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  18:09:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/02  18:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/02  18:10:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/02  18:10:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/02  18:10:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/02  18:10:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/02  18:10:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  18:12:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2025/04/02  18:12:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/02  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  18:42:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  18:42:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  18:42:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  18:42:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  18:45:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  19:05:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  19:06:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  20:19:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  20:19:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  20:19:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  20:19:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  20:22:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
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2025/04/02  20:42:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  20:43:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  21:56:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  21:56:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  21:56:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  21:56:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  21:59:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  22:19:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  22:20:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  23:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  23:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  23:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/02  23:33:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/02  23:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/02  23:54:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/02  23:55:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/02  23:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  23:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/02  23:59:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/03  00:00:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 ad 59 00 00
2025/04/03  00:00:18.0  XRT_FLD_DIS_401_OG [0x191]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/03  00:14:54.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/03  00:14:56.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/03  00:14:58.0  XRT_QT_PROG_SET_403_OG [0x193]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2025/04/03  00:15:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  01:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  01:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  01:59:58.0  XRT_FOCUS_POSITION_418_OG [0x1a2]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/03  02:00:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 00 00 56 35
2025/04/03  02:00:18.0  XRT_FLD_DIS_401_OG [0x191]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/03  02:14:54.0  XRT_ARS_DIS_427_OG [0x1ab]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/03  02:14:56.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/03  02:14:58.0  XRT_QT_PROG_SET_421_OG [0x1a5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2025/04/03  02:15:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  03:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/03  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/03  04:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/03  04:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/03  04:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/03  04:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/03  04:00:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  04:02:56.0  XRT_QT_PROG_SET_422_OG [0x1a6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2025/04/03  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/03  04:08:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  04:08:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  04:08:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  04:08:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
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2025/04/03  04:11:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/03  04:29:00.5  XRT_Custom_430_OG [0x1ae]
2025/04/03  04:30:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  05:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  05:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  05:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  05:38:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/03  05:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/03  06:06:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/03  06:07:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  06:15:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  06:15:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  06:15:04.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/03  06:15:24.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/03  06:15:26.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/03  06:15:28.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/03  06:18:04.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2025/04/03  06:18:06.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  06:25:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  06:25:26.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/03  06:25:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/03  06:25:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/03  06:25:48.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/03  06:25:50.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/03  06:25:52.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/03  06:25:54.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  06:28:26.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0c
2025/04/03  06:28:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/03  06:28:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  07:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  07:18:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  07:18:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  07:18:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/03  07:21:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/03  07:43:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/03  07:44:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  08:58:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  08:58:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  08:58:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  08:58:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/03  09:01:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/03  09:20:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/03  09:21:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  10:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  10:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  10:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/03  10:38:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/03  10:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/03  10:54:30.0  XRT_Custom_430_OG [0x1ae]
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2025/04/03  10:55:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/03  11:28:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/03  11:44:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
```