XRT Timeline to be uploaded on 2025/04/05

Period: 2025/04/05 12:06:00 - 2025/04/10 11:34:00

* * * * * **Normal mode** * * * * *

XOB #1BA9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/05 12:19:00 - 04/05 18:04:54	Track (97.6, -199.0) @ 04/05 12:16:00	# OP start + 10min + AR 14048 obs.
04/05 18:18:00 - 04/06 03:59:54	Track (150.0, -200.1) @ 04/05 18:15:00	AR 14048 obs.

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 8-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
04/05 18:08:00 - 04/05 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5 min
04/06 06:07:00 - 04/06 06:50:00	Fixed (0.0, 0.0)	HOP 349 & 448 + synoptic, shifted 15.0 min

PROG= 03 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 55 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 98 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 76 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 41 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 18 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 86 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 54 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1D2E: HOP349 - 3-filter Synoptics (Al-mesh[5/181/1443], Al-poly[12/256/2048], thin-Be[24/723/5795] with 512x512 G-band+Leak - 375min cad) + CME

Term	Pointing (x, y)	Comment
04/06 04:03:00 - 04/06 05:37:30	Fixed (0.0, 0.0)	HOP 349 & 448 + synoptic, shifted 15.0 min

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn= 66 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 22 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 61 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 30 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 15-time(s) 1500.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 74 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

* * * * * Flare mode * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
04/05 12:19:00 - 04/05 18:04:54	Track (97.6, -199.0) @ 04/05 12:16:00	# OP start + 10min + AR 14048 obs.
04/05 18:18:00 - 04/06 03:59:54	Track (150.0, -200.1) @ 04/05 18:15:00	AR 14048 obs.
04/06 04:03:00 - 04/06 05:37:30	Fixed (0.0, 0.0)	HOP 349 & 448 + synoptic, shifted 15.0 min

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

* * * * * Active Region Search * * * * *

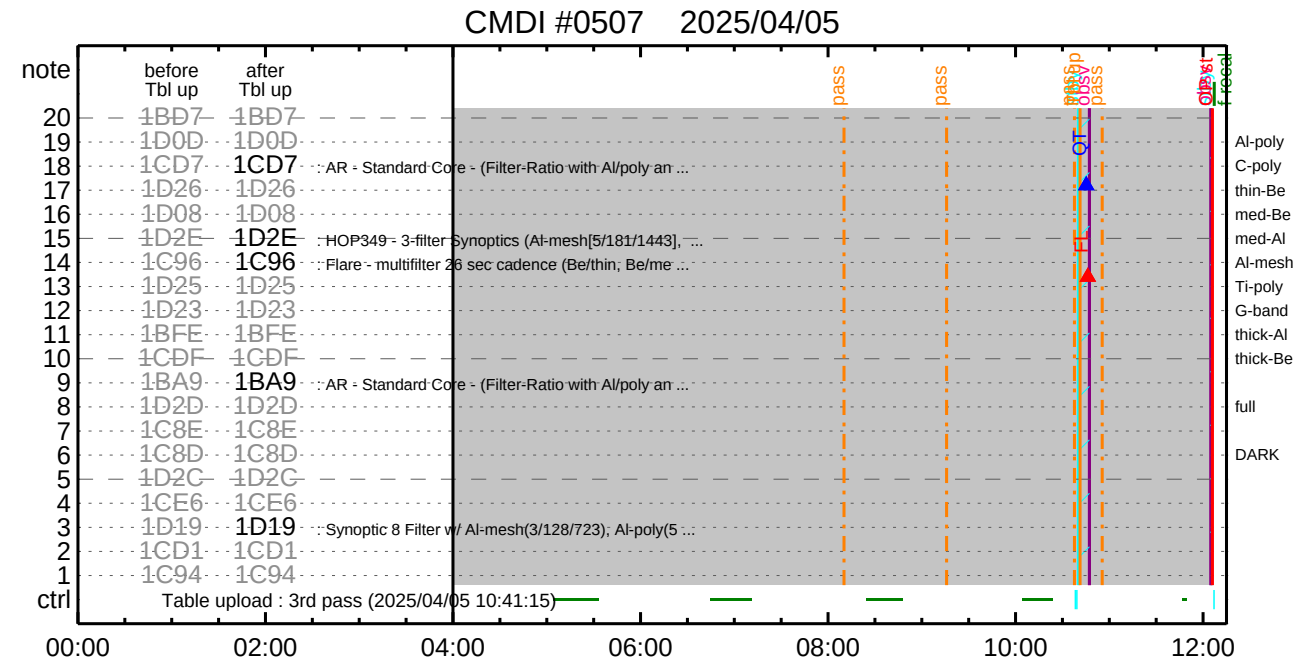
NOT USED

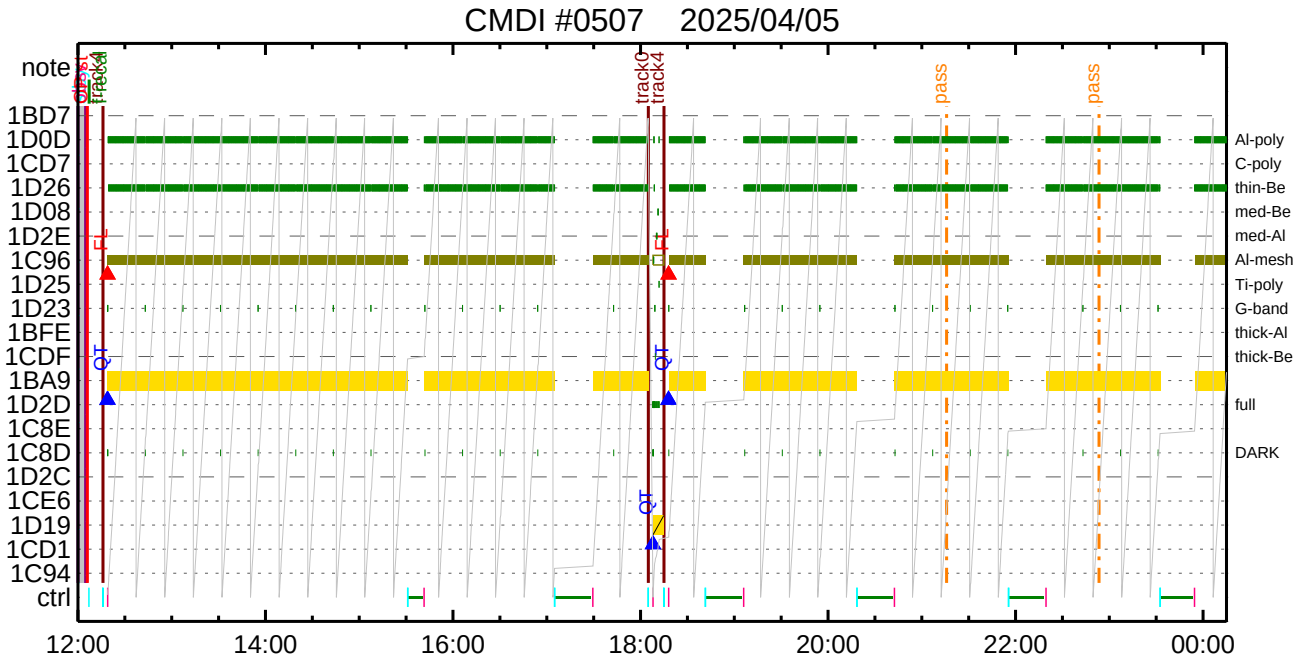
* * * * * Flare Detection * * * * *

FLD Patrol

Term	Pointing (x, y)	Comment
04/05 10:42:15 - 04/05 18:05:18	cannot be identified	
04/05 18:15:18 - 04/06 06:00:24	Track (150.0, -200.1) @ 04/05 18:15:00	AR 14048 obs.

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec


CMDI #0507 2025/04/05
note
before Tbl up
after Tbl up
20 1BD7 1BD7
19 1D0D 1D0D
18 1CD7 1CD7 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
17 1D26 1D26
16 1D08 1D08
15 1D2E 1D2E : HOP349 - 3-filter Synoptics (Al-mesh[5/181/1443], ...
14 1C96 1C96 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
13 1D25 1D25
12 1D23 1D23
11 1BFE 1BFE
10 1CDF 1CDF
9 1BA9 1BA9 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
8 1D2D 1D2D
7 1C8E 1C8E
6 1C8D 1C8D
5 1D2C 1D2C
4 1CE6 1CE6
3 1D19 1D19 : Synoptic 8 Filter w/ Al-mesh(3/128/723); Al-poly(5 ...
2 1CD1 1CD1
1 1C94 1C94
ctrl Table upload : 3rd pass (2025/04/05 10:41:15)
pass
pass
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0507 2025/04/05
note
track0
track4
pass
pass
FL
QT
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
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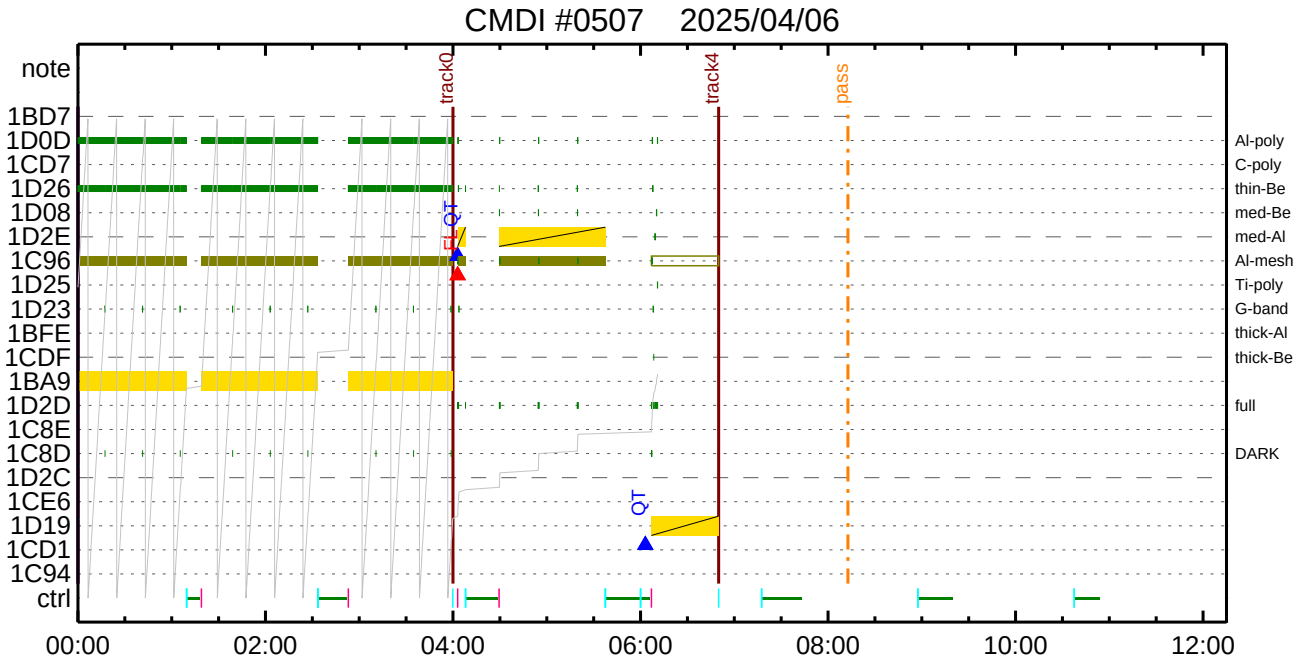
CMDI #0507 2025/04/06
note
track0
track4
pass
FL
QT
1BD7
1D0D
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1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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02:00
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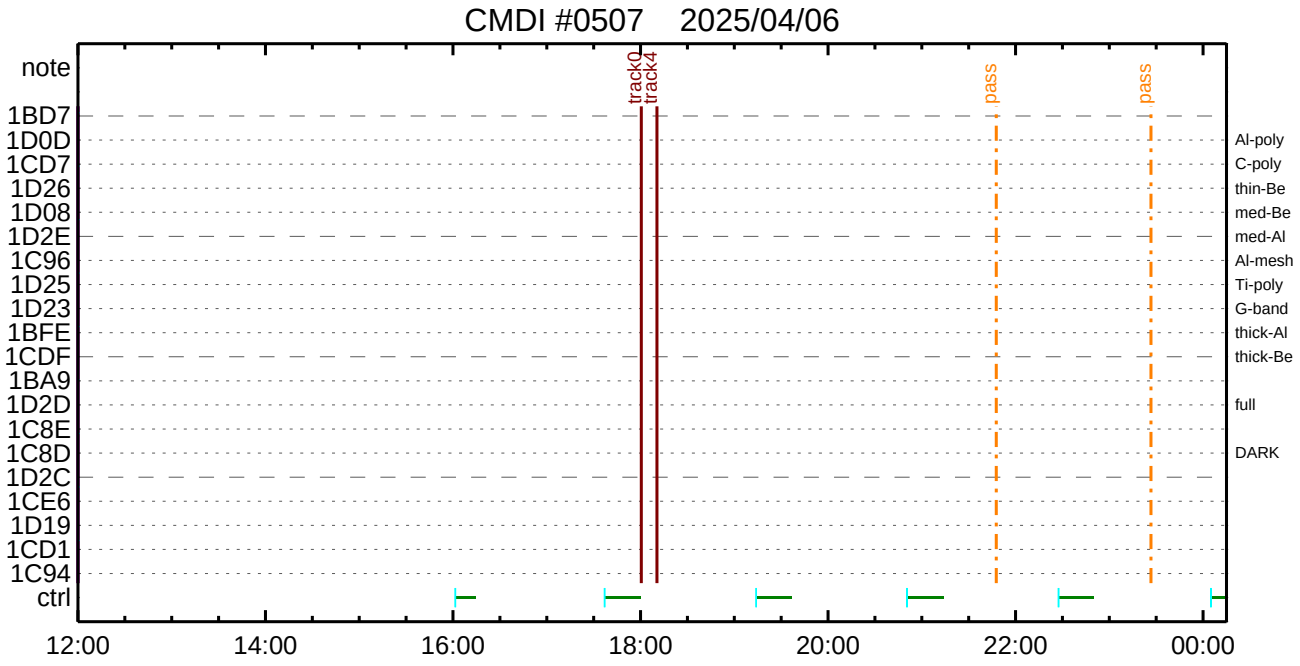
CMDI #0507 2025/04/06
note
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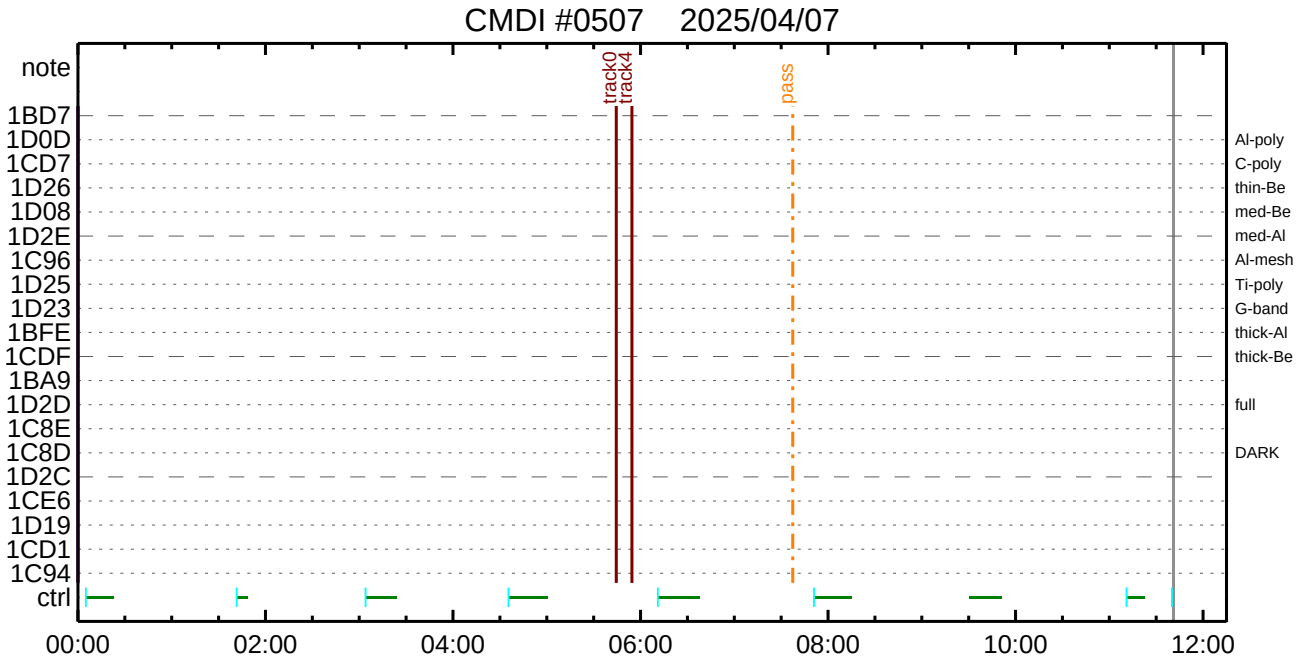
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
track0
track4
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0507 2025/04/07
note
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1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
track0
track4
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
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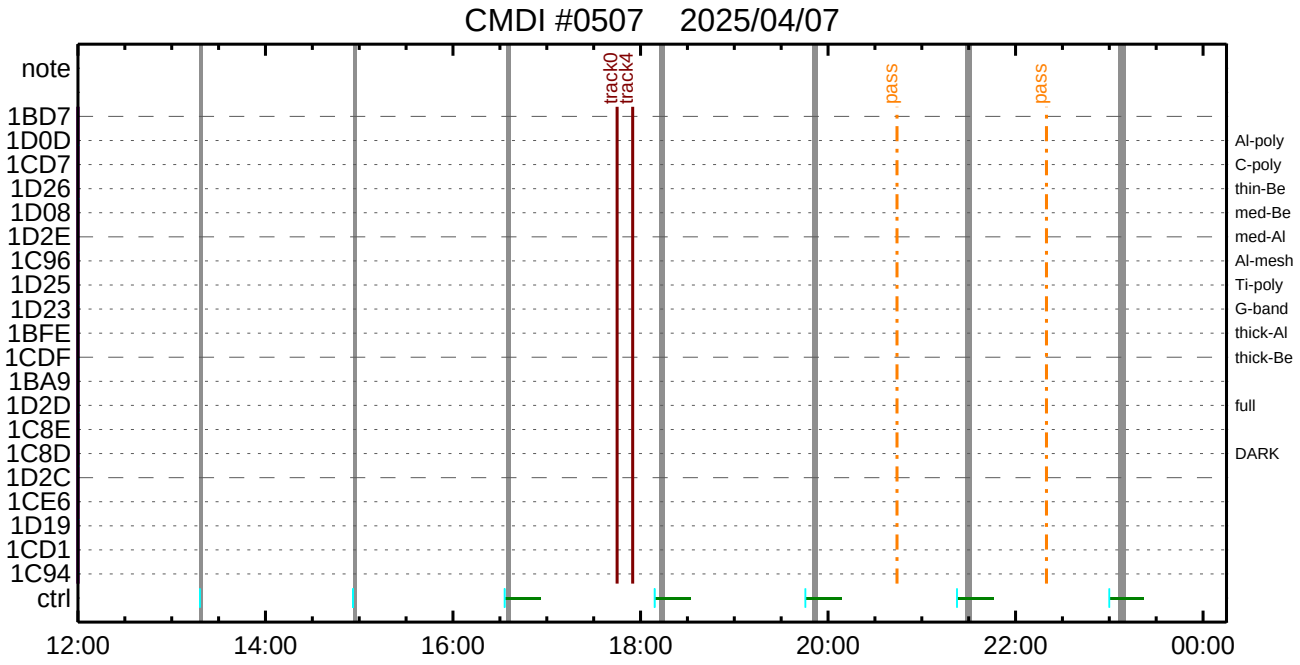
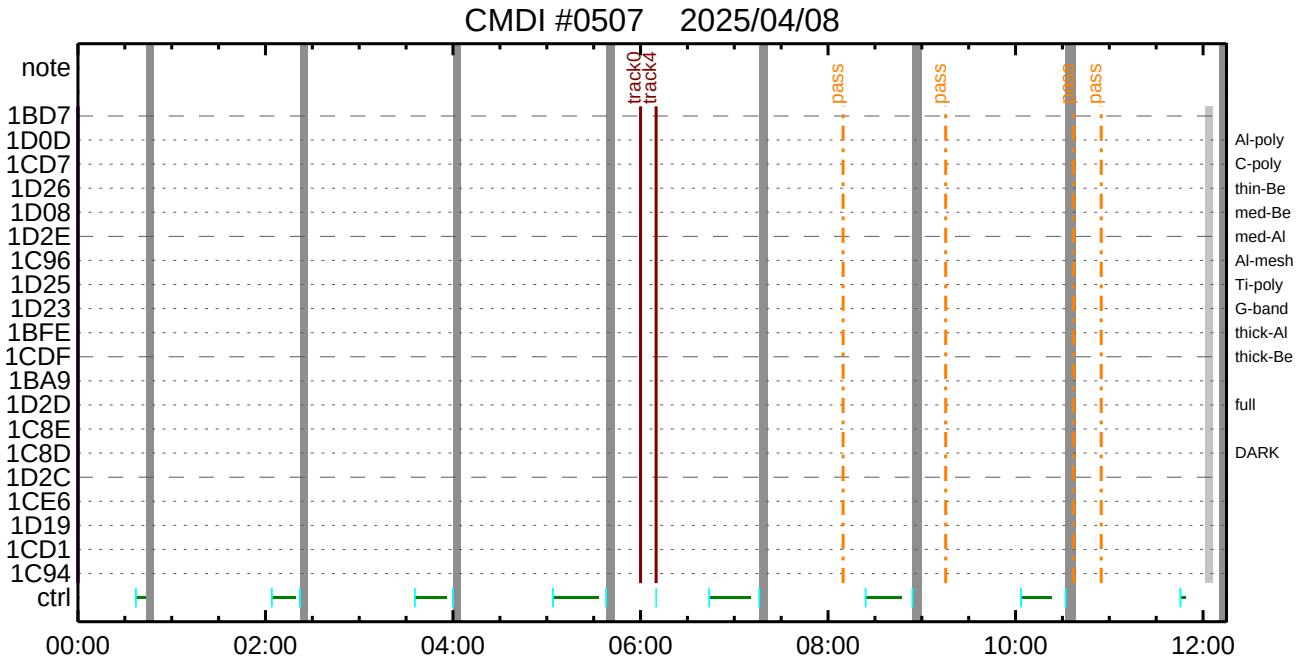
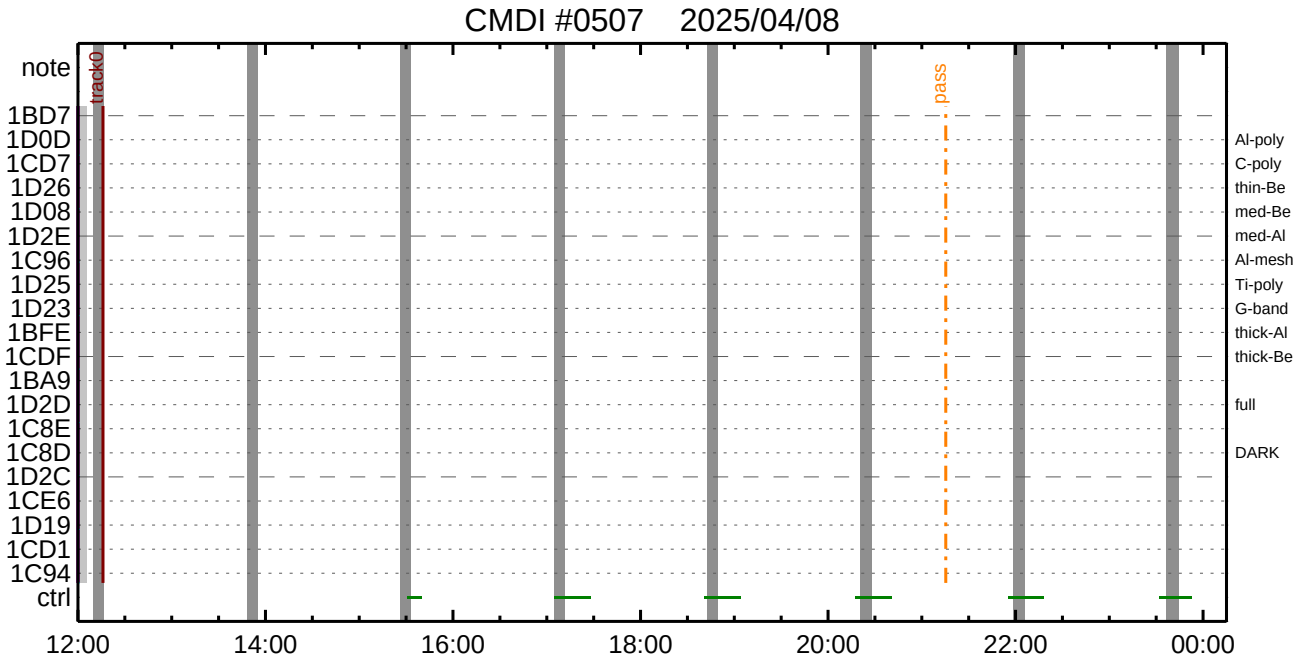
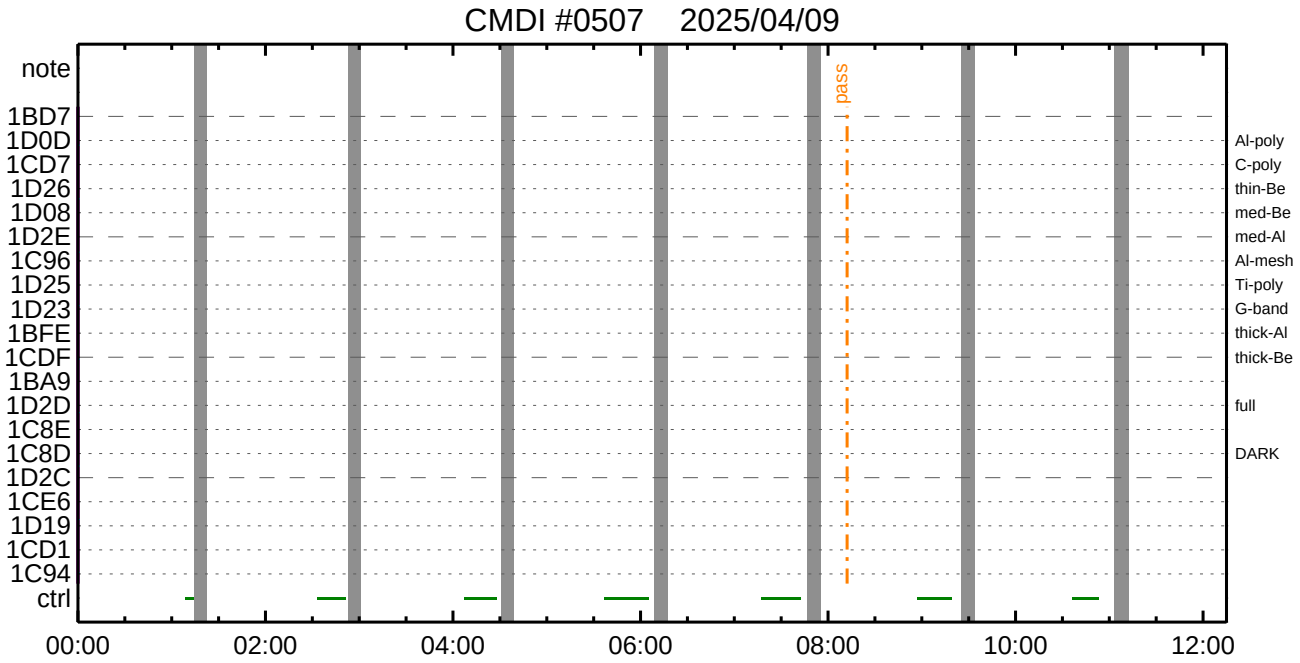
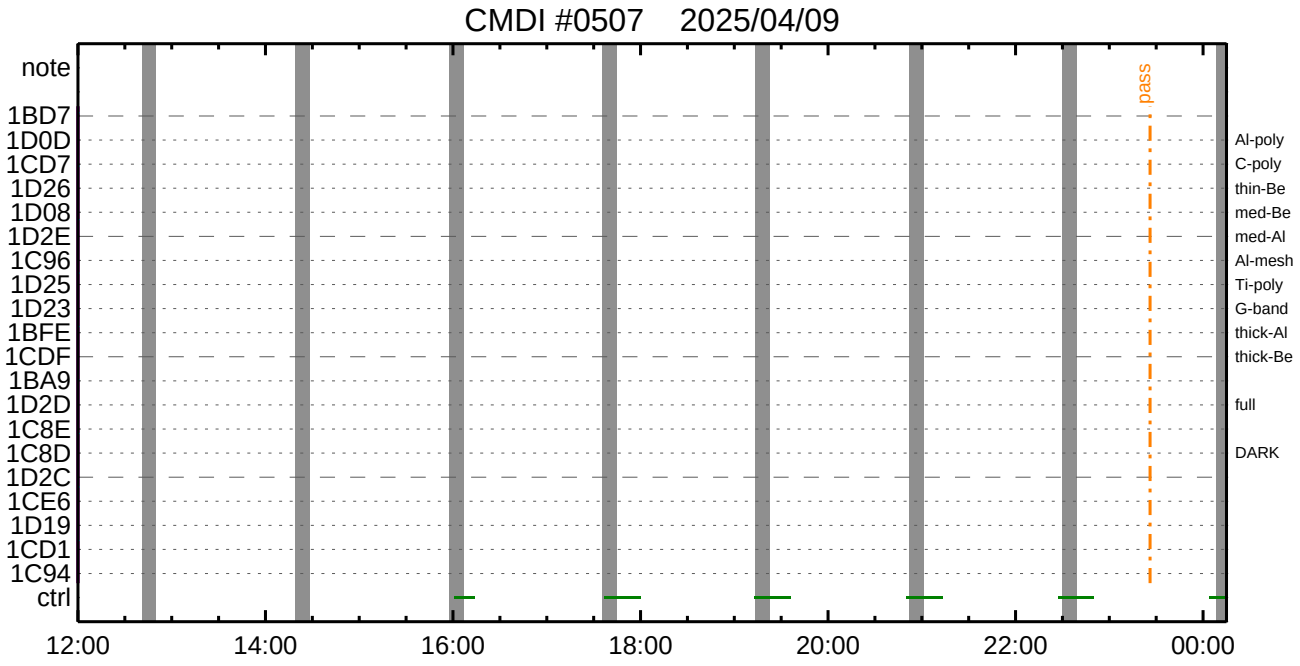
10:00
12:00

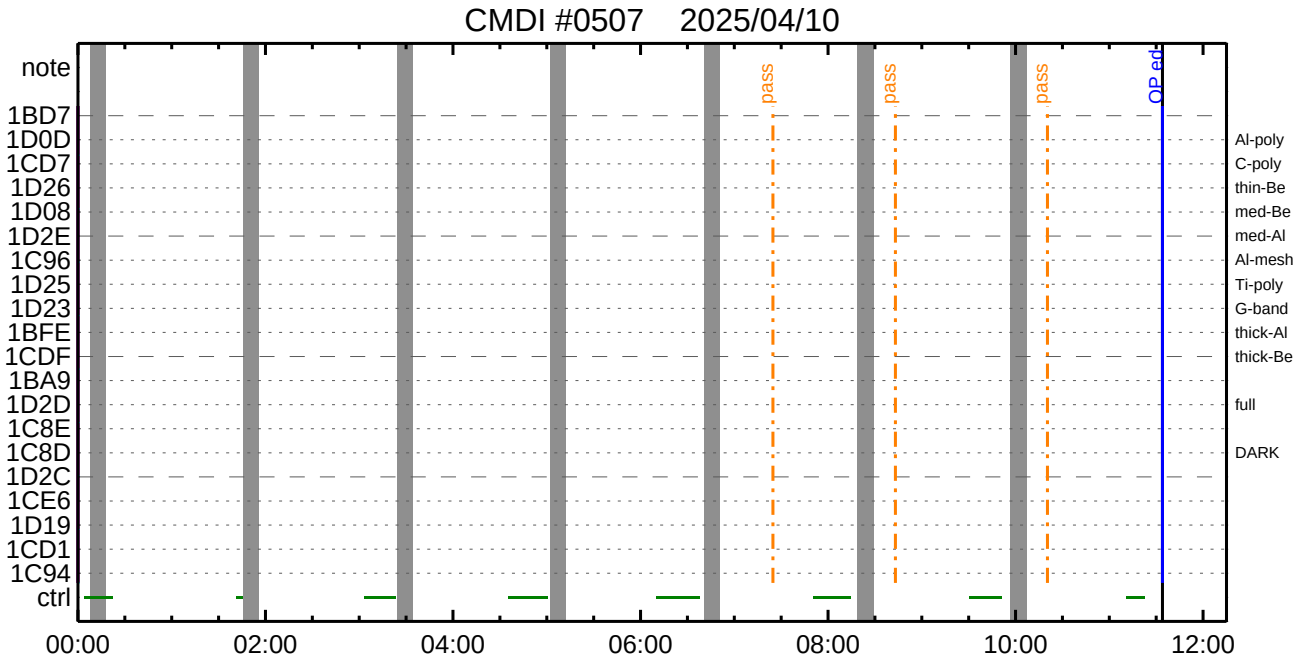

CMDI #0507 2025/04/07
note
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
track0
track4
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

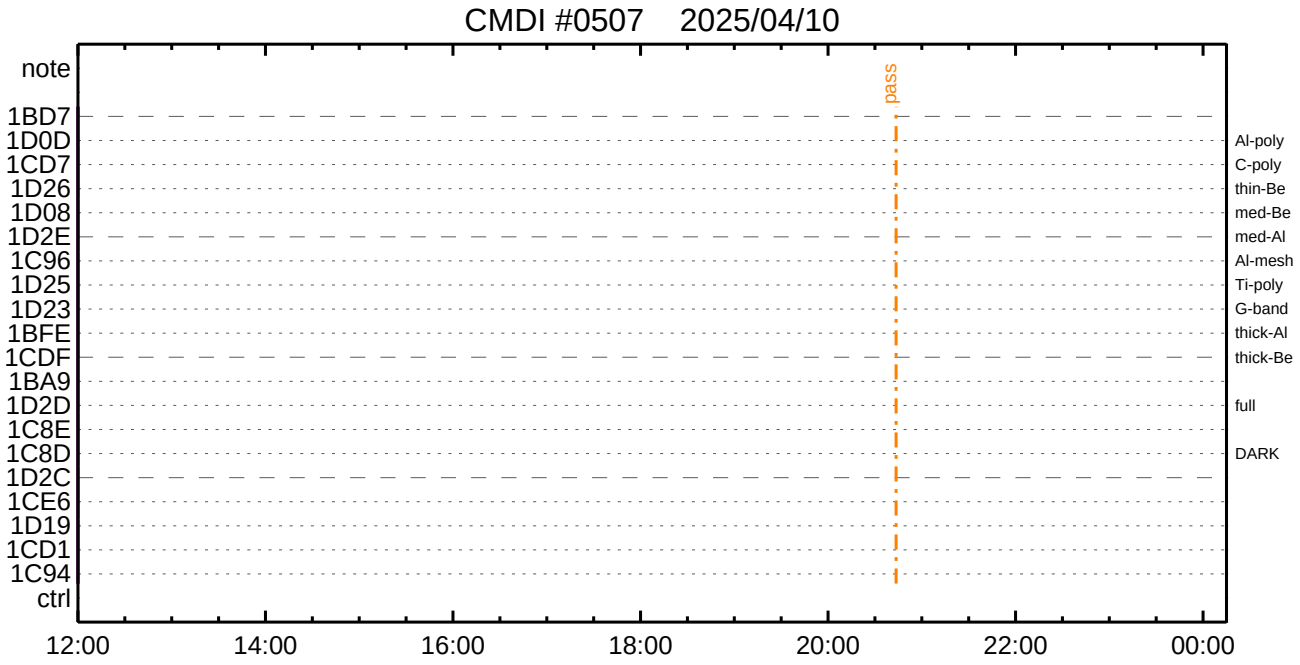

CMDI #0507 2025/04/08
note
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
track0
track4
pass
pass
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0507 2025/04/08
note
track0
pass
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0507 2025/04/09
note
pass
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0507 2025/04/09
note
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0507 2025/04/10
note
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0507 2025/04/10
note
1BD7
1D0D
1CD7
1D26
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1C8E
1C8D
1D2C
1CE6
1D19
1CD1
1C94
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

(a) Spacecraft Operation Procedure (real-commands)

```
main-199 2025-04-05 14:17:41 194 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0016     C. ****************************
0017     C.
0018   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0019   . S. OP     op-199:OP
0020     ()
0021   . S. OG     og-199:OG
0022     ()
0023     C.
0024   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0025     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0026   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027     BC        (20 00 7f 01 02)
0028     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0029     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0030     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0031     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0032     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0033   +. DC 01-22 DHU_MODE_CHNG
0034     BC        (07 0b f8)
0035     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0036     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0037     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0038     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0039     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0040   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0041     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0042   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0043     C.
0044     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0045   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046     BC        (20 80 7f 01 02)
0047     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0048     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0049     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0050     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0051     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0052   +. DC 01-22 DHU_MODE_CHNG
0053     BC        (07 0b f8)
0054     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0055     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0056     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0057     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0058     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0059   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0060     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0061   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0062     C.
0063     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065     BC        (21 00 41 01 02)
0066     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0067     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0068     C.                        ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0069     C.                        ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0070     C.                        ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0071   +. DC 01-22 DHU_MODE_CHNG
0072     BC        (07 0b f8)
0073     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     7
0074     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0075     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0076     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0077     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0078   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0079     C.                        ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0080   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0081     C.
0082   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ¬¥À¥ó¥×½ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0083     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0084   +. DC 01-22 DHU_MODE_CHNG
0085     BC        (02 0a f8)
0086     C.                        ¢¢[HK1_PKT_FORM_NO]             EQ     2
0087     C.                        ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0088     C.                        ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0089     C.                        ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0090     C.
0091   . C. ********************************
0092     C. TI-CMD SET (OPOG STOP/COPY/START)
0093     C. ********************************
0094     C.
0095   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
```

```
0096     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0097     C.
0098   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0099   +. TI 2025-04-05 12:01:00.0
0100     DC 01-B3 DHU_OP_STOP
0101     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0102     C.
0103   +. TI 2025-04-05 12:01:01.0
0104     DC 01-B4 DHU_OP_COPY
0105     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0106     C.
0107   +. TI 2025-04-05 12:01:01.0
0108     DC 01-B5 DHU_OPOG_COPY
0109     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0110     C.
0111   +. TI 2025-04-05 12:05:59.5
0112     DC 01-B2 DHU_OP_START
0113     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0114     C.
0115     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0116     C.                      ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0117     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     4
0118     C.                      ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0119     C.                      ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0120     C.
0121   . C. ****************************
0122     C. TIÎÎ°è¥À¥ó¥×
0123     C. ****************************
0124     C.
0125     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0126   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127     BC       (03 ab 03 01 02)
0128     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0129     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0130     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0131     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0132     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0133   +. DC 01-22 DHU_MODE_CHNG
0134     BC       (07 0b f8)
0135     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
0136     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0137     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0138     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0139     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0140     C.
0141   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0142     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0143     C.
0144   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0145     C.
0146   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0147   +. DC 01-22 DHU_MODE_CHNG
0148     BC       (02 0a f8)
0149     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
0150     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0151     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0152     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0153     C.
0154     C.
0155     C. ************ XRT START ************
0156     C. Execute, after the success of OP upload.
0157   +. TI 2025-04-05 12:05:00.0
0158     DC 07-F0 MDP_XRT_MODE_STBY
0159     BC       (c3)
0160   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0161     C.
0162     C. ************ XRT END ************
0163   . C. Stop EIS observation and temporarily disable EIS mode changes
0164     C.
0165     C.
0166     C. ********** Start EIS operation (TI set) **********
0167     C. Execute, after the success of OP upload.
0168     C. Set EIS TI-commands
0169   +. TI 2025-04-05 12:05:30.0
0170     DC 07-FC EIS_MODE_MANU
0171     BC       (21 02)
0172   +. TI 2025-04-05 12:05:40.0
0173     DC 07-FC EIS_MODE_CHG_DIS
0174     BC       (22)
0175   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0176     C. *********** End EIS operation (TI set) ***********
0177     C.
0178     C.
0179     C.
0180   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0181     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0182   . S. DC-BC dcbc-402:DCBC
0183     (MDP_known_event)
0184     C.
0185     C.
0186   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0187   . S. DC-BC dcbc-153:DCBC
0188     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189     C.
0190     C.
0191   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0192     C.
0193   . C. *********** LOS ***********
```

0194 C.

```
(a) Spacecraft Operation Procedure (real-commands)

main-200 2025-04-05 14:17:41 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132   . C. Verify EIS_MODE_CHG_FLG is ENA
0133   +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135   . C. Verify EIS in MANUAL mode
0136   . C. Estimated OBSTBL upload time is 27s
0137     C. *********************************
0138     C. EIS START OBSTBL LOAD
0139     C. *********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142   +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147   +. TI 2025-04-05 12:05:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150   . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. *********************************
0152     C. EIS END OBSTBL LOAD
0153     C. *********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. *********** LOS ***********
0169     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-201 2025-04-05 14:17:41 100 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +. DC 07-F0 MDP_XRT_CTRL_MANU
0020     BC       (c1)
0021   +  DC 07-F0 MDP_XRT_MODE_STBY
0022     BC       (c3)
0023   . C. ----------- Success Verify ?              OK / NG____
0024     C.
0025     C. XRT Obs. Table Upload
0026   . S. RAM   ram-291:MDP_OBS_X
0027     ()
0028     C.
0029   +. DC 07-F0 MDP_DUMP_XRTTBL
0030     BC       (84 07 00 00 00 3a d4)
0031   . C. ----------- Comparison Check ?                        OK / ERR ____
0032     C.
0033     C.
0034   +. DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 01 b1 b1 04 04)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 02 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 03 b1 b1 08 08)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 04 b1 b1 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 05 85 83 06 06)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 06 85 83 06 06)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 07 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 08 80 80 20 08)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 09 80 80 08 20)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0a 80 80 08 08)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0f 80 80 06 06)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 10 80 80 08 08)
0058   +  DC 07-F0 MDP_XRT_FLD_ENA
0059     BC       (d8)
0060   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061     BC       (c8)
0062   +  DC 07-F0 MDP_XRT_ARS_DIS
0063     BC       (d5)
0064   +  DC 07-F0 MDP_XRT_AEC_RESET
0065     BC       (d0)
0066   +  DC 07-F0 MDP_XRT_FLD_RESET
0067     BC       (da)
0068   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069     BC       (c4 12)
0070   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071     BC       (c5 0e)
0072   . C. ---------- Success Verify ?            OK / NG ____
0073     C.
0074     C.
0075   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0076     C.
0077   +. DC 07-F0 MDP_XRT_MODE_OBSV
0078     BC       (c2)
0079   +. TI 2025-04-05 12:05:02.0
0080     DC 07-F0 MDP_XRT_MODE_OBSV
0081     BC       (c2)
0082   . C. ---------- Success Verify ?            OK / NG ____
0083     C.
0084     C. ************ XRT END ***********
0085     C.
0086   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0087     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0088   . S. DC-BC dcbc-402:DCBC
0089     (MDP_known_event)
0090     C.
0091     C.
0092   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0093   . S. DC-BC dcbc-153:DCBC
0094     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095     C.
```

```
0096     C.
0097   . C. ¡ãLOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0098     C.
0099   . C. *********** LOS ***********
0100     C.
```

```
*** OP Sequence for XRT ***

2025/04/05  12:07:00.5  XRT_CTRL_MANU_425_OG [0x1a9]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  12:07:10.5  XRT_FOCUS_RECALIBRATE_407_OG [0x197]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2025/04/05  12:11:10.5  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/05  12:15:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  12:15:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  12:15:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/05  12:16:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/05  12:16:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/05  12:16:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/05  12:16:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/05  12:16:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/05  12:16:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  12:18:56.0  XRT_QT_PROG_SET_405_OG [0x195]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2025/04/05  12:18:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/05  12:19:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  15:31:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  15:31:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  15:31:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  15:31:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/05  15:34:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/05  15:40:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/05  15:41:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  17:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  17:05:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  17:05:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  17:05:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/05  17:08:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/05  17:28:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/05  17:29:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  18:04:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  18:04:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  18:04:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/05  18:05:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/05  18:05:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/05  18:05:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/05  18:05:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/05  18:07:58.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2025/04/05  18:08:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  18:14:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  18:14:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  18:14:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/05  18:15:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/05  18:15:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/05  18:15:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/05  18:15:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/05  18:15:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/05  18:15:26.0  XRT_FLD_RESET_434_OG [0x1b2]
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                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  18:17:56.0  XRT_QT_PROG_SET_405_OG [0x195]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 09
2025/04/05  18:17:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/05  18:18:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  18:41:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  18:41:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  18:41:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  18:41:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/05  18:44:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/05  19:05:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/05  19:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  20:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  20:18:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  20:18:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  20:18:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/05  20:21:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/05  20:41:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/05  20:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  21:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  21:55:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  21:55:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  21:55:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/05  21:58:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/05  22:18:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/05  22:19:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/05  23:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  23:32:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/05  23:32:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/05  23:32:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/05  23:35:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/05  23:53:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/05  23:54:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/06  01:09:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  01:09:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  01:09:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  01:09:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  01:12:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  01:18:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/06  01:19:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/06  02:33:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  02:33:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  02:33:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  02:33:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  02:36:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  02:52:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/06  02:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/06  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  03:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/06  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/04/06  04:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
```

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2025/04/06  04:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/06  04:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/06  04:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/06  04:00:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  04:02:56.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2025/04/06  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/06  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/06  04:08:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  04:08:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  04:08:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  04:08:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  04:11:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  04:28:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/06  04:29:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/06  05:37:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  05:37:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  05:37:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  05:37:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  05:40:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  06:00:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  06:00:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  06:00:04.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/06  06:00:24.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/06  06:00:26.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/06  06:00:28.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/06  06:03:04.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 03
2025/04/06  06:06:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/06  06:07:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/06  06:50:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/06  06:50:00.5  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  06:50:02.0  XRT_TCIB_XRT_S_HTR_A_ENA_428_OG [0x1ac]
                                TCIB_XRT_S_HTR_A_ENA 0 04-BC
2025/04/06  07:17:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  07:17:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  07:17:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  07:17:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  07:20:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  08:57:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  08:57:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  08:57:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  08:57:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  09:00:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  10:37:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  10:37:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  10:37:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  10:37:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  10:40:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  16:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  16:01:32.0  XRT_CTRL_MANU_402_OG [0x192]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  16:01:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  16:01:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  16:04:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  17:37:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  17:37:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  17:37:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  17:37:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  17:40:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  18:00:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/04/06  18:10:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/06  19:14:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  19:14:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  19:14:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  19:14:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  19:17:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  20:50:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  20:50:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  20:50:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  20:50:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  20:53:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/06  22:27:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  22:27:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/06  22:27:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/06  22:27:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/06  22:30:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  00:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  00:05:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  00:05:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  00:05:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  00:08:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  01:41:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  01:41:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  01:41:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  01:41:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  01:44:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  03:04:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  03:04:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  03:04:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  03:04:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  03:07:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  04:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  04:35:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  04:35:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  04:35:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  04:38:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  05:44:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
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2025/04/07  05:54:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/07  06:11:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  06:11:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  06:11:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  06:11:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  06:14:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  07:51:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  07:51:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  07:51:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  07:51:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  07:54:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  11:11:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  11:11:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  11:11:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  11:11:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  11:14:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  11:40:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  11:40:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  11:40:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  11:40:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  11:43:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  13:18:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  13:18:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  13:18:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  13:18:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  13:21:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  14:56:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  14:56:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  14:56:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  14:56:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  14:59:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  16:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  16:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  16:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  16:33:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  16:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  17:45:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/07  17:55:00.5  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/07  18:09:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  18:09:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  18:09:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  18:09:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  18:12:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  19:45:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  19:45:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  19:45:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  19:45:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
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                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  19:48:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  21:22:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  21:22:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  21:22:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  21:22:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  21:25:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/07  23:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  23:00:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/07  23:00:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/07  23:00:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/07  23:03:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  00:37:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  00:37:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  00:37:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  00:37:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  00:40:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  02:04:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  02:04:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  02:04:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  02:04:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  02:07:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  02:22:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  02:22:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  02:22:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  02:22:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  02:25:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  03:35:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  03:35:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  03:35:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  03:35:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  03:38:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  04:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  04:00:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  04:00:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  04:00:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  04:03:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  05:04:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  05:04:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  05:04:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  05:04:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  05:07:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  05:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  05:38:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  05:38:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  05:38:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  05:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
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2025/04/08  06:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/08  06:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 03 64 01 ca
2025/04/08  06:10:00.5  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  06:44:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  06:44:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  06:44:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  06:44:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  06:47:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  07:16:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  07:16:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  07:16:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  07:16:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  07:19:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  08:24:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  08:24:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  08:24:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  08:24:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  08:27:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  08:54:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  08:54:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  08:54:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  08:54:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  08:57:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  10:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  10:03:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  10:03:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  10:03:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  10:06:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  10:32:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  10:32:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  10:32:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  10:32:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  10:35:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  11:45:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  11:45:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/08  11:45:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/08  11:45:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/08  11:48:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/08  12:16:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
```