

Period: 2025/04/08 12:06:00 - 2025/04/12 11:01:00

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XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -All/mesh(2048ms) - 1x1, All/Poly(1443ms) - 2x2 - w leak image-1msCCD														
Term		Pointing (x, y)					Comment							
04/09 13:23:00 - 04/09 13:29:54		Fixed (-528.4, 528.4)					Post bakeout Q4							
PROG= 20		1-time(s)												
Subr= 1		1-time(s)					2.0sec							
Seqn= 14		1-time(s)					2.0sec							
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec	
Subr= 2		1-time(s)					120.0sec							

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
04/09 13:33:00 - 04/09 13:39:54	Fixed (0.0, 0.0)	Post bakeout synopsics
04/09 18:03:00 - 04/09 18:09:54	Fixed (0.0, 0.0)	synoptic
04/10 05:13:30 - 04/10 05:53:24	Fixed (0.0, 0.0)	HOP 349 & 448 + synoptic, shifted -16.5 min

PROG= 03	1-time(s)
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[illegible]

XOB #1BA9: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/09 13:43:00 - 04/09 17:35:30	Track (542.3, -160.4) @ 04/09 13:40:00	(End of synoptics) + AR 14054 obs.
04/09 18:13:00 - 04/09 21:14:54	Track (574.2, -163.2) @ 04/09 18:10:00	AR 14054
04/10 05:56:30 - 04/10 11:10:30	Track (652.1, -171.0) @ 04/10 05:53:30	AR 14054 obs.

PROG= 09 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec												
	Seqn= 92	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2	5-time(s)	2.0sec												
	Seqn= 47	1-time(s)	2.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Seqn= 96	8-time(s)	30.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms														
Term			Pointing (x, y)						Comment					
04/09 21:18:00 - 04/10 03:59:54			Fixed (-20.0, -958.0)						HOP 81 S pole fast					
PROG= 10 Inf.-time(s)														
┌	Subr= 1		1-time(s)		2.0sec									
	└ Seqn= 16		2-time(s)		2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
	Subr= 2		1-time(s)		2.0sec									
	└ Seqn= 90		1-time(s)		30.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
	Subr= 3		60-time(s)		60.0sec									
	└ Seqn= 24		1-time(s)		30.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)		Q=90	0	0	2.0sec
	Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer

XOB #1D2E: HOP349 - 3-filter Synoptics (Al-mesh[5/181/1443], Al-poly[12/256/2048], thin-Be[24/723/5795] with 512x512 G-band+Leak - 375min cad) + CME														
Term			Pointing (x, y)				Comment							
04/10 04:03:00 - 04/10 04:35:00			Fixed (0.0, 0.0)				HOP 349 & 448 + synoptic, shifted -16.5 min							
PROG= 15 Inf.-time(s)														
Subr= 1		1-time(s)		300.0sec										
Seqn= 66		1-time(s)		2.0sec										
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 22		1-time(s)		2.0sec										
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 61		1-time(s)		2.0sec										
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 30		1-time(s)		2.0sec										
└ Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		15-time(s)		1500.0sec										
Seqn= 8		1-time(s)		2.0sec										
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 74		1-time(s)		2.0sec										
└ med-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ med-Be/Open		med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec										
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 29		1-time(s)		2.0sec										
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G														
Term			Pointing (x, y)				Comment							
04/09 13:43:00 - 04/09 17:35:30			Track (542.3, -160.4) @ 04/09 13:40:00				(End of synoptics) + AR 14054 obs.							
04/09 18:13:00 - 04/09 21:14:54			Track (574.2, -163.2) @ 04/09 18:10:00				AR 14054							
04/09 21:18:00 - 04/10 03:59:54			Fixed (-20.0, -958.0)				HOP 81 S pole fast							
04/10 04:03:00 - 04/10 04:35:00			Fixed (0.0, 0.0)				HOP 349 & 448 + synoptic, shifted -16.5 min							
04/10 05:56:30 - 04/10 11:10:30			Track (652.1, -171.0) @ 04/10 05:53:30				AR 14054 obs.							
PROG= 14 30-time(s)														
Subr= 1		20-time(s)		2.0sec										
Seqn= 11		1-time(s)		2.0sec										
└ Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 73		1-time(s)		10.0sec										
└ thin-Be/Open		med-Be/Open close		Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
└ med-Be/Open		Open/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
└ Open/thick-Be		Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 10		1-time(s)		2.0sec										
└ med-Al/Open		med-Al/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
└ Open/thick-Be		Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec										
└ Al-poly/Open		Al-poly/thick-Al close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 87		1-time(s)		2.0sec										
└ Open/G-band		Open/G-band open		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/G-band		Open/G-band close		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec

Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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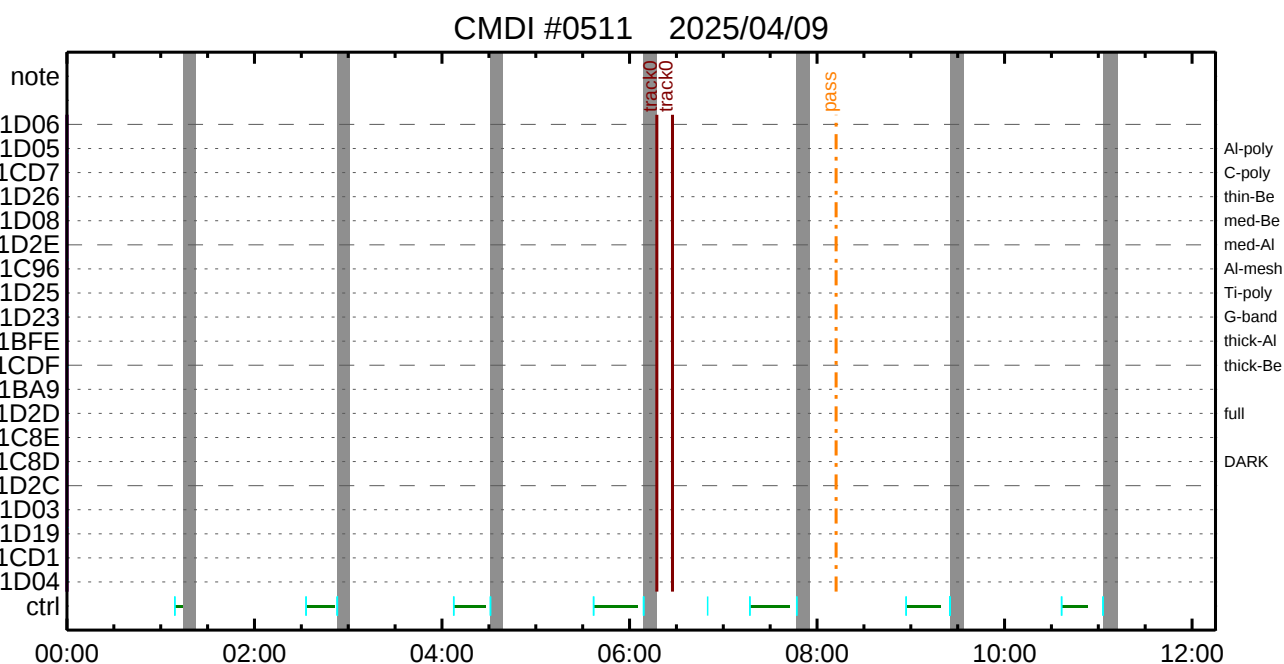
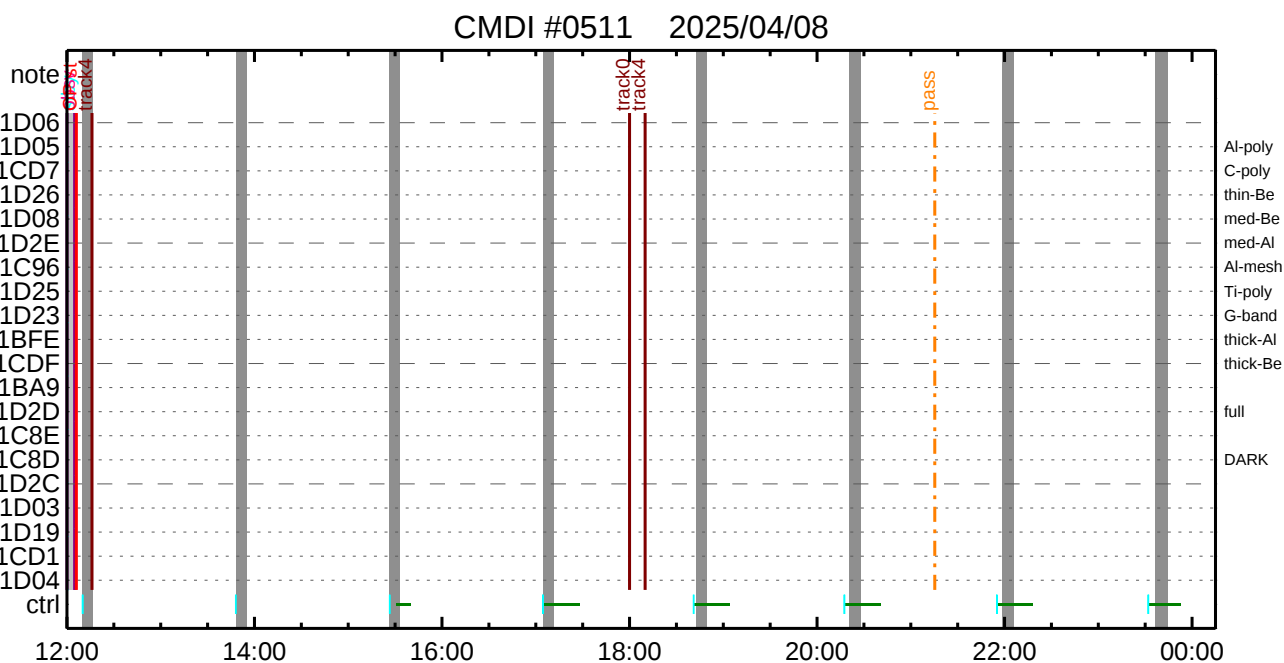
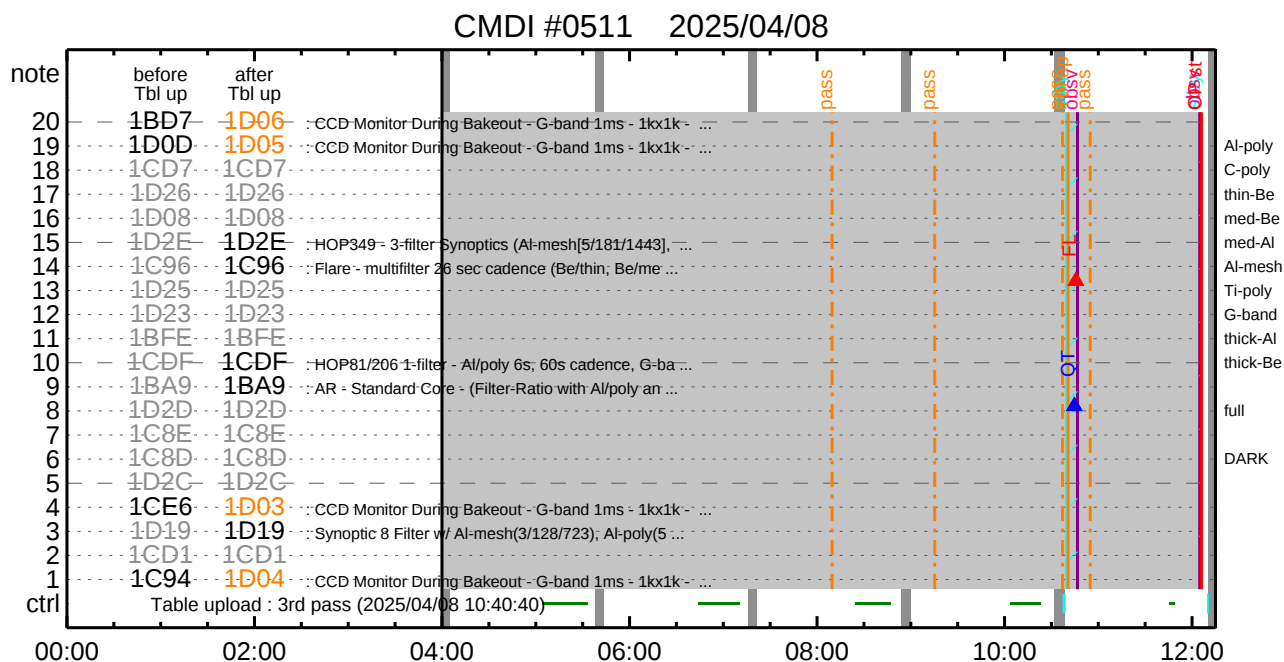
NOT USED

* * * * *

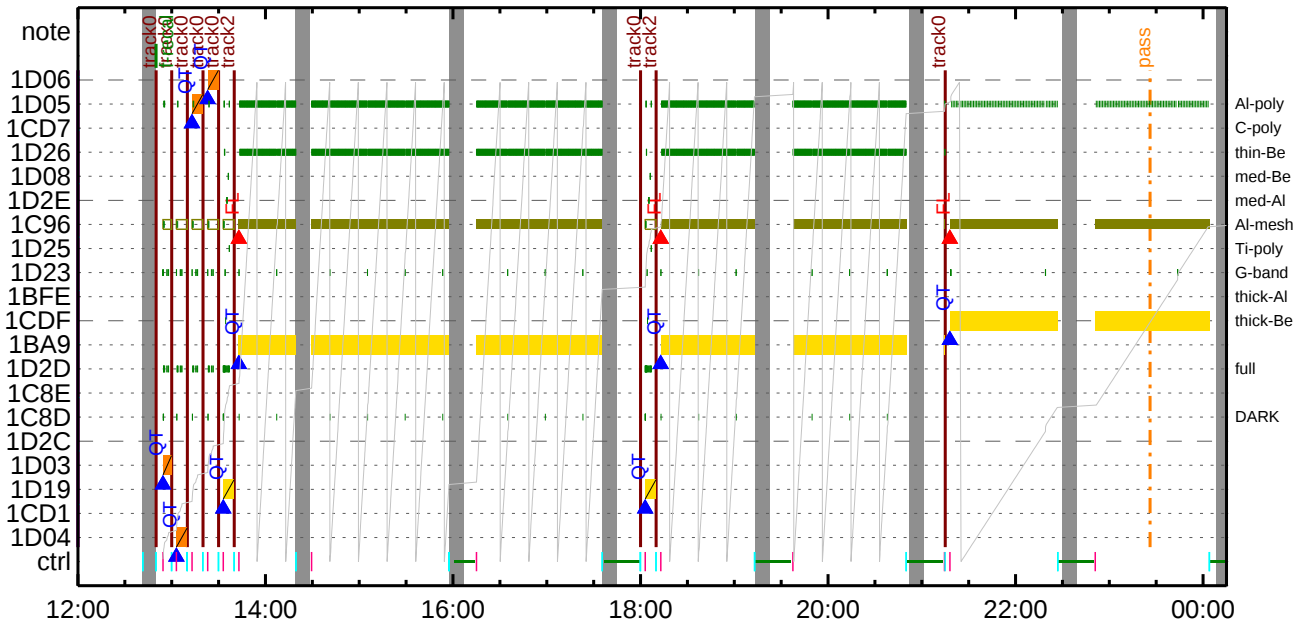
Flare Detection

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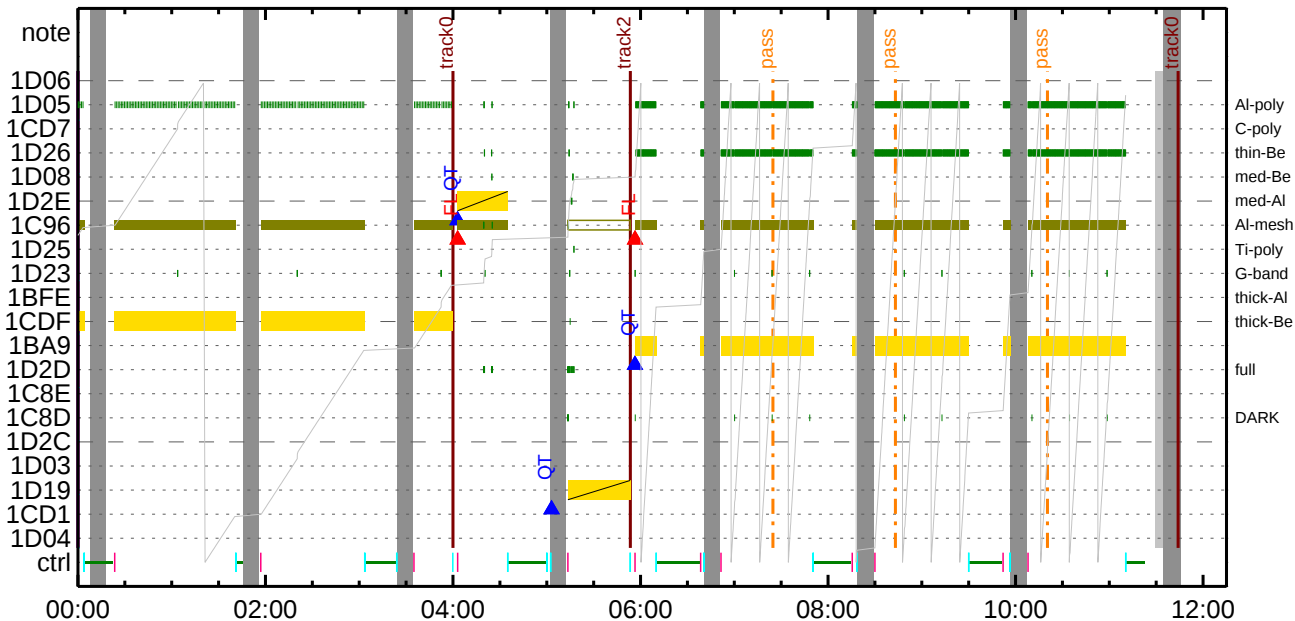
FLD Patrol												
Term			Pointing (x, y)					Comment				
04/08 10:41:40 - 04/09 12:54:22			cannot be identified									
04/09 13:40:18 - 04/09 18:00:18			Track (542.3, -160.4) @ 04/09 13:40:00 (End of synoptics) + AR 14054 obs.									
04/09 18:10:18 - 04/10 05:00:24			Track (574.2, -163.2) @ 04/09 18:10:00 AR 14054									
04/10 05:53:48 - 04/12 11:01:00			Track (652.1, -171.0) @ 04/10 05:53:30 AR 14054 obs.									
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	



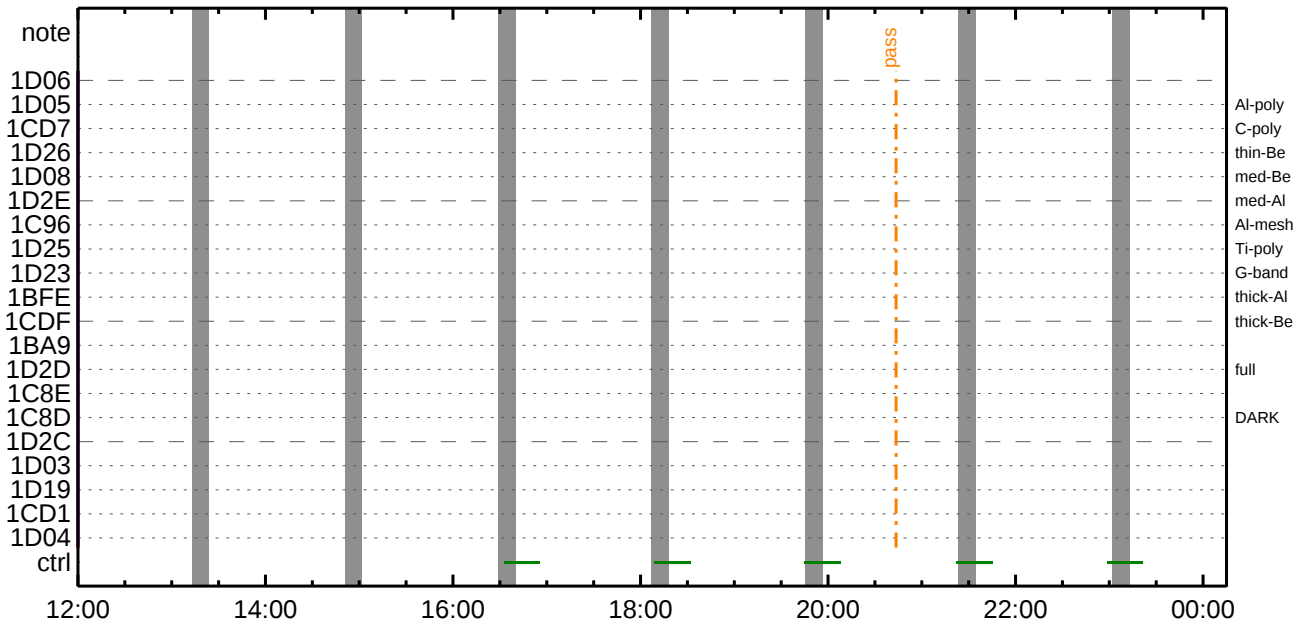
CMDI #0511 2025/04/09



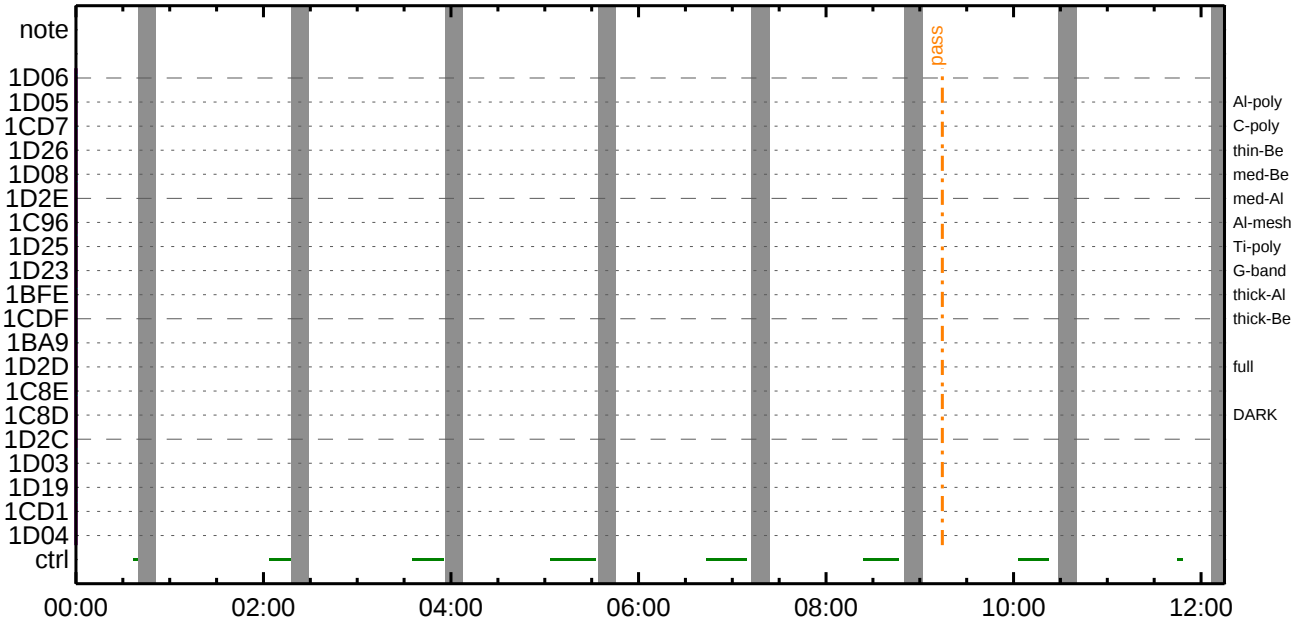
CMDI #0511 2025/04/10



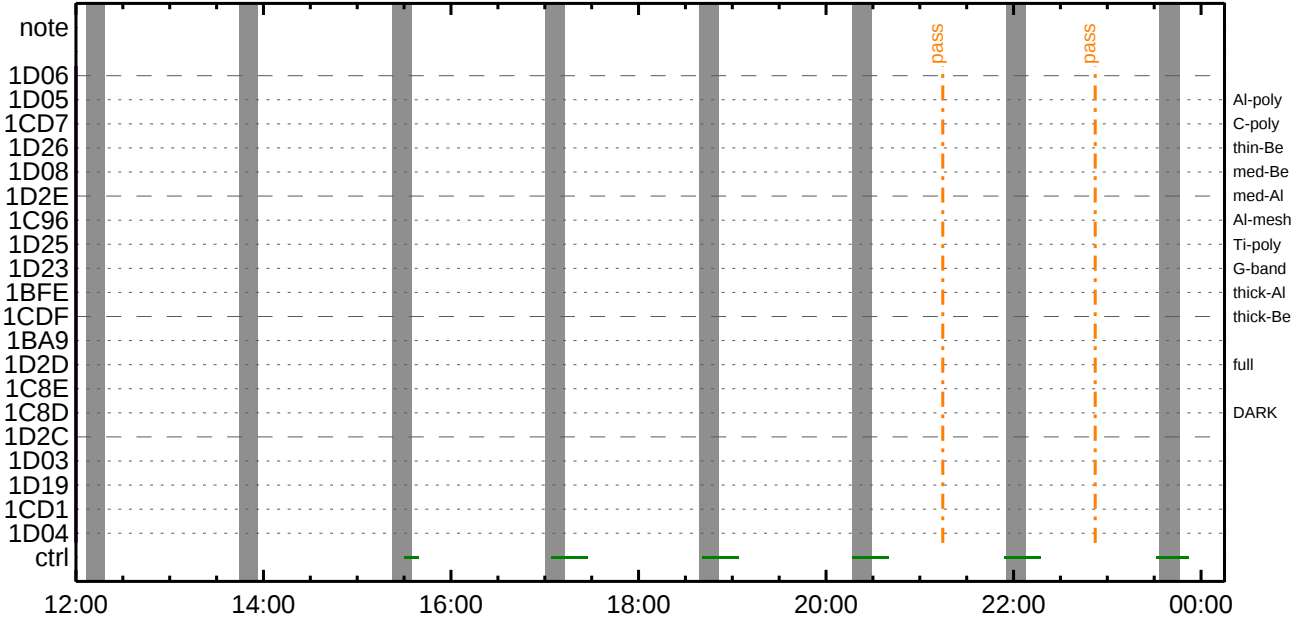
CMDI #0511 2025/04/10



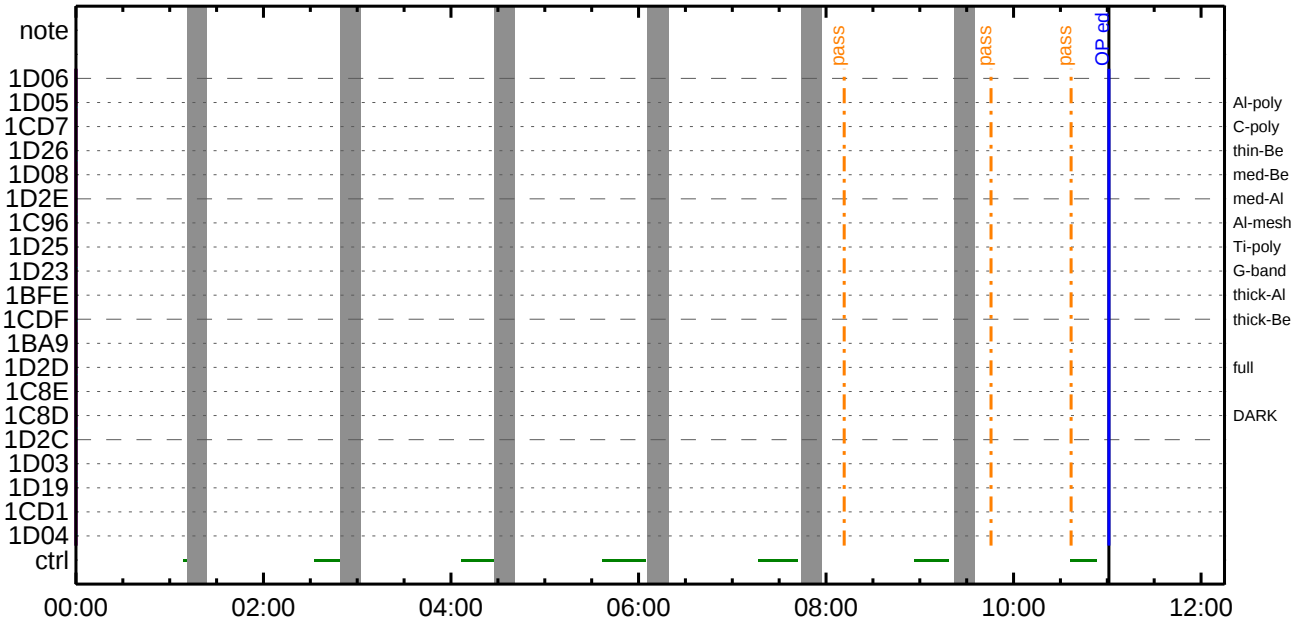
CMDI #0511 2025/04/11

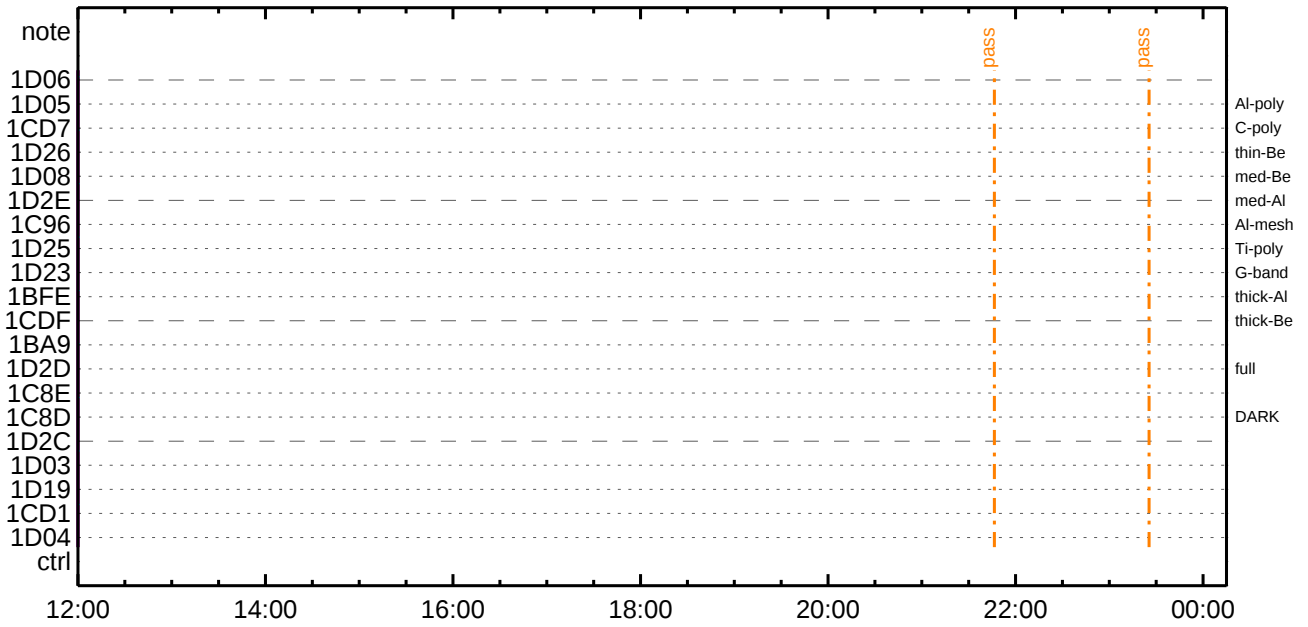


CMDI #0511 2025/04/11



CMDI #0511 2025/04/12





(a) Spacecraft Operation Procedure (real-commands)

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main-203 2025-04-08 12:32:37 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥$¥Á¥~¼Ä»Ü;ä
0005 C.
0006 C. ¥À¥ß;¼¥³¥Ð¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¼¿¼Ä¼•µ°Æ»Í×ÁÇ¼Í¥Ç¥Á¥×¥í;¼¥É;ËËè¼µ•ííË;ËËË¼°ÇÖ¼•¼¿¼í¹Ç¼Í;ÇÀ®,ù¼¹¼è¼µ¼ÇÁ÷¿®¼•¼Ë¼µ¼³¼È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥í;¼¥É;|¥Á¥ó¥×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OG¥í;¼¥É;ä
0019 . S. OP op-203:OP
0020 ()
0021 . S. OG og-203:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPíî°è¥Á¥ó¥×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0036 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥À¥ó¥×¼°áî»¼³îÇ$
0041 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¼î¾Ë¹Ç•è²îOK¼³îÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0055 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥À¥ó¥×¼°áî»¼³îÇ$
0060 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¼î¾Ë¹Ç•è²îOK¼³îÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0074 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥À¥ó¥×¼°áî»¼³îÇ$
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¼î¾Ë¹Ç•è²îOK¼³îÇ$
0081 C.
0082 . C. ***** °Ë²¼¼¼î¾Ä´¼¼°¼ËË¬¼°Á÷¿® (¼¼¼µ-¥À¥ó¥×¼°è¼Ç¼¼°ÁÖÄ¼¼Ç¼°¼¼¬¼è¼¼¹Ç¼Ç¼¼) *****
0083 C. DHU¥ä;¼¥É;Ë¼ý¼, ¥í;¼¥É;Ë¼òíá¼¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0087 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOAD¼¬Á÷¿®NG¼î¾í¹Ç;Ç°Ë²¼¼¼îTI-CMDÁ÷¿®¼î¾Ä¹Ö¼•¼Ë¼µ¼³¼È;£
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0096	C.	mPm; j; cSETmEDDUMPmIÆ±°iYÑY¹mÇ¹Ôm m³mÈ;j		
0097	C.			
0098	. C.	TIY³YDYOYÉmðÄDİç (UT)		
0099	+. TI	2025-04-08 12:01:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+. TI	2025-04-08 12:01:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+. TI	2025-04-08 12:01:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+. TI	2025-04-08 12:05:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼mİÄê¾iİÑmİYÁYŞYÄY⁻¹àiÜ		
0116	C.	çç [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	çç [HK1_TI_CMD_NUM]	EQ	4
0118	C.	çç [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	çç [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	. C.	*****		
0122	C.	TIİİ°èYÄYOY×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL (0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	çç [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	çç [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	çç [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	çç [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	çç [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+. DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	çç [HK1_PKT_FORM_NO]	EQ	7
0136	C.	çç [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	çç [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	çç [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	çç [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	. C.	YÄYOY×½ªİ»mð³İÇŞ		
0142	C.	çç [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	. C.	RAM ID=TI_TBLmİ¾È¹Ç•ë²İOKmð³İÇŞ		
0145	C.			
0146	. C.	DHUWâ;¼YÉ;È¼Y½,Yi;¼YÈ;Èmðİáª¹		
0147	+. DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	çç [HK1_PKT_FORM_NO]	EQ	2
0150	C.	çç [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	çç [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	çç [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.			
0155	C.	***** XRT START *****		
0156	C.	Execute, after the success of OP upload.		
0157	+. TI	2025-04-08 12:05:00.0		
0158	DC	07-F0 MDP_XRT_MODE_STBY		
0159	BC	(c3)		
0160	. C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0161	C.			
0162	C.	***** XRT END *****		
0163	. C.	Stop EIS observation and temporarily disable EIS mode changes		
0164	C.			
0165	C.			
0166	C.	***** Start EIS operation (TI set) *****		
0167	C.	Execute, after the success of OP upload.		
0168	C.	Set EIS TI-commands		
0169	+. TI	2025-04-08 12:05:30.0		
0170	DC	07-FC EIS_MODE_MANU		
0171	BC	(21 02)		
0172	+. TI	2025-04-08 12:05:40.0		
0173	DC	07-FC EIS_MODE_CHG_DIS		
0174	BC	(22)		
0175	. C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0176	C.	***** End EIS operation (TI set) *****		
0177	C.			
0178	C.			
0179	C.			
0180	. C.	***** MDP 'üÄİmİ»ö¾YmÈÄDm¹mèDCBC•×²è *****		
0181	C.	(¾Ä°İYÖYÄYÉYDPEYÄYÇYÈmÈ¾¾¼m¼Ä»Üm¹mè)		
0182	. S.	DC-BC dcbbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	. C.	***** YDY¹•İ Daily±çİÑmÈ'Øm¹mèDCBC•×²è *****		
0187	. S.	DC-BC dcbbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	. C.	;äLOS¥ÄYŞYÄY⁻¼Ä»Ü;ä		
0192	C.			
0193	. C.			


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main-204 2025-04-08 12:37:37 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŸçŸÃŸ×Ÿí;¼ŸÉ;ËÈè²µ•íîÊ;ËÈÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Öòò¹íî,ñ•;çÉÔÍ×ñÈXÂÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÄÖñ¬°ÄÄêñ•ñççé;ç°Ê²¼ñî°EÄ,¼ê½Çñò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñççñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0045 C.
0046 . C. ;ãŸçŸóŸEŸEËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ã;ç°Ê²¼ñò¼Ä¹Ôñ¹ñè;£
0055 C. ŸçŸóŸEŸEËÄÚÂØñäÄ•Ä°²öÈòñ¬¼áññ¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñççñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0071 C.
0072 . C. ;ãŸçŸóŸEŸEËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5917402.5 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 16s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-04-08 12:05:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0165 C. (¾å°î¥Ó¥Å¥Ê¥Ð¥Ê¥Å¥Ç¥ÊñÊ¼¾ñ¼Å»Û¹ñë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±¿ÍÑñÊ'Øñ¹ñëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥Á¥S¥Å¥¹¼Å»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-205 2025-04-08 12:32:37 108 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;āAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Āí;Ēñ¿ñÄñ•µ°Eñİ×ÄÇñİŸçŸÄŸ×Ÿí;¼ŸÉ;ĒĒè½µ•íĒĒ;ĒñĒ¼°ÇÖñ•ñ¿¼ı¹Çñİ;çÄ®,ùñ¹ñĒñDñÇÄ÷¿®ñ•ñĒñññ³ñĒ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 c0 c0 10 10)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 40 c0 10 10)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 40 40 10 10)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b c0 40 10 10)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0c 80 80 20 08)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0d 80 80 08 20)
0060 +. DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0e 80 80 08 08)
0062 +. DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 0f 80 80 06 06)
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 10 80 80 08 08)
0066 +. DC 07-F0 MDP_XRT_FLD_ENA
0067 BC (d8)
0068 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0069 BC (c8)
0070 +. DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 +. DC 07-F0 MDP_XRT_AEC_RESET
0073 BC (d0)
0074 +. DC 07-F0 MDP_XRT_FLD_RESET
0075 BC (da)
0076 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0077 BC (c4 09)
0078 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0079 BC (c5 0e)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C.
0083 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0084 C.
0085 +. DC 07-F0 MDP_XRT_MODE_OBSV
0086 BC (c2)
0087 +. TI 2025-04-08 12:05:02.0
0088 DC 07-F0 MDP_XRT_MODE_OBSV
0089 BC (c2)
0090 . C. ----- Success Verify ? OK / NG ____
0091 C.
0092 C. ***** XRT END *****
0093 C.
0094 . C. ***** MDP 'ûĀîñİ»ö¼ŸñĒĀDñ¹ñĒDCBC•×²è *****
0095 C. (¼ā°İŸÓŸÄŸĒŸDŸĒŸÄŸçŸĒŸĒ½¼ññ¼Ä»Üñ¹ñĒ)
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0096 . S. DC-BC dcbc-402:DCBC
0097 (MDP_known_event)
0098 C.
0099 C.
0100 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ě'Ø¤¹¤ěDCBC•x²è *****
0101 . S. DC-BC dcbc-153:DCBC
0102 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0103 C.
0104 C.
0105 . C. ;ăLOS¥Á¥S¥Ã¥~¼Â»Ü;ă
0106 C.
0107 . C. ***** LOS *****
0108 C.
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Apr 08, 25 12:32

XRT_OGLIST_0511.chk

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*** OP Sequence for XRT ***

2025/04/08	12:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	12:10:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	12:10:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	12:10:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	12:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	12:16:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 64 01 ca				
2025/04/08	13:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	13:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	13:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	13:48:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	13:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	15:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	15:26:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	15:26:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	15:26:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	15:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	17:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	17:04:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	17:04:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	17:04:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	17:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00 00				
2025/04/08	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 64 01 ca				
2025/04/08	18:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	18:41:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	18:41:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	18:41:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	18:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	20:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	20:17:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	20:17:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	20:17:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	20:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	21:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	21:55:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	21:55:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	21:55:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	21:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/08	23:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	23:32:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/08	23:32:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/08	23:32:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/08	23:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/09	01:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/09	01:09:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/09	01:09:04.0	XRT_FLD_RESET_415_OG [0x19f]							

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2025/04/09	01:09:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	01:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	02:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	02:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	02:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	02:33:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	02:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	02:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	02:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	02:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	02:53:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	02:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	04:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	04:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	04:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	04:07:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	04:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	04:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	04:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	04:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	04:31:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	04:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	05:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	05:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	05:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	05:37:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	05:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	06:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	06:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	06:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	06:09:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	06:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	06:17:30.0	AOCs_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	06:27:30.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2025/04/09	06:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 18 e5 b4 73
2025/04/09	06:50:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_445_OG [0x1bd]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	07:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2025/04/09	07:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	07:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	07:17:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	07:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	07:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	07:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	07:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	07:47:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	07:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09			MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2025/04/09	13:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	d1	07	d1 07
2025/04/09	13:12:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2025/04/09	13:12:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2025/04/09	13:12:56.0	XRT_FLD_DIS_429_OG [0x1ad]	MDP_XRT_FLD_DIS	1	07-F0		d9		
2025/04/09	13:12:58.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2025/04/09	13:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2025/04/09	13:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	13:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	13:19:58.0	XRT_FOCUS_POSITION_432_OG [0x1b0]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2025/04/09	13:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	d1	07	2e f9
2025/04/09	13:22:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2025/04/09	13:22:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2025/04/09	13:22:56.0	XRT_FLD_DIS_429_OG [0x1ad]	MDP_XRT_FLD_DIS	1	07-F0		d9		
2025/04/09	13:22:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2025/04/09	13:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2025/04/09	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	13:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	13:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2025/04/09	13:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00
2025/04/09	13:30:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0		d9		
2025/04/09	13:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2025/04/09	13:30:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2025/04/09	13:32:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2025/04/09	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2025/04/09	13:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	13:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	13:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/04/09	13:40:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	02	03	64	01 ca
2025/04/09	13:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2025/04/09	13:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2025/04/09	13:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2025/04/09	13:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2025/04/09	13:40:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0		da		
2025/04/09	13:42:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2025/04/09	13:42:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/04/09	13:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2025/04/09	14:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	14:19:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	14:19:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2025/04/09	14:19:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2025/04/09	14:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2025/04/09	14:28:30.0	XRT_Custom_430_OG [0x1ae]							
2025/04/09	14:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2025/04/09	15:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	15:57:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2025/04/09	15:57:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2025/04/09	15:57:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							

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2025/04/09	16:00:44.0	MDP_XRT_PREFLR_STRT	1	07-F0	e8
		XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	16:14:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/09	16:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/09	17:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	17:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	17:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	17:35:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	17:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/04/09	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/04/09	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/04/09	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/04/09	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/09	18:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/04/09	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/09	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/09	18:10:00.0	AOCS_ORe-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	02 03 64 01 ca
2025/04/09	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/09	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/04/09	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/09	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/09	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	18:12:56.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2025/04/09	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/04/09	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/09	19:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	19:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	19:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	19:13:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	19:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	19:36:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/09	19:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/09	20:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	20:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	20:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/09	20:50:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/09	20:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/09	21:13:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/09	21:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/09	21:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/09	21:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/09	21:15:00.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 55 26 01 ca
2025/04/09	21:15:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/09	21:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			

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2025/04/09	21:15:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/04/09	21:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/04/09	21:15:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/04/09	21:17:56.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/09	21:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2025/04/09	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/04/09	22:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/09	22:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/09	22:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/09	22:27:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/09	22:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/09	22:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/09	22:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/10	00:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	00:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	00:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/10	00:04:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/10	00:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/10	00:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/10	00:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	01:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	01:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/10	01:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/10	01:41:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/10	01:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/10	01:56:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	01:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/10	03:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/10	03:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/10	03:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/10	03:03:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	03:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/10	03:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/10	03:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/10	03:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/10	03:24:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	03:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/10	03:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/10	03:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/10	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/10	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/10	04:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/04/10	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	00 00 00 00
2025/04/10	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/04/10	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/04/10	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/04/10	04:00:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/04/10	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f

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2025/04/10	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/04/10	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/04/10	04:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	04:35:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	04:35:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/10	04:35:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/10	04:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/10	05:00:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	05:00:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	05:00:04.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/04/10	05:00:24.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/04/10	05:00:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/04/10	05:00:28.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/04/10	05:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	05:02:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	05:02:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/10	05:02:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/10	05:03:04.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2025/04/10	05:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/10	05:12:30.0	XRT_Custom_430_OG [0x1ae]							
2025/04/10	05:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/04/10	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	05:53:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	05:53:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/04/10	05:53:30.0	AOCs_Ore-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	02	03	64	01	ca
2025/04/10	05:53:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/04/10	05:53:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/04/10	05:53:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/04/10	05:53:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/04/10	05:53:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/10	05:56:26.0	XRT_QT_PROG_SET_405_OG [0x195]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2025/04/10	05:56:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/04/10	05:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/04/10	06:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	06:10:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	06:10:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/10	06:10:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/10	06:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/10	06:37:30.0	XRT_Custom_430_OG [0x1ae]							
2025/04/10	06:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/04/10	06:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	06:40:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/04/10	06:40:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/04/10	06:40:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/04/10	06:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/04/10	06:50:30.0	XRT_Custom_430_OG [0x1ae]							
2025/04/10	06:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/04/10	07:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2025/04/10	07:50:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	07:50:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/04/10	07:50:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/04/10	07:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/04/10	08:14:30.0	XRT_Custom_430_OG [0x1ae]		
2025/04/10	08:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/04/10	08:18:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	08:18:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	08:18:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/04/10	08:18:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/04/10	08:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/04/10	08:29:00.0	XRT_Custom_430_OG [0x1ae]		
2025/04/10	08:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/04/10	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	09:30:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	09:30:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/04/10	09:30:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/04/10	09:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/04/10	09:51:00.0	XRT_Custom_430_OG [0x1ae]		
2025/04/10	09:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/04/10	09:56:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	09:56:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	09:56:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/04/10	09:56:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/04/10	09:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/04/10	10:07:00.0	XRT_Custom_430_OG [0x1ae]		
2025/04/10	10:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/04/10	11:10:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	11:10:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/04/10	11:10:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/04/10	11:10:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/04/10	11:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/04/10	11:44:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00