

Period: 2025/04/12 11:01:00 - 2025/04/17 11:32:00

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/1443)														
Term			Pointing (x, y)				Comment							
04/12 18:03:00 - 04/12 18:09:54			Fixed (0.0, 0.0)				synoptic							
04/13 05:46:00 - 04/13 05:52:54			Fixed (0.0, 0.0)				synoptic, shifted -17.0 min							
04/13 17:46:30 - 04/13 17:53:24			Fixed (0.0, 0.0)				synoptic, shifted -16.5 min							
04/14 06:03:00 - 04/14 06:09:54			Fixed (0.0, 0.0)				synoptic							
04/14 18:03:00 - 04/14 18:09:54			Fixed (0.0, 0.0)				synoptic							
04/15 06:22:30 - 04/15 06:29:24			Fixed (0.0, 0.0)				synoptic, shifted 19.5 min							
PROG= 03 1-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 5 1-time(s) 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec														
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec														
Seqn= 55 1-time(s) 2.0sec														
└ Open/Al-mesh Open/Al-mesh close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 98 1-time(s) 2.0sec														
└ Al-poly/Open Al-poly/Open close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 76 1-time(s) 2.0sec														
└ thin-Be/Open thin-Be/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ thin-Be/Open thin-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 23 1-time(s) 4.0sec														
└ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=90 0 0 2.0sec														
└ Open/G-band Open/G-band close Safe Norm 1ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Subr= 2 1-time(s) 2.0sec														
Seqn= 41 1-time(s) 2.0sec														
└ Open/thick-Be Open/thick-Be close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
Seqn= 18 1-time(s) 2.0sec														
└ med-Al/Open med-Al/thick-Al close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ med-Al/Open med-Al/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ med-Al/Open med-Al/Open close Safe Norm 32.0s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 86 1-time(s) 2.0sec														
└ med-Be/Open Open/thick-Al close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ med-Be/Open med-Be/Open close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Seqn= 54 1-time(s) 2.0sec														

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D2E: HOP349 - 3-filter Synoptics (Al-mesh[5/181/1443], Al-poly[12/256/2048], thin-Be[24/723/5795] with 512x512 G-band+Leak - 375min cad) + CME												
Term			Pointing (x, y)			Comment						
04/13 04:03:00 - 04/13 05:42:54			Fixed (0.0, 0.0)			HOP 349						
04/14 04:03:00 - 04/14 05:59:54			Fixed (0.0, 0.0)			HOP 349						
04/15 04:03:00 - 04/15 06:19:24			Fixed (0.0, 0.0)			HOP 349						

PROG= 15 Inf-time(s)												
Subr= 1		1-time(s)		300.0sec								
Seqn= 66		1-time(s)		2.0sec								
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh		Open/Ti-poly		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 22		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/Open		close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 61		1-time(s)		2.0sec								
thin-Be/Open		thin-Be/Open		close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 30		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0 0 2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0 0 2.0sec
Subr= 2		15-time(s)		1500.0sec								
Seqn= 8		1-time(s)		2.0sec								
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
Seqn= 74		1-time(s)		2.0sec								
med-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
med-Be/Open		med-Be/Open		close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2 0 2.0sec
Seqn= 6		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
Seqn= 29		1-time(s)		2.0sec								
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G												
Term			Pointing (x, y)			Comment						
04/12 11:14:00 - 04/12 17:35:30			Track (747.1, 143.1) @ 04/12 11:11:00			# OP start + 10min / AR 14055 obs.						
04/12 18:13:00 - 04/13 03:59:54			Track (782.9, 137.4) @ 04/12 18:10:00			AR 14055 obs.						
04/13 04:03:00 - 04/13 05:42:54			Fixed (0.0, 0.0)			HOP 349						
04/13 05:56:00 - 04/13 17:43:24			Track (-571.7, 148.3) @ 04/13 05:53:00			AR 14060 obs.						
04/13 17:56:30 - 04/14 03:59:54			Track (-477.7, 153.6) @ 04/13 17:53:30			AR 14060 obs.						
04/14 04:03:00 - 04/14 05:59:54			Fixed (0.0, 0.0)			HOP 349						
04/14 06:13:00 - 04/14 17:59:54			Track (-375.3, 157.9) @ 04/14 06:10:00			AR 14060 obs.						
04/14 18:13:00 - 04/15 03:59:54			Track (-270.2, 160.7) @ 04/14 18:10:00			AR 14060 obs.						
04/15 04:03:00 - 04/15 06:19:24			Fixed (0.0, 0.0)			HOP 349						
04/15 06:32:30 - 04/15 11:41:54			Track (-158.9, 162.3) @ 04/15 06:29:30			AR 14060 obs.						

PROG= 14 30-time(s)												
Subr= 1		20-time(s)		2.0sec								
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn= 73		1-time(s)		10.0sec								
thin-Be/Open		med-Be/Open		close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
med-Be/Open		Open/thick-Al		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 10		1-time(s)		2.0sec								
med-Al/Open		med-Al/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn= 87		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

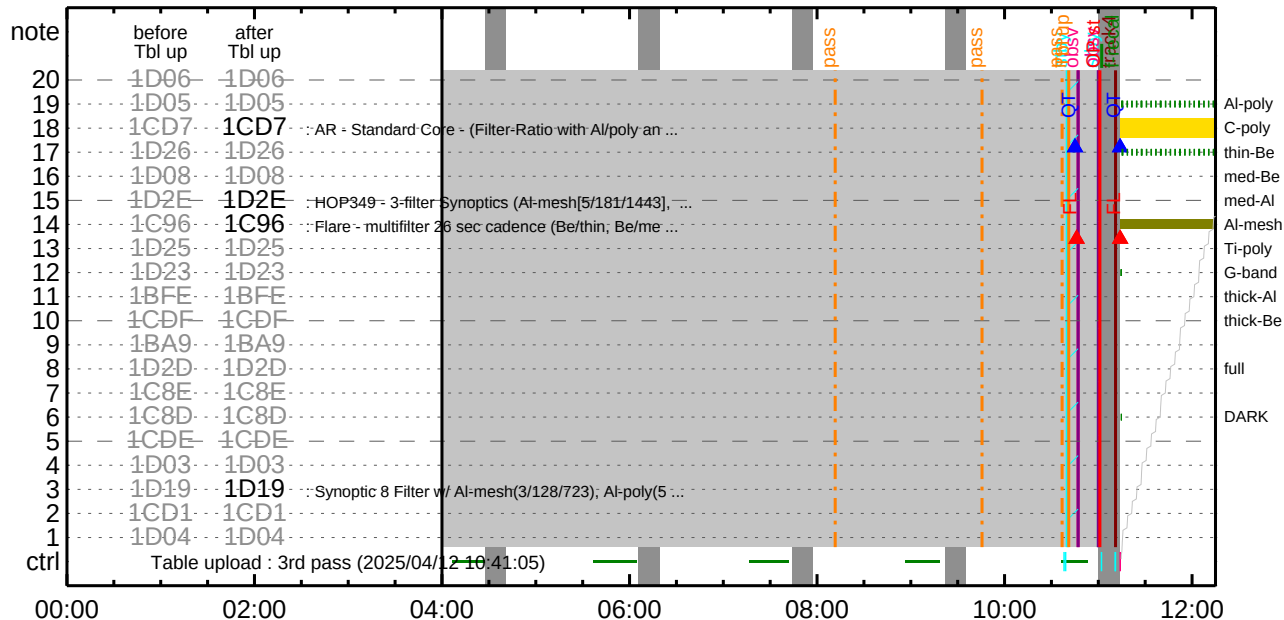
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Flare Detection

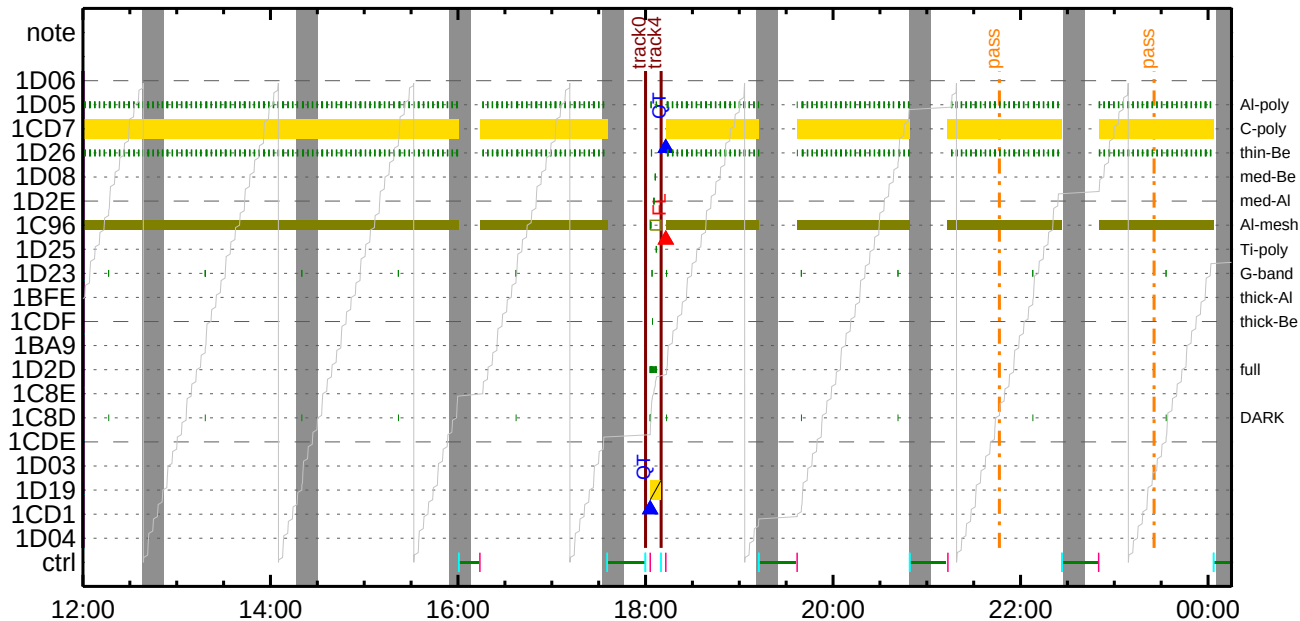
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FLD Patrol												
Term			Pointing (x, y)						Comment			
04/12 10:42:05 - 04/12 18:00:18			cannot be identified									
04/12 18:10:18 - 04/13 05:43:18			Track (782.9, 137.4)		@ 04/12 18:10:00		AR 14055 obs.					
04/13 05:53:18 - 04/13 17:43:48			Track (-571.7, 148.3)		@ 04/13 05:53:00		AR 14060 obs.					
04/13 17:53:48 - 04/14 06:00:18			Track (-477.7, 153.6)		@ 04/13 17:53:30		AR 14060 obs.					
04/14 06:10:18 - 04/14 18:00:18			Track (-375.3, 157.9)		@ 04/14 06:10:00		AR 14060 obs.					
04/14 18:10:18 - 04/15 06:19:48			Track (-270.2, 160.7)		@ 04/14 18:10:00		AR 14060 obs.					
04/15 06:29:48 - 04/17 11:32:00			Track (-158.9, 162.3)		@ 04/15 06:29:30		AR 14060 obs.					
Al-poly/Open			Al-poly/Open		close	Safe	Norm	4ms	Obs	8x8	Q=50	
Default Filter			Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	
											Comp.	AEC Buffer Interval

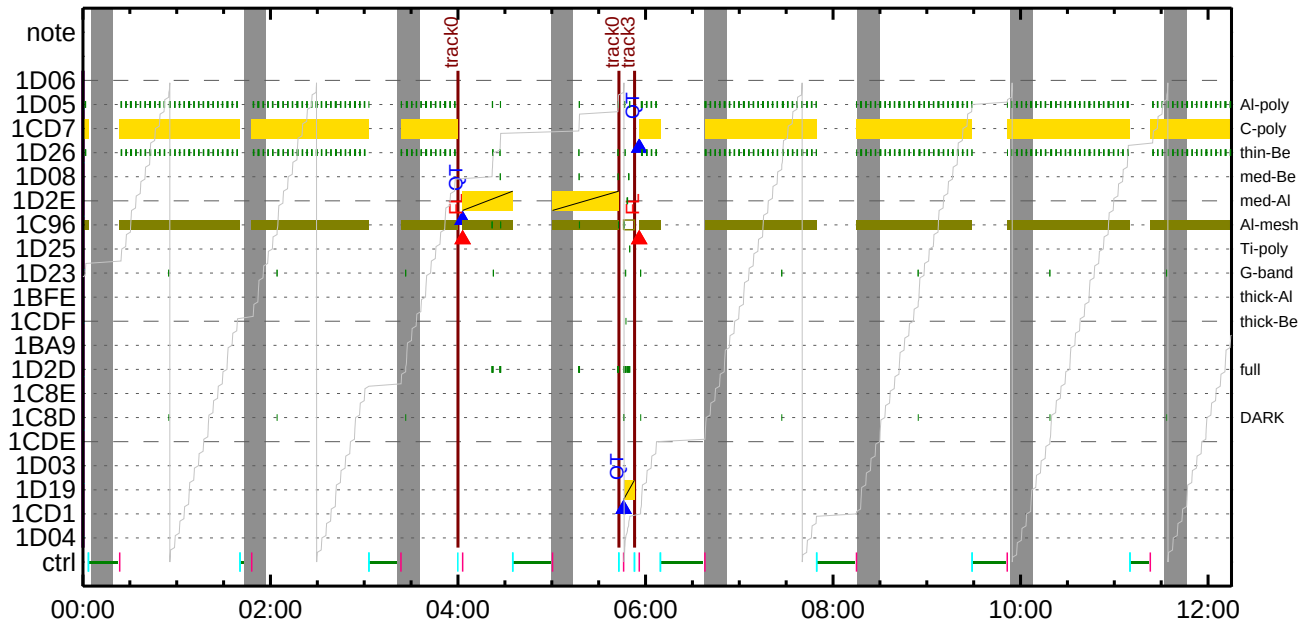
CMDI #0520 2025/04/12



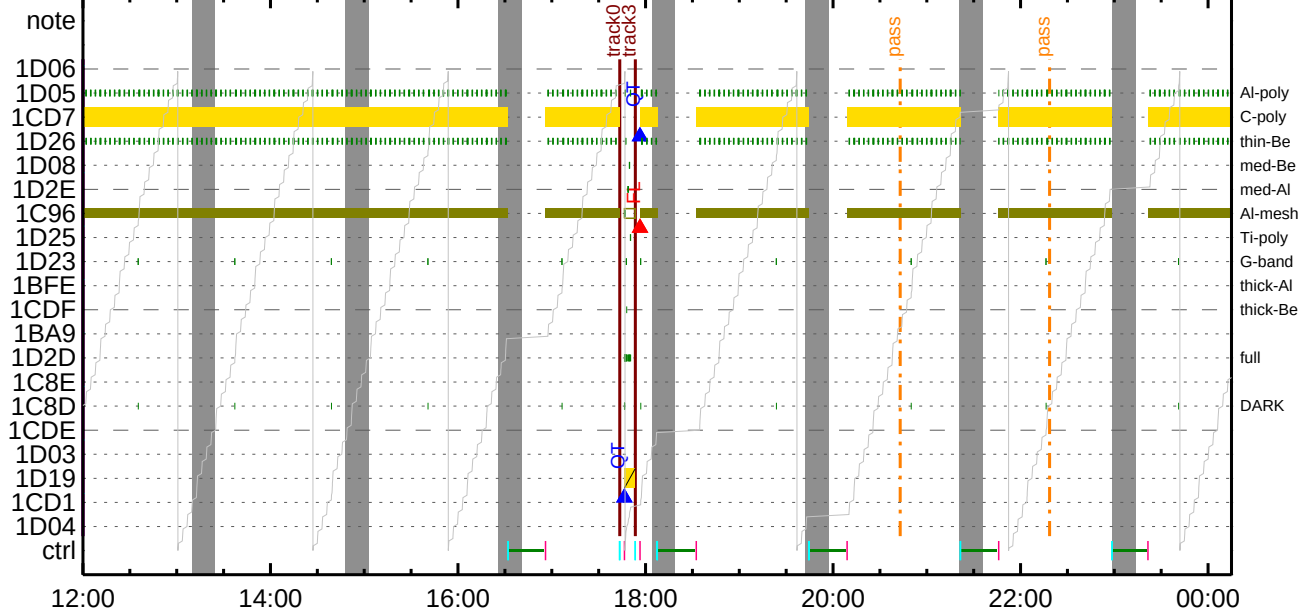
CMDI #0520 2025/04/12



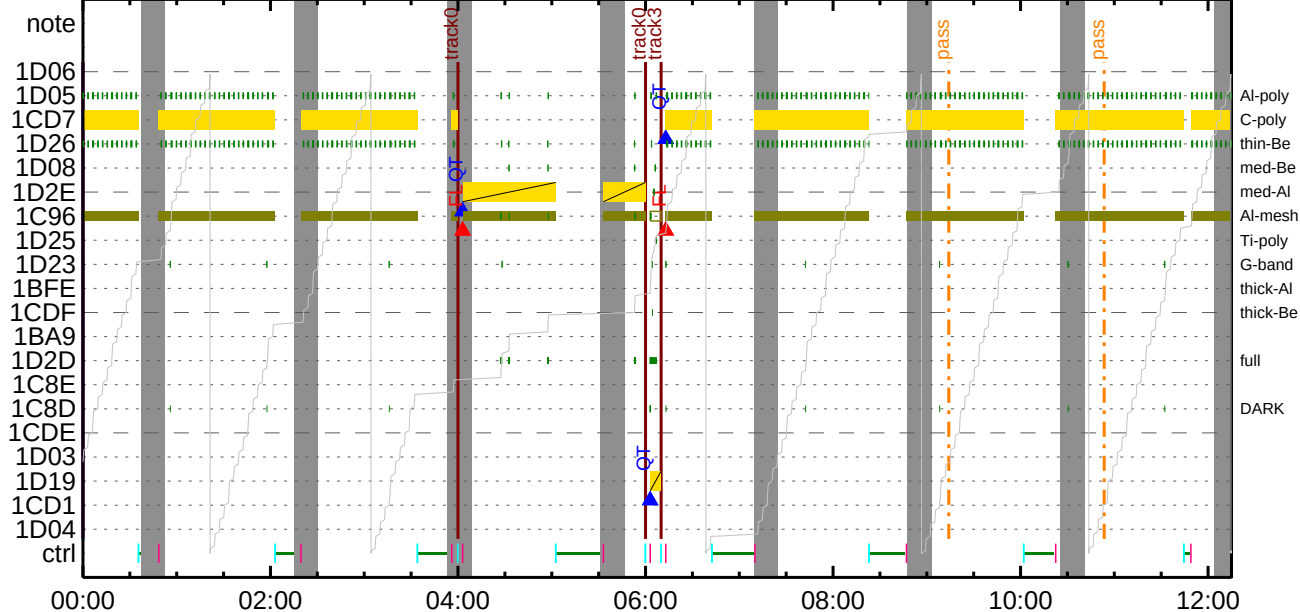
CMDI #0520 2025/04/13



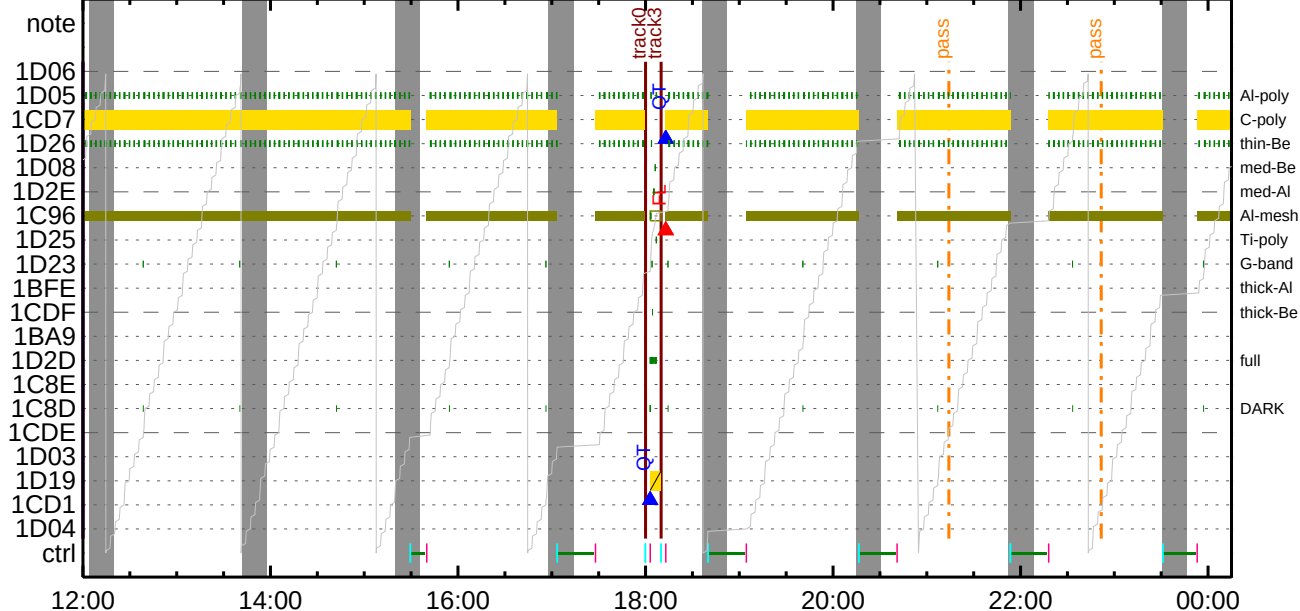
CMDI #0520 2025/04/13



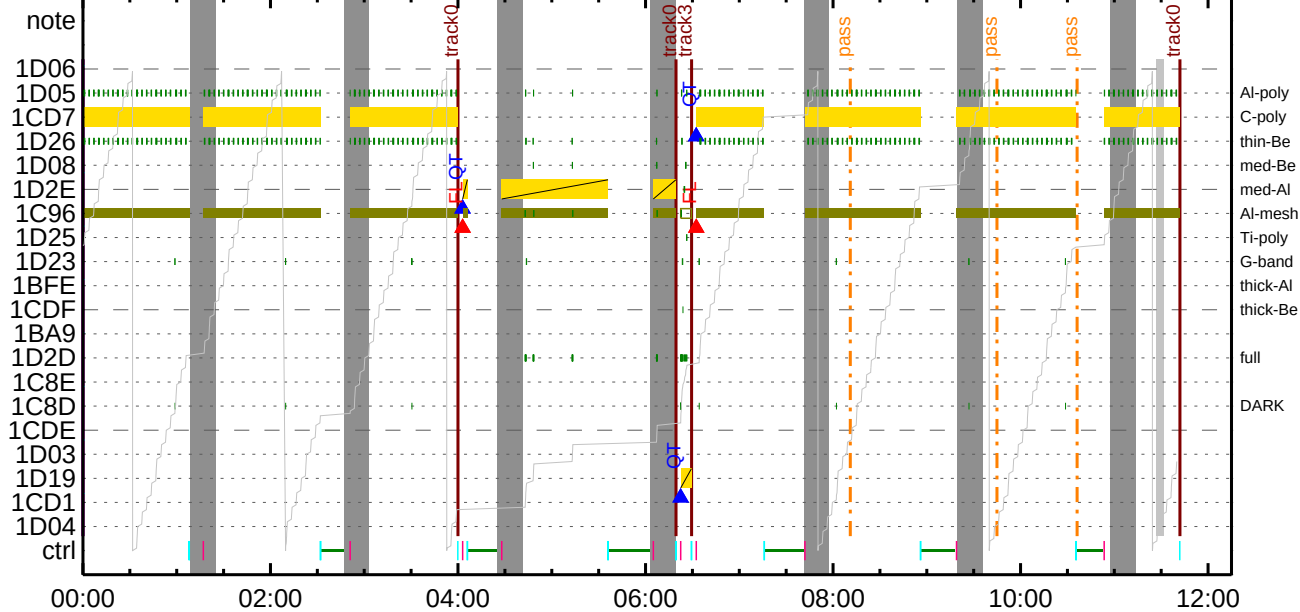
CMDI #0520 2025/04/14



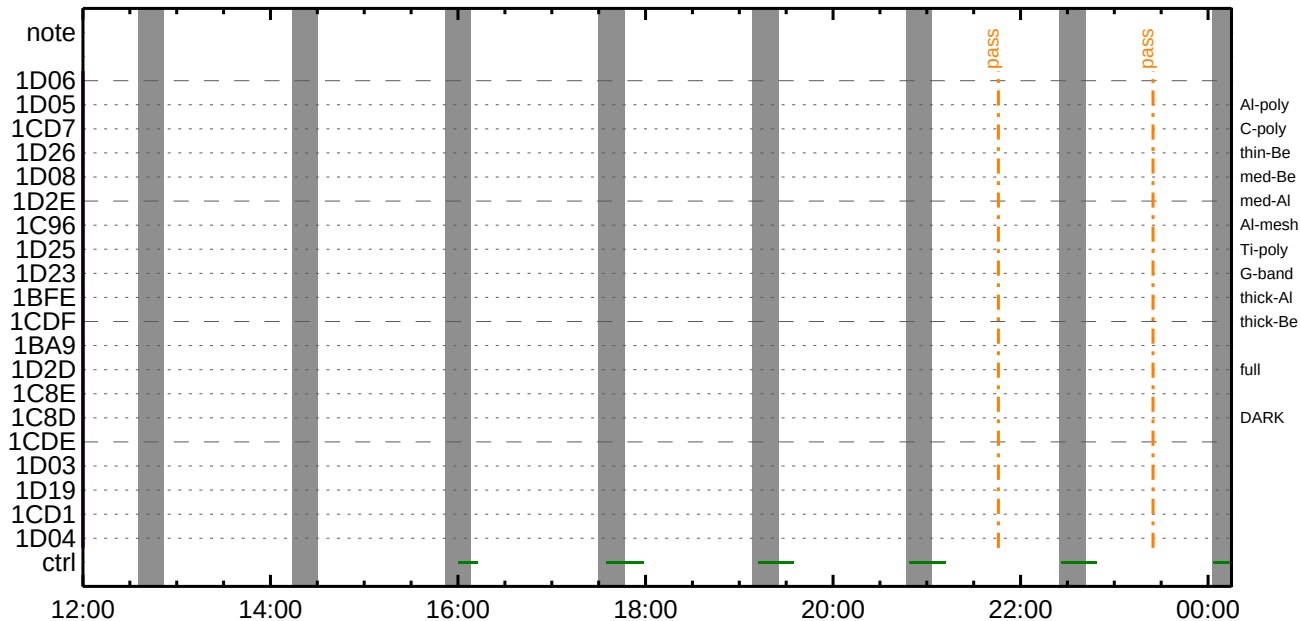
CMDI #0520 2025/04/14



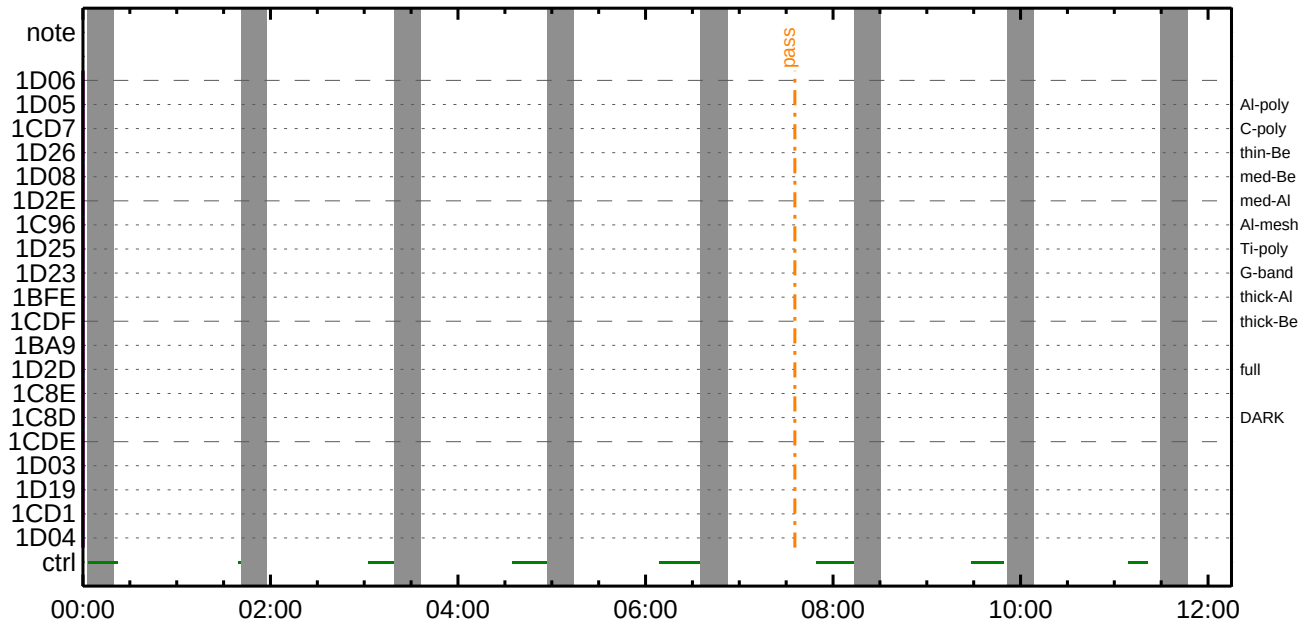
CMDI #0520 2025/04/15



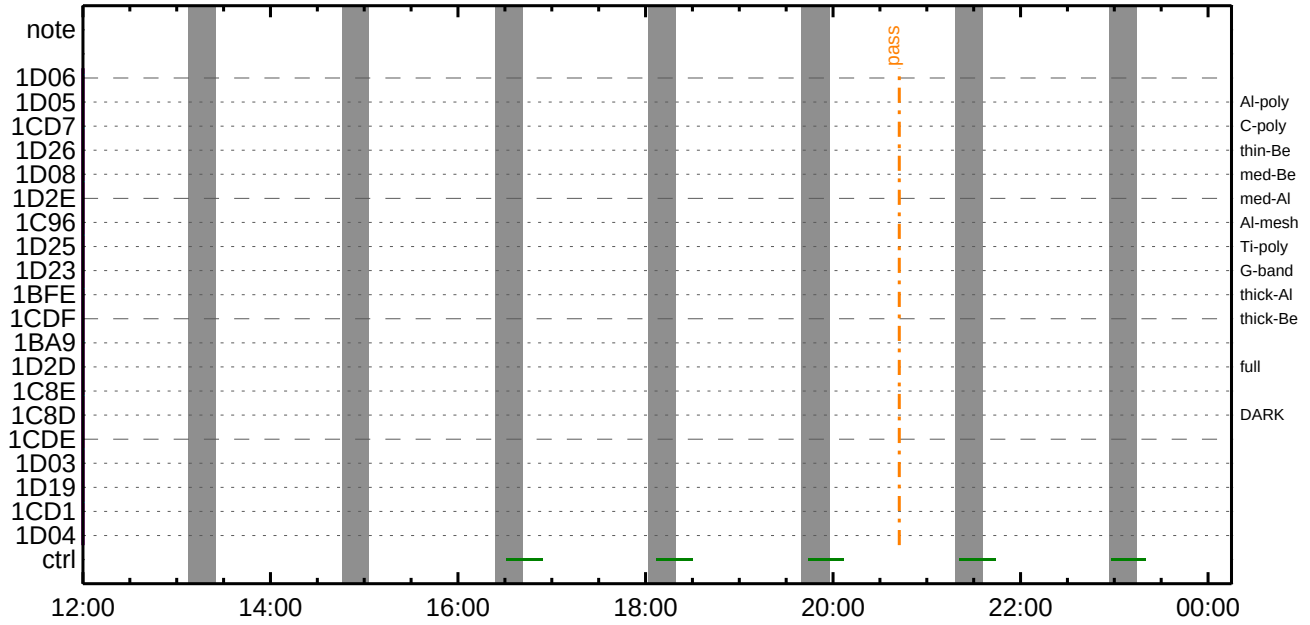
CMDI #0520 2025/04/15



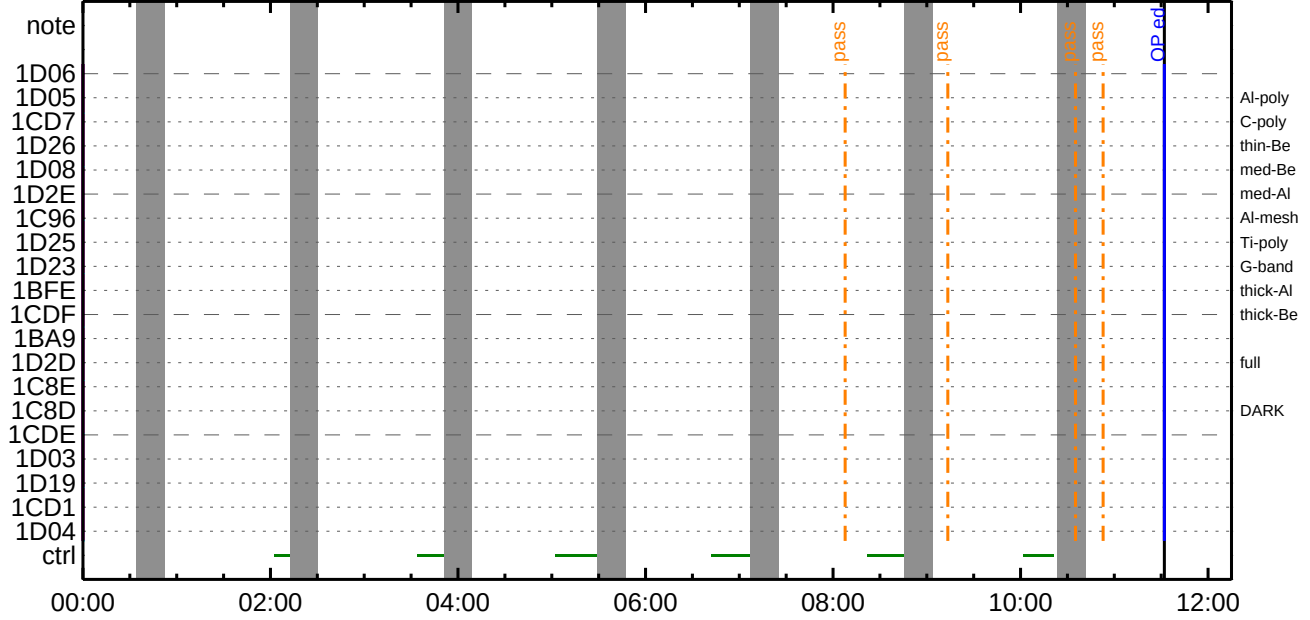
CMDI #0520 2025/04/16



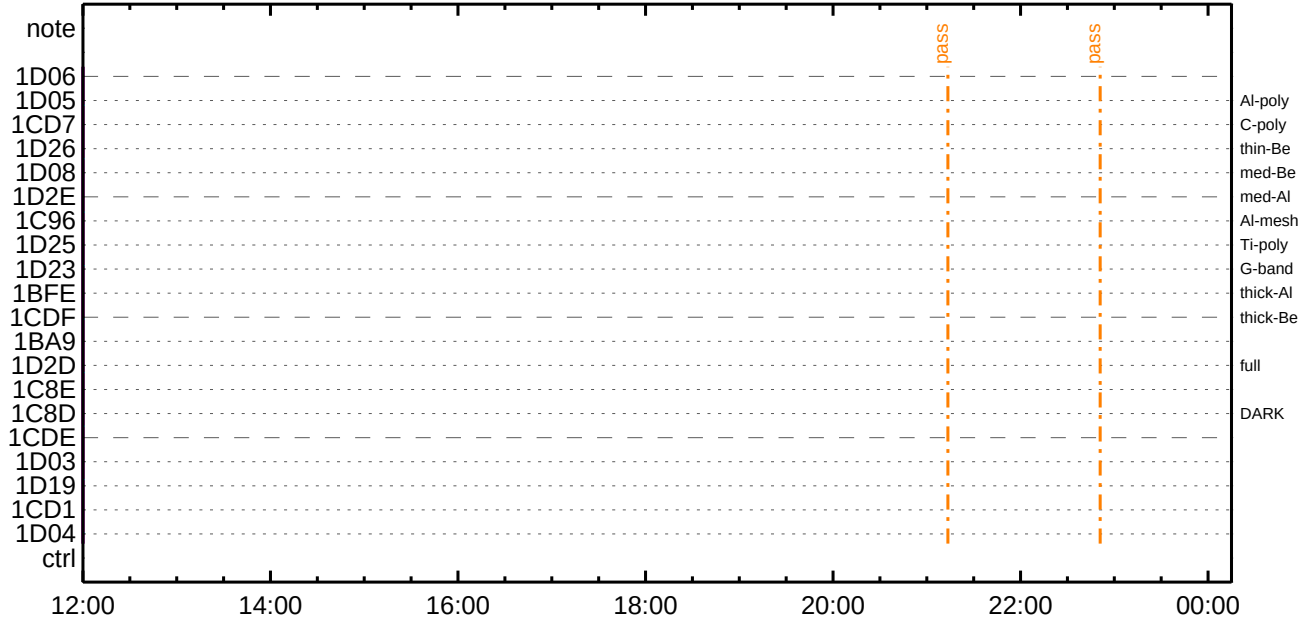
CMDI #0520 2025/04/16



CMDI #0520 2025/04/17



CMDI #0520 2025/04/17



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main-211 2025-04-12 11:21:00 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸŞŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËðçðÀð•µ°E»Í×ÅÇñÎŸçŸÄŸ×Ÿí;¼ŸÉ;ÊÈèèµ•íîÊ;ÊÈÈ¼°ÇÔð•ðç¾ì¹çðİ;çÀ®,ùð¹ðèðÞðÇÄ÷ç®ð•ðÈððð³ðÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XĂ÷ç®µ;ON
0016 C. *****
0017 C. °EA, í×ÈÝðäLOSðÞðÇðî»p´Òðò¹íî, ð•;çÊÖÍ×ðÈXÃOONðî¹ÒðÈðîðÈððð³ðÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÂÖð¬°ÄÄêð•ðççé;ç°Ê²¼ðî¹°EA,¼ê½çðò¼Ä¹Òð¹ðè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëð•ðçð¾ì¹çðİ;ç°Ê²¼ðî¹Ä¹Òð»ð°;çDCBC-150ðØçÊðà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ò,;¾Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò,;¾Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò,;¾Ú)
0045 C.
0046 . C. ;ãŸçŸóŸEŸÊÄÚÂØ;ÊÄ•Äº²öÈò;Ë,ãðî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ò,;¾Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò,;¾Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò,;¾Ú)
0052 C.
0053 C.
0054 . C. PT1°EA, ð¬¼«E°Ää»ßð•ðçç, ä;ç°Ê²¼ðò¼Ä¹Òð¹ðè;£
0055 C. ŸçŸóŸEŸÊÄÚÂØðäÄ•Äº²öÈòð¬¼áð¾ì¹çðİ´°î»ð¹ðèðÞðÇÄÔðÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëð•ðçð¾ì¹çðİ;ç°Ê²¼ðî¹Ä¹Òð»ð°;çDCBC-151ðØçÊðà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ò,;¾Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò,;¾Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò,;¾Ú)
0071 C.
0072 . C. ;ãŸçŸóŸEŸÊÄÚÂØ;ÊÄ•Äº²öÈò;Ë,ãðî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ò,;¾Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ò,;¾Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ò,;¾Ú)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;çXA÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXĂ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```


0096	C.			
0097	C.			
0098	C.	*****		
0099	C.	OP/OG¥1;¼¥É; ¥À¥ó¥×		
0100	C.	*****		
0101	C.			
0102	C.	;ãOP/OG¥1;¼¥É;ä		
0103	S.	OP op-211:OP		
0104	()			
0105	S.	OG og-211:OG		
0106	()			
0107	C.			
0108	C.	;ãNMOG&OPîî°è¥À¥ó¥×;ä		
0109	C.	NMOG(0x200000-0x207FFF;\$ 32 kbyte)		
0110	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0111	BC	(20 00 7f 01 02)		
0112	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	40
0113	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0
0114	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	127
0115	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0116	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0117	+. DC	01-22 DHU_MODE_CHNG		
0118	BC	(07 0b f8)		
0119	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7
0120	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0121	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0122	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0123	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0124	C.	¥À¥ó¥×½°î»¤ð³îÇ\$		
0125	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0126	C.	RAM ID=NMOG¤î¾È¹Ç•ë²îOK¤ð³îÇ\$		
0127	C.			
0128	C.	NMOG(0x208000-0x20FFFF;\$ 32 kbyte)		
0129	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0130	BC	(20 80 7f 01 02)		
0131	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	41
0132	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0
0133	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	127
0134	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0135	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0136	+. DC	01-22 DHU_MODE_CHNG		
0137	BC	(07 0b f8)		
0138	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7
0139	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0140	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0141	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0142	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0143	C.	¥À¥ó¥×½°î»¤ð³îÇ\$		
0144	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0145	C.	RAM ID=NMOG¤î¾È¹Ç•ë²îOK¤ð³îÇ\$		
0146	C.			
0147	C.	NMOG(0x210000-0x2100FF;\$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)		
0148	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0149	BC	(21 00 41 01 02)		
0150	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	42
0151	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0
0152	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	65
0153	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0154	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0155	+. DC	01-22 DHU_MODE_CHNG		
0156	BC	(07 0b f8)		
0157	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7
0158	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0159	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0160	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0161	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0162	C.	¥À¥ó¥×½°î»¤ð³îÇ\$		
0163	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0164	C.	RAM ID=NMOG, RAM ID=OP¤î¾È¹Ç•ë²îOK¤ð³îÇ\$		
0165	C.			
0166	C.	***** °È²¼¤î¾Ä´¶Á°¤ÈÉ¬¤°Á÷÷@ (¾âµ-¥À¥ó¥×¼ê½Ç¤ðÄÓÃæ¤Ç½°¤¬¤ë¾ì¹Ç¤Ç¤â) *****		
0167	C.	DHU¥â;¼¥É;È¼ý½,¥ì;¼¥É;È¤ðîÄ¤¹		
0168	+. DC	01-22 DHU_MODE_CHNG		
0169	BC	(02 0a f8)		
0170	C.	¢¢[HK1_PKT_FORM_NO]	EQ	2
0171	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S
0172	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K
0173	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0174	C.			
0175	C.	*****		
0176	C.	TI-CMD SET (OPOG STOP/COPY/START)		
0177	C.	*****		
0178	C.			
0179	C.	NOTICE ;\$ OPOG UPLOAD¤¬Á÷÷@NG¤î¾ì¹Ç;¢°È²¼¤î¾TI-CMDÁ÷÷@¤î¾Ä¹Ô¤¤¤È¤¤¤¤¤¤;£		
0180	C.	¤¤¤¤;¢SET¤ÈDUMP¤î¾±°ì¾¥¹¹¤Ç¹Ô¤¤¤¤¤¤;£		
0181	C.			
0182	C.	TI¥³¥¤¥ó¥É¤ðÄBîÇ (UT)		
0183	+. TI	2025-04-12 10:56:00.0		
0184	DC	01-B3 DHU_OP_STOP		
0185	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0186	C.			
0187	+. TI			

```

0194 C.
0195 +. TI 2025-04-12 11:00:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê¼¤İÄê¼İİ¤İŸÄŸ$ŸÄŸ~¹aiÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]      EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]      EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]      EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¼È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHUŸâ;¼ŸÉ;Ê¼ý½,Ÿi;¼ŸÈ;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0237 C.
0238 C.
0239 C. ***** XRT START *****
0240 C. Execute, after the success of OP upload.
0241 +. TI 2025-04-12 11:00:00.0
0242 DC 07-F0 MDP_XRT_MODE_STBY
0243 BC      (c3)
0244 C.          [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0245 C.
0246 C. ***** XRT END *****
0247 C. Stop EIS observation and temporarily disable EIS mode changes
0248 C.
0249 C.
0250 C. ***** Start EIS operation (TI set) *****
0251 C. Execute, after the success of OP upload.
0252 C. Set EIS TI-commands
0253 +. TI 2025-04-12 11:00:30.0
0254 DC 07-FC EIS_MODE_MANU
0255 BC      (21 02)
0256 +. TI 2025-04-12 11:00:40.0
0257 DC 07-FC EIS_MODE_CHG_DIS
0258 BC      (22)
0259 C.          [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0260 C. ***** End EIS operation (TI set) *****
0261 C.
0262 C.
0263 C.
0264 C. ***** MDP ´ûÄİ¤İ»ö¼Ÿ¤ÈÄ¤¤¹¤èDCBC•×²è *****
0265 C. (¼å°İŸÖŸÄŸÈŸŸŸÈŸâŸçŸë¤È½¼¤¤¼Ä»Ü¤¹¤è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŸĐŸ¹•İ Daily±;İİ¤İ»Ø¤¹¤èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOSŸÄŸ$ŸÄŸ~¼Ä»Ü;ä
0276 C.
0277 C. ***** LOS *****
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-212 2025-04-12 11:31:00 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¿¿Ä»•µ°Æ»Í×ÄÇ»ÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËè¿µ•ííË;Ë»Ë¼°ÇÖ»•¿¿¼¿¹¿Ç»Í;çÄ®,ù»¹»è»Ð»ÇÄ÷¿®»•»Ë»»»³»È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ()
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 C. Verify EIS in MANUAL mode
0052 C. Estimated OBSTBL upload time is 16s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ()
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2025-04-12 11:00:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP ´ûÄî»Í»ó¼Ÿ»ËÄÐ»¹»èDCBC•×²è *****
0072 C. (¾ä°íŸóŸÄŸËŸÐŸŸËŸäŸçŸè»Ë½¾»¼Ä»Ü»¹»è)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** ŸÐŸ¹•Ï Daily±¿íÑ»Ë´Ø»¹»èDCBC•×²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0083 C.
0084 . C. ***** LOS *****
0085 C.
```

[illegible]

0096 C.
0097 . C. ;ãLOS¥Á¥\$¥Ã¥~¼Ã»Ü;ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

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*** OP Sequence for XRT ***

2025/04/12	11:02:00.5	XRT_CTRL_MANU_425_OG [0x1a9]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	11:02:10.5	XRT_FOCUS_RECALIBRATE_407_OG [0x197]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2025/04/12	11:06:10.5	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/12	11:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	11:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	11:10:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/12	11:11:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 03 8d 01 db
2025/04/12	11:11:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/12	11:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/04/12	11:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/12	11:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/12	11:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/12	11:13:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/04/12	11:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/04/12	11:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/12	16:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	16:00:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	16:00:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/12	16:00:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/12	16:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/12	16:13:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/12	16:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/12	17:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	17:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	17:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/12	17:35:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/12	17:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/12	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/04/12	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/04/12	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/04/12	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/04/12	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/12	18:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/04/12	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/12	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/12	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/12	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 03 8d 01 db
2025/04/12	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/12	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/04/12	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/12	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/12	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/12	18:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12

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2025/04/12	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5	0e
2025/04/12	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/12	19:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/12	19:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/12	19:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/04/12	19:12:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/04/12	19:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/04/12	19:36:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/12	19:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/12	20:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/12	20:49:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/12	20:49:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/04/12	20:49:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/04/12	20:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/04/12	21:12:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/12	21:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/12	22:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/12	22:26:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/12	22:26:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/04/12	22:26:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/04/12	22:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/04/12	22:49:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/12	22:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/13	00:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	00:03:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	00:03:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/04/13	00:03:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/04/13	00:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/04/13	00:22:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	00:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/13	01:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	01:40:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	01:40:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/04/13	01:40:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/04/13	01:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/04/13	01:47:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	01:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/13	03:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	03:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	03:03:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2025/04/13	03:03:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2025/04/13	03:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2025/04/13	03:22:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	03:23:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/04/13	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/04/13	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa	00
2025/04/13	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00	00
2025/04/13	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0 d8	
2025/04/13	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8	
2025/04/13	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			

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2025/04/13	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/13	04:00:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/13	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2025/04/13	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/04/13	04:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	04:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	04:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	04:35:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	04:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	04:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	05:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/04/13	05:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	05:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	05:42:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	05:43:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/04/13	05:43:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2025/04/13	05:43:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/04/13	05:43:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/04/13	05:45:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/13	05:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/04/13	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	05:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	05:52:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	05:53:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/13	05:53:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 8d 01 db
2025/04/13	05:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/13	05:53:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/04/13	05:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/13	05:53:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/13	05:55:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	05:55:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/04/13	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/04/13	06:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	06:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	06:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	06:09:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	06:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	06:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	06:38:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/04/13	07:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	07:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	07:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	07:49:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	07:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	08:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	08:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/04/13	09:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/04/13	09:29:02.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	09:29:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	09:29:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	09:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	09:50:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	09:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	11:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	11:10:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	11:10:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	11:10:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	11:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	11:22:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	11:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	16:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	16:32:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	16:32:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	16:32:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	16:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	16:55:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	16:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	17:43:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	17:43:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	17:43:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/04/13	17:43:30.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/04/13	17:43:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/04/13	17:43:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/04/13	17:43:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/13	17:46:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/04/13	17:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	17:53:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/04/13	17:53:30.0	AOCs_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 03 8d 01 db
2025/04/13	17:53:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/13	17:53:50.5	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/04/13	17:53:52.5	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/13	17:53:54.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/13	17:53:56.5	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	17:56:26.5	XRT_QT_PROG_SET_442_OG [0x1ba]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/04/13	17:56:28.5	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/04/13	17:56:30.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	18:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	18:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	18:07:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	18:07:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	18:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	18:31:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	18:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/04/13	19:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	19:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	19:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	19:44:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	19:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	20:08:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	20:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	21:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	21:21:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	21:21:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	21:21:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	21:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	21:45:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/13	21:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/13	22:58:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	22:58:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/13	22:58:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/13	22:58:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/13	23:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/13	23:20:30.5	XRT_Custom_430_OG [0x1ae]			
2025/04/13	23:21:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/14	00:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	00:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	00:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/14	00:35:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/14	00:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/14	00:47:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	00:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/14	02:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	02:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	02:03:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/14	02:03:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/14	02:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/14	02:18:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	02:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/14	03:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	03:34:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	03:34:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/14	03:34:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/14	03:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/14	03:55:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	03:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/14	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/14	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/04/14	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/04/14	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/04/14	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/04/14	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/04/14	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/04/14	04:00:24.0	XRT_FLD_RESET_446_OG [0x1be]			

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2025/04/14	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2025/04/14	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/04/14	05:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	05:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	05:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	05:02:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	05:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	05:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	05:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2025/04/14	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	06:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/04/14	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCs_Ore-point_Start_2_OG [0x098]				
2025/04/14	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	AOCU_NM	5	02-76	00	00 00 00 00
2025/04/14	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/04/14	06:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/04/14	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/04/14	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03
2025/04/14	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	06:10:00.0	AOCs_Ore-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2025/04/14	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCs_Ore-point_Start_3_OG [0x099]				
2025/04/14	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	AOCU_NM	5	02-76	03	03 8d 01 db
2025/04/14	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/04/14	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/04/14	06:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/04/14	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/04/14	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	06:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2025/04/14	06:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/04/14	06:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	06:42:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	06:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	07:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	07:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	08:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	08:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]				
2025/04/14	08:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	08:23:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	08:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	08:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	08:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	10:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]				
2025/04/14	10:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	10:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2025/04/14	10:02:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	10:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	10:21:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	10:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	11:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	11:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	11:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	11:44:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	11:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	11:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	11:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	15:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	15:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	15:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	15:29:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	15:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	15:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	15:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	17:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	17:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	17:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	17:03:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/04/14	17:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/04/14	17:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	17:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/04/14	18:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/04/14	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/04/14	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/04/14	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/04/14	18:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2025/04/14	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	18:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/04/14	18:10:00.0	AOCs_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 03 8d 01 db	
2025/04/14	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/04/14	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/04/14	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/04/14	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/04/14	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/04/14	18:12:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2025/04/14	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/04/14	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/04/14	18:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/04/14	18:40:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2025/04/14	18:40:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/14	18:40:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/04/14	18:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/04/14	19:03:30.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	19:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/04/14	20:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/14	20:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/14	20:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/14	20:16:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/04/14	20:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/04/14	20:40:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	20:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/04/14	21:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/14	21:53:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/14	21:53:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/14	21:53:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/04/14	21:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/04/14	22:17:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	22:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/04/14	23:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/14	23:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/14	23:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/14	23:31:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/04/14	23:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/04/14	23:52:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/14	23:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/04/15	01:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/15	01:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/15	01:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/15	01:08:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/04/15	01:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/04/15	01:16:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/15	01:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/04/15	02:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/15	02:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/15	02:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/15	02:32:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/04/15	02:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/04/15	02:50:00.0	XRT_Custom_430_OG [0x1ae]			
2025/04/15	02:51:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/04/15	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/04/15	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22 ff aa 00
		XRT_FOCUS_POSITION			
2025/04/15	04:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]	5	02-76	00 00 00 00 00
		AOCU_NM			
2025/04/15	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8
		MDP_XRT_FLD_ENA			
2025/04/15	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8
		MDP_XRT_FLRCTRL_ENA			
2025/04/15	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0
		MDP_XRT_AEC_RESET			
2025/04/15	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
		MDP_XRT_ARS_DIS			
2025/04/15	04:00:24.0	XRT_FLD_RESET_446_OG [0x1be]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/04/15	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4 0f
		MDP_XRT_QT_PROG_SET			
2025/04/15	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			

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2025/04/15	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/04/15	04:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	04:06:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	04:06:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/04/15	04:06:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/04/15	04:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/04/15	04:27:00.0	XRT_Custom_430_OG [0x1ae]						
2025/04/15	04:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/04/15	05:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	05:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	05:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/04/15	05:36:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/04/15	05:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/04/15	06:04:00.0	XRT_Custom_430_OG [0x1ae]						
2025/04/15	06:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/04/15	06:19:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	06:19:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	06:19:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2025/04/15	06:19:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00 00 00
2025/04/15	06:19:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2025/04/15	06:19:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2025/04/15	06:19:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/04/15	06:22:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2025/04/15	06:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/04/15	06:29:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	06:29:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	06:29:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2025/04/15	06:29:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	03	8d 01 db
2025/04/15	06:29:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/04/15	06:29:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/04/15	06:29:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2025/04/15	06:29:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/04/15	06:29:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/04/15	06:32:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2025/04/15	06:32:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e	
2025/04/15	06:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/04/15	07:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	07:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	07:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/04/15	07:16:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/04/15	07:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/04/15	07:41:00.0	XRT_Custom_430_OG [0x1ae]						
2025/04/15	07:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/04/15	08:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	08:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/04/15	08:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/04/15	08:56:06.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/04/15	08:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

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2025/04/15	09:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/15	09:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/04/15	10:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/15	10:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/04/15	10:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/04/15	10:35:36.0	XRT_PREFLR_STRT_417_OG [0x1a1]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/04/15	10:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/04/15	10:52:30.0	XRT_Custom_430_OG [0x1ae]				
2025/04/15	10:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/04/15	11:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/04/15	11:42:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00