XRT Timeline to be uploaded on 2025/04/24

Period: 2025/04/24 11:15:00 - 2025/04/29 12:32:00

* * * * * **Normal mode** * * * * *

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/24 11:28:00 - 04/24 17:24:30	Track (637.5, -42.8) @ 04/24 11:25:00	# OP start + 10min, AR14062
04/24 18:10:30 - 04/25 03:59:54	Track (680.8, -46.8) @ 04/24 18:07:30	AR14062
04/25 05:53:00 - 04/25 17:40:24	Track (749.2, -54.2) @ 04/25 05:50:00	AR14062

PROG= 18 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 180.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1D2F: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(12/256/2048), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/5

Term	Pointing (x, y)	Comment
04/24 18:00:30 - 04/24 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
04/25 05:43:00 - 04/25 05:49:54	Fixed (0.0, 0.0)	synoptic, shifted 2hr -20.0 min, HOP 349 & 448
04/25 17:43:30 - 04/25 17:50:24	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min
04/26 06:00:00 - 04/26 06:06:54	Fixed (0.0, 0.0)	synoptic, shifted 2hr -3.0 min, HOP349&448

PROG= 17 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 66 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 22 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 65 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 17 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 33 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 89 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D2E: HOP349 - 3-filter Synoptics (Al-mesh[5/181/1443], Al-poly[12/256/2048], thin-Be[24/723/5795] with 512x512 G-band+Leak - 375min cad) + CME

Term	Pointing (x, y)	Comment
04/25 04:03:00 - 04/25 05:39:54	Fixed (0.0, 0.0)	synoptic, shifted 2hr -20.0 min, HOP 349 & 448
04/26 04:09:00 - 04/26 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted 2hr -3.0 min, HOP349&448

PROG= 15 Inf.-time(s)

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Subr= 1 1-time(s) 300.0sec													
Seqn= 66 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 22 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 61 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 15-time(s) 1500.0sec													
Seqn= 8 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec													
	med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
04/25 17:53:30 - 04/25 23:59:54	Track (808.9, -62.3) @ 04/25 17:50:30	AR14062
04/26 06:10:00 - 04/26 11:17:00	Track (858.1, -70.9) @ 04/26 06:07:00	AR14062

PROG= 11 Inf.-time(s)

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Subr= 1 1-time(s) 2.0sec													
Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec													
Seqn= 47 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77 4-time(s) 300.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1CDE: HOP393/336 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 7

Term	Pointing (x, y)	Comment
04/26 00:03:00 - 04/26 03:31:30	Track (-21.0, 37.8) @ 04/26 00:00:00	HOP393

PROG= 05 Inf.-time(s)

	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Subr= 1 1-time(s) 2.0sec													
Seqn= 30 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 52 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Subr= 2 30-time(s) 720.0sec													
Seqn= 97 2-time(s) 2.0sec													
	Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

* * * * * **Flare mode** * * * * *

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
04/24 11:28:00 - 04/24 17:24:30	Track (637.5, -42.8) @ 04/24 11:25:00	# OP start + 10min, AR14062
04/24 18:10:30 - 04/25 03:59:54	Track (680.8, -46.8) @ 04/24 18:07:30	AR14062
04/25 04:03:00 - 04/25 05:39:54	Fixed (0.0, 0.0)	synoptic, shifted 2hr -20.0 min, HOP 349 & 448
04/25 05:53:00 - 04/25 17:40:24	Track (749.2, -54.2) @ 04/25 05:50:00	AR14062
04/25 17:53:30 - 04/25 23:59:54	Track (808.9, -62.3) @ 04/25 17:50:30	AR14062
04/26 00:03:00 - 04/26 03:31:30	Track (-21.0, 37.8) @ 04/26 00:00:00	HOP393
04/26 04:09:00 - 04/26 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted 2hr -3.0 min, HOP349&448
04/26 06:10:00 - 04/26 11:17:00	Track (858.1, -70.9) @ 04/26 06:07:00	AR14062

PROG= 14 30-time(s)

Subr= 1 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

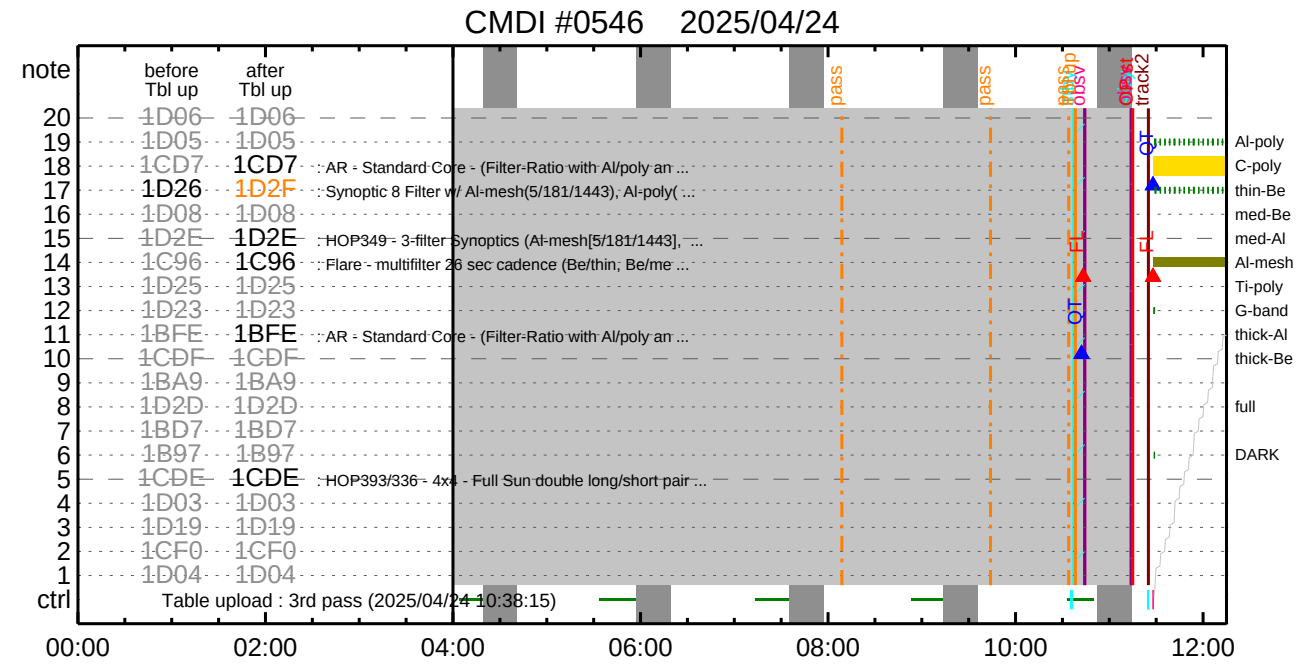
NOT USED

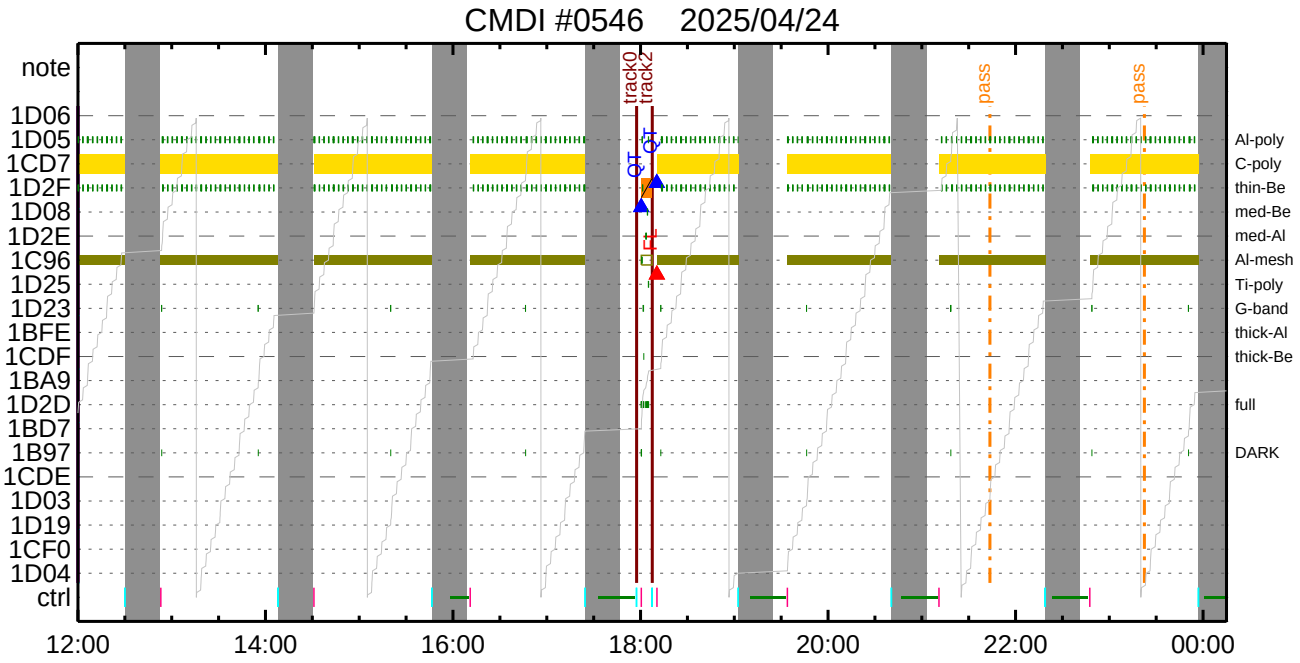
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

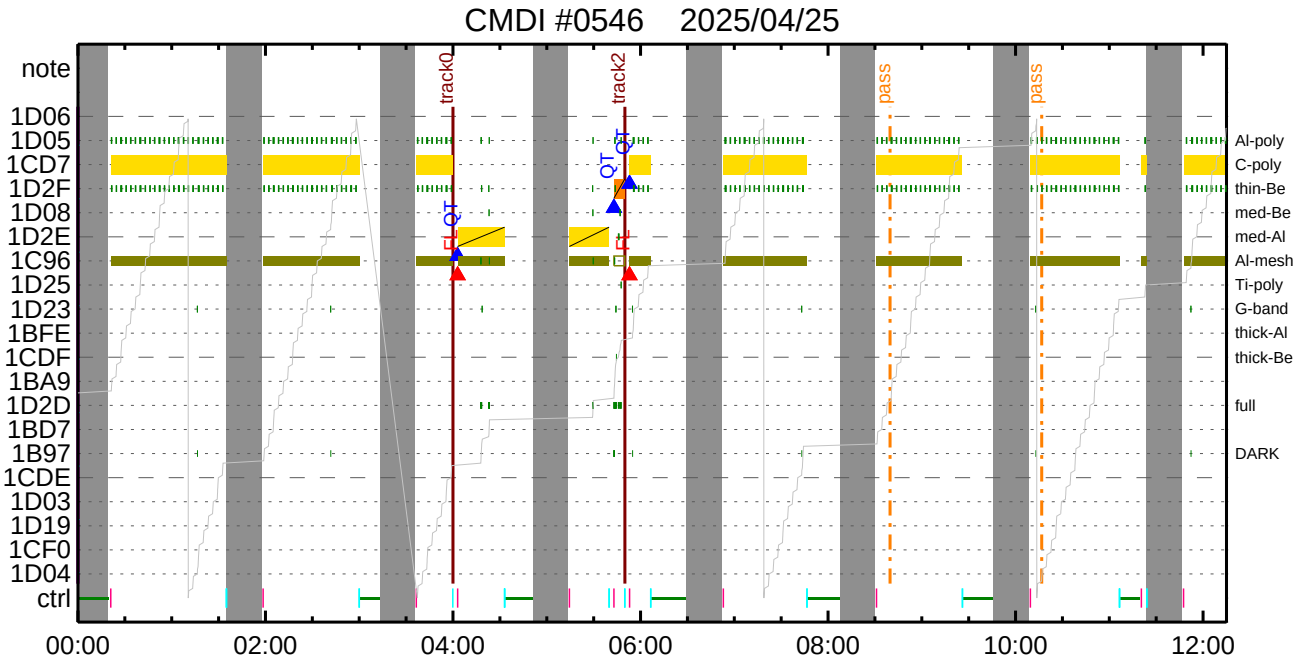
FLD Patrol

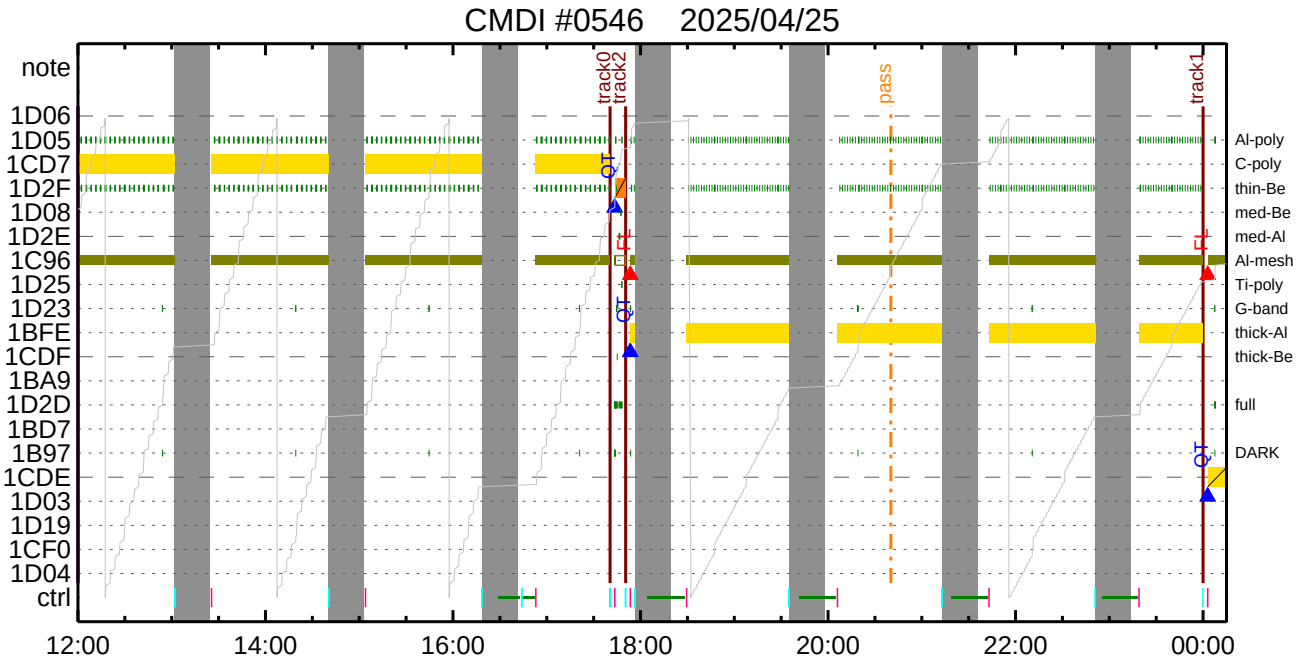
Term	Pointing (x, y)	Comment
04/24 10:39:15 - 04/24 17:57:48	cannot be identified	
04/24 18:07:48 - 04/25 05:40:18	Track (680.8, -46.8) @ 04/24 18:07:30	AR14062
04/25 05:50:18 - 04/25 17:40:48	Track (749.2, -54.2) @ 04/25 05:50:00	AR14062
04/25 17:50:48 - 04/26 05:57:18	Track (808.9, -62.3) @ 04/25 17:50:30	AR14062
04/26 06:07:18 - 04/29 12:32:00	Track (858.1, -70.9) @ 04/26 06:07:00	AR14062

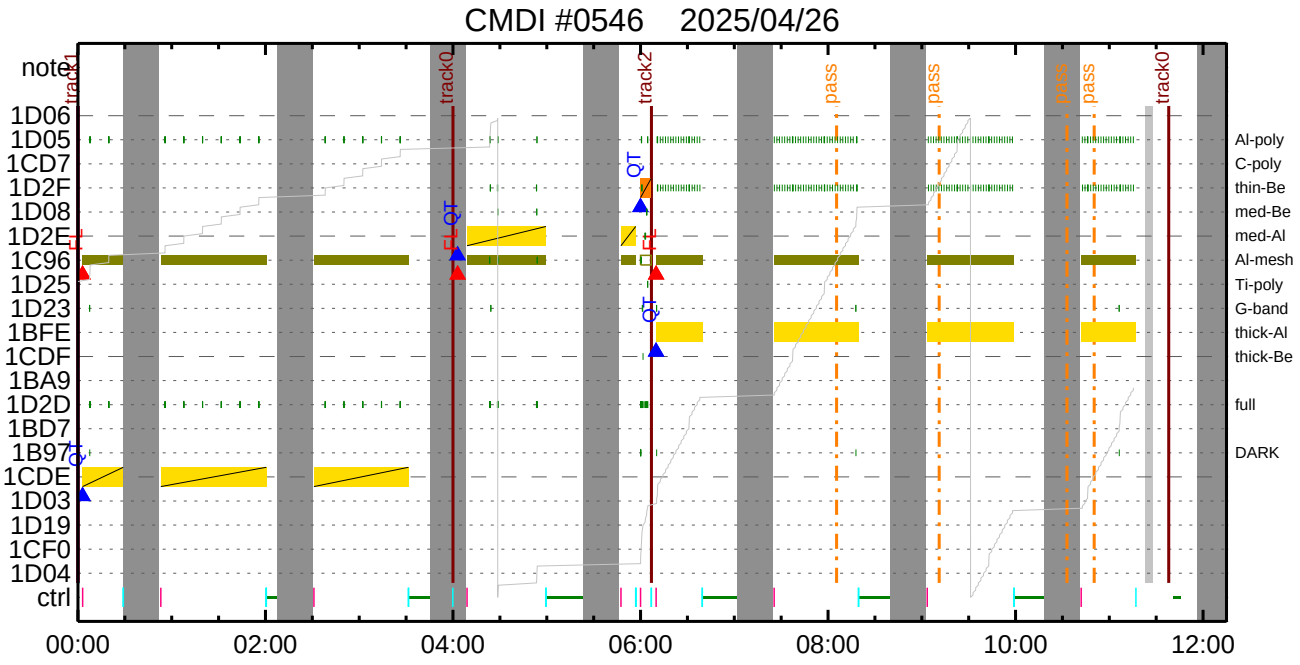
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer		Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec

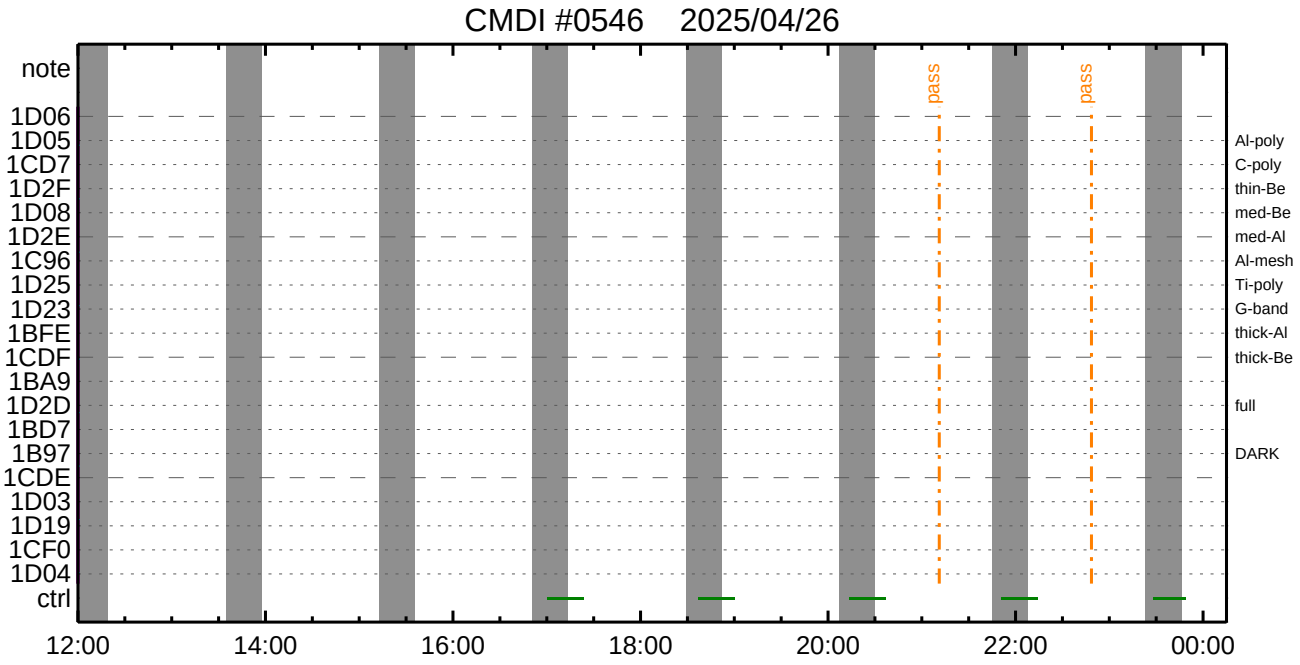

CMDI #0546 2025/04/24
note
before Tbl up
after Tbl up
1D06 1D06
1D05 1D05
1CD7 1CD7
1D26 1D2F
1D08 1D08
1D2E 1D2E
1C96 1C96
1D25 1D25
1D23 1D23
1BFE 1BFE
1CDF 1CDF
1BA9 1BA9
1D2D 1D2D
1BD7 1BD7
1B97 1B97
1CDE 1CDE
1D03 1D03
1D19 1D19
1CF0 1CF0
1D04 1D04
ctrl
: AR - Standard Core - (Filter-Ratio with Al/poly an ...
: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(...
: HOP349 - 3-filter Synoptics (Al-mesh[5/181/1443], ...
: Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
: AR - Standard Core - (Filter-Ratio with Al/poly an ...
: HOP393/336 - 4x4 - Full Sun double long/short pair ...
Table upload : 3rd pass (2025/04/24 10:38:15)
pass
pass
pass
obsv
obsv
track2
QT
FL
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

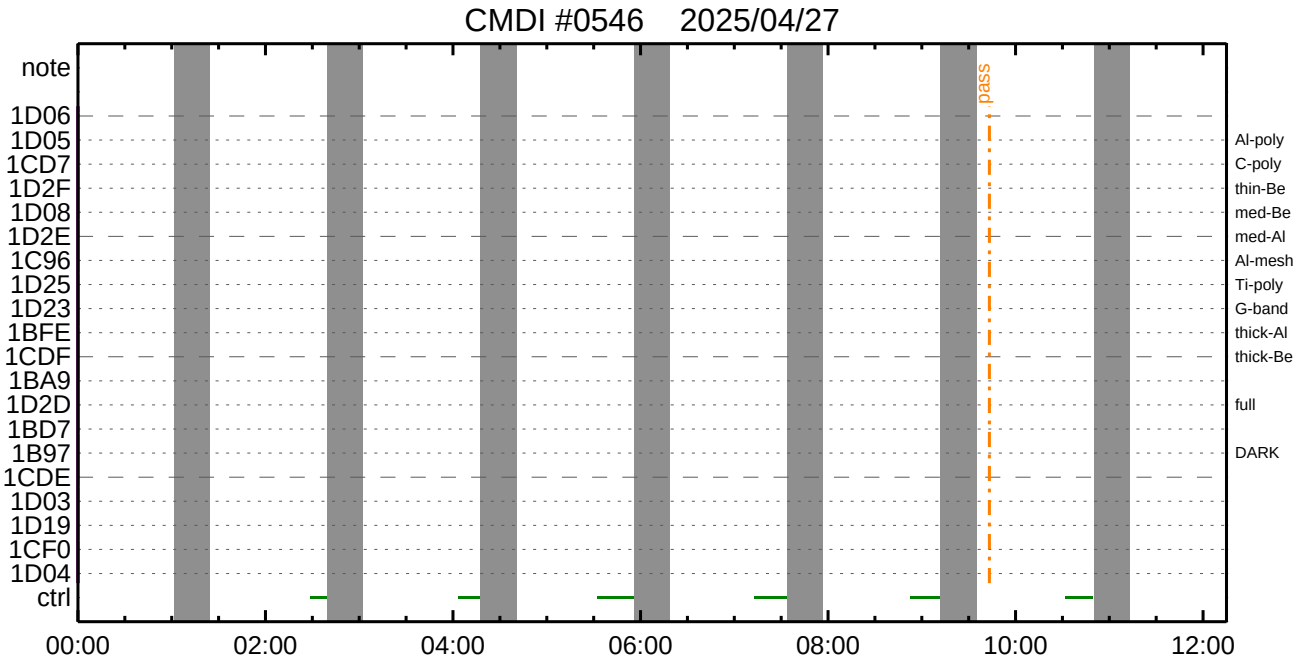

CMDI #0546 2025/04/24
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
track0
track2
QT
QT
FL
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

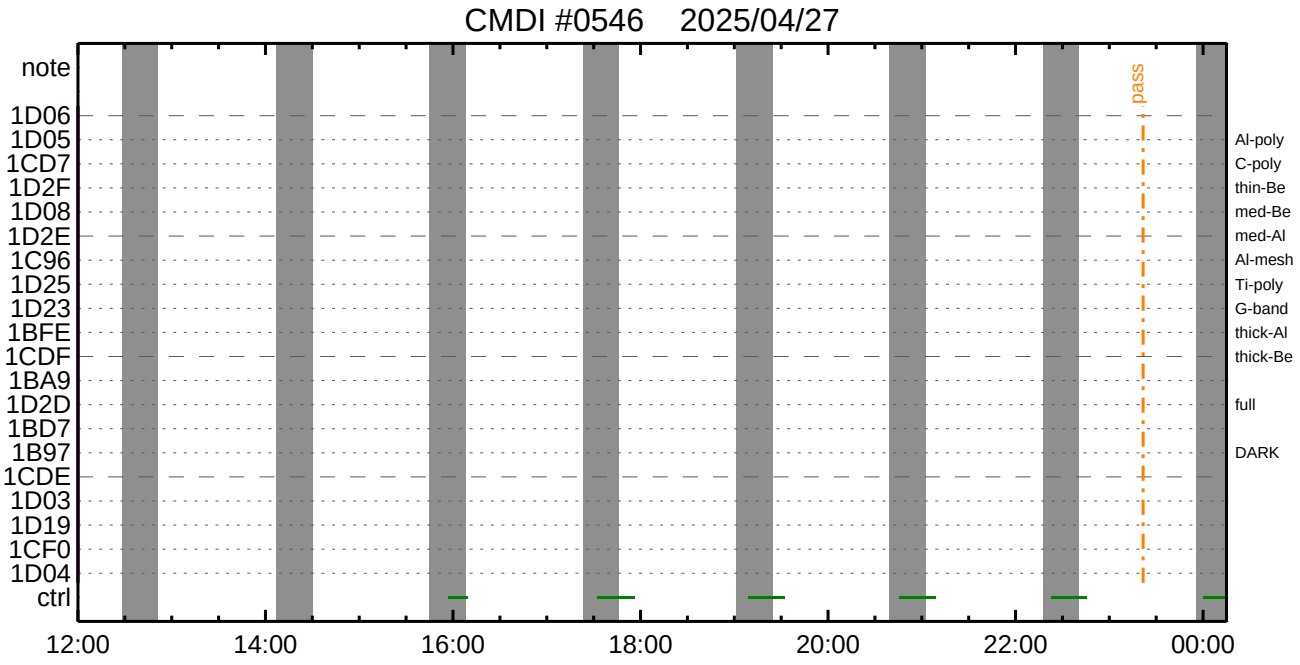

CMDI #0546 2025/04/25
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
track0
track2
FL
QT
QT
QT
FL
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

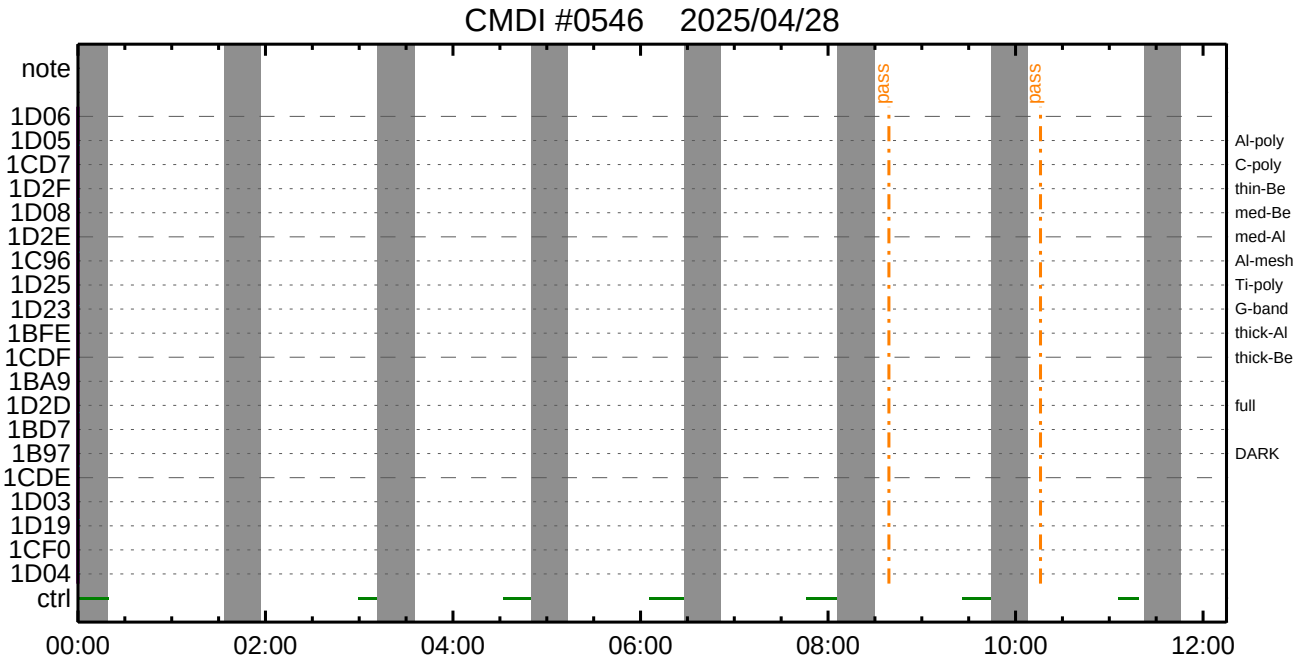

CMDI #0546 2025/04/25
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
track0
track2
pass
track1
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

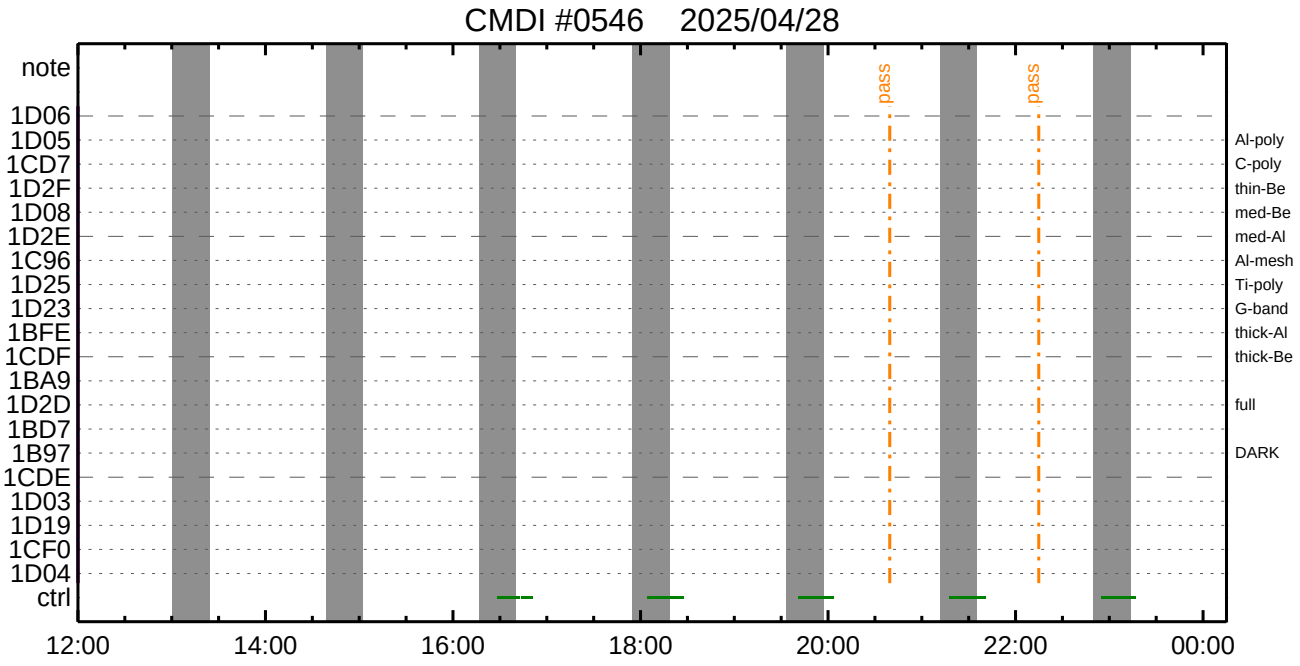

CMDI #0546 2025/04/26
note
track1
track0
track2
pass
pass
pass
pass
track0
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

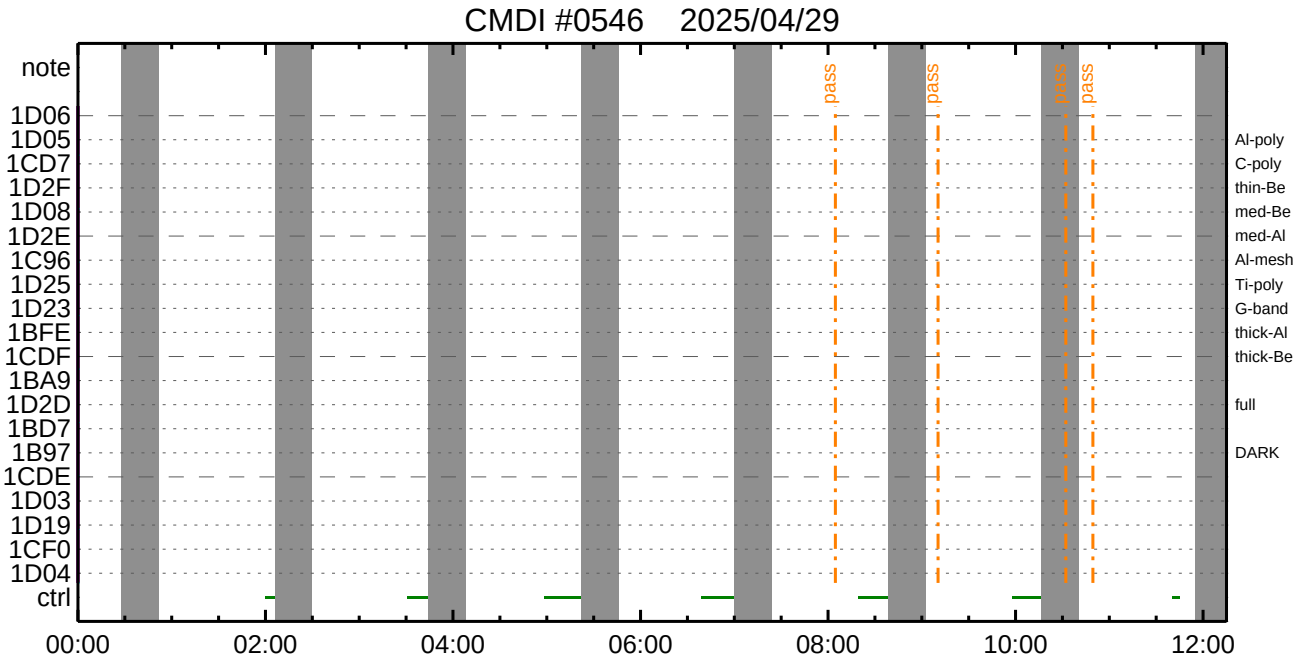

CMDI #0546 2025/04/26
note
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

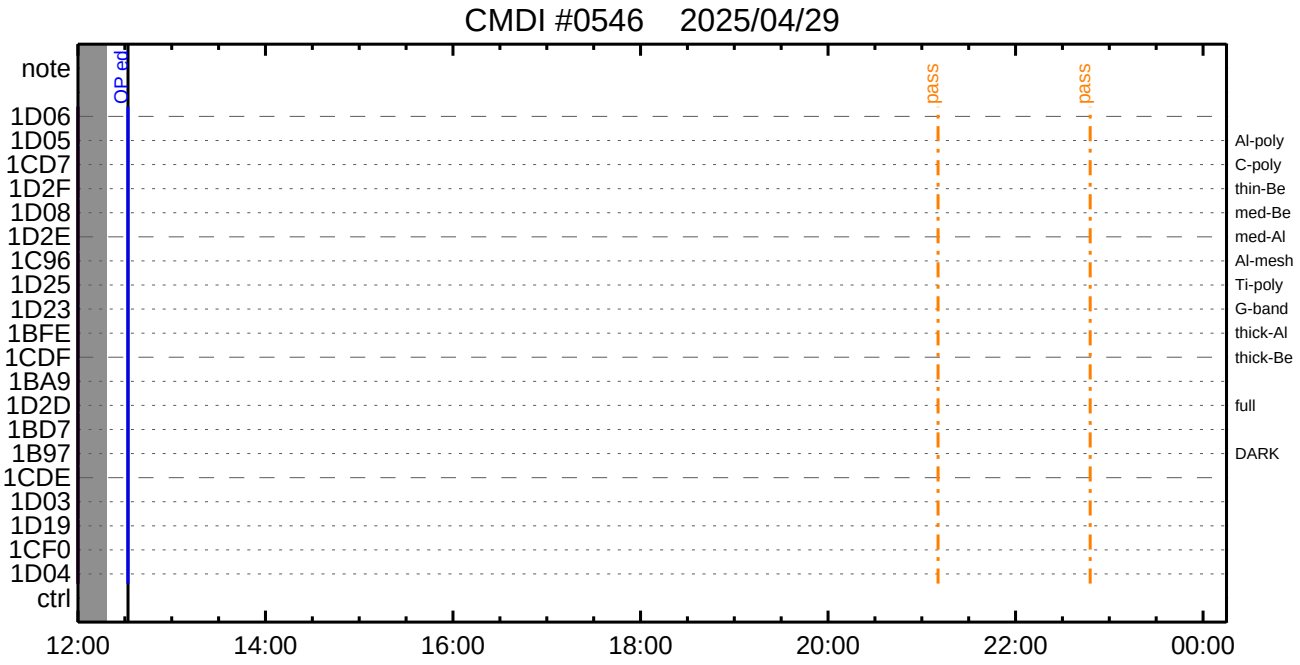

CMDI #0546 2025/04/27
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0546 2025/04/27
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0546 2025/04/28
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0546 2025/04/28
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0546 2025/04/29
note
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
pass
pass
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0546 2025/04/29
note
OP ed
1D06
1D05
1CD7
1D2F
1D08
1D2E
1C96
1D25
1D23
1BFE
1CDF
1BA9
1D2D
1BD7
1B97
1CDE
1D03
1D19
1CF0
1D04
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-232 2025-04-24 11:07:37 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
    C.
    C.
  . C. ****************************
    C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
    C. ****************************
    C.
  . C. ¡ãOP/OG¥í¡¼¥É¡ä
  . S. OP    op-232:OP
    ()
  . S. OG    og-232:OG
    ()
    C.
  . C. ¡ãNMOG&OPÎÎºè¥À¥ó¥×¡ä
    C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (20 00 7f 01 02)
    C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
    C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (20 80 7f 01 02)
    C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
    C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (21 00 41 01 02)
    C.                     ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
    C.                     ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                     ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
    C.                     ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                     ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                     ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
  . C. ***** ºÊ²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼êÆç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
    C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
 +. DC 01-22 DHU_MODE_CHNG
    BC        (02 0a f8)
    C.                     ¢¢[HK1_PKT_FORM_NO]             EQ     2
    C.                     ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
    C.                     ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
    C.                     ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.
  . C. *********************************
    C. TI-CMD SET (OPOG STOP/COPY/START)
    C. *********************************
    C.
  . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢ºÊ²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
    C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
    C.
  . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
 +. TI 2025-04-24 11:10:00.0
    DC 01-B3 DHU_OP_STOP
    C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2025-04-24 11:10:01.0
    DC 01-B4 DHU_OP_COPY
    C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2025-04-24 11:10:01.0
    DC 01-B5 DHU_OPOG_COPY
    C.                     ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2025-04-24 11:14:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]           EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]            EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]             EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]           EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]           EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]            EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]           EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2025-04-24 11:14:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2025-04-24 11:14:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ************
0256     C. Execute, after the success of OP upload.
0257   +. TI 2025-04-24 11:14:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ************
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-233 2025-04-24 11:07:37 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
    C.
    C.
  . C. ***** AOCS Commands (Tracking Curve Upload) ******
    C. Upload the Orbit Element and the Target Attitude
    C. RAM-ID:TARGET_ATT
  . S. RAM   ram-150:TARGET_ATT
    ()
    C.
    C.
    C. Set the dump memory area of TARGET_ATT
 +. DC 02-48 AOCU_DUMP_SET
    BC       (07 00 00 00 18 00)
    C.
    C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
    C.
    C.
    C. Change the TLMFormatNo for the AOCS Dump Format
 +. DC 01-22 DHU_MODE_CHNG
    BC       (04 0b f8)
    C.
    C. Wait for AOCSDUMP to end
    C.
  . C. Check the dump memory
    C.
    C.                                      Result = OK [ ]
    C.
 +. DC 01-22 DHU_MODE_CHNG
    BC       (02 0a f8)
    C.
    C.                         <A_***>[TLM STS] FMT = 2 [ ]
    C.
 +. DC 02-8E AOCU_ORB_UPD
  . C.
  . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
 +. DC 07-FC EIS_MODE_CHG_ENA
    BC       (20)
  . C. Verify EIS_MODE_CHG_FLG is ENA
 +. DC 07-FC EIS_MODE_MANU
    BC       (21 02)
  . C. Verify EIS in MANUAL mode
  . C. Estimated OBSTBL upload time is 6s
    C. *********************************
    C. EIS START OBSTBL LOAD
    C. *********************************
  . S. RAM   ram-820:EIS_OBSTBL
    ()
 +. DC 07-FC EIS_DUMP_OBSTBL
    BC       (07 07 07 00 00 70 00)
    C.
    C. Execute, after the success of OBSTBL upload.
    C. Set EIS TI-commands
 +. TI 2025-04-24 11:14:50.0
    DC 07-FC EIS_MODE_CHG_ENA
    BC       (20)
  . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
    C. *********************************
    C. EIS END OBSTBL LOAD
    C. *********************************
    C.
  . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
    C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
  . S. DC-BC dcbc-402:DCBC
    (MDP_known_event)
    C.
    C.
  . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
  . S. DC-BC dcbc-153:DCBC
    (SPECIAL-CMD_DAILY_OPERATIN_DCB)
    C.
    C.
  . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
    C.
  . C. *********** LOS ***********
    C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-234 2025-04-24 11:07:37 100 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017   +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019   +. DC 07-F0 MDP_XRT_CTRL_MANU
0020     BC       (c1)
0021   +  DC 07-F0 MDP_XRT_MODE_STBY
0022     BC       (c3)
0023   . C. ----------- Success Verify ?              OK / NG____
0024     C.
0025     C. XRT Obs. Table Upload
0026   . S. RAM   ram-291:MDP_OBS_X
0027     ()
0028     C.
0029   +. DC 07-F0 MDP_DUMP_XRTTBL
0030     BC       (84 07 00 00 00 3a d4)
0031   . C. ----------- Comparison Check ?                        OK / ERR ____
0032     C.
0033     C.
0034   +. DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 01 b1 b1 04 04)
0036   +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 02 b1 b1 08 08)
0038   +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 03 b1 b1 08 08)
0040   +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 04 b1 b1 06 06)
0042   +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 05 85 83 06 06)
0044   +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 06 85 83 06 06)
0046   +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 07 80 80 20 20)
0048   +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 08 80 80 20 08)
0050   +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 09 80 80 08 20)
0052   +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0a 80 80 08 08)
0054   +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0f 80 80 06 06)
0056   +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 10 80 80 08 08)
0058   +  DC 07-F0 MDP_XRT_FLD_ENA
0059     BC       (d8)
0060   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061     BC       (c8)
0062   +  DC 07-F0 MDP_XRT_ARS_DIS
0063     BC       (d5)
0064   +  DC 07-F0 MDP_XRT_AEC_RESET
0065     BC       (d0)
0066   +  DC 07-F0 MDP_XRT_FLD_RESET
0067     BC       (da)
0068   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069     BC       (c4 0b)
0070   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071     BC       (c5 0e)
0072   . C. ---------- Success Verify ?            OK / NG ____
0073     C.
0074     C.
0075   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0076     C.
0077   +. DC 07-F0 MDP_XRT_MODE_OBSV
0078     BC       (c2)
0079   +. TI 2025-04-24 11:14:02.0
0080     DC 07-F0 MDP_XRT_MODE_OBSV
0081     BC       (c2)
0082   . C. ---------- Success Verify ?            OK / NG ____
0083     C.
0084     C. ************ XRT END ***********
0085     C.
0086   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0087     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0088   . S. DC-BC dcbc-402:DCBC
0089     (MDP_known_event)
0090     C.
0091     C.
0092   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0093   . S. DC-BC dcbc-153:DCBC
0094     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095     C.
```

```
0096     C.
0097   . C. ¡ãLOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0098     C.
0099   . C. *********** LOS ***********
0100     C.
```

```
*** OP Sequence for XRT ***

2025/04/24  11:24:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  11:24:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  11:24:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/24  11:25:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 03 8d 01 db
2025/04/24  11:25:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/24  11:25:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/24  11:25:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/24  11:25:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/24  11:25:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  11:27:56.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2025/04/24  11:27:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/24  11:28:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  12:30:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  12:30:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  12:30:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  12:30:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  12:33:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  12:52:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/24  12:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  14:08:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  14:08:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  14:08:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  14:08:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  14:11:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  14:30:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/24  14:31:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  15:46:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  15:46:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  15:46:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  15:46:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  15:49:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  16:10:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/24  16:11:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  17:24:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  17:24:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  17:24:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  17:24:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  17:27:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  17:57:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  17:57:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  17:57:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/24  17:57:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/24  17:57:48.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/24  17:57:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/24  17:57:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/24  18:00:28.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2025/04/24  18:00:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
```

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2025/04/24  18:07:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  18:07:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  18:07:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/24  18:07:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 03 8d 01 db
2025/04/24  18:07:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/24  18:07:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/24  18:07:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/24  18:07:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/24  18:07:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  18:10:26.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2025/04/24  18:10:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/24  18:10:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  19:02:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  19:02:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  19:02:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  19:02:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  19:05:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  19:33:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/24  19:34:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  20:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  20:40:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  20:40:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  20:40:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  20:43:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  21:10:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/24  21:11:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  22:19:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  22:19:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  22:19:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  22:19:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/24  22:22:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/24  22:46:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/24  22:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/24  23:57:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  23:57:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/24  23:57:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/24  23:57:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  00:00:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  00:20:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  00:21:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  01:35:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  01:35:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  01:35:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  01:35:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  01:38:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  01:57:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  01:58:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  03:00:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  03:00:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
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2025/04/25  03:00:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  03:00:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  03:03:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  03:35:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  03:36:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  03:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/25  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/25  04:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/25  04:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/25  04:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/25  04:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/25  04:00:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  04:02:56.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2025/04/25  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/25  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  04:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  04:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  04:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  04:33:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  04:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  05:13:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  05:14:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  05:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  05:39:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  05:39:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/25  05:40:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/25  05:40:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/25  05:40:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/25  05:42:58.0  XRT_QT_PROG_SET_404_OG [0x194]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2025/04/25  05:43:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  05:49:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  05:49:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  05:49:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/25  05:50:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 02 03 8d 01 db
2025/04/25  05:50:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/25  05:50:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/25  05:50:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/25  05:50:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/25  05:50:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  05:52:56.0  XRT_QT_PROG_SET_442_OG [0x1ba]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2025/04/25  05:52:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/25  05:53:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  06:06:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  06:06:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  06:06:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  06:06:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  06:09:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
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                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  06:52:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  06:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  07:46:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  07:46:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  07:46:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  07:46:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  07:49:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  08:30:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  08:31:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  09:26:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  09:26:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  09:26:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  09:26:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  09:29:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  10:08:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  10:09:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  11:06:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  11:06:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  11:06:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  11:06:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  11:09:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  11:19:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  11:20:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  11:24:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  11:24:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  11:24:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  11:24:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  11:27:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  11:46:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  11:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  13:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  13:02:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  13:02:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  13:02:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  13:05:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  13:24:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  13:25:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  14:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  14:40:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  14:40:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  14:40:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  14:43:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  15:03:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  15:04:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  16:18:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  16:18:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  16:18:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  16:18:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  16:21:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  16:44:00.0  XRT_CTRL_MANU_400_OG [0x190]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  16:44:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  16:44:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  16:44:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  16:47:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  16:52:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  16:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  17:40:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  17:40:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  17:40:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/25  17:40:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/04/25  17:40:48.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/25  17:40:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/25  17:40:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/25  17:43:28.0  XRT_QT_PROG_SET_404_OG [0x194]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2025/04/25  17:43:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  17:50:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  17:50:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  17:50:28.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/25  17:50:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 02 03 8d 01 db
2025/04/25  17:50:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/25  17:50:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/25  17:50:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/25  17:50:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/25  17:50:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  17:53:26.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2025/04/25  17:53:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/25  17:53:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  17:56:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  17:56:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  17:56:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  17:56:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  17:59:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  18:28:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  18:29:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  19:35:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  19:35:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  19:35:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  19:35:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  19:38:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  20:05:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  20:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  21:13:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  21:13:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  21:13:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  21:13:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  21:16:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  21:42:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  21:43:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
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2025/04/25  22:51:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  22:51:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  22:51:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/25  22:51:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/25  22:54:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/25  23:18:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/25  23:19:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/25  23:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/25  23:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/26  00:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 03 8d 01 db
2025/04/26  00:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/26  00:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/26  00:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/26  00:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/26  00:00:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  00:02:56.0  XRT_QT_PROG_SET_438_OG [0x1b6]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2025/04/26  00:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/26  00:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  00:29:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  00:29:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  00:29:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  00:29:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  00:32:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  00:52:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  00:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  02:00:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  02:00:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  02:00:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  02:00:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  02:03:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  02:30:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  02:31:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  03:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  03:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  03:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  03:31:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  03:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  03:59:56.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/26  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2025/04/26  04:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/26  04:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/26  04:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/26  04:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/26  04:00:24.0  XRT_FLD_RESET_446_OG [0x1be]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  04:02:56.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2025/04/26  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/26  04:08:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  04:09:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  04:59:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  04:59:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  04:59:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  04:59:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  05:02:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  05:46:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  05:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  05:56:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  05:56:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  05:56:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/04/26  05:57:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/04/26  05:57:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/04/26  05:57:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/26  05:59:58.0  XRT_QT_PROG_SET_404_OG [0x194]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2025/04/26  06:00:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  06:06:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  06:06:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  06:06:58.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/04/26  06:07:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 02 03 8d 01 db
2025/04/26  06:07:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/04/26  06:07:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/04/26  06:07:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/04/26  06:07:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/04/26  06:07:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  06:09:56.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2025/04/26  06:09:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/04/26  06:10:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  06:39:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  06:39:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  06:39:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  06:39:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  06:42:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  07:24:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  07:25:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  08:19:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  08:19:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  08:19:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  08:19:36.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  08:22:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  09:02:30.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  09:03:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/04/26  09:59:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  09:59:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  09:59:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/04/26  09:59:06.0  XRT_PREFLR_STRT_417_OG [0x1a1]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/04/26  10:02:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/04/26  10:41:00.0  XRT_Custom_430_OG [0x1ae]
2025/04/26  10:42:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
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2025/04/26  11:17:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/04/26  11:38:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
```