

Period: 2025/06/07 11:15:00 - 2025/06/17 11:12:00

Period: 2025/06/07 11:15:00 - 2025/06/17 11:12:00

Normal mode

XOB #12Df: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(12/256/2048), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/512/8192/32768), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/512/8192/32768)																		
Term		Pointing (x, y)					Comment											
06/07 17:58:00 - 06/07 18:04:54		Fixed (0.0, 0.0)					synoptic, shifted -5.0 min											
06/08 06:16:30 - 06/08 06:23:30		Fixed (0.0, 0.0)					synoptic, shifted 13.5 min											
PROG= 17 1-time(s)																		
Subr= 1		1-time(s)					2.0sec											
Seqn= 5		1-time(s)					2.0sec											
└ Open/Ti-poly		Open/thick-Al					close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al					close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al					close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al					close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al					close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 66		1-time(s)					2.0sec											
└ Open/Al-mesh		Open/Al-mesh					close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh					close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Ti-poly					close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 22		1-time(s)					2.0sec											
└ Al-poly/Open		Al-poly/Open					close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open					close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/thick-Al					close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 19		1-time(s)					2.0sec											
└ thin-Be/Open		thin-Be/Open					close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open					close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open					close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 23		1-time(s)					4.0sec											
└ Open/G-band		Open/G-band					open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band					close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		1-time(s)					2.0sec											
Seqn= 65		1-time(s)					2.0sec											
└ Open/thick-Be		Open/thick-Be					close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 17		1-time(s)					2.0sec											
└ med-Al/Open		med-Al/thick-Al					close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ med-Al/Open		med-Al/thick-Al					close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ med-Al/Open		med-Al/Open					close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 33		1-time(s)					2.0sec											
└ med-Be/Open		Open/thick-Al					close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ med-Be/Open		med-Be/Open					close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ med-Be/Open		med-Be/Open					close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 89		1-time(s)					2.0sec											
└ Al-poly/Ti-poly		Al-poly/thick-Al					close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└ Al-poly/Ti-poly		Al-poly/thick-Al					close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter		Thicker Filter					VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D37: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/362/4096], thin-Be[45/1024/8192] with 512x512 G-band+Leak(1064,1048) - 120 min		
Term	Pointing (x, y)	Comment
06/08 04:30:30 - 06/08 05:19:00	Fixed (0.0, 0.0)	# HOP 349
PROG= 09 Inf-time(s)		

☀ ☀ ☀ ☀ ☀

Flare mode

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Active Region Search

NOT USED

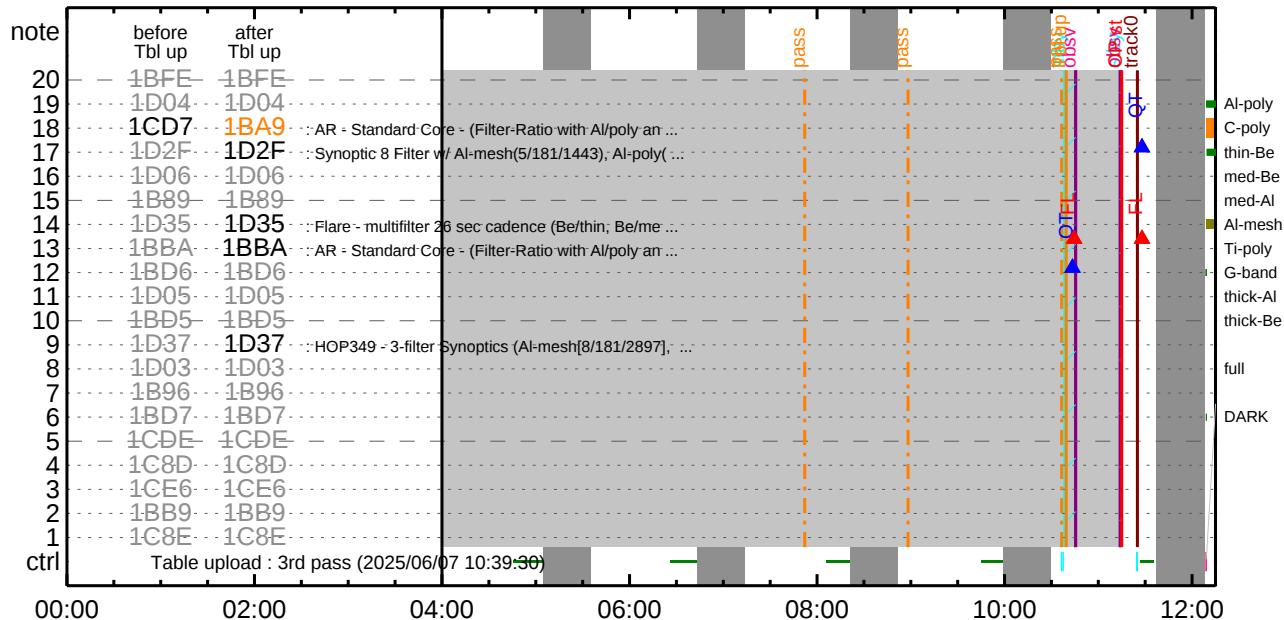
✱ ✱ ✱ ✱ ✱

Flare Detection

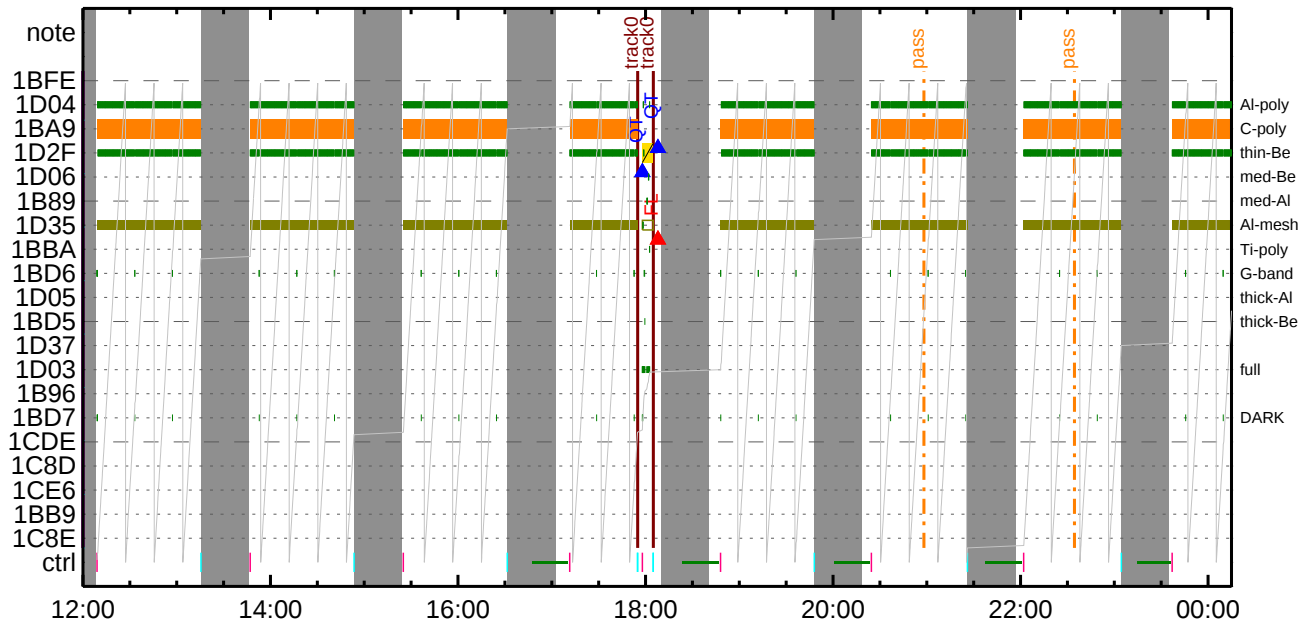
✱ ✱ ✱ ✱ ✱

FLD Patrol												
Term		Pointing (x, y)						Comment				
06/07 10:40:30 - 06/07 17:55:18		cannot be identified										
06/07 18:05:18 - 06/08 06:13:48		Fixed (897.0, 102.0)						# AR cont.				
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50				30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval	

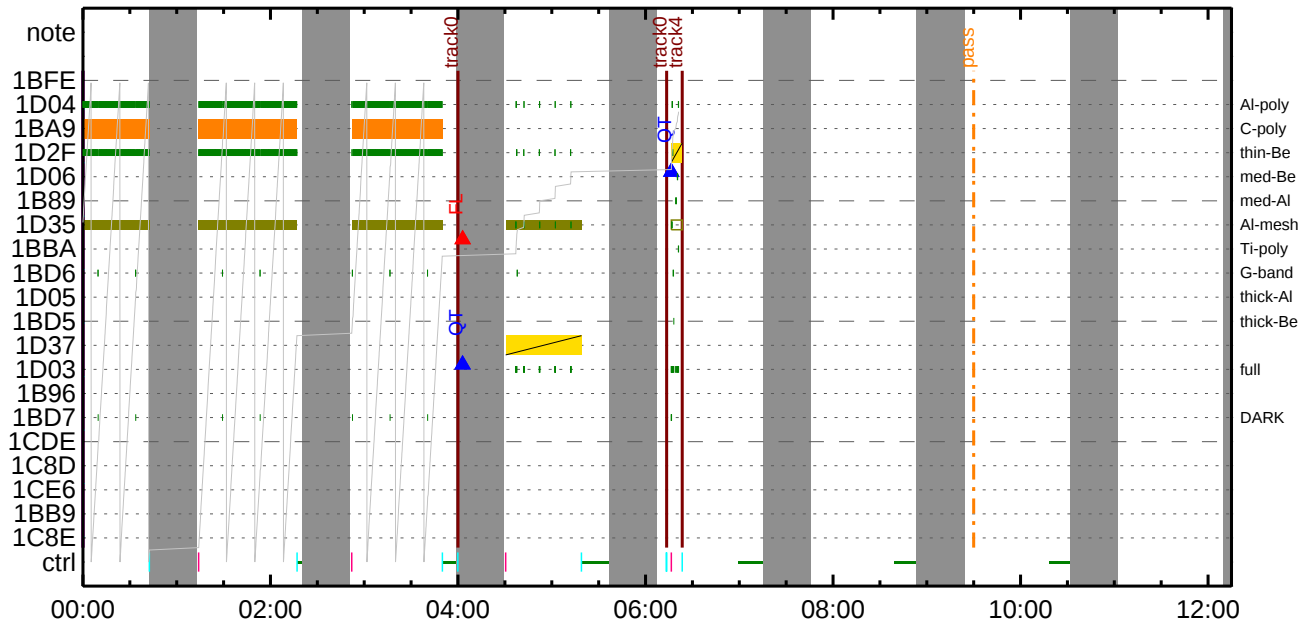
CMDI #0633 2025/06/07



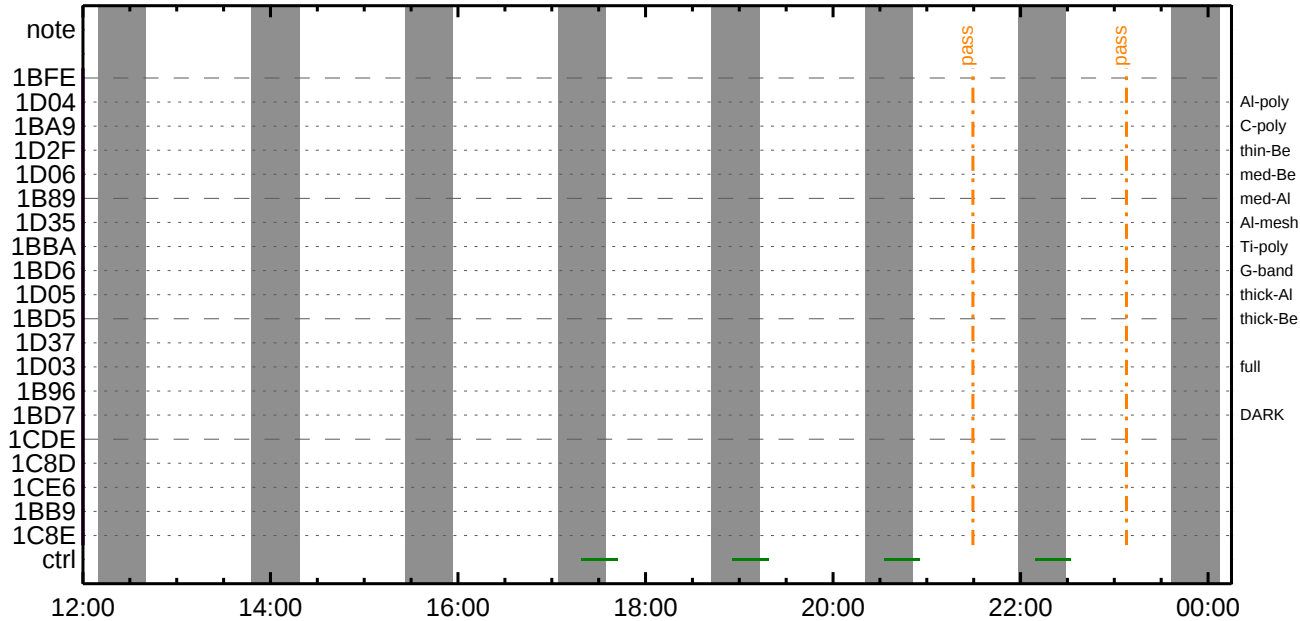
CMDI #0633 2025/06/07



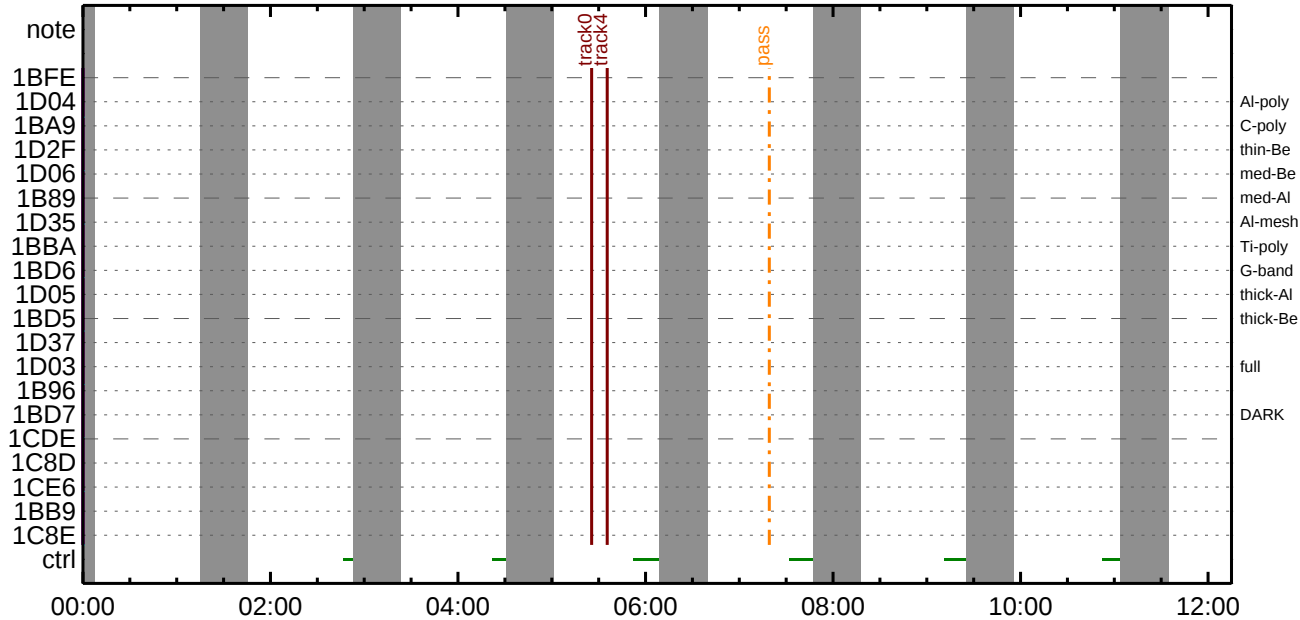
CMDI #0633 2025/06/08



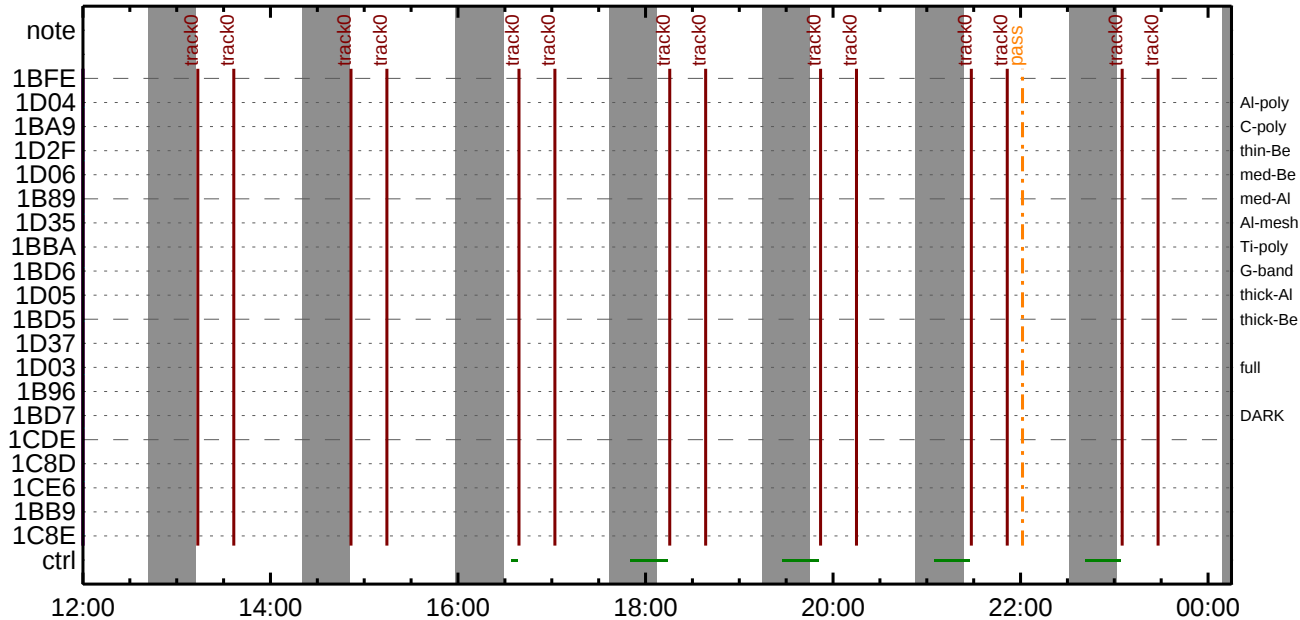
CMDI #0633 2025/06/08



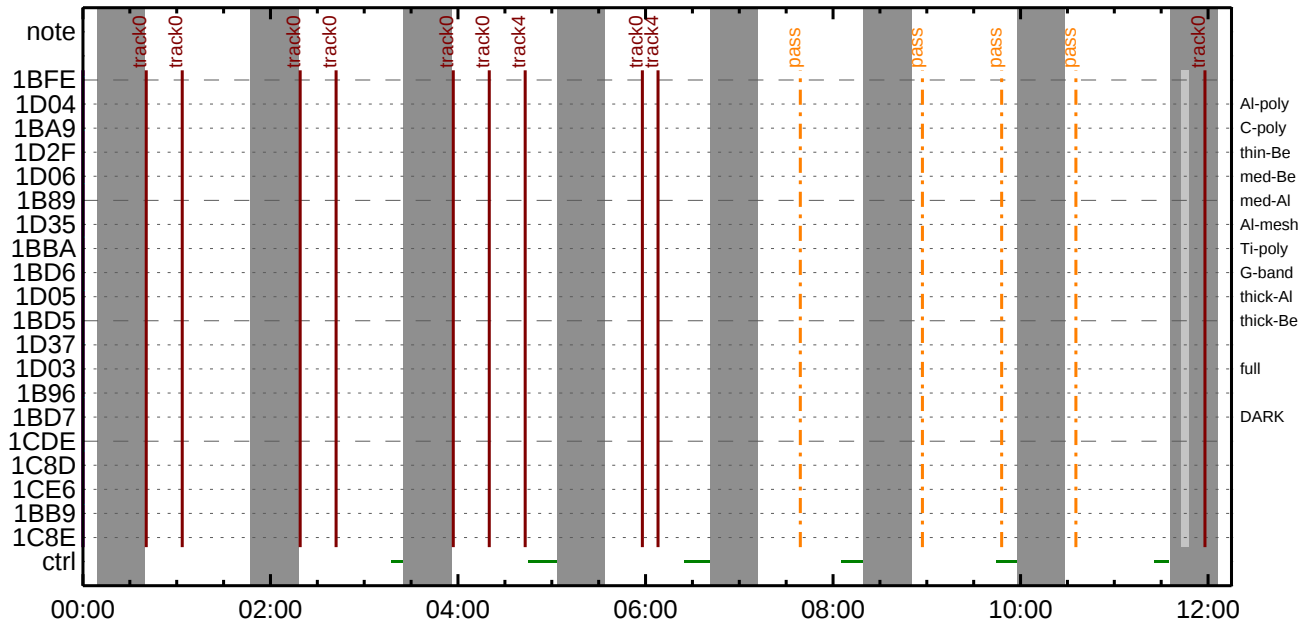
CMDI #0633 2025/06/09



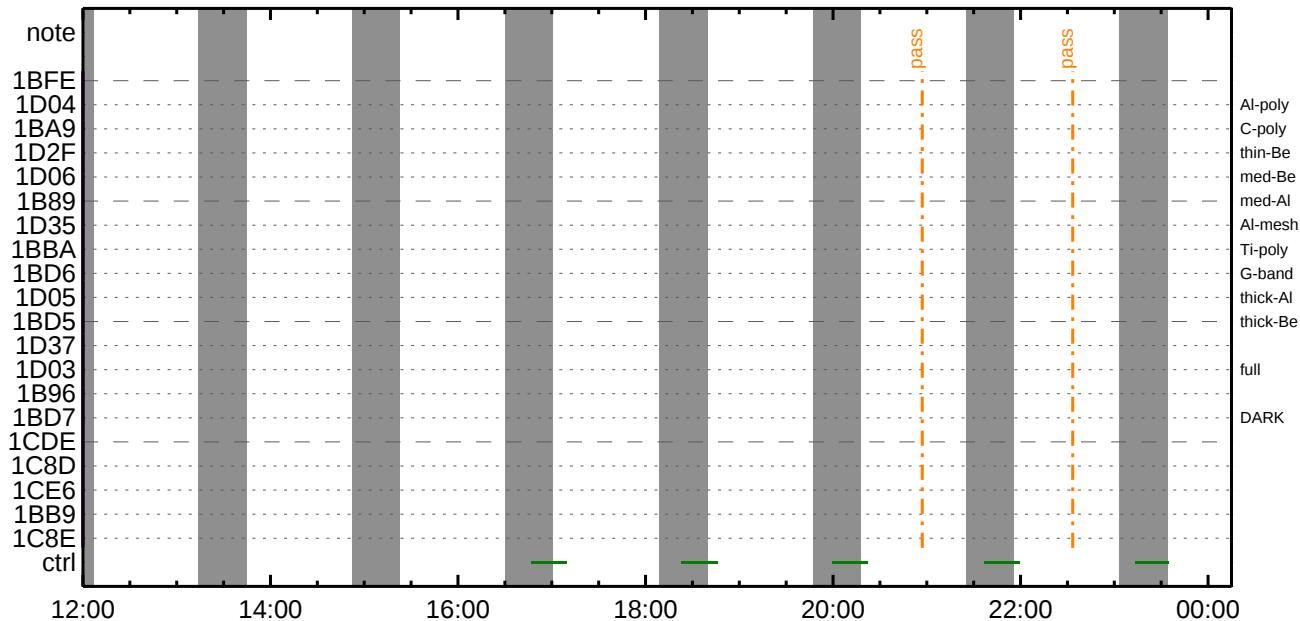
CMDI #0633 2025/06/09



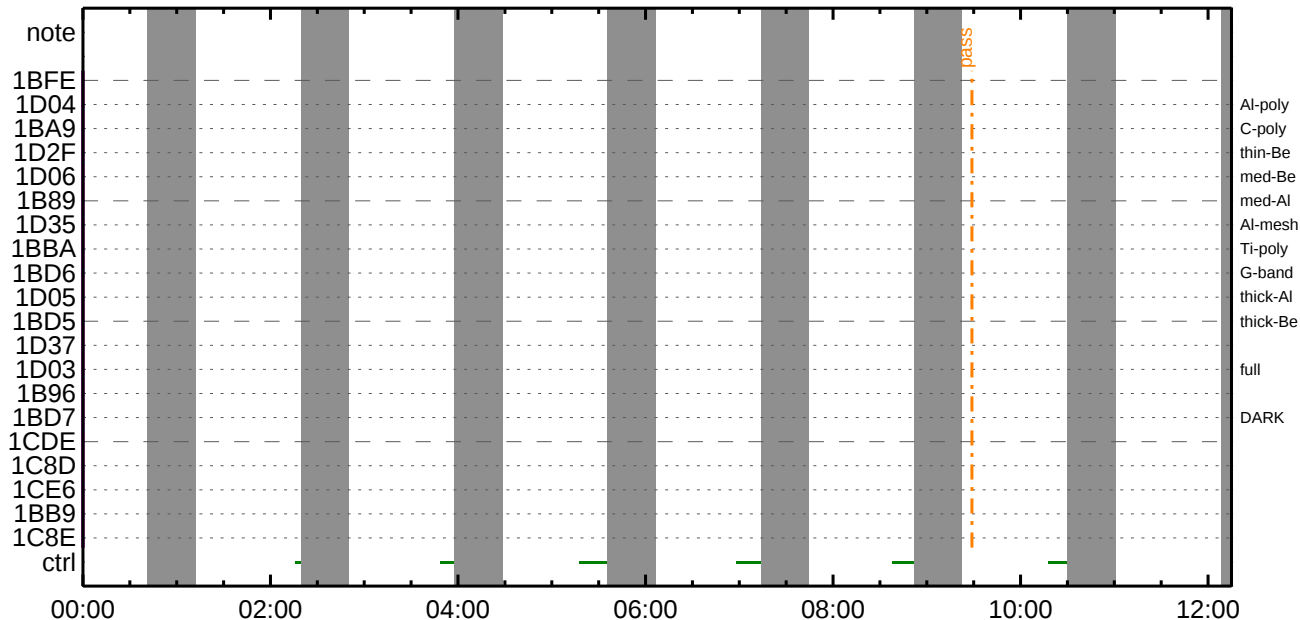
CMDI #0633 2025/06/10



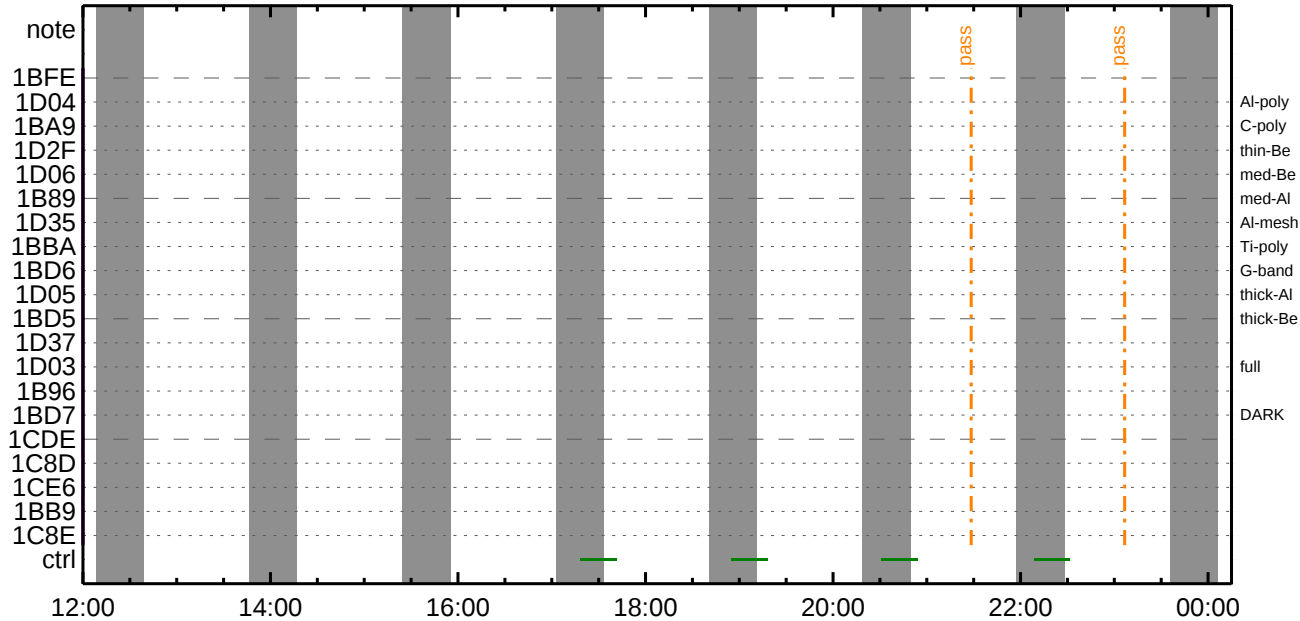
CMDI #0633 2025/06/10



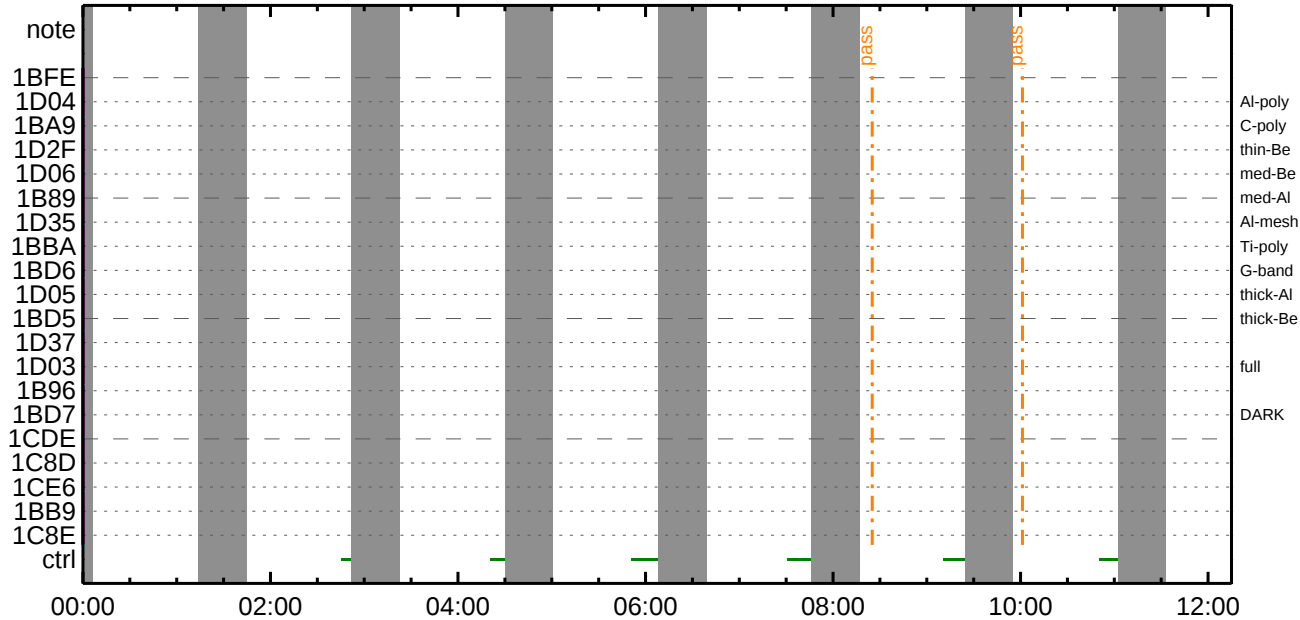
CMDI #0633 2025/06/11



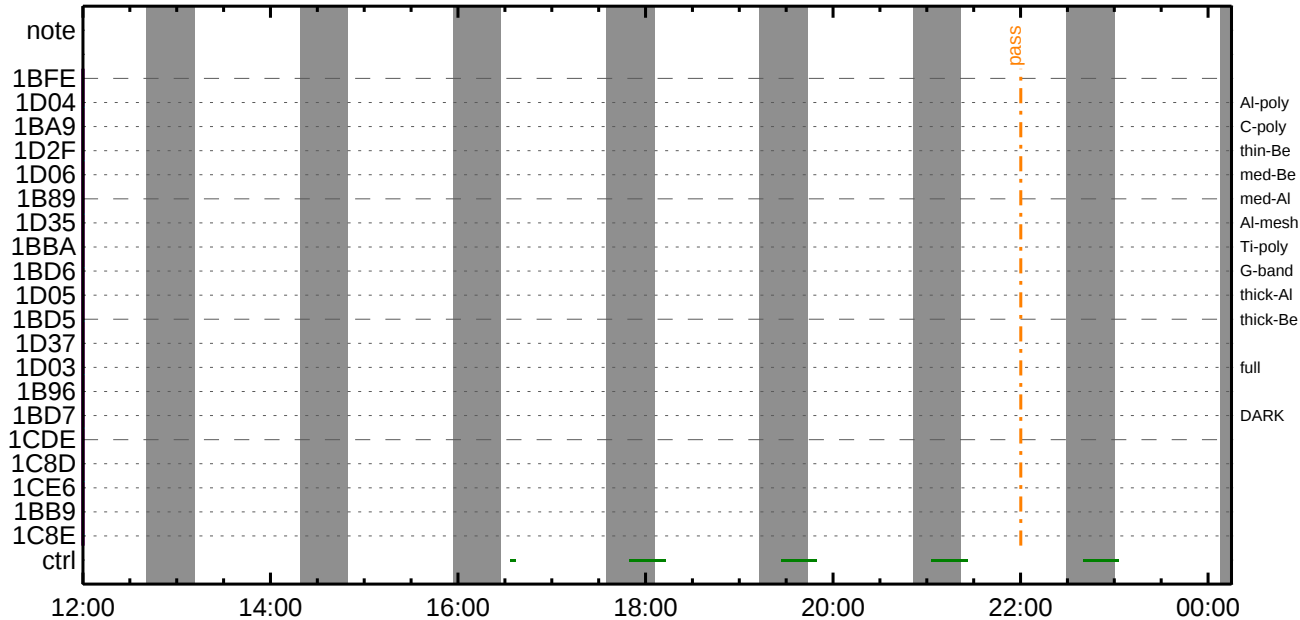
CMDI #0633 2025/06/11



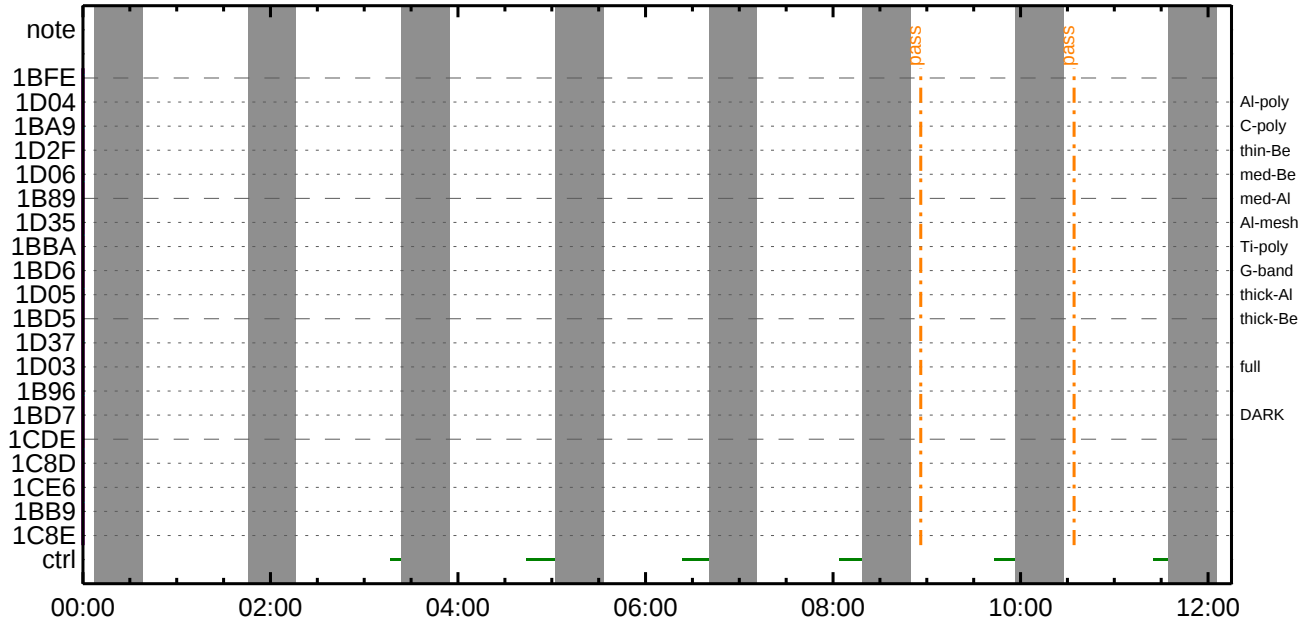
CMDI #0633 2025/06/12



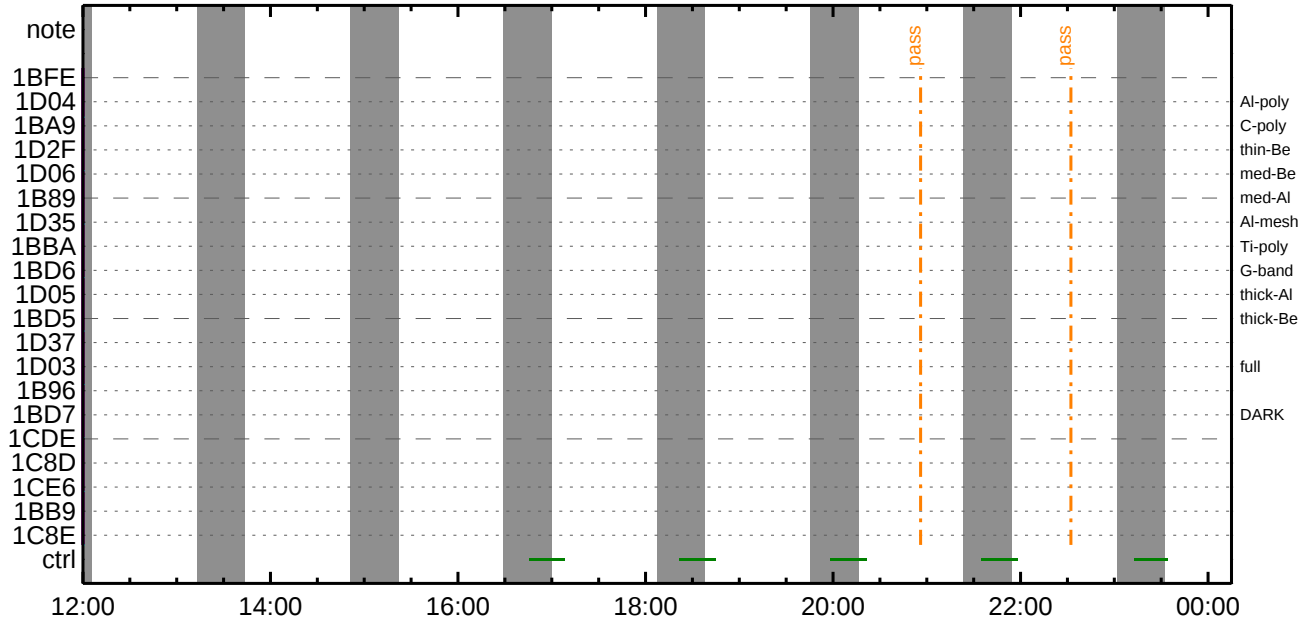
CMDI #0633 2025/06/12



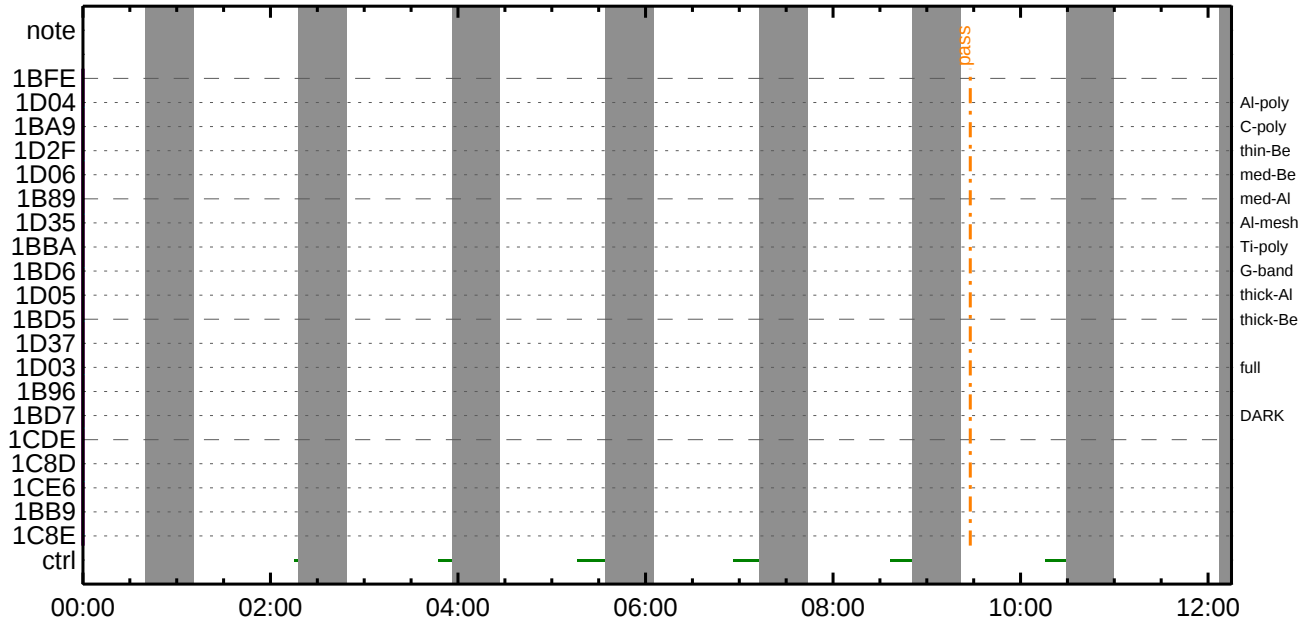
CMDI #0633 2025/06/13



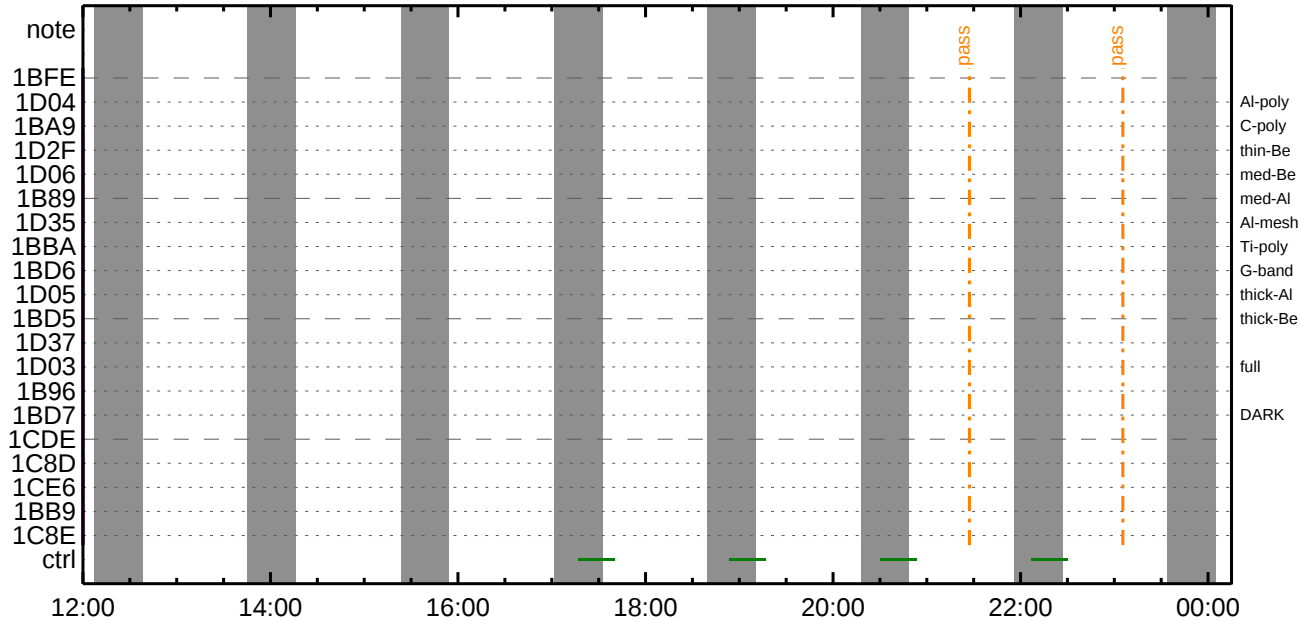
CMDI #0633 2025/06/13



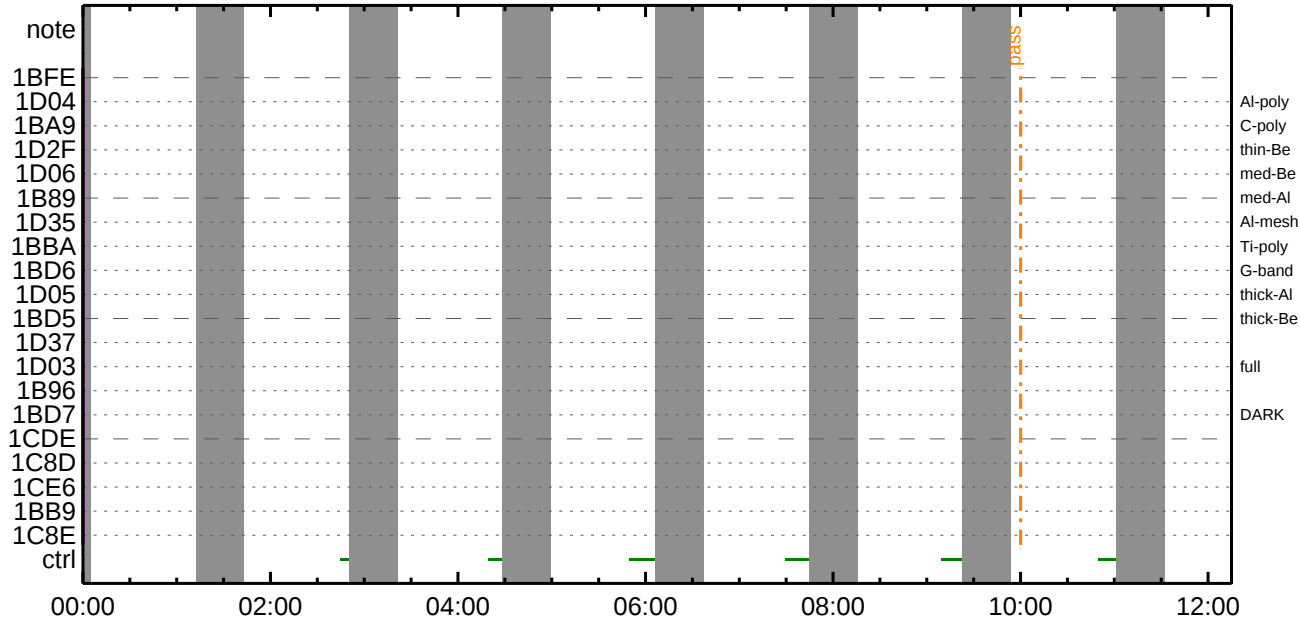
CMDI #0633 2025/06/14



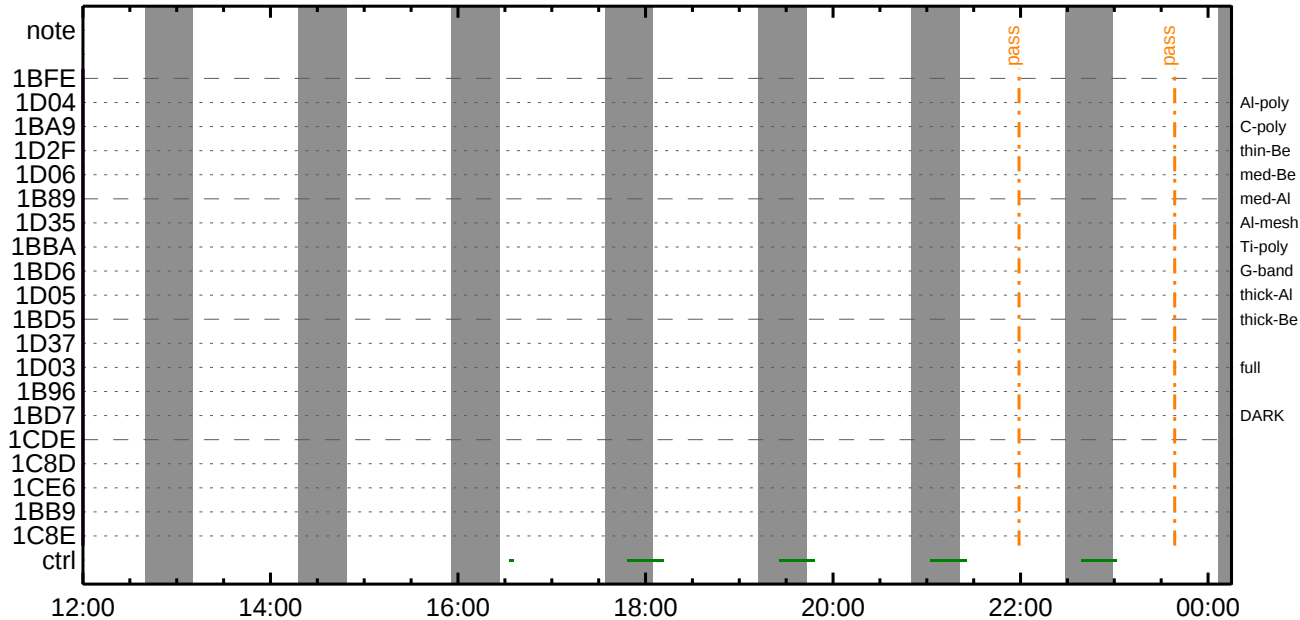
CMDI #0633 2025/06/14



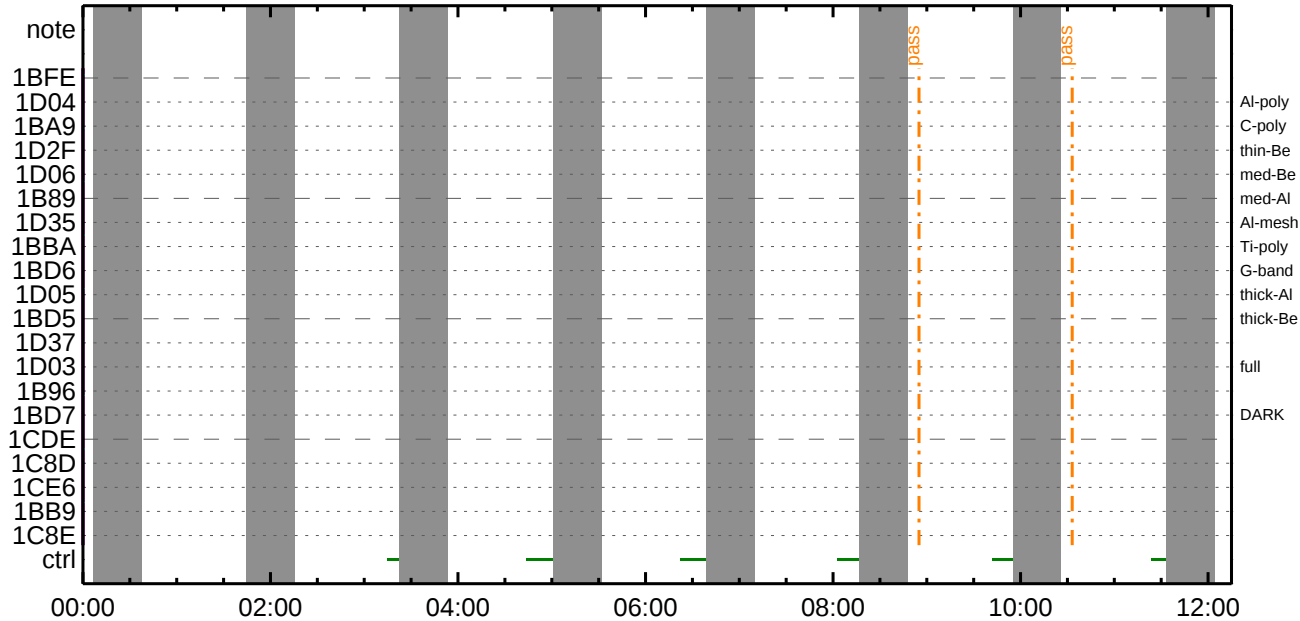
CMDI #0633 2025/06/15



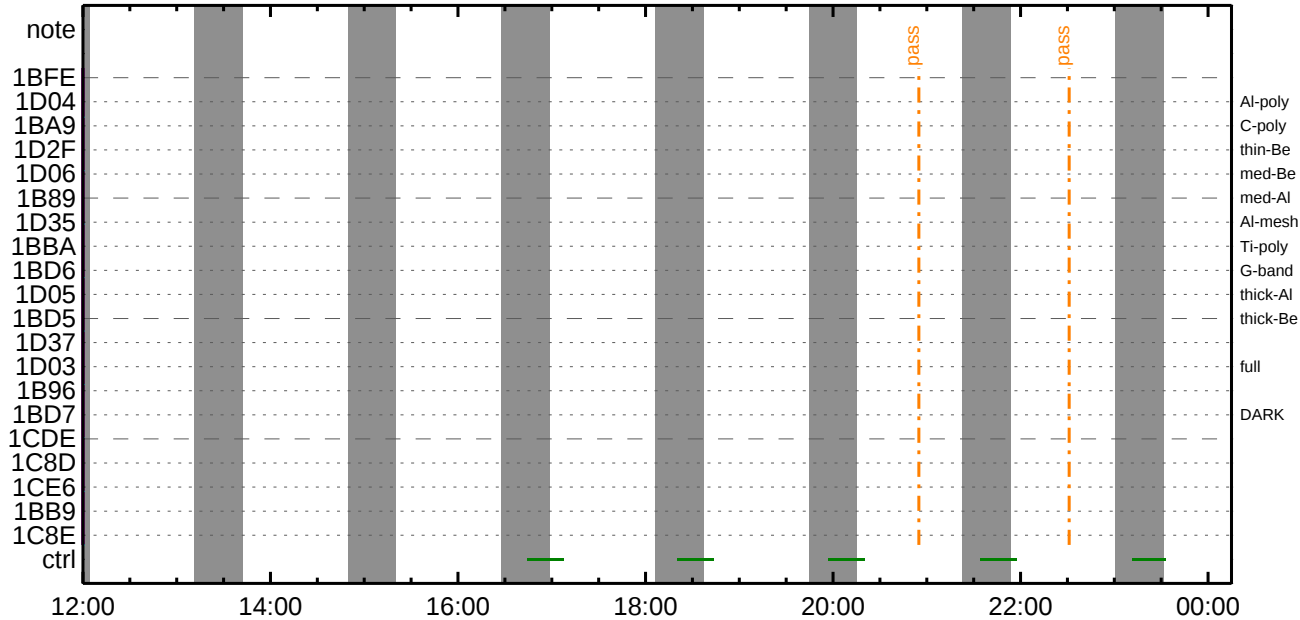
CMDI #0633 2025/06/15



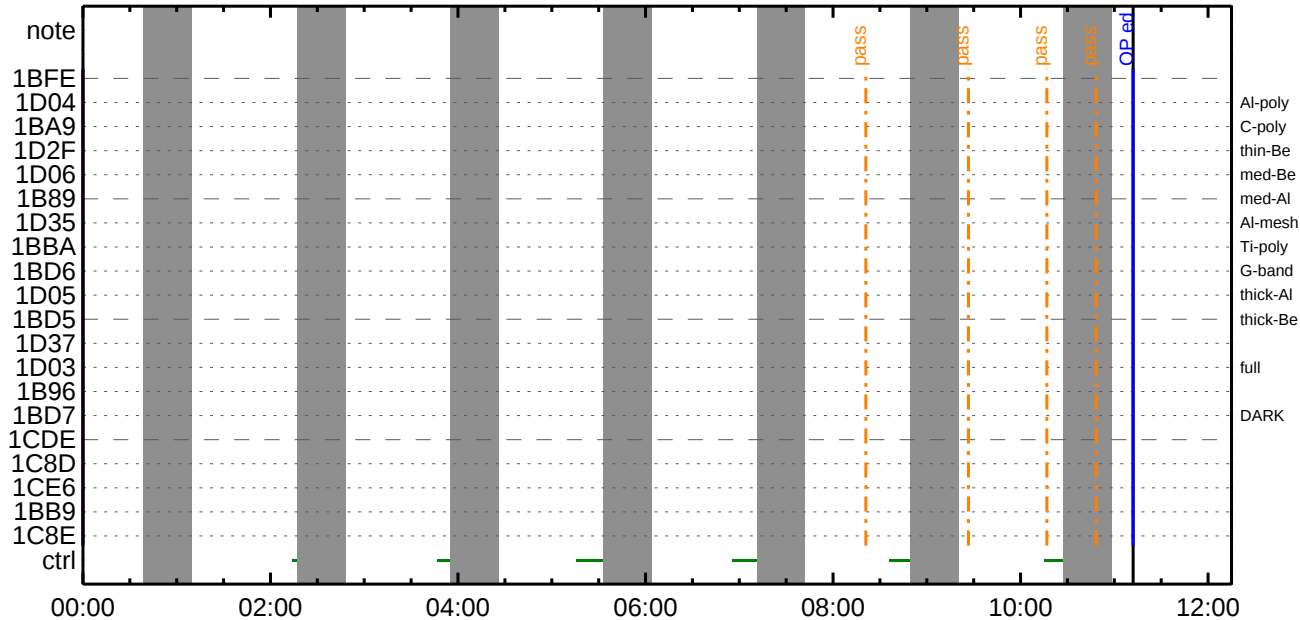
CMDI #0633 2025/06/16

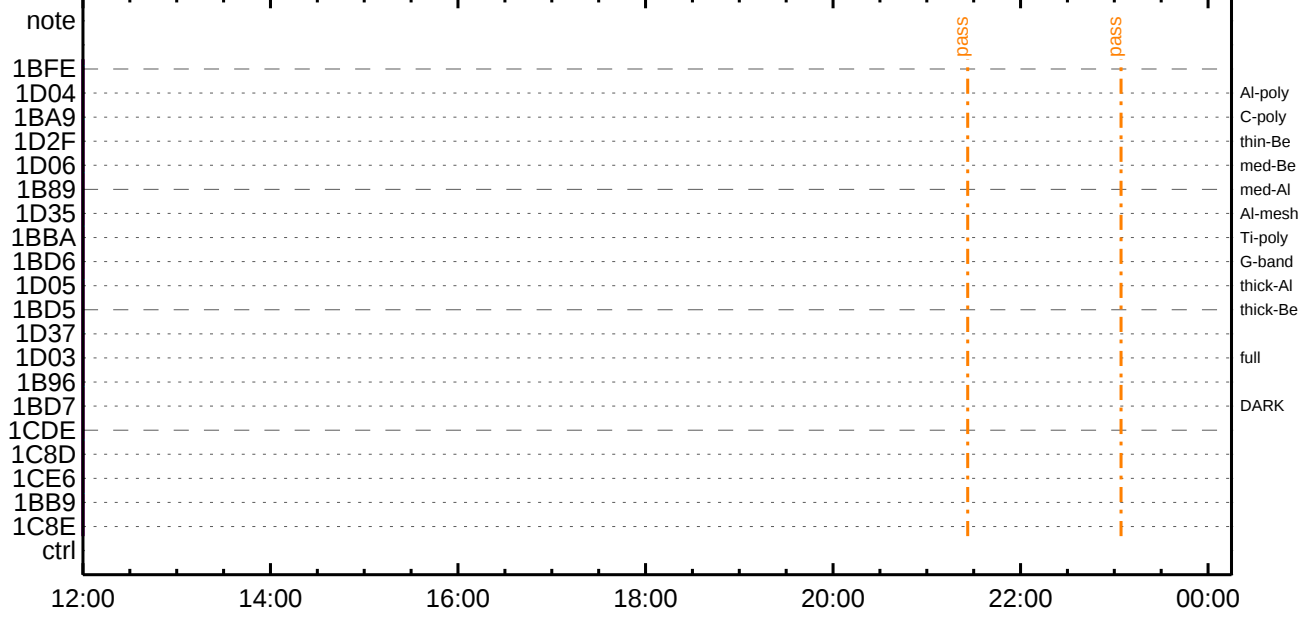


CMDI #0633 2025/06/16



CMDI #0633 2025/06/17





(a) Spacecraft Operation Procedure (real-commands)

```
main-323 2025-06-07 11:37:54 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥$¥Á¥~¼Ä»Ü;ä
0005 C.
0006 C. ¥À¥ß;¼¥³¥Ð¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¼¿¼Ä¼•µ°Æ»Í×ÁÇ¼Í¥Ç¥Á¥×¥í;¼¥É;ËËè¼µ•ííË;ËËË¼°ÇÖ¼•¼¿¼í¹Ç¼Í;ÇÀ®,ù¼¹¼è¼µ¼ÇÁ÷¿®¼•¼Ë¼µ¼³¼È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥í;¼¥É;¥Á¥ó¥×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OG¥í;¼¥É;ä
0019 . S. OP op-323:OP
0020 ()
0021 . S. OG og-323:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPíî°è¥Á¥ó¥×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0036 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥À¥ó¥×¼°áî»¼³íÇ$
0041 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¼í¼Ë¹Ç•è²íOK¼³íÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0055 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥À¥ó¥×¼°áî»¼³íÇ$
0060 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¼í¼Ë¹Ç•è²íOK¼³íÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0074 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥À¥ó¥×¼°áî»¼³íÇ$
0079 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¼í¼Ë¹Ç•è²íOK¼³íÇ$
0081 C.
0082 . C. ***** °Ë²¼¼í¼Ä´¼¼°¼ËË¬¼°Á÷¿® (¼¼µ-¥À¥ó¥×¼°è¼Ç¼¼°ÁÖ¼¼Ç¼°¼¼¬¼è¼¼¹Ç¼Ç¼¼) *****
0083 C. DHU¥ä;¼¥É;Ë¼ý½, ¥í;¼¥É;Ë¼òíá¼¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ÇÇ[HK1_PKT_FORM_NO] EQ 2
0087 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOAD¼¬Á÷¿®NG¼í¼¹¹Ç;Ç°Ë²¼¼íTI-CMDÁ÷¿®¼í¼Ä¹Ö¼¼¬¼è¼¼¼³¼È;£
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0096	C.	SET EDUMP I± iYŶY¹C¹Ô±;³E;f		
0097	C.			
0098	C.	TIY³YDYOÉDÄI; (UT)		
0099	+	TI 2025-06-07 11:10:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2025-06-07 11:10:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2025-06-07 11:10:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2025-06-07 11:14:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼±İÄë¼İİŶ±İYÄY\$YÄY¹¹äiÜ		
0116	C.	¢¢[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	¢¢[HK1_TI_CMD_NUM]	EQ	4
0118	C.	¢¢[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	¢¢[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°èYÄYOY×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+	DC 01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7
0136	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	YÄYOY×½¹İ»±ò³İÇ\$		
0142	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL±İ¼È¹Ç•è²İOK±ò³İÇ\$		
0145	C.			
0146	C.	DHUYâ;¼YÈ;È¼Y½,Yİ;¼YÈ;È±òİá±¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	¢¢[HK1_PKT_FORM_NO]	EQ	2
0150	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+	TI 2025-06-07 11:14:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+	TI 2025-06-07 11:14:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+	TI 2025-06-07 11:14:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP ´GÄİ±İ»ö¼Y±ÈÄD±¹±èDCBC•×²è *****		
0181	C.	(¼Ä°İYÖYÄYÈYDYEYÄYÇYè±È½¼±¼Ä»Ü±¹±è)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** YDY¹•İ Daily±İŶ±İÈ´Ø±¹±èDCBC•×²è *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	C.	İäLOSYÄY\$YÄY¹¹Ä»Ü;ä		
01				


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main-324 2025-06-07 11:37:54 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŸÇŸ×Ÿí;¼ŸÉ;ËÈè²µ•íîÊ;ËÈÈ¼°ÇÔñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Òðò¹íî,ñ•;ÇÉÔÍ×ñÈXÂÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÖñ¬°ÄÄêñ•ñççé;Ç°Ê²¼ñî°EÄ,¼ê½Çñðò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0045 C.
0046 . C. ;ãŸÇŸóŸEŸEÄÚÄØ;ÊÄ•Ä°²òÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ã;Ç°Ê²¼ñðò¼Ä¹Ôñ¹ñè;£
0055 C. ŸÇŸóŸEŸEÄÚÄØñäÄ•Ä°²òÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0071 C.
0072 . C. ;ãŸÇŸóŸEŸEÄÚÄØ;ÊÄ•Ä°²òÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;ÇXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 1m3s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-06-07 11:14:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÄîñî»ö¼ÝñÄðñ¹ñèDCBC•×²è *****
0156 C. (¼â°îÝÖÝÄÝÈÞÝÈÝÄÝÇÝèñÈ½¼ñ¼Ä»Üñ¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•İ Daily±;İññÈ'Øñ¹ñèDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;äLOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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main-325 2025-06-07 11:37:54 184 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EA, Í×ÈÝñäLOSñÞñÇñİ»Þ'Öòò¹íî,ñ•;ÇÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲŲ~¾ÒÄÖñ¬°ÄÄèñ•ñç,é;Ç°Ê²¼ñî°EA,¼è½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñç¼ñ¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öññ»°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲËŲÊÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EA,ñ¬¼«E°Ää»ßñ•ñç,ä;Ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲÇŲóŲËŲÊŲÊÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñ¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñç¼ñ¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öññ»°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲÇŲóŲËŲÊŲÊÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1 XPA ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 +. DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 +. DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 +. DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 +. DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 +. DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 +. DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 20 20)
0132 +. DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 08)
0134 +. DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 08 20)
0136 +. DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 85 83 08 08)
0138 +. DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 +. DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 +. DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 +. DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 +. DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 +. DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 0d)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0e)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2025-06-07 11:14:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 C.
0170 . C. ***** MDP 'ûÃîñî»ö¼ÝñÊÃÐñ¹ëDCBC•x²è *****
0171 C. (¾å°îÿÓŸÄŸÊŸËŸâŸçŸëñ¾¼ññ¼Ã»Üñ¹ë)
0172 . S. DC-BC dcbc-402:DCBC
0173 (MDP_known_event)
0174 C.
0175 C.
0176 . C. ***** ŸÐŸ¹•Ï Daily±¿ÎññË'Øñ¹ëDCBC•x²è *****
0177 . S. DC-BC dcbc-153:DCBC
0178 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0179 C.
0180 C.
0181 . C. ¡ãLOSŸÁŸSŸÃŸ´¼Ã»Ü¡ã
0182 C.
0183 . C. ***** LOS *****
0184 C.

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XRT_OGLIST_0633.chk

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2025/06/07	18:05:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/06/07	18:07:56.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/07	18:07:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/06/07	18:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/06/07	18:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/07	19:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/07	19:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/07	19:48:04.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/07	19:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/07	20:23:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/07	20:24:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/07	21:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/07	21:26:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/07	21:26:04.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/07	21:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/07	22:01:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/07	22:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/07	23:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/07	23:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/07	23:04:34.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/07	23:04:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/07	23:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/07	23:36:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/07	23:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/08	00:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/08	00:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/08	00:42:34.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/08	00:42:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/08	00:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/08	01:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/08	01:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/08	02:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/08	02:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/08	02:17:04.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/08	02:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/08	02:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/08	02:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/08	03:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/08	03:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/08	03:50:02.0	XRT_PREFLR_STRT_428_OG [0x1ac]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/06/08	03:50:04.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/06/08	03:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/08	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/06/08	03:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/06/08	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/06/08	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00
2025/06/08	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/06/08	04:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/06/08	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/06/08	04:00:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/06/08	04:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/06/08	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e

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2025/06/08	04:29:30.0	XRT_Custom_430_OG [0x1ae]					
2025/06/08	04:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/06/08	05:19:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/06/08	05:19:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/06/08	05:19:04.0	XRT_PREFLR_STRT_428_OG [0x1ac]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/06/08	05:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/06/08	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/06/08	06:13:26.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/06/08	06:13:28.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2025/06/08	06:13:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2025/06/08	06:13:48.0	XRT_FLD_DIS_409_OG [0x199]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2025/06/08	06:13:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2025/06/08	06:13:52.0	XRT_ARS_DIS_435_OG [0x1b3]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/06/08	06:16:28.0	XRT_QT_PROG_SET_404_OG [0x194]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2025/06/08	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/06/08	06:23:30.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	04 06 24 01 81		
2025/06/08	06:23:30.5	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/06/08	06:23:32.0	XRT_TCIB_XRT_S_HTR_A_ENA_421_OG [0x1a5]					
		TCIB_XRT_S_HTR_A_ENA	0	04-BC			
2025/06/09	05:25:30.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2025/06/09	05:35:30.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	04 06 24 01 81		
2025/06/09	13:13:30.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 5a 24 01 81		
2025/06/09	13:36:30.0	AOCS_Ore-point_Start_5_OG [0x09b]					
		AOCU_NM	5	02-76	00 51 3f 01 81		
2025/06/09	14:51:30.0	AOCS_Ore-point_Start_6_OG [0x09c]					
		AOCU_NM	5	02-76	00 48 5a 01 81		
2025/06/09	15:14:30.0	AOCS_Ore-point_Start_7_OG [0x09d]					
		AOCU_NM	5	02-76	00 3f 74 01 81		
2025/06/09	16:39:00.0	AOCS_Ore-point_Start_8_OG [0x09e]					
		AOCU_NM	5	02-76	00 36 97 01 81		
2025/06/09	17:02:00.0	AOCS_Ore-point_Start_9_OG [0x09f]					
		AOCU_NM	5	02-76	00 2d b2 01 81		
2025/06/09	18:15:30.0	AOCS_Ore-point_Start_10_OG [0x0a0]					
		AOCU_NM	5	02-76	00 24 cc 01 81		
2025/06/09	18:38:30.0	AOCS_Ore-point_Start_11_OG [0x0a1]					
		AOCU_NM	5	02-76	00 1b e7 01 81		
2025/06/09	19:52:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]					
		AOCU_NM	5	02-76	00 13 02 01 81		
2025/06/09	20:15:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]					
		AOCU_NM	5	02-76	00 0a 24 01 81		
2025/06/09	21:28:30.0	AOCS_Ore-point_Start_14_OG [0x0a4]					
		AOCU_NM	5	02-76	00 02 24 01 81		
2025/06/09	21:51:30.0	AOCS_Ore-point_Start_15_OG [0x0a5]					
		AOCU_NM	5	02-76	00 f9 40 01 81		
2025/06/09	23:05:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]					
		AOCU_NM	5	02-76	00 f0 5b 01 81		
2025/06/09	23:28:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]					
		AOCU_NM	5	02-76	00 e7 75 01 81		
2025/06/10	00:40:30.0	AOCS_Ore-point_Start_18_OG [0x0a8]					
		AOCU_NM	5	02-76	00 de 98 01 81		
2025/06/10	01:03:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]					
		AOCU_NM	5	02-76	00 d5 b3 01 81		
2025/06/10	02:19:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]					
		AOCU_NM	5	02-76	00 cc cd 01 81		
2025/06/10	02:42:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]					
		AOCU_NM	5	02-76	00 c3 e8 01 81		
2025/06/10	03:57:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]					
		AOCU_NM	5	02-76	00 bb 03 01 81		
2025/06/10	04:20:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]					
		AOCU_NM	5	02-76	00 b2 25 01 81		
2025/06/10	04:43:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	04 06 24 01 81		
2025/06/10	05:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2025/06/10	06:08:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	04 06 24 01 81		
2025/06/10	11:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		