

Period: 2025/07/01 11:07:00 - 2025/07/05 11:05:00

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -All/mesh(2048ms) - 1x1, All/Poly(1443ms) - 2x2 - w leak image-1msCCD														
Term		Pointing (x, y)					Comment							
07/02 13:13:00 - 07/02 13:19:54		Fixed (-528.4, 528.4)					Post bakeout Q4							
PROG= 15 1-time(s) Subr= 1 1-time(s) 2.0sec Seqn= 14 1-time(s) 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec Subr= 2 1-time(s) 120.0sec														

XOB #1D2F: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(12/256/2048), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/5

Term		Pointing (x, y)				Comment									
07/02 13:23:00 - 07/02 13:29:54		Fixed (0.0, 0.0)				Post bakeout synoptics									
PROG= 17															
Subr= 1		1-time(s)		2.0sec											
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec	
Seqn= 66		1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 22		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 19		1-time(s)		2.0sec											
thin-Be/Open		thin-Be/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 23		1-time(s)		4.0sec											
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Subr= 2		1-time(s)		2.0sec											
Seqn= 65		1-time(s)		2.0sec											
Open/thick-Be		Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec	
Seqn= 17		1-time(s)		2.0sec											
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 33		1-time(s)		2.0sec											
med-Be/Open		Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
med-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
med-Be/Open		med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Seqn= 89		1-time(s)		2.0sec											
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC	Buffer	Interval	

XOB #1B96: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
07/02 13:33:00 - 07/02 15:54:54	Track (501.4, 82.8) @ 07/02 13:30:00	(End of synoptic) +AR obs
07/02 16:08:06 - 07/02 22:29:54	Track (27.0, -634.5) @ 07/02 16:05:00	HOP 508
07/03 02:33:00 - 07/03 03:59:54	Track (593.0, 85.7) @ 07/03 02:30:00	AR obs
07/03 06:49:00 - 07/03 10:42:30	Track (621.0, 86.9) @ 07/03 06:46:00	AR obs
PROG= 07 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 1ms	Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms	Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 10-time(s) 360.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s	Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open Al-poly/Open close	Safe Norm 125ms	Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 1.00s	Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval	

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795

Term	Pointing (x, y)	Comment
07/02 15:58:00 - 07/02 16:04:54	Fixed (0.0, 0.0)	synoptic, shifted -125.0 min
07/03 06:39:00 - 07/03 06:45:54	Fixed (0.0, 0.0)	HOP 349 + 448 / synoptic, shifted 36.0 min

PROG= 10 1-time(s)

└ Subr= 1 1-time(s) 2.0sec

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+L														
Term			Pointing (x, y)			Comment								
07/03 04:03:00 - 07/03 06:35:54			Fixed (0.0, 0.0)			HOP 349 + 448 / synoptic, shifted 36.0 min								
PROG= 13 Inf.-time(s)														
└ Subr= 1		1-time(s)		300.0sec										
└ Seqn=100		1-time(s)		2.0sec										
└┐		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0 0 2.0sec
└┐		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0 0 2.0sec
└┐		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0 0 2.0sec
└ Seqn= 84		1-time(s)		2.0sec										
└┐		Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0 0 2.0sec
└┐		Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0 0 2.0sec
└┐		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0 0 2.0sec
└ Seqn= 57		1-time(s)		2.0sec										

	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 81		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=95	0	0	2.0sec
	Subr= 2		6-time(s)	1200.0sec									
	Seqn= 8		1-time(s)	2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Seqn= 6		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Seqn= 29		1-time(s)	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL

Term	Pointing (x, y)	Comment
07/02 13:33:00 - 07/02 15:54:54	Track (501.4, 82.8) @ 07/02 13:30:00	(End of synoptic) +AR obs
07/02 16:08:06 - 07/02 22:29:54	Track (27.0, -634.5) @ 07/02 16:05:00	HOP 508
07/03 02:33:00 - 07/03 03:59:54	Track (593.0, 85.7) @ 07/03 02:30:00	AR obs
07/03 06:49:00 - 07/03 10:42:30	Track (621.0, 86.9) @ 07/03 06:46:00	AR obs

PROG= 14 30-time(s)

	Subr= 1		1-time(s)	2.0sec									
	Seqn= 68		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
	Subr= 2		20-time(s)	2.0sec									
	Seqn= 11		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Seqn= 73		1-time(s)	10.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Subr= 3		1-time(s)	2.0sec									
	Seqn= 10		1-time(s)	2.0sec									
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 11		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Seqn= 87		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C96: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL

Term	Pointing (x, y)	Comment
07/03 04:03:00 - 07/03 06:35:54	Fixed (0.0, 0.0)	HOP 349 + 448 / synoptic, shifted 36.0 min

PROG= 18 30-time(s)

	Subr= 1		20-time(s)	2.0sec									
	Seqn= 11		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Seqn= 73		1-time(s)	10.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Subr= 2		1-time(s)	2.0sec									
	Seqn= 10		1-time(s)	2.0sec									
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Seqn= 11		1-time(s)	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Seqn= 87		1-time(s)	2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

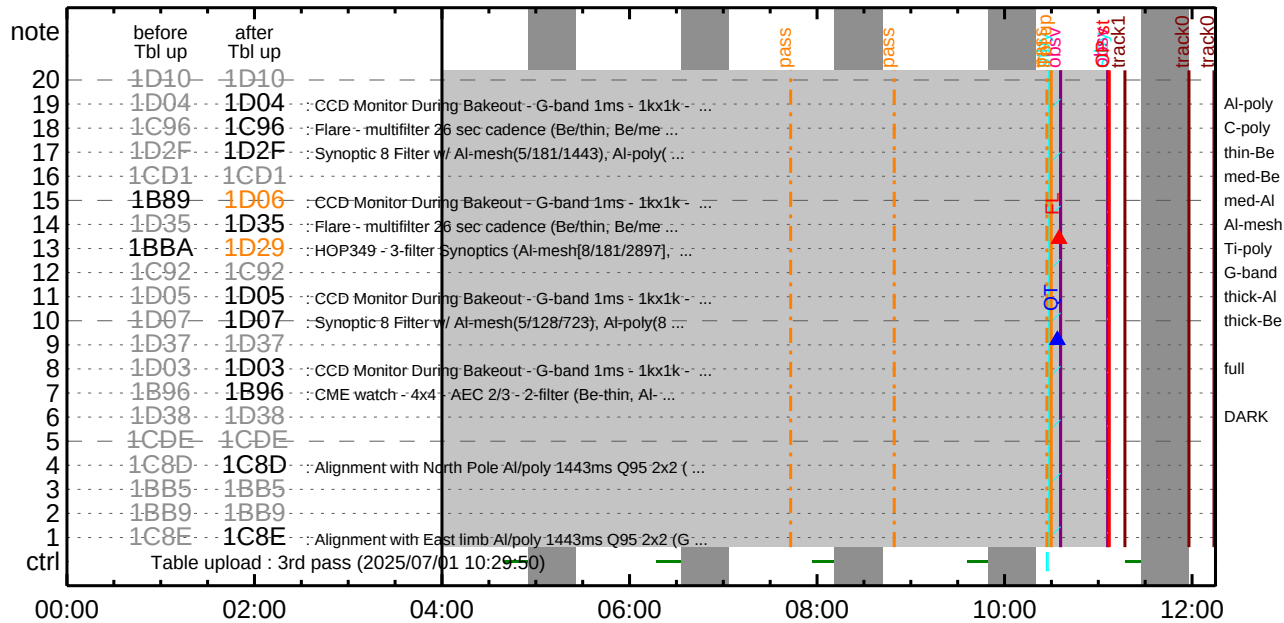
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Flare Detection

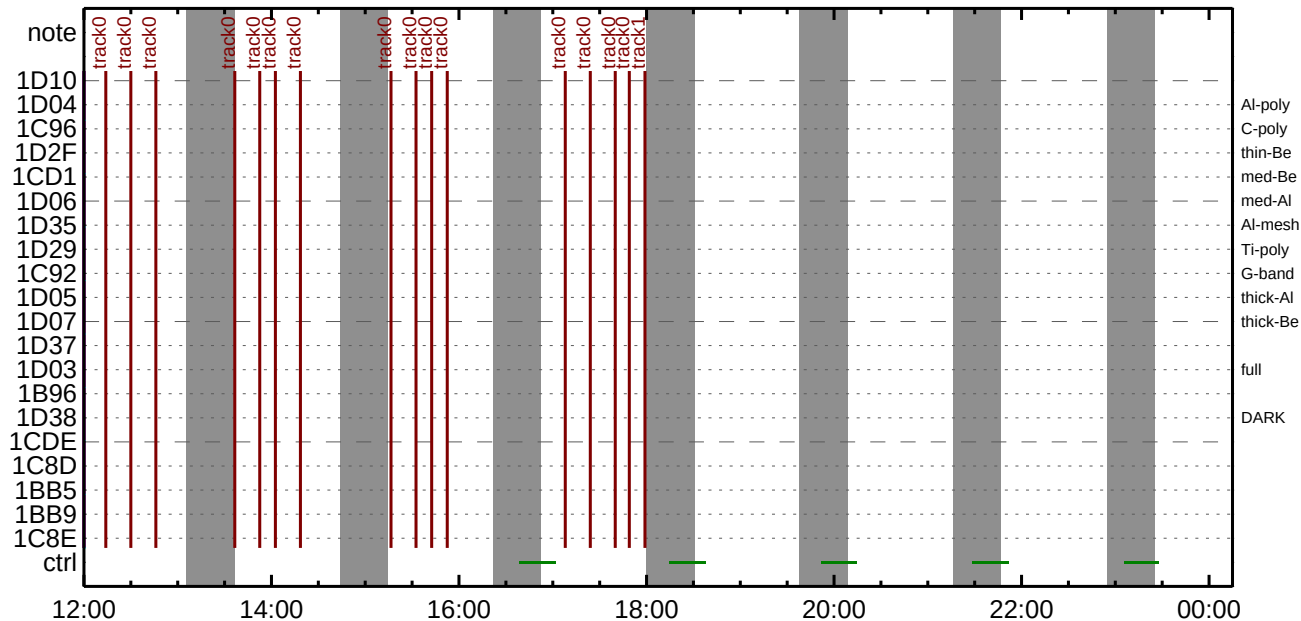
* * * * *

FLD Patrol											
Term			Pointing (x, y)						Comment		
07/01 10:30:50 - 07/02 12:44:22			cannot be identified								
07/02 13:30:16 - 07/02 15:55:18			Track (501.4, 82.8) @ 07/02 13:30:00 (End of synoptic) +AR obs								
07/02 16:05:21 - 07/02 22:30:18			Track (27.0, -634.5) @ 07/02 16:05:00 HOP 508								
07/03 02:30:16 - 07/03 06:36:18			Track (593.0, 85.7) @ 07/03 02:30:00 AR obs								
07/03 06:46:16 - 07/05 11:05:00			Track (621.0, 86.9) @ 07/03 06:46:00 AR obs								
Al-poly/Open		Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp. AEC Buffer Interval

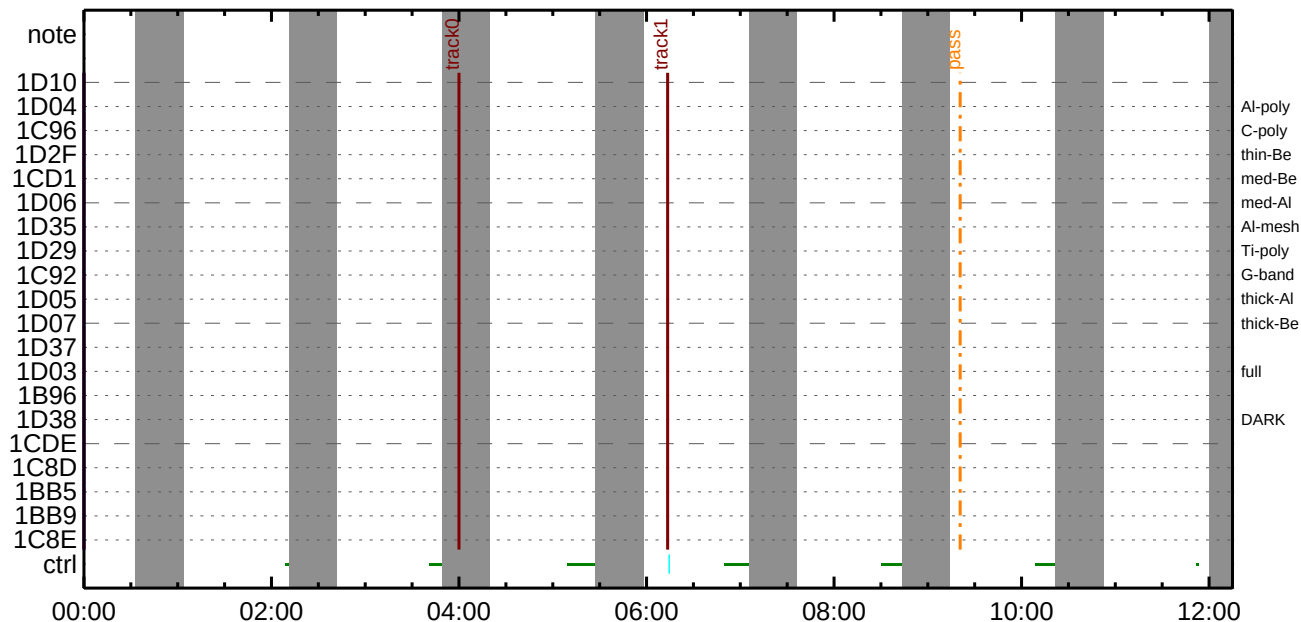
CMDI #0679 2025/07/01



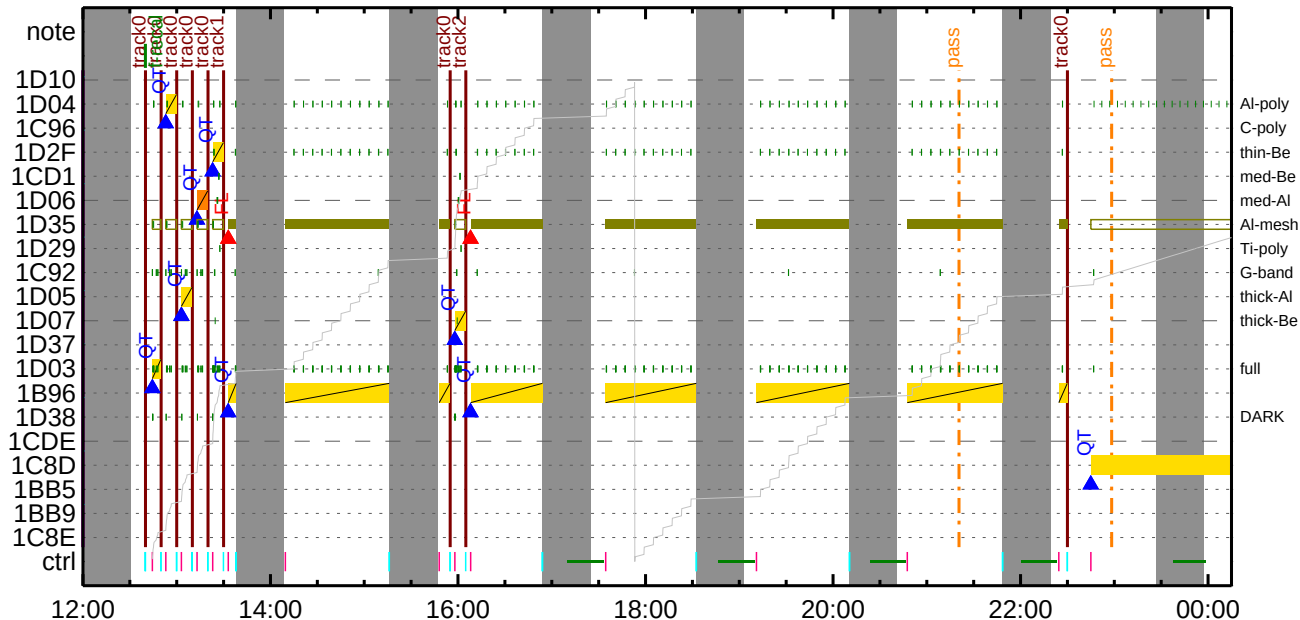
CMDI #0679 2025/07/01



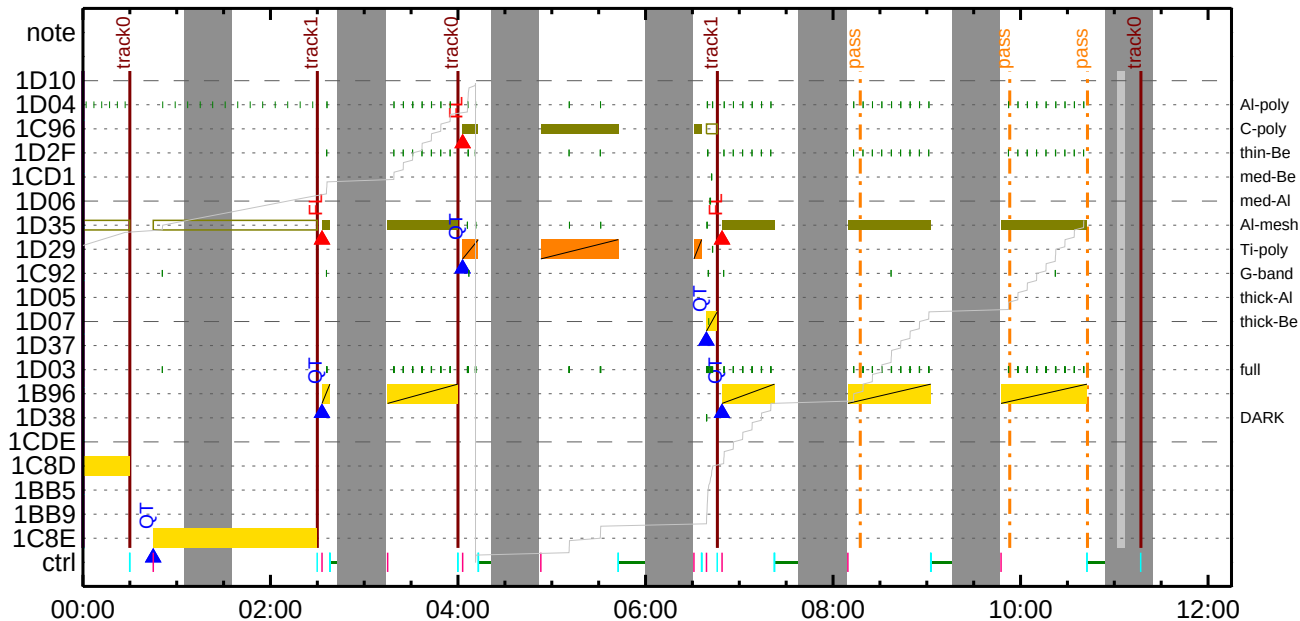
CMDI #0679 2025/07/02



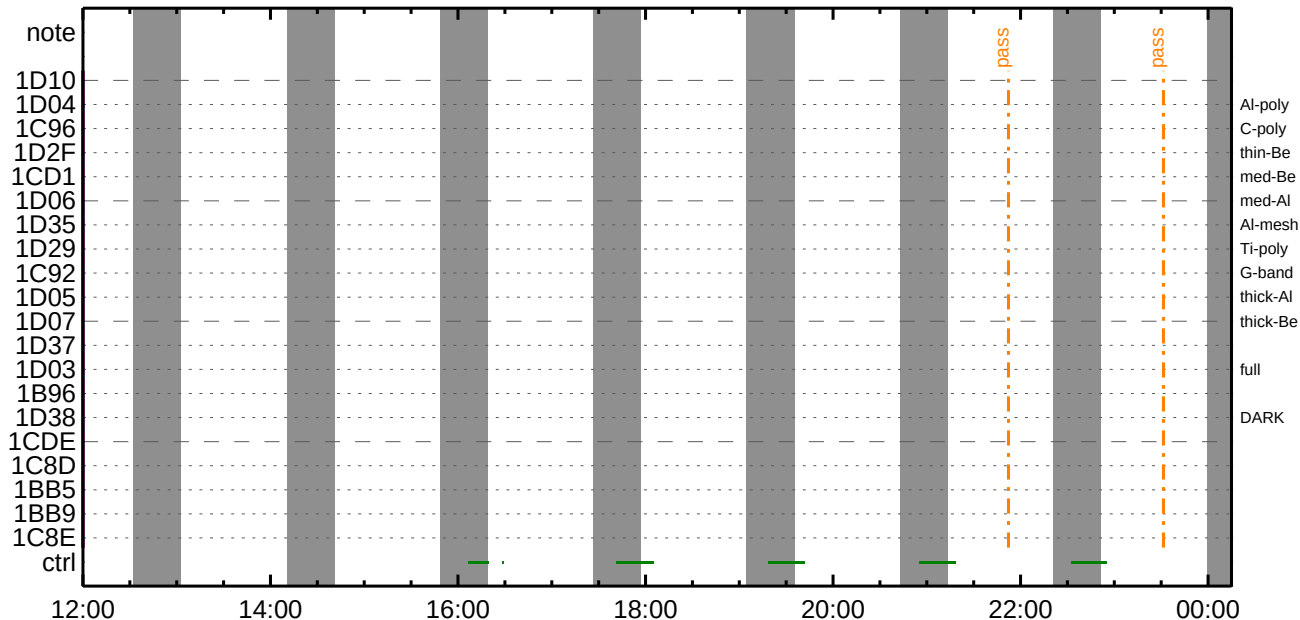
CMDI #0679 2025/07/02



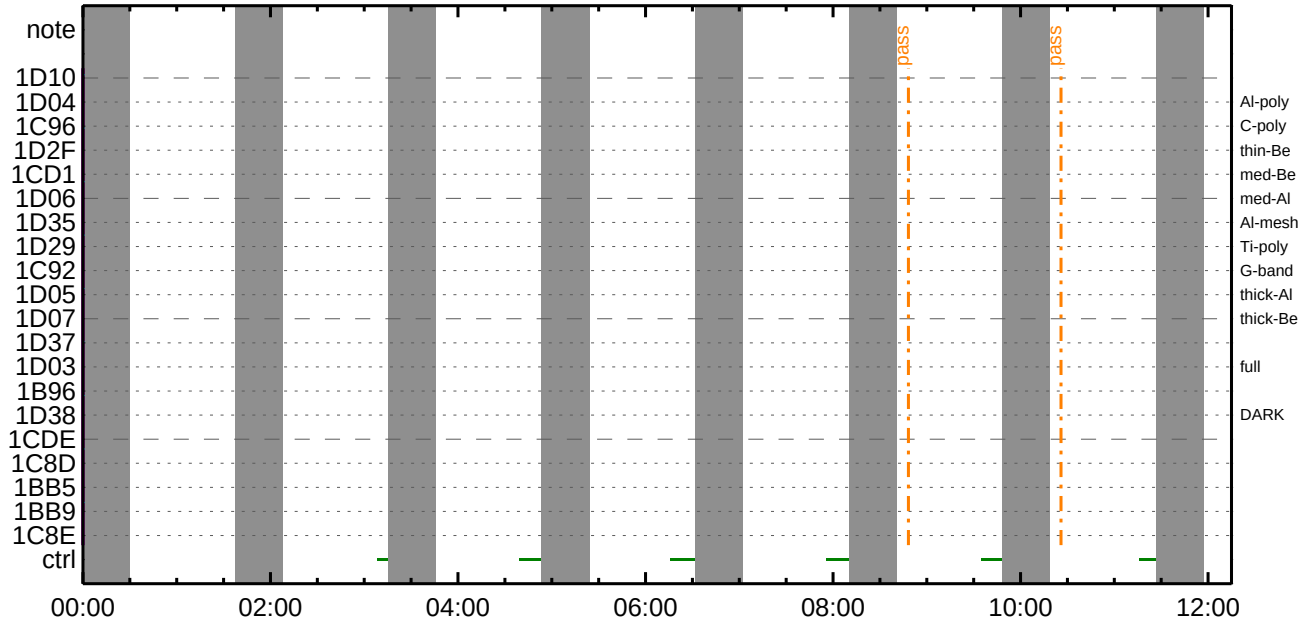
CMDI #0679 2025/07/03



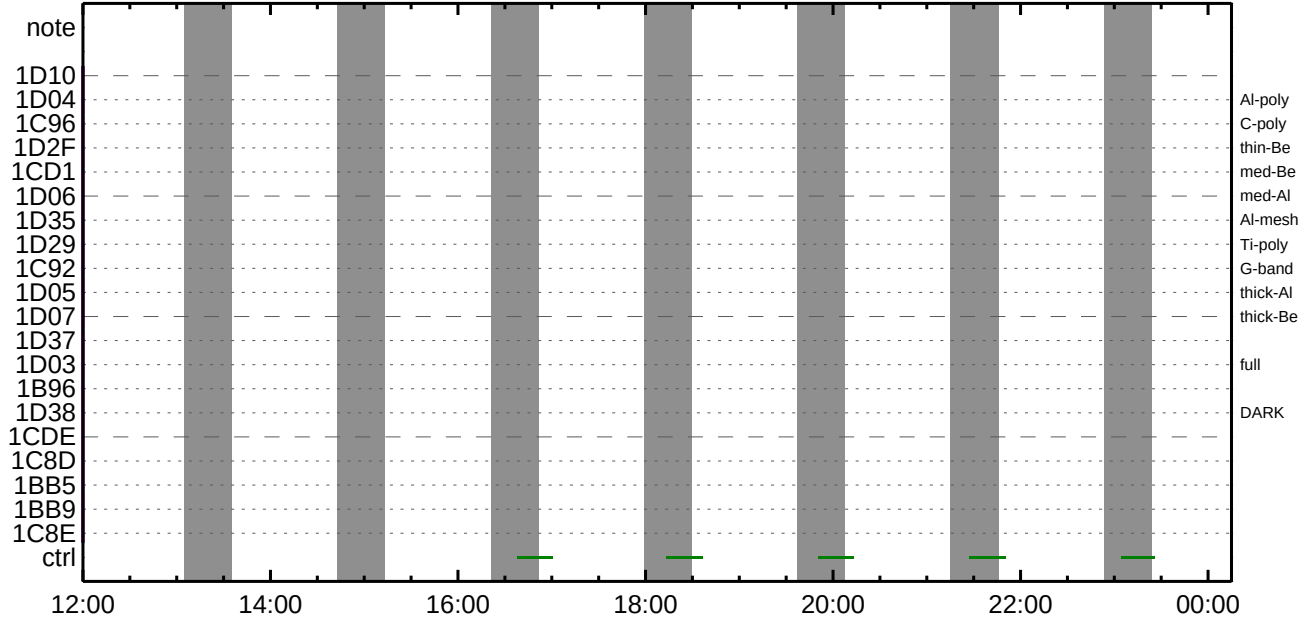
CMDI #0679 2025/07/03



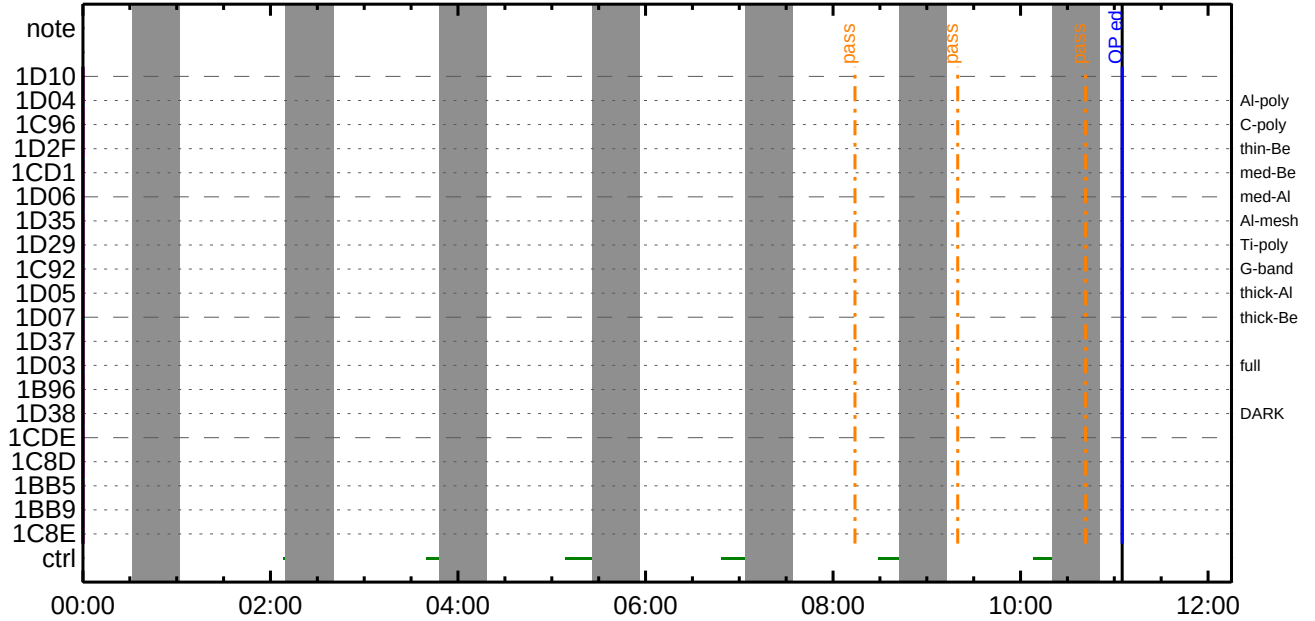
CMDI #0679 2025/07/04

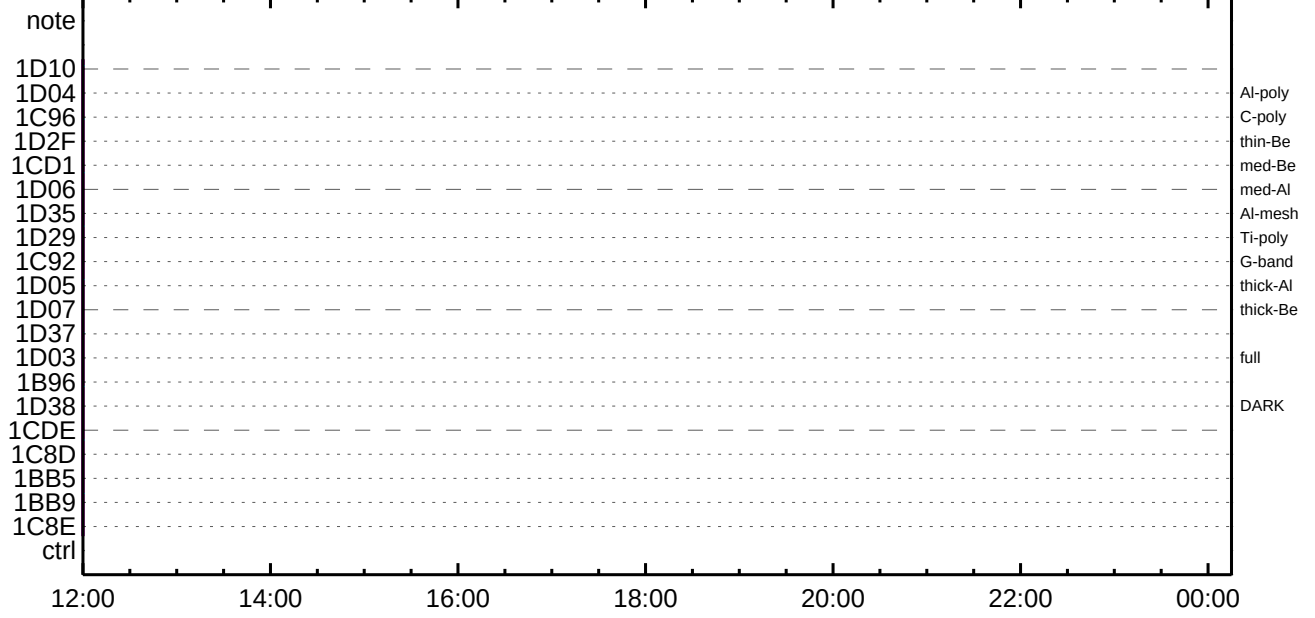


CMDI #0679 2025/07/04



CMDI #0679 2025/07/05





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main-368 2025-07-01 12:35:44 194 33 SOLAR-B MAIN //
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0095 . C. NOTICE ;\$ OPOG UPLOAD¤¬Ä÷¿®NG¤Î¾¼¹Ç;ç°Ê²¼¤ÏTI-CMDÄ÷¿®¤Ï¼Ä¹Ô¤•¤Æ¤¤¤³¤È;£

0096	C.				
0097	C.				
0098	.	C.	TIY³YP¥ó¥É¤ðÄDİ; (UT)		
0099	+	TI	2025-07-01 11:02:00.0		
0100	DC	01-B3 DHU_OP_STOP			
0101	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.				
0103	+	TI	2025-07-01 11:02:01.0		
0104	DC	01-B4 DHU_OP_COPY			
0105	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.				
0107	+	TI	2025-07-01 11:02:01.0		
0108	DC	01-B5 DHU_OPOG_COPY			
0109	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.				
0111	+	TI	2025-07-01 11:06:59.5		
0112	DC	01-B2 DHU_OP_START			
0113	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.				
0115	C.		°Ê²¼¤İÄê%ĩÍŃ¤İ¥Á¥\$¥Ã¥~¹àìÜ		
0116	C.		¢¢[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.		¢¢[HK1_TI_CMD_NUM]	EQ	4
0118	C.		¢¢[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.		¢¢[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.				
0121	.	C.	*****		
0122	C.	TIİî°è¥À¥ó¥×			
0123	C.	*****			
0124	C.				
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)			
0126	+	DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)			
0128	C.		¢¢[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.		¢¢[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.		¢¢[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.		¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.		¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)			
0135	C.		¢¢[HK1_PKT_FORM_NO]	EQ	7
0136	C.		¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.		¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.		¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.		¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.				
0141	.	C.	¥À¥ó¥×½ªİ»¤ò³İÇ\$		
0142	C.		¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.				
0144	.	C.	RAM ID=TI_TBL¤İ¾È¹Ç•ë²ÌOK¤ò³İÇ\$		
0145	C.				
0146	.	C.	DHUYâ;¼¥É;Ê¼Ý½,¥ì;¼¥É;Ë¤òİá¤¹		
0147	+	DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)			
0149	C.		¢¢[HK1_PKT_FORM_NO]	EQ	2
0150	C.		¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.		¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.		¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.				
0154	C.				
0155	C.		***** XRT START *****		
0156	C.		Execute, after the success of OP upload.		
0157	+	TI	2025-07-01 11:06:00.0		
0158	DC	07-F0 MDP_XRT_MODE_STBY			
0159	BC	(c3)			
0160	.	C.	[] [HK1_TI_CMD_NUM] EQ 1COUNTUP		
0161	C.				
0162	C.		***** XRT END *****		
0163	.	C.	Stop EIS observation and temporarily disable EIS mode changes		
0164	C.				
0165	C.				
0166	C.		***** Start EIS operation (TI set) *****		
0167	C.		Execute, after the success of OP upload.		
0168	C.		Set EIS TI-commands		
0169	+	TI	2025-07-01 11:06:30.0		
0170	DC	07-FC EIS_MODE_MANU			
0171	BC	(21 02)			
0172	+	TI	2025-07-01 11:06:40.0		
0173	DC	07-FC EIS_MODE_CHG_DIS			
0174	BC	(22)			
0175	.	C.	[] [HK1_TI_CMD_NUM] EQ 2 COUNTUP		
0176	C.		***** End EIS operation (TI set) *****		
0177	C.				
0178	C.				
0179	C.				
0180	.	C.	***** MDP ‘ĞĂİ¤İ»ö¼Ý¤ĖÄ¤¤¹¤ēDCBC•×²è *****		
0181	C.		(¼ă°İıÖ¥Ä¥Ė¥P¥Ė¥ä¥ç¥ē¤Ė¼¤¤¼Ä»Ü¤¹¤ē)		
0182	.	S.	DC-BC dcbc-402:DCBC		
0183			(		


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0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÄñ•µ°E»Í×ÄÇñÍŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•ííË;ËñÉ¼°ÇÖñ•ñç¼í¹ÇñÏ;çÄ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ËÝñäLOSñÐñÇñÍ»p´Öðòíí,ñ•;çÉÖÍ×ñËXÄÓONñÏ¹ÖñËñíñËñññ³ñË;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÖÄÖñ¬°ÄÄèñ•ñç,ç°Ë²¼ñí°EÄ,¼è½Çñðò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÄ,
0033 C. *****
0034 C. °° RESTART;ËPT1;Ëñ•ñç¼ñ¼í¹ÇñÏ;ç°Ë²¼ñí¼Ä¹Öñññ°;çDCBC-150ñØçËñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲËËÄÚÄØ;ËÄ•Ä°²öËò;Ë,ãñí°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ë²¼ñðò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲËËÄÚÄØñäÄ•Ä°²öËòñ¬¼áñ¼í¹ÇñÏ´°í»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÄ,
0059 C. *****
0060 C. °° RESTART;ËPT2;Ëñ•ñç¼ñ¼í¹ÇñÏ;ç°Ë²¼ñí¼Ä¹Öñññ°;çDCBC-151ñØçËñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲËËÄÚÄØ;ËÄ•Ä°²öËò;Ë,ãñí°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 4833246.0 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 57s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-07-01 11:06:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîÎ»ö¼Ý¤ÊÄ¤¹¤ëDCBC•×²è *****
0165 C. (%â°ï¥Ö¥Ä¥Ë¥Ð¥Ë¥Á¥¢¥§¤È½¾¼¤Å»Ü¤¹¤é)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±¿İŃ¤Ë'Ø¤¹¤ëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 C. ;ãLOS¥Ä¥$¥Ä¥~¼Å»Ü;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

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0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŸçŸÃŸ×Ÿí;¼ŸÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÐñÇñÎ»p´Öòò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç [HK1_XPA_ON/OFF] EQ ON
0025 C. çç [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç [HK1_XMOD_ON/OFF] EQ ON
0027 C. çç [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÄÖñ¬°ÄÄêñ•ñç;æ;ç°Ê²¼ñî°EÄ,¼ê½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñç¼ñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŸçŸóŸEŸEËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŸçŸóŸEŸEËÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñ¼¹¹Çñİ´°î»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñç¼ñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŸçŸóŸEŸEËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç [HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç [HK1_REP_STA/STP] EQ STOP
0087 C. çç [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç [HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç [HK1 XPA ON/OFF] EO OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 08 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 c0 c0 10 10)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 40 c0 10 10)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 40 40 10 10)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c c0 40 10 10)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0d 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0e 80 60 20 18)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_AEC_RESET
0155 BC (d0)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 0a)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 0e)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2025-07-01 11:06:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 C.
0176 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÃÐñ¹ñëDCBC•×²è *****
0177 C. (%â°î¥Ó¥Ã¥Ê¥Ð¥Ë¥Ã¥¥¥¥ñÊ¼ñ¼Ã»Ûñ¹ñë)
0178 . S. DC-BC dcbc-402:DCBC
0179 (MDP_known_event)
0180 C.
0181 C.
0182 . C. ***** ¥Ð¥¹•ï Daily±¿íññÊ'Øñ¹ñëDCBC•×²è *****
0183 . S. DC-BC dcbc-153:DCBC
0184 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0185 C.
0186 C.
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0188 C.
0189 . C. ***** LOS *****
0190 C.
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2025/07/02	13:02:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/02	13:02:56.0	XRT_FLD_DIS_432_OG [0x1b0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/02	13:02:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/02	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/07/02	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/02	13:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:09:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:10:00.0	AOCS_ORe-point_Start_20_OG [0x0aa]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/07/02	13:12:52.0	XRT_ARS_DIS_427_OG [0x1ab]	AOCU_NM	5	02-76	00 d1 07 2e f9
2025/07/02	13:12:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/02	13:12:56.0	XRT_FLD_DIS_432_OG [0x1b0]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/02	13:12:58.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/02	13:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2025/07/02	13:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/02	13:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:20:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/07/02	13:20:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2025/07/02	13:20:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/02	13:20:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/02	13:22:58.0	XRT_QT_PROG_SET_404_OG [0x194]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/02	13:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2025/07/02	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/02	13:29:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/07/02	13:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 06 97 01 81
2025/07/02	13:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/07/02	13:30:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/07/02	13:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/07/02	13:30:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/02	13:32:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/02	13:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2025/07/02	13:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/07/02	13:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/02	13:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	13:38:06.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/02	13:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/02	14:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/02	14:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/07/02	15:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/02	15:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	15:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/02	15:16:06.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/02	15:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/02	15:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/02	15:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/07/03	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/07/03	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/07/03	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/07/03	04:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2025/07/03	04:02:58.0	XRT_FL_PROG_SET_422_OG [0x1a6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	12
2025/07/03	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/07/03	04:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	04:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	04:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/07/03	04:13:06.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/07/03	04:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/07/03	04:52:00.0	XRT_Custom_430_OG [0x1ae]					
2025/07/03	04:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/07/03	05:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	05:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	05:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/07/03	05:42:36.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/07/03	05:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/07/03	06:30:00.0	XRT_Custom_430_OG [0x1ae]					
2025/07/03	06:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/07/03	06:35:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	06:35:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	06:35:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/07/03	06:36:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/07/03	06:36:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/07/03	06:36:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/07/03	06:38:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2025/07/03	06:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/07/03	06:45:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	06:45:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/07/03	06:46:00.0	AOCs_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	06 97 01 81
2025/07/03	06:46:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/07/03	06:46:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/07/03	06:46:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/07/03	06:46:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/07/03	06:46:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/07/03	06:48:56.0	XRT_QT_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2025/07/03	06:48:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/07/03	06:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/07/03	07:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	07:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	07:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/07/03	07:22:36.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/07/03	07:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/07/03	08:08:30.0	XRT_Custom_430_OG [0x1ae]					
2025/07/03	08:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/07/03	09:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/07/03	09:02:32.0	XRT_CTRL_MANU_402_OG [0x192]					

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2025/07/03	09:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/03	09:02:36.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/03	09:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/03	09:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/03	09:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/03	10:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/03	10:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/03	10:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/03	10:42:36.0	XRT_PREFLR_STRT_410_OG [0x19a]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/03	10:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/03	11:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/03	11:17:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00