

Period: 2025/07/29 12:01:00 - 2025/08/02 10:55:00

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5128/723), Al-poly(8181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795)														
Term		Pointing (x, y)				Comment								
07/29 18:03:00 - 07/29 18:09:54		Fixed (0.0, 0.0)				synoptic								
07/30 06:24:30 - 07/30 07:11:30		Fixed (0.0, 0.0)				synoptic, shifted 21.5 min								
07/30 17:58:00 - 07/30 18:04:54		Fixed (0.0, 0.0)				synoptic, shifted -5.0 min								
07/31 05:41:00 - 07/31 05:47:54		Fixed (0.0, 0.0)				synoptic, shifted -22.0 min								
PROG= 10 1-time(s)														
└ Subr= 1 1-time(s) 2.0sec														
└ Seqn= 5 1-time(s) 2.0sec														
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└ Seqn= 26 1-time(s) 2.0sec														
└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└ Seqn= 99 1-time(s) 2.0sec														

	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec													
	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
07/29 18:13:00 - 07/30 01:59:54	Track (761.3, 162.1) @ 07/29 18:10:00	HOP507/509 AR
07/30 02:03:00 - 07/30 03:44:54	Track (113.7, -301.7) @ 07/30 02:00:00	HOP173 AR14155
07/30 04:48:00 - 07/30 06:21:24	Track (196.8, -470.9) @ 07/30 04:45:00	HOP173 AR 14157
07/31 02:03:00 - 07/31 03:59:54	Track (323.4, -297.3) @ 07/31 02:00:00	HOP173 AR14155
07/31 04:13:00 - 07/31 05:37:54	Track (401.4, -400.0) @ 07/31 04:10:00	HOP173 AR14157

PROG= 16 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 92 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec													
Seqn= 47 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 180.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CC7: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(12/181/1443) + Thin

Term	Pointing (x, y)	Comment
07/30 03:48:00 - 07/30 04:44:54	Fixed (0.0, 0.0)	Synoptic for HOP173
07/31 04:03:00 - 07/31 04:09:54	Fixed (0.0, 0.0)	Synoptic for HOP173

PROG= 18 1-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 55 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

└	Seqn= 23		1-time(s)		2.0sec										
	Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
	Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

XOB #1BD7: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak last)

Term				Pointing (x, y)				Comment											
07/30 18:08:00 - 07/31 01:59:54				Track (844.9, 180.0) @ 07/30 18:05:00				HOP509/507 AR											
PROG= 08 Inf.-time(s)																			
┌	Subr= 1		8-time(s)		900.0sec														
	┌	Seqn= 8		1-time(s)		2.0sec													
		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec				
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec					
	┌	Seqn= 6		1-time(s)		2.0sec													
		Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec				
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec					
┌	Subr= 2		1-time(s)		2.0sec														
	┌	Seqn= 30		1-time(s)		2.0sec													
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0	0	2.0sec				
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0	0	2.0sec						
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval						

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G-band

Term		Pointing (x, y)		Comment												
07/29 16:14:00 - 07/29 17:59:54		Track (752.4, 160.7) @ 07/29 16:11:00		AR14149												
07/29 18:13:00 - 07/30 01:59:54		Track (761.3, 162.1) @ 07/29 18:10:00		HOP507/509 AR												
07/30 02:03:00 - 07/30 03:44:54		Track (113.7, -301.7) @ 07/30 02:00:00		HOP173 AR14155												
07/30 04:48:00 - 07/30 06:21:24		Track (196.8, -470.9) @ 07/30 04:45:00		HOP173 AR 14157												
07/30 13:02:00 - 07/30 17:17:00		Track (830.7, 176.0) @ 07/30 12:59:00		AR14149												
07/30 18:08:00 - 07/31 01:59:54		Track (844.9, 180.0) @ 07/30 18:05:00		HOP509/507 AR												
07/31 02:03:00 - 07/31 03:59:54		Track (323.4, -297.3) @ 07/31 02:00:00		HOP173 AR14155												
07/31 04:13:00 - 07/31 05:37:54		Track (401.4, -400.0) @ 07/31 04:10:00		HOP173 AR14157												
07/31 05:51:00 - 07/31 09:24:00		Track (355.2, -296.2) @ 07/31 05:48:00		AR14155												
PROG= 14 30-time(s)																
┌	Subr= 1		1-time(s)		2.0sec											
	┌	Seqn= 68		1-time(s)		2.0sec										
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=95	0	0	2.0sec	
	┌	Subr= 2		20-time(s)		2.0sec										
		┌	Seqn= 11		1-time(s)		2.0sec									
	Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec	
	┌	Seqn= 73		1-time(s)		10.0sec										
		thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec	
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec	
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec	
	┌	Subr= 3		1-time(s)		2.0sec										
		┌	Seqn= 10		1-time(s)		2.0sec									
			med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
			Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
		┌	Seqn= 11		1-time(s)		2.0sec									
Al-poly/Open	Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec		
┌	Seqn= 87		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec		
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

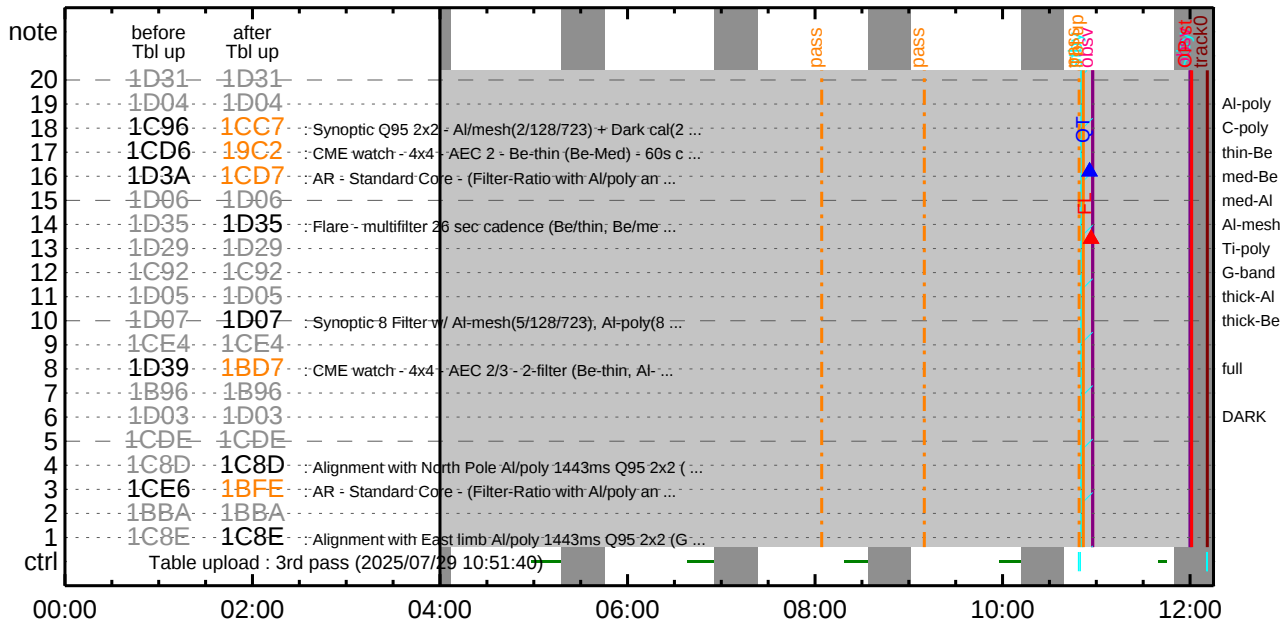
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Flare Detection

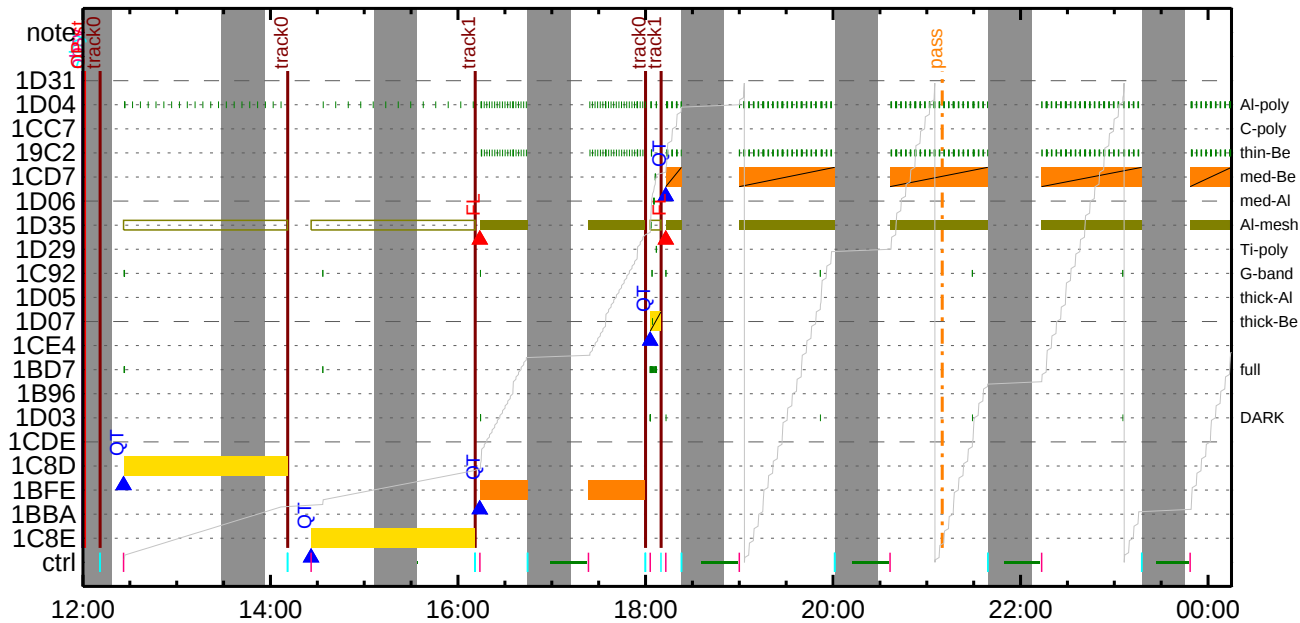
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FLD Patrol													
Term		Pointing (x, y)						Comment					
07/29 10:52:40 - 07/29 12:11:18		cannot be identified											
07/29 16:11:18 - 07/29 18:00:18		Track (	752.4,	160.7)	@ 07/29 16:11:00	AR14149							
07/29 18:10:16 - 07/30 03:45:18		Track (	761.3,	162.1)	@ 07/29 18:10:00	HOP507/509 AR							
07/30 04:45:18 - 07/30 06:21:48		Track (	196.8,	-470.9)	@ 07/30 04:45:00	HOP173 AR 14157							
07/30 12:59:18 - 07/30 17:55:18		Track (	830.7,	176.0)	@ 07/30 12:59:00	AR14149							
07/30 18:05:16 - 07/31 04:00:18		Track (	844.9,	180.0)	@ 07/30 18:05:00	HOP509/507 AR							
07/31 04:10:18 - 07/31 05:38:18		Track (	401.4,	-400.0)	@ 07/31 04:10:00	HOP173 AR14157							
07/31 05:48:18 - 08/02 10:55:00		Track (	355.2,	-296.2)	@ 07/31 05:48:00	AR14155							
Al-poly/Open		Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8			Q=50	30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

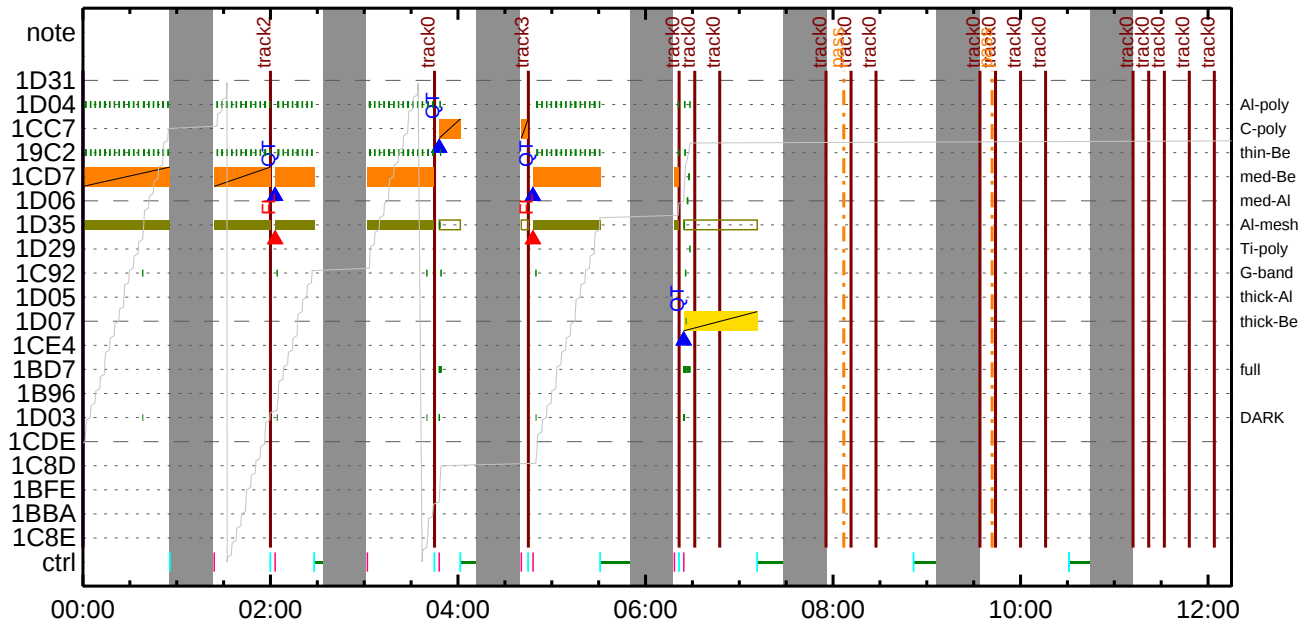
CMDI #0735 2025/07/29



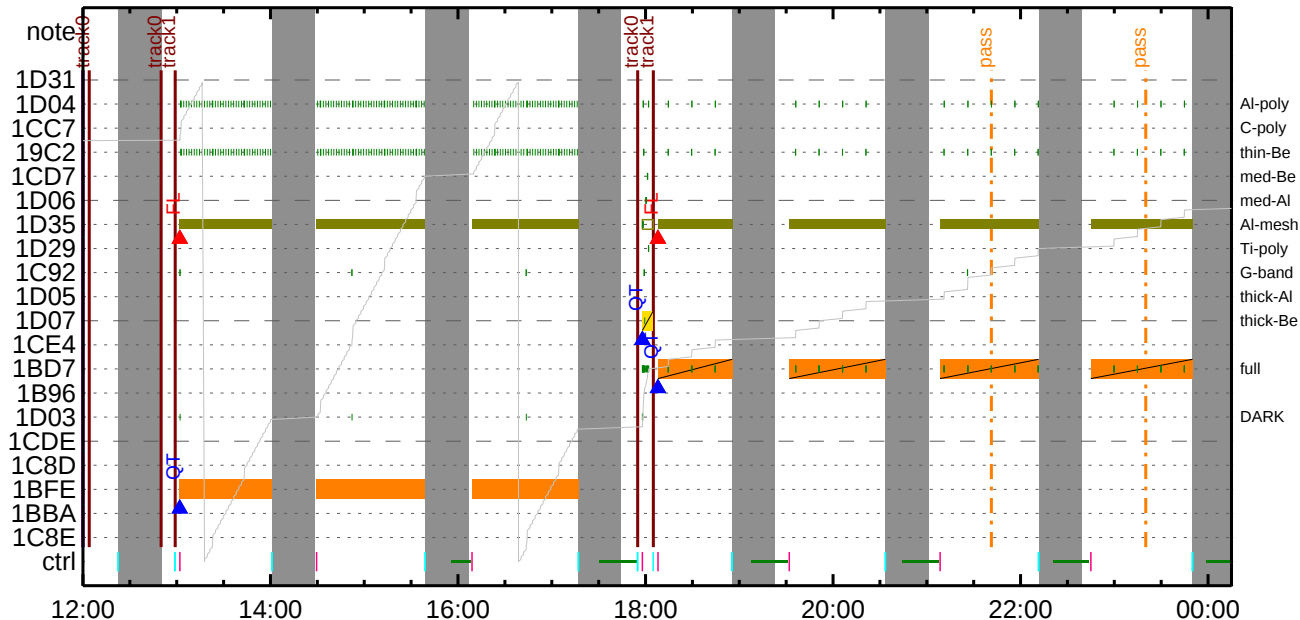
CMDI #0735 2025/07/29



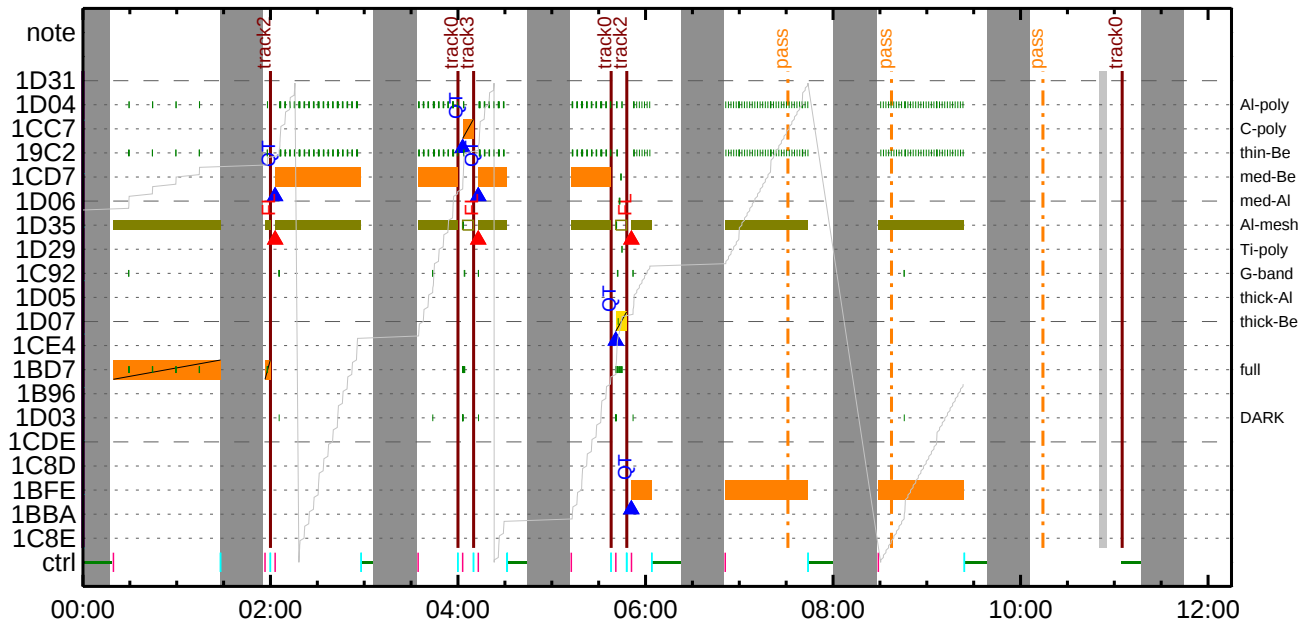
CMDI #0735 2025/07/30



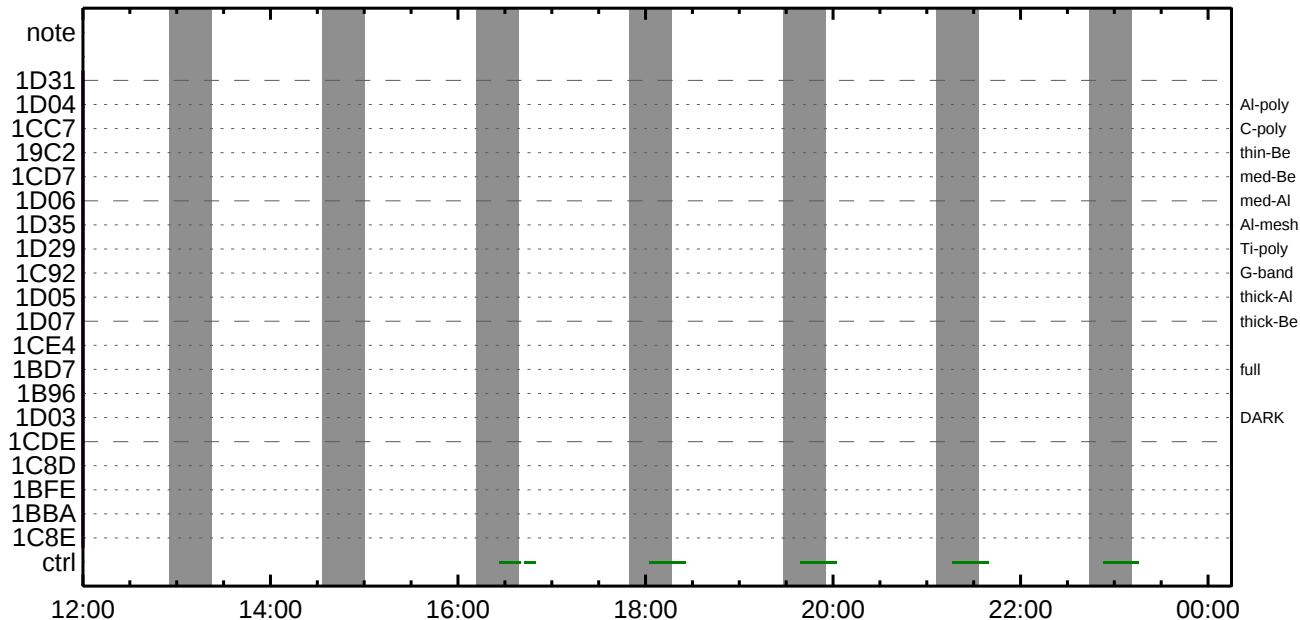
CMDI #0735 2025/07/30



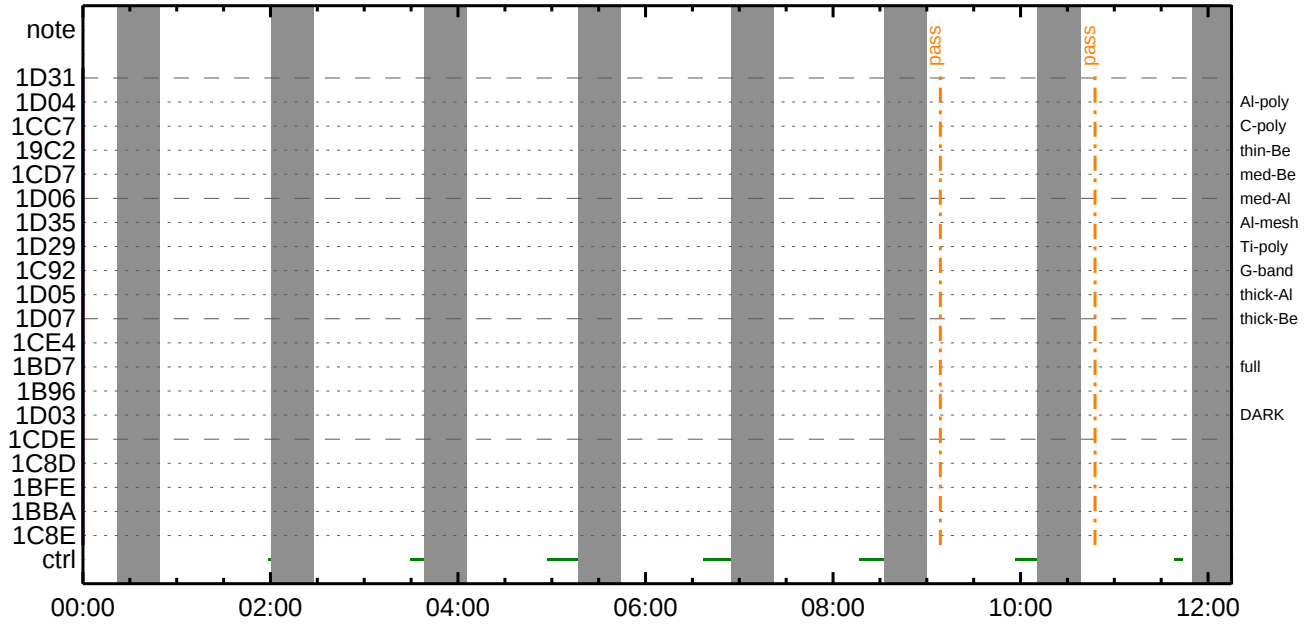
CMDI #0735 2025/07/31



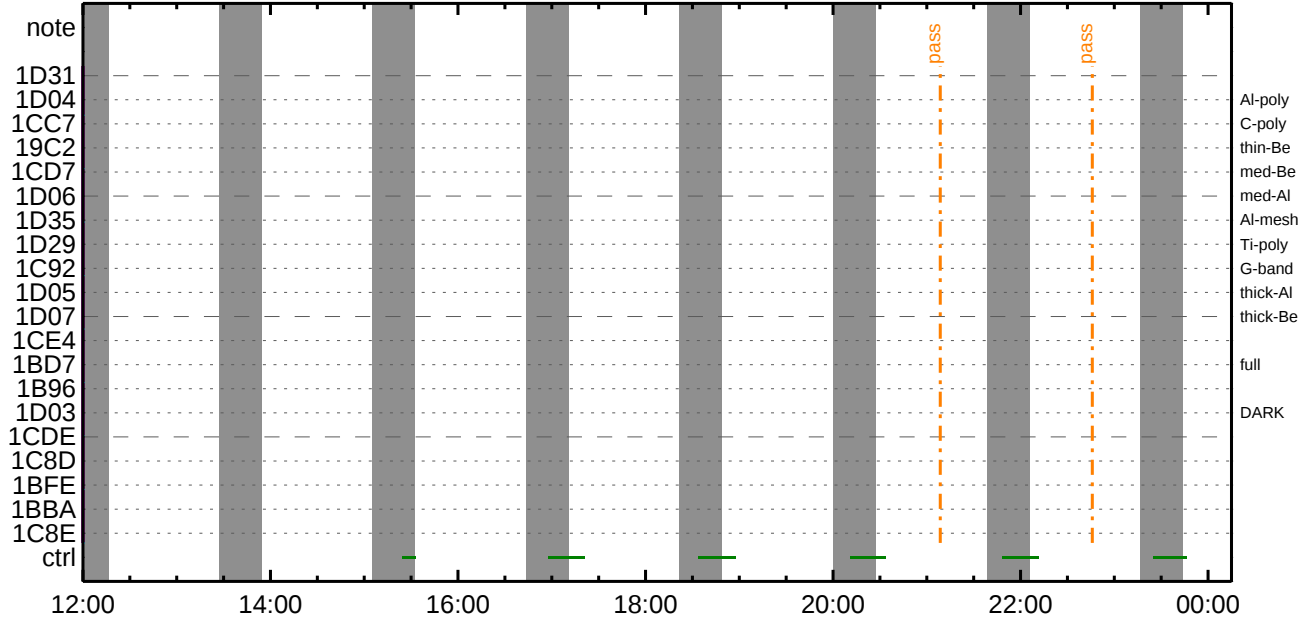
CMDI #0735 2025/07/31



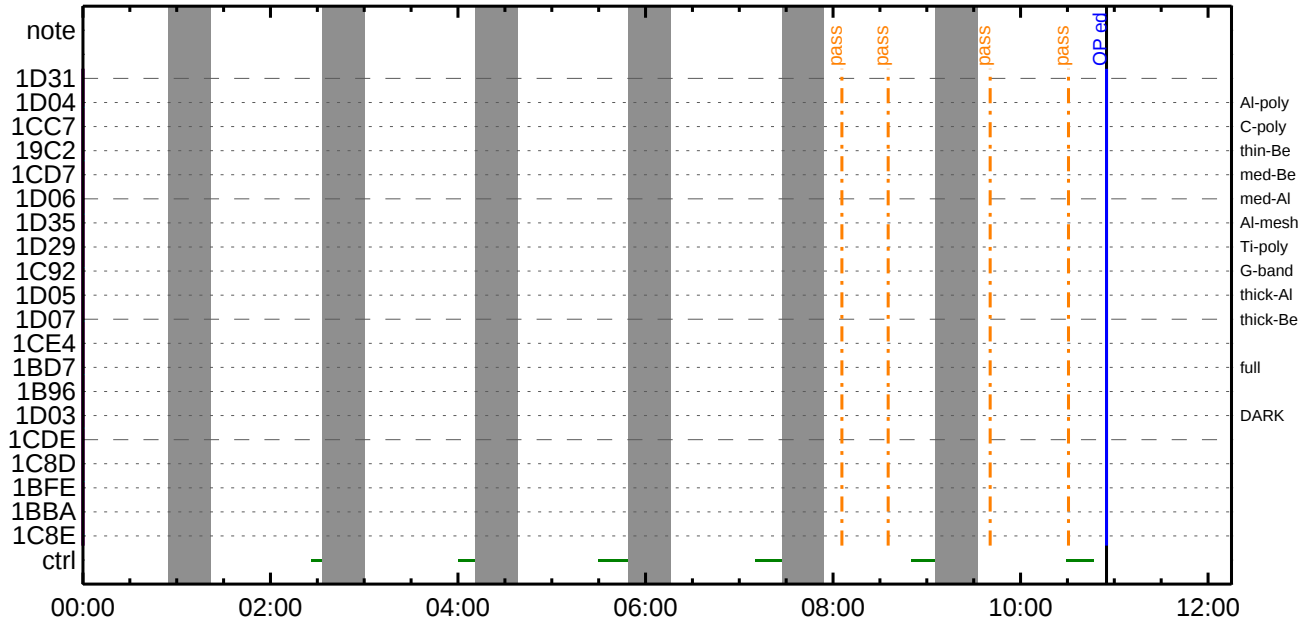
CMDI #0735 2025/08/01



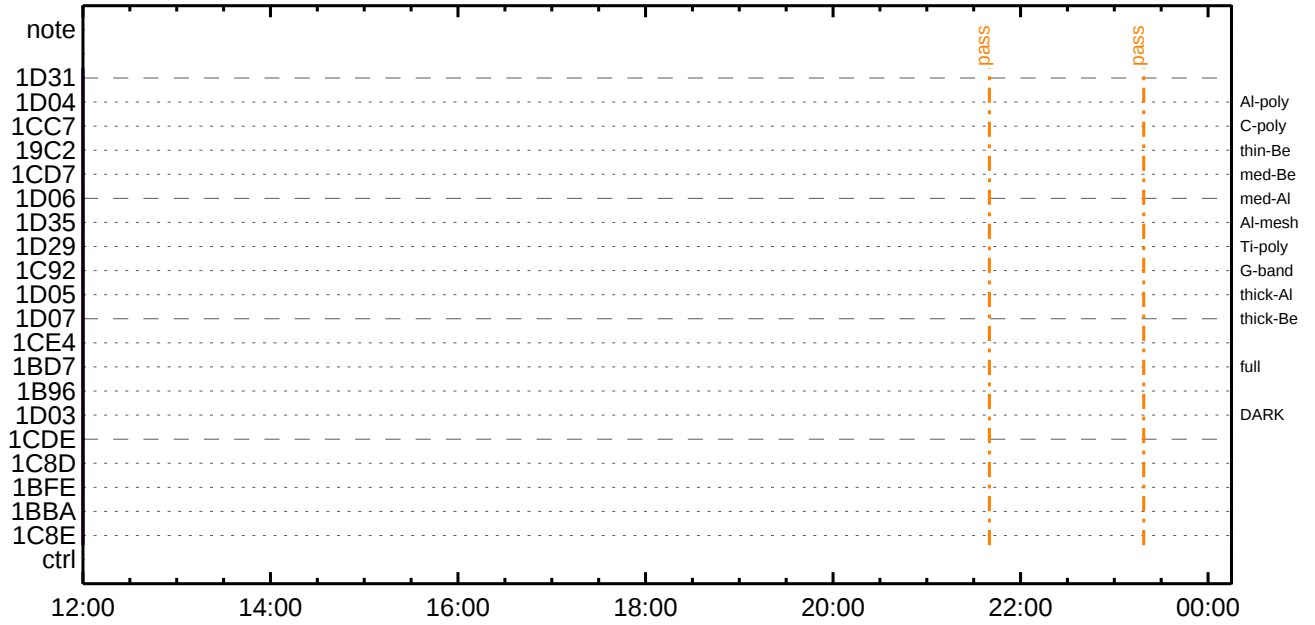
CMDI #0735 2025/08/01



CMDI #0735 2025/08/02



CMDI #0735 2025/08/02



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main-417 2025-07-29 14:46:07 205 33 SOLAR-B MAIN //
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main-417 2025-07-29 14:46:07 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸPŸóŸÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçñÄñ•µ°E»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèñµ•ííÉ;ËñÉ¼°ÇÖñ•ñç¼í¹çñí;çÄ®, ùñ¹ñèñPñÇÁ÷ç®ñ•ñËñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÄŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-417:OP
0020 ()
0021 . S. OG og-417:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPîî°èŸÄŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñò³îÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñò³îÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñî¼Ë¹ç•ë²îOKñò³îÇ$
0081 C.
0082 . C. ***** °Ë²¼ñî¼Ä´¶Ä°ñËË¬ñ°Á÷ç® (¼âµ-ŸÄŸóŸ×¼è½çñòÄÓÄæñÇ½ñ•ñè¼í¹çñÇñâ) *****
0083 C. DHUŸä;¼ŸÉ;Ë¼ý½,Ÿí;¼ŸÉ;Ëñòíäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñî¼í¹ç;ç°Ë²¼ñîTI-CMDÁ÷ç®ñî¼Ä¹Ôñ•ñËñññ³ñÈ;£

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0096	C.	*****		
0097	C.	*****		
0098	C.	TI 2025-07-29 11:56:00.0		
0099	+	TI	2025-07-29 11:56:00.0	
0100	DC	01-B3	DHU_OP_STOP	
0101	C.	HK1_TI_CMD_NUM	EQ	1COUNTUP
0102	C.	*****		
0103	+	TI	2025-07-29 11:56:01.0	
0104	DC	01-B4	DHU_OP_COPY	
0105	C.	HK1_TI_CMD_NUM	EQ	1COUNTUP
0106	C.	*****		
0107	+	TI	2025-07-29 11:56:01.0	
0108	DC	01-B5	DHU_OPOG_COPY	
0109	C.	HK1_TI_CMD_NUM	EQ	1COUNTUP
0110	C.	*****		
0111	+	TI	2025-07-29 12:00:59.5	
0112	DC	01-B2	DHU_OP_START	
0113	C.	HK1_TI_CMD_NUM	EQ	1COUNTUP
0114	C.	*****		
0115	C.	*****		
0116	C.	HK1_TI_CMD_ENA/DIS	EQ	ENA
0117	C.	HK1_TI_CMD_NUM	EQ	4
0118	C.	HK1_NEXT_EXEC_PIM	EQ	DHU
0119	C.	HK1_NEXT_EXEC_DC	EQ	0xB3
0120	C.	*****		
0121	C.	*****		
0122	C.	*****		
0123	C.	*****		
0124	C.	*****		
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+	DC	01-23	DHU_DMA_DMP_PRM_SET
0127	BC	(03 ab 03 01 02)		
0128	C.	HK1_DMP_TOP_ADRS_1	EQ	07
0129	C.	HK1_DMP_TOP_ADRS_0	EQ	2B
0130	C.	HK1_DMP_BLOCK_NUM	EQ	3
0131	C.	HK1_DMP_REPEAT_NUM	EQ	0
0132	C.	HK1_DMA_DMP_PIM	EQ	DHU
0133	+	DC	01-22	DHU_MODE_CHNG
0134	BC	(07 0b f8)		
0135	C.	HK1_PKT_FORM_NO	EQ	7
0136	C.	HK1_PKT_GEN_TIME	EQ	0.25 s
0137	C.	HK1_S_TLM_BIT_RATE	EQ	32k
0138	C.	HK1_X_TLM_BIT_RATE	EQ	4M
0139	C.	HK1_DMP_CHK_FLG	EQ	EXEC
0140	C.	*****		
0141	C.	*****		
0142	C.	HK1_DMP_CHK_FLG	EQ	NON
0143	C.	*****		
0144	C.	RAM ID=TI_TBL 0x03AEFF; 0x03AB00		
0145	C.	*****		
0146	C.	*****		
0147	+	DC	01-22	DHU_MODE_CHNG
0148	BC	(02 0a f8)		
0149	C.	HK1_PKT_FORM_NO	EQ	2
0150	C.	HK1_PKT_GEN_TIME	EQ	0.5S
0151	C.	HK1_S_TLM_BIT_RATE	EQ	32K
0152	C.	HK1_X_TLM_BIT_RATE	EQ	4M
0153	C.	*****		
0154	C.	*****		
0155	C.	SOT TI command set		
0156	C.	*****		
0157	C.	Execute, after the success of OP upload.		
0158	+	TI	2025-07-29 12:00:16.0	
0159	DC	07-F0	MDP_SOT_MODE_STBY	
0160	BC	(41)		
0161	C.	*****		
0162	C.	HK1_TI_CMD_NUM	= 1 CNTUP []	
0163	C.	*****		
0164	C.	***** SOT END *****		
0165	C.	Stop EIS observation and temporarily disable EIS mode changes		
0166	C.	*****		
0167	C.	*****		
0168	C.	***** Start EIS operation (TI set) *****		
0169	C.	Execute, after the success of OP upload.		
0170	C.	Set EIS TI-commands		
0171	+	TI	2025-07-29 12:00:30.0	
0172	DC	07-FC	EIS_MODE_MANU	
0173	BC	(21 02)		
0174	+	TI	2025-07-29 12:00:40.0	
0175	DC	07-FC	EIS_MODE_CHG_DIS	
0176	BC	(22)		
0177	C.	[]	HK1_TI_CMD_NUM	EQ 2 COUNTUP
0178	C.	***** End EIS operation (TI set) *****		
0179	C.	*****		
0180	C.	*****		
0181	C.	*****		
0182	C.	***** XRT START *****		
0183	C.	Execute, after the success of OP upload.		
0184	+	TI	2025-07-29 12:00:00.0	
0185	DC	07-F0	MDP_XRT_MODE_STBY	
0186	BC	(c3)		
0187	C.	[]	HK1_TI_CMD_NUM	EQ 1COUNTUP
0188	C.	*****		
0189	C.	***** XRT END *****		
0190	C.	*****		
0191	C.	***** MDP 0x03AEFF; 0x03AB00 *****		
0192	C.	*****		
0193	S.	DC-BC dcdbc-402:DCBC		

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0194      (MDP_known_event)
0195      C.
0196      C.
0197      . C. ***** 𐄂𐄂𐄂!•İ Daily±ĭİÑ𐄂Ē'Ø𐄂¹𐄂ĕDCBC•x²è *****
0198      . S. DC-BC dcbc-153:DCBC
0199      (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200      C.
0201      C.
0202      . C. ĭāLOS𐄂Á𐄂$𐄂Ā𐄂⁻¼Ā»Üĭä
0203      C.
0204      . C. ***** LOS *****
0205      C.
```

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main-418 2025-07-29 14:07:07 210 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲSŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EA, Í×ÈÝñäLOSñÞñÇñİ»Þ'Öòò¹íî,ñ•;ÇÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲ~¾ÒÄÖñ¬°ÄÄèñ•ñççé;Ç°Ê²¼ñî°EA,¼ê½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. °° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲÈÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EA,ñ¬¼«E°Ää»ßñ•ñçç,ä;Ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲÇŲóŲÉŲÈÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. °° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0071 C.
0072 . C. ;ãŲÇŲóŲÉŲÈÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1 XPA ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 7252463.5 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. *****
0139 C. SOT table upload
0140 C. *****
0141 . C. < Stop SP table >
0142 +. DC 07-F0 MDP_SP_CTRL_MANU
0143 BC (61)
0144 C. -----
0145 C. MDP_SP_CTRL_MODE = MANU [ ]
0146 C. -----
0147 C.
0148 . C. <Upload SP Observation Table>
0149 . S. RAM ram-281:MDP_OBS_S
0150 ()
0151 C.
0152 . C. < Dump RAMID=MDP_OBS_S >
0153 +. DC 07-F0 MDP_DUMP_SPTBL
0154 BC (83 07 00 00 00 38 b8)
0155 C. -----
0156 C. MDP_OBS_S verify = OK/NG [ ]
0157 C. -----
0158 C.
0159 C. *****
0160 C. SOT TI command set
0161 C. *****
0162 C. Execute, after the success of TBL upload.
0163 +. TI 2025-07-29 12:00:18.0
0164 DC 07-F0 MDP_SOT_MODE_OBSV
0165 BC (40)
0166 . C. -----
0167 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0168 C. -----
0169 C.
0170 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0171 +. DC 07-FC EIS_MODE_CHG_ENA
0172 BC (20)
0173 . C. Verify EIS_MODE_CHG_FLG is ENA
0174 +. DC 07-FC EIS_MODE_MANU
0175 BC (21 02)
0176 . C. Verify EIS in MANUAL mode
0177 . C. Estimated OBSTBL upload time is 0ms
0178 C. *****
0179 C. EIS START OBSTBL LOAD
0180 C. *****
0181 . S. RAM ram-822:EIS_OBSTBL
0182 ()
0183 +. DC 07-FC EIS_DUMP_OBSTBL
0184 BC (07 07 07 00 00 70 00)
0185 C.
0186 C. Execute, after the success of OBSTBL upload.
0187 C. Set EIS TI-commands
0188 +. TI 2025-07-29 12:00:50.0
0189 DC 07-FC EIS_MODE_CHG_ENA
0190 BC (20)
0191 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0192 C. *****
0193 C. EIS END OBSTBL LOAD

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0194 C. *****
0195 C.
0196 . C. ***** MDP 'úÃîñî»ô¼ýñëâðñ¹ñëDCBC•×²è *****
0197 C. (¾â°î¥ó¥Ã¥ë¥ð¥ë¥â¥ç¥ëñë½¼ññ¼â»Ûñ¹ñë)
0198 . S. DC-BC dcbc-402:DCBC
0199 (MDP_known_event)
0200 C.
0201 C.
0202 . C. ***** ¥ð¥¹•ï Daily±¿íññë'øñ¹ñëDCBC•×²è *****
0203 . S. DC-BC dcbc-153:DCBC
0204 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0205 C.
0206 C.
0207 . C. ;ãLOS¥Á¥\$¥Ã¥~¼â»Û;ã
0208 C.
0209 . C. ***** LOS *****
0210 C.

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main-419 2025-07-29 14:46:07 188 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÄñ•µ°E»Í×ÄÇñÍŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•ííË;ËñÉ¼°ÇÖñ•ñç¼í¹Çñİ;çÄ®,ùñ¹ñèñðñÇÄ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ËÝñäLOSñðñÇñİ»p´Öðòíí,ñ•;çÉÖÍ×ñËXÄÓONñİ¹ÖñËñíñËñññ³ñË;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÖÄÖñ¬°ÄÄèñ•ñç,ç°Ë²¼ñİ°EÄ,¼è½Çñðò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äí¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ËPT1;Ëñ•ñç¼ñ¼í¹Çñİ;ç°Ë²¼ñİ¼Ä¹Öñññ°;çDCBC-150ñØçËñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲËËÄÚÄØ;ËÄ•Ä°²öÈò;Ë,ãñí°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ë²¼ñðò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲËËÄÚÄØñäÄ•Ä°²öÈòñ¬¼áññ¼í¹Çñİ´°í»ñ¹ñèñðñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äí¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ËPT2;Ëñ•ñç¼ñ¼í¹Çñİ;ç°Ë²¼ñİ¼Ä¹Öñññ°;çDCBC-151ñØçËñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲËËÄÚÄØ;ËÄ•Ä°²öÈò;Ë,ãñí°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 +. DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ( )
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 +. DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 +. DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 +. DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 +. DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 +. DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 80 80 20 20)
0130 +. DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 60 20 18)
0132 +. DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 a0 80 18 20)
0134 +. DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 85 83 06 06)
0136 +. DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 20 08)
0138 +. DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 80 80 08 20)
0140 +. DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0c 80 80 08 08)
0142 +. DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0f 80 80 06 06)
0144 +. DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 10 80 80 08 08)
0146 +. DC 07-F0 MDP_XRT_FLD_ENA
0147 BC (d8)
0148 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0149 BC (c8)
0150 +. DC 07-F0 MDP_XRT_ARS_DIS
0151 BC (d5)
0152 +. DC 07-F0 MDP_XRT_AEC_RESET
0153 BC (d0)
0154 +. DC 07-F0 MDP_XRT_FLD_RESET
0155 BC (da)
0156 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0157 BC (c4 11)
0158 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0159 BC (c5 0e)
0160 . C. ----- Success Verify ? OK / NG ____
0161 C.
0162 C.
0163 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0164 C.
0165 +. DC 07-F0 MDP_XRT_MODE_OBSV
0166 BC (c2)
0167 +. TI 2025-07-29 12:00:02.0
0168 DC 07-F0 MDP_XRT_MODE_OBSV
0169 BC (c2)
0170 . C. ----- Success Verify ? OK / NG ____
0171 C.
0172 C. ***** XRT END *****
0173 C.
0174 . C. ***** MDP 'ûÃîî»ö¼Ý¤ÊÃ¤¹¤ëDCBC•x²è *****
0175 C. (¼â°îÝÔÝÃÝÊÝ¤ÝÊÝ¤âÝ¢Ýë¤Ê½¼¤¤¼Ã»Ü¤¹¤ë)
0176 . S. DC-BC dcbc-402:DCBC
0177 (MDP_known_event)
0178 C.
0179 C.
0180 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ¤Ê'Ø¤¹¤ëDCBC•x²è *****
0181 . S. DC-BC dcbc-153:DCBC
0182 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0183 C.
0184 C.
0185 . C. ;ãLOS¥Ã¥$¥Ã¥¬¼Ã»Ü;ã
0186 C.
0187 . C. ***** LOS *****
0188 C.

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*** OP Sequence for XRT ***

2025/07/29	12:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	12:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	12:10:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/07/29	12:11:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 ad 59 00 00
2025/07/29	12:11:18.0	XRT_FLD_DIS_421_OG [0x1a5]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/29	12:25:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/29	12:25:56.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/29	12:25:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2025/07/29	12:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	14:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	14:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	14:10:58.0	XRT_FOCUS_POSITION_417_OG [0x1a1]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/07/29	14:11:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 56 35
2025/07/29	14:11:18.0	XRT_FLD_DIS_421_OG [0x1a5]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/29	14:25:54.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/29	14:25:56.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/29	14:25:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/07/29	14:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	16:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	16:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	16:10:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/07/29	16:11:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 05 ca 01 b2
2025/07/29	16:11:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/07/29	16:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/07/29	16:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/07/29	16:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/29	16:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	16:13:56.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/07/29	16:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/07/29	16:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	16:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	16:44:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	16:44:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	16:44:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/29	16:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/29	17:22:30.0	XRT_Custom_430_OG [0x1ae]			
2025/07/29	17:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/07/29	18:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/07/29	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/29	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/29	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/29	18:02:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/07/29	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/07/29	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	18:09:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/07/29	18:10:00.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 05 ca 01 b2
2025/07/29	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/07/29	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/07/29	18:10:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/07/29	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/29	18:10:24.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	18:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2025/07/29	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/07/29	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	18:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	18:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	18:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	18:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/29	18:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/29	18:59:00.0	XRT_Custom_430_OG [0x1ae]				
2025/07/29	19:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	20:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	20:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	20:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	20:01:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/29	20:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/29	20:35:30.0	XRT_Custom_430_OG [0x1ae]				
2025/07/29	20:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	21:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	21:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	21:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	21:39:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/29	21:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/29	22:12:30.0	XRT_Custom_430_OG [0x1ae]				
2025/07/29	22:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/29	23:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	23:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/29	23:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/29	23:17:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/29	23:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/29	23:47:30.0	XRT_Custom_430_OG [0x1ae]				
2025/07/29	23:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	00:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	00:55:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	00:55:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	00:55:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	00:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	01:23:00.0	XRT_Custom_430_OG [0x1ae]				
2025/07/30	01:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	01:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00

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2025/07/30	02:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02	05	ca	01	b2
2025/07/30	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/07/30	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/07/30	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/07/30	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/30	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	02:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2025/07/30	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/07/30	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	02:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	02:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	02:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	02:28:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/07/30	02:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/30	03:01:00.0	XRT_Custom_430_OG [0x1ae]							
2025/07/30	03:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	03:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	03:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	03:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/07/30	03:45:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2025/07/30	03:45:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/07/30	03:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/07/30	03:45:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/30	03:47:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2025/07/30	03:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	04:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	04:01:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	04:01:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	04:01:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/07/30	04:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/30	04:39:30.0	XRT_Custom_430_OG [0x1ae]							
2025/07/30	04:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	04:44:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	04:44:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	04:44:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/07/30	04:45:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03	05	ca	01	b2
2025/07/30	04:45:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/07/30	04:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/07/30	04:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/07/30	04:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/30	04:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	04:47:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2025/07/30	04:47:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/07/30	04:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	05:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	05:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	05:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	05:31:06.0	XRT_PREFLR_STRT_404_OG [0x194]							

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2025/07/30	05:34:14.0	MDP_XRT_PREFLR_STRT	1	07-F0	e8
		XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	06:17:30.0	XRT_Custom_430_OG [0x1ae]			
2025/07/30	06:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	06:21:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	06:21:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	06:21:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/07/30	06:21:30.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/07/30	06:21:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/07/30	06:21:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/07/30	06:21:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/30	06:24:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/07/30	06:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	06:31:30.0	AOCS_Ore-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76	00 00 00 00 ac cd
2025/07/30	06:47:30.0	AOCS_Ore-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76	00 00 00 00 d6 67
2025/07/30	07:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	07:11:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	07:11:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	07:11:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	07:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	07:55:30.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00 00
2025/07/30	08:11:30.0	AOCS_Ore-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 00 00 00 29 99
2025/07/30	08:27:30.0	AOCS_Ore-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 00 00 00 53 33
2025/07/30	08:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	08:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	08:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	08:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	08:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	09:34:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 d6 36 b7 8e
2025/07/30	09:44:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]			
		AOCU_NM	5	02-76	00 b4 b5 db 75
2025/07/30	10:00:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]			
		AOCU_NM	5	02-76	00 ac 5b 00 00
2025/07/30	10:16:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]			
		AOCU_NM	5	02-76	00 b4 b5 24 8b
2025/07/30	10:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	10:31:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	10:31:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	10:31:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	10:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	11:12:00.0	AOCS_Ore-point_Start_15_OG [0x0a5]			
		AOCU_NM	5	02-76	00 d6 36 48 72
2025/07/30	11:22:00.0	AOCS_Ore-point_Start_16_OG [0x0a6]			
		AOCU_NM	5	02-76	00 29 ca b7 8e
2025/07/30	11:32:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]			
		AOCU_NM	5	02-76	00 4b 4b db 75
2025/07/30	11:48:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]			
		AOCU_NM	5	02-76	00 53 a5 00 00
2025/07/30	12:04:00.0	AOCS_Ore-point_Start_19_OG [0x0a9]			
		AOCU_NM	5	02-76	00 4b 4b 24 8b
2025/07/30	12:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	12:22:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	12:22:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	12:22:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	12:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	12:50:00.0	AOCS_Ore-point_Start_20_OG [0x0aa]			

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2025/07/30	12:58:54.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	00	29	db	48	72
2025/07/30	12:58:56.0	MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/07/30	12:58:58.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	12:59:00.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/07/30	12:59:18.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	05	ca	01	b2
2025/07/30	12:59:20.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/07/30	12:59:22.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/07/30	12:59:24.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/07/30	12:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/30	13:01:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	13:01:58.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2025/07/30	13:02:00.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/07/30	14:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	14:01:02.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	14:01:04.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	14:01:06.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	14:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/30	14:28:30.0	XRT_CUSTOM_430_OG [0x1ae]								
2025/07/30	14:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	15:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	15:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	15:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	15:39:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/30	15:42:14.0	XRT_CUSTOM_430_OG [0x1ae]								
2025/07/30	16:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	16:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	17:17:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	17:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	17:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/30	17:17:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/30	17:20:14.0	XRT_CUSTOM_430_OG [0x1ae]								
2025/07/30	17:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	17:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	17:54:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/07/30	17:55:00.0	AOCs_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00	00	00
2025/07/30	17:55:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/07/30	17:55:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/07/30	17:55:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/30	17:57:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2025/07/30	17:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/30	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/30	18:04:56.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/07/30	18:05:00.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	05	ca	01	b2
2025/07/30	18:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/07/30	18:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/07/30	18:05:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/07/30	18:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				

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2025/07/30	18:05:24.0	XRT_FLD_RESET_446_OG [0x1be]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	18:07:56.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/07/30	18:07:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/07/30	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	18:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	18:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	18:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	18:55:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	18:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	19:31:00.0	XRT_Custom_430_OG [0x1ae]			
2025/07/30	19:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	20:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	20:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	20:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	20:33:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	20:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	21:07:30.0	XRT_Custom_430_OG [0x1ae]			
2025/07/30	21:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	22:11:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	22:11:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	22:11:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	22:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/30	22:44:00.0	XRT_Custom_430_OG [0x1ae]			
2025/07/30	22:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/30	23:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	23:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/30	23:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/30	23:50:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/30	23:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/31	00:18:30.0	XRT_Custom_430_OG [0x1ae]			
2025/07/31	00:19:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/31	01:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/31	01:28:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/31	01:28:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/31	01:28:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/07/31	01:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/07/31	01:55:30.0	XRT_Custom_430_OG [0x1ae]			
2025/07/31	01:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/07/31	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/31	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/07/31	01:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/07/31	02:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	02 05 ca 01 b2
2025/07/31	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/07/31	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/07/31	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/07/31	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/07/31	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/07/31	02:02:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10

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2025/07/31	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/07/31	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/31	02:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	02:58:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	02:58:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/31	02:58:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/07/31	03:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/31	03:33:30.0	XRT_Custom_430_OG [0x1ae]							
2025/07/31	03:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/31	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/07/31	04:00:00.0	AOCs_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2025/07/31	04:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/07/31	04:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/07/31	04:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/31	04:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2025/07/31	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/31	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	04:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	04:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/07/31	04:10:00.0	AOCs_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	03	00	00	00	00
2025/07/31	04:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/07/31	04:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/07/31	04:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/07/31	04:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/31	04:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/31	04:12:56.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2025/07/31	04:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2025/07/31	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/31	04:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	04:31:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	04:31:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/07/31	04:31:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/07/31	04:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/07/31	05:11:30.0	XRT_Custom_430_OG [0x1ae]							
2025/07/31	05:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/31	05:37:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	05:37:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	05:37:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/07/31	05:38:00.0	AOCs_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2025/07/31	05:38:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/07/31	05:38:20.5	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/07/31	05:38:22.5	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/07/31	05:40:58.5	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2025/07/31	05:41:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/07/31	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/07/31	05:47:56.0	XRT_CTRL_MANU_402_OG [0x192]							

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2025/07/31	05:47:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	1	07-F0	c1
2025/07/31	05:48:00.0	AOCs_ORe-point_Start_5_OG [0x09b]	4	07-F8	22 fe 97 00
2025/07/31	05:48:18.0	XRT_FLD_ENA_411_OG [0x19b]	5	02-76	02 05 ca 01 b2
2025/07/31	05:48:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	d8
2025/07/31	05:48:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0
2025/07/31	05:48:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
2025/07/31	05:48:26.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da
2025/07/31	05:50:56.0	XRT_QT_PROG_SET_403_OG [0x193]	2	07-F0	c4 03
2025/07/31	05:50:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0	c5 0e
2025/07/31	05:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2025/07/31	06:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/07/31	06:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/07/31	06:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/07/31	06:04:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/07/31	06:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/07/31	06:50:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/07/31	06:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/07/31	07:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/07/31	07:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/07/31	07:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/07/31	07:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/07/31	07:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/07/31	08:28:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/07/31	08:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/07/31	09:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/07/31	09:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/07/31	09:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/07/31	09:24:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/07/31	09:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/07/31	11:05:00.0	AOCs_ORe-point_Start_4_OG [0x09a]	5	02-76	00 00 00 00 00