# XRT Timeline to be uploaded on 2025/08/05

Period: 2025/08/05 10:21:00 - 2025/08/09 10:51:00

✲ ✲ ✲ ✲ ✲ **Normal mode** ✲ ✲ ✲ ✲ ✲

**XOB #1CE6: HOP81/206 1-filter - Al/poly 6s, 120s cadence, G-band - 384x384 1ms**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/05 11:10:00 - 08/05 18:06:24 | Fixed ( -19.0, -982.0) | # OP start + 10min, HOP81 |

**PROG= 20 Inf.-time(s)**

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 16 2-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |

- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 90 1-time(s) 30.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=90 | 0 | 0 | 2.0sec |

- **Subr= 3 60-time(s) 60.0sec**
  - **Seqn= 24 1-time(s) 120.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 5.66s | Obs | 1x1 | 384x384 (1064, 1048) | Q=90 | 0 | 0 | 2.0sec |

**XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/05 18:09:30 - 08/05 18:16:24 | Fixed ( 0.0, 0.0) | synoptic, shifted 6.5 min |
| 08/06 05:38:00 - 08/06 05:44:54 | Fixed ( 0.0, 0.0) | synoptic, HOP349 |
| 08/06 18:26:30 - 08/06 18:33:24 | Fixed ( 0.0, 0.0) | synoptic, shifted 23.5 min |
| 08/07 06:09:00 - 08/07 06:15:54 | Fixed ( 0.0, 0.0) | synoptic, HOP349 |

**PROG= 10 1-time(s)**

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 5 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 8x8 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 2048x512 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 500ms | Obs | 1x1 | 512x2048 (1024, 1024) | DPCM | 0 | 0 | 2.0sec |

  - **Seqn= 26 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 5ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 125ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 707ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

  - **Seqn= 99 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 177ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

  - **Seqn= 83 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 32ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 500ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

  - **Seqn= 23 1-time(s) 4.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 41 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 32.0s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |

  - **Seqn= 17 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 250ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 8.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| med-Al/Open | med-Al/Open | close | Safe | Norm | 32.0s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

  - **Seqn= 33 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| med-Be/Open | Open/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| med-Be/Open | med-Be/Open | close | Safe | Norm | 5.66s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| med-Be/Open | med-Be/Open | close | Safe | Norm | 22.6s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

  - **Seqn= 75 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Ti-poly | Al-poly/thick-Al | close | Safe | Norm | 44ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Al-poly/Ti-poly | Al-poly/thick-Al | close | Safe | Norm | 2.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |

**XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/05 18:19:30 - 08/06 03:34:54 | Track ( 92.5, -64.2) @ 08/05 18:16:30 | AR 14168 |
| 08/06 18:36:30 - 08/07 04:05:54 | Track ( 308.0, -59.7) @ 08/06 18:33:30 | AR 14168 |

**PROG= 02 Inf.-time(s)**

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 92 1-time(s) 2.0sec**

| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |

- **Subr= 2 5-time(s) 2.0sec**
  - **Seqn= 47 1-time(s) 2.0sec**

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| **Seqn= 96 4-time(s) 120.0sec** | | | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/06 03:38:00 - 08/06 05:34:54 | Fixed ( 0.0, 0.0) | synoptic, HOP349 |
| 08/07 04:09:00 - 08/07 06:05:54 | Fixed ( 0.0, 0.0) | synoptic, HOP349 |

**PROG= 13 Inf.-time(s)**

**Subr= 1 1-time(s) 300.0sec**

**Seqn=100 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 8ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 177ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 2.83s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Seqn= 84 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 16ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 177ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 4.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Seqn= 57 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 63ms | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 1.00s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| thin-Be/Open | thin-Be/Open | close | Safe | Norm | 11.3s | Obs | 2x2 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Seqn= 81 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 512x512 (1064, 1048) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 512x512 (1064, 1048) | Q=95 | 0 | 0 | 2.0sec |

**Subr= 2 6-time(s) 1200.0sec**

**Seqn= 8 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.00s | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 4x4 | 2048x2048 (1024, 1024) | DPCM | 2 | 0 | 2.0sec |

**Seqn= 6 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 125ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 3 | 0 | 2.0sec |
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 1.00s | Obs | 4x4 | 2048x2048 (1024, 1024) | DPCM | 2 | 0 | 2.0sec |

**Seqn= 29 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 125ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 3 | 0 | 2.0sec |
| Open/Al-mesh | Open/Al-mesh | close | Safe | Norm | 250ms | Obs | 4x4 | 2048x2048 (1024, 1024) | Q=98 | 2 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1C8D: Alignment with North Pole Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 5min cad**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/06 06:00:00 - 08/06 07:44:54 | Fixed ( 0.0, 930.0) | Co-alignment North |

**PROG= 04 1-time(s)**

**Subr= 1 1-time(s) 2.0sec**

**Seqn= 23 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Subr= 2 24-time(s) 300.0sec**

**Seqn= 69 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 2x2 | 2048x1536 (1024, 768) | Q=95 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1C8E: Alignment with East limb Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 8 min cad**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/06 08:00:00 - 08/06 09:44:54 | Fixed ( -970.0, 0.0) | Co-alignment East |

**PROG= 01 1-time(s)**

**Subr= 1 1-time(s) 2.0sec**

**Seqn= 23 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=90 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 2048x2048 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |

**Subr= 2 15-time(s) 480.0sec**

**Seqn= 70 1-time(s) 2.0sec**

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | med-Be/Open | close | Safe | Norm | 1.41s | Obs | 2x2 | 1536x2048 (1280, 1024) | Q=95 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

**XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/06 10:04:00 - 08/06 17:48:00 | Track ( 231.3, -62.0) @ 08/06 09:45:00 | AR 14168 |
| 08/07 06:19:00 - 08/07 11:33:54 | Track ( 406.1, -55.6) @ 08/07 06:16:00 | AR 14168 |

| | | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 16 Inf.-time(s)** | | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 92 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1064, 1048) | DPCM | 0 | 0 | 2.0sec |
| Open/Ti-poly | Open/thick-Al | close | Safe | Dark | 16.0s | Obs | 1x1 | 384x384 (1064, 1048) | Q=98 | 0 | 0 | 2.0sec |
| **Subr= 2 5-time(s) 2.0sec** | | | | | | | | | | | | | |
| **Seqn= 47 1-time(s) 2.0sec** | | | | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 2 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 3 | 0 | 2.0sec |
| **Seqn= 96 4-time(s) 180.0sec** | | | | | | | | | | | | | |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 0 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 1 | 2.0sec |
| Al-poly/Open | thin-Be/Open | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1064, 1048) | Q=95 | 1 | 2 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

## ✲ ✲ ✲ ✲ ✲ Flare mode ✲ ✲ ✲ ✲ ✲

**XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/05 11:10:00 - 08/05 18:06:24 | Fixed ( -19.0, -982.0) | # OP start + 10min, HOP81 |
| 08/05 18:19:30 - 08/06 03:34:54 | Track ( 92.5, -64.2) @ 08/05 18:16:30 | AR 14168 |
| 08/06 03:38:00 - 08/06 05:34:54 | Fixed ( 0.0, 0.0) | synoptic, HOP349 |
| 08/06 10:04:00 - 08/06 17:48:00 | Track ( 231.3, -62.0) @ 08/06 09:45:00 | AR 14168 |
| 08/06 18:36:30 - 08/07 04:05:54 | Track ( 308.0, -59.7) @ 08/06 18:33:30 | AR 14168 |
| 08/07 04:09:00 - 08/07 06:05:54 | Fixed ( 0.0, 0.0) | synoptic, HOP349 |
| 08/07 06:19:00 - 08/07 11:33:54 | Track ( 406.1, -55.6) @ 08/07 06:16:00 | AR 14168 |

| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| **PROG= 14 30-time(s)** | | | | | | | | | | | | |
| **Subr= 1 1-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 68 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 0 | 0 | 2.0sec |
| **Subr= 2 20-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 11 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| **Seqn= 73 1-time(s) 10.0sec** | | | | | | | | | | | | |
| thin-Be/Open | med-Be/Open | close | Safe | Norm | 125ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| med-Be/Open | Open/thick-Al | close | Safe | Norm | 250ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 2.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| **Subr= 3 1-time(s) 2.0sec** | | | | | | | | | | | | |
| **Seqn= 10 1-time(s) 2.0sec** | | | | | | | | | | | | |
| med-Al/Open | med-Al/thick-Al | close | Safe | Norm | 500ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| Open/thick-Be | Open/thick-Be | close | Safe | Norm | 2.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=95 | 3 | 0 | 2.0sec |
| **Seqn= 11 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Al-poly/Open | Al-poly/thick-Al | close | Safe | Norm | 125ms | Obs | 2x2 | 512x512 (1024, 1024) | Q=95 | 2 | 0 | 2.0sec |
| **Seqn= 87 1-time(s) 2.0sec** | | | | | | | | | | | | |
| Open/G-band | Open/G-band | open | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/G-band | Open/G-band | close | Safe | Norm | 1ms | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 1x1 | 384x384 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Open/thick-Al | Open/thick-Al | close | Safe | Dark | 1.00s | Obs | 2x2 | 512x512 (1024, 1024) | Q=98 | 0 | 0 | 2.0sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |

## ✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

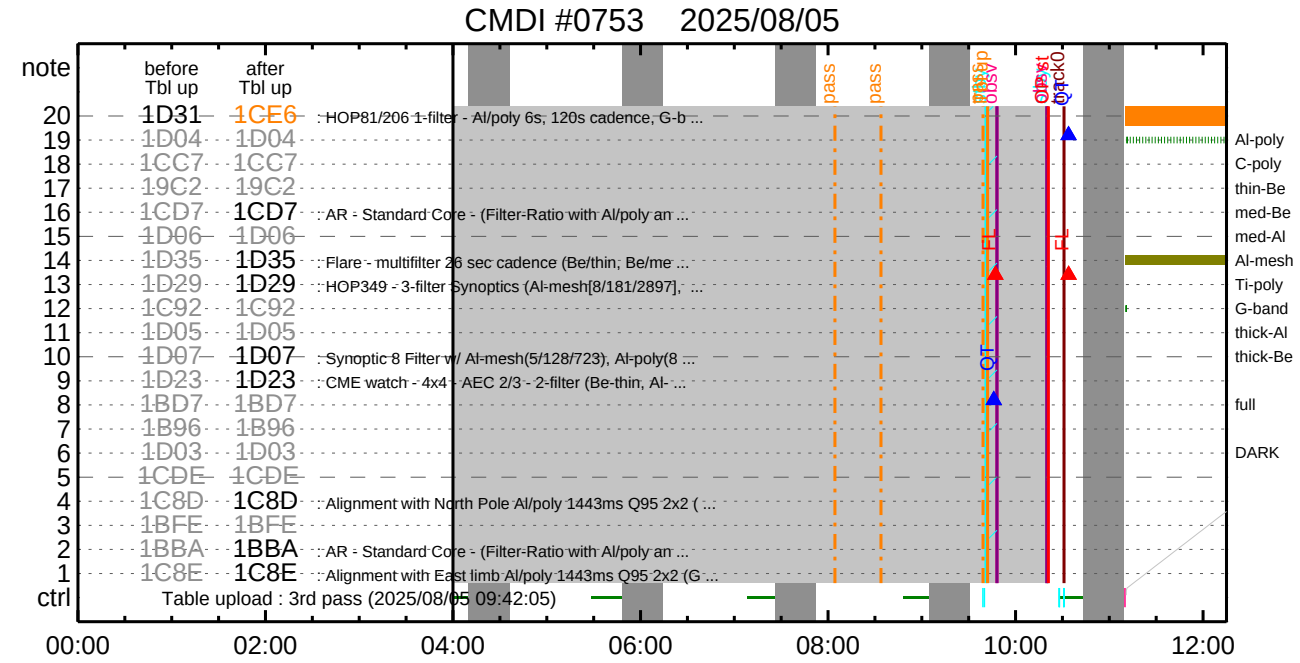
NOT USED

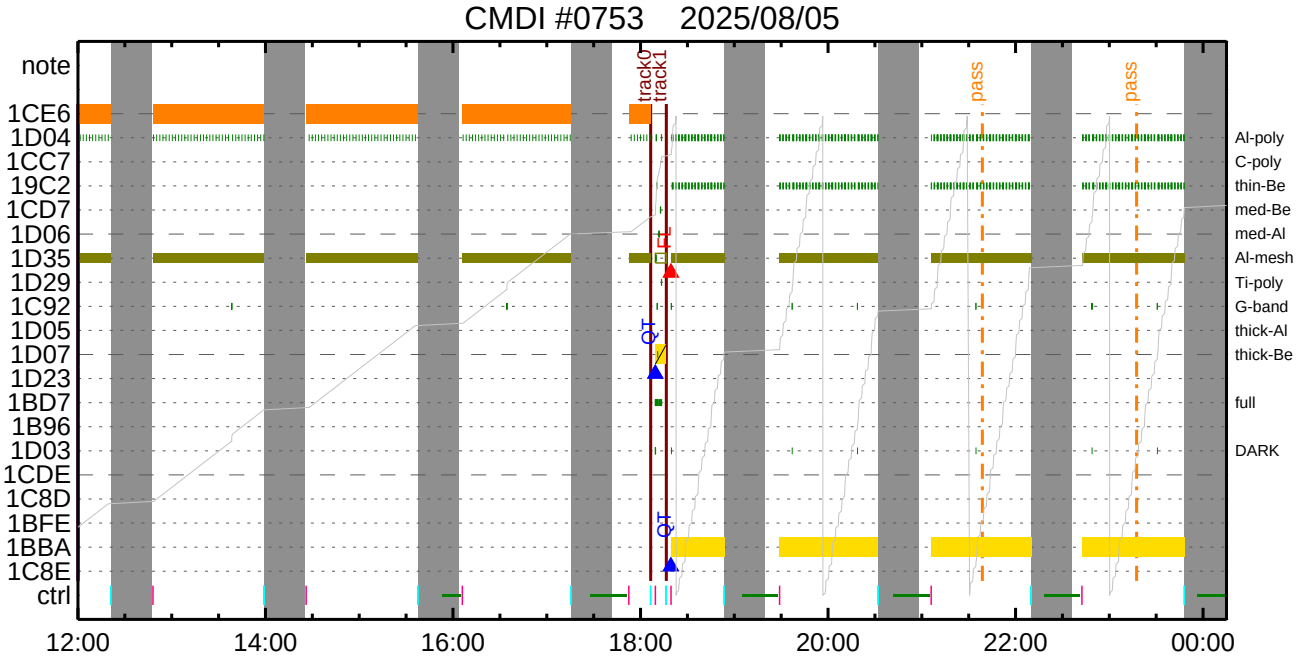
## ✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

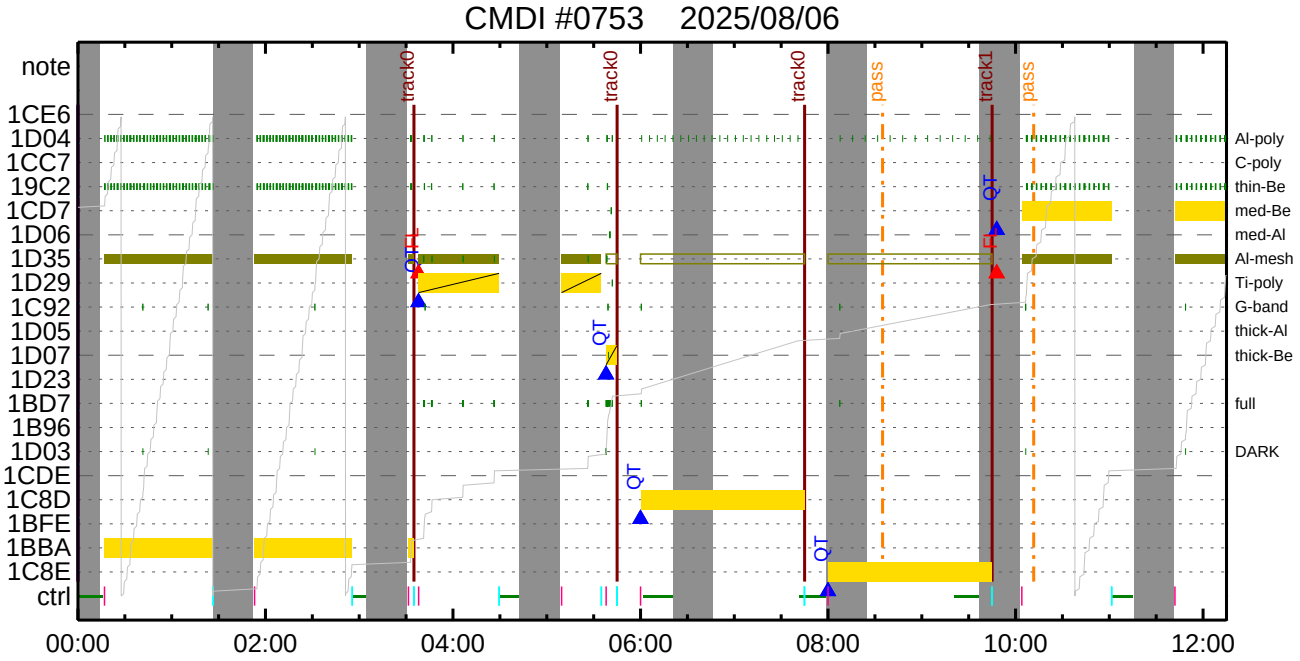
**FLD Patrol**

| Term | Pointing (x, y) | Comment |
|---|---|---|
| 08/05 09:43:05 - 08/05 18:06:48 | cannot be identified | |
| 08/05 18:16:48 - 08/06 05:35:18 | Track ( 92.5, -64.2) @ 08/05 18:16:30 | AR 14168 |
| 08/06 09:45:18 - 08/06 18:23:48 | Track ( 231.3, -62.0) @ 08/06 09:45:00 | AR 14168 |
| 08/06 18:33:48 - 08/07 06:06:18 | Track ( 308.0, -59.7) @ 08/06 18:33:30 | AR 14168 |
| 08/07 06:16:18 - 08/09 10:51:00 | Track ( 406.1, -55.6) @ 08/07 06:16:00 | AR 14168 |

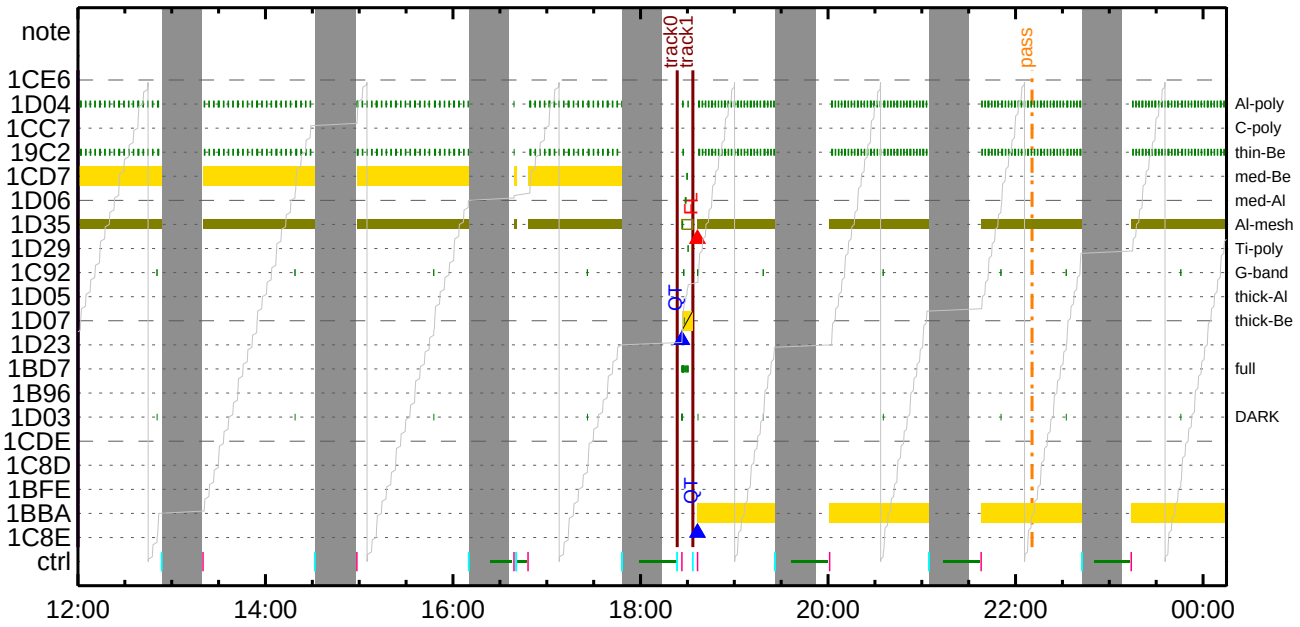
| | | | | | | | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|
| Al-poly/Open | Al-poly/Open | close | Safe | Norm | 4ms | Obs | 8x8 | | Q=50 | | | 30sec |
| Default Filter | Thicker Filter | VLS | mode | image | Exp. | CCD | Bin | ROI: size (center) | Comp. | AEC | Buffer | Interval |


CMDI #0753 2025/08/05
note
before Tbl up
after Tbl up
20 1D31 1CE6 : HOP81/206 1-filter - Al/poly 6s, 120s cadence, G-b ...
19 1D04 1D04
18 1CC7 1CC7
17 19C2 19C2
16 1CD7 1CD7 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
15 1D06 1D06
14 1D35 1D35 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
13 1D29 1D29 : HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], ...
12 1C92 1C92
11 1D05 1D05
10 1D07 1D07 : Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8 ...
9 1D23 1D23 : CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al- ...
8 1BD7 1BD7
7 1B96 1B96
6 1D03 1D03
5 1CDE 1CDE
4 1C8D 1C8D : Alignment with North Pole Al/poly 1443ms Q95 2x2 ( ...
3 1BFE 1BFE
2 1BBA 1BBA : AR - Standard Core - (Filter-Ratio with Al/poly an ...
1 1C8E 1C8E : Alignment with East limb Al/poly 1443ms Q95 2x2 (G ...
ctrl
Table upload : 3rd pass (2025/08/05 09:42:05)
pass
pass
QT
FL
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

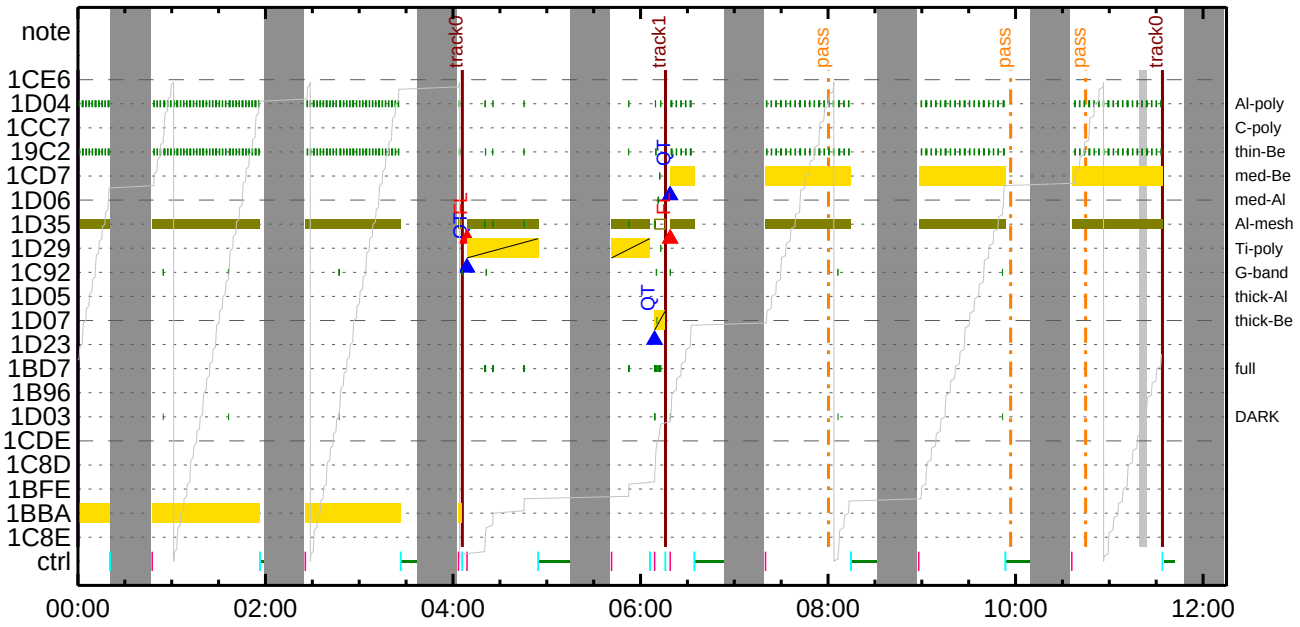

CMDI #0753 2025/08/05
note
1CE6
1D04
1CC7
19C2
1CD7
1D06
1D35
1D29
1C92
1D05
1D07
1D23
1BD7
1B96
1D03
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
track0
track1
pass
pass
FL
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0753 2025/08/06
note
1CE6
1D04
1CC7
19C2
1CD7
1D06
1D35
1D29
1C92
1D05
1D07
1D23
1BD7
1B96
1D03
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
track0
track0
track0
pass
track1
pass
QT
FL
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

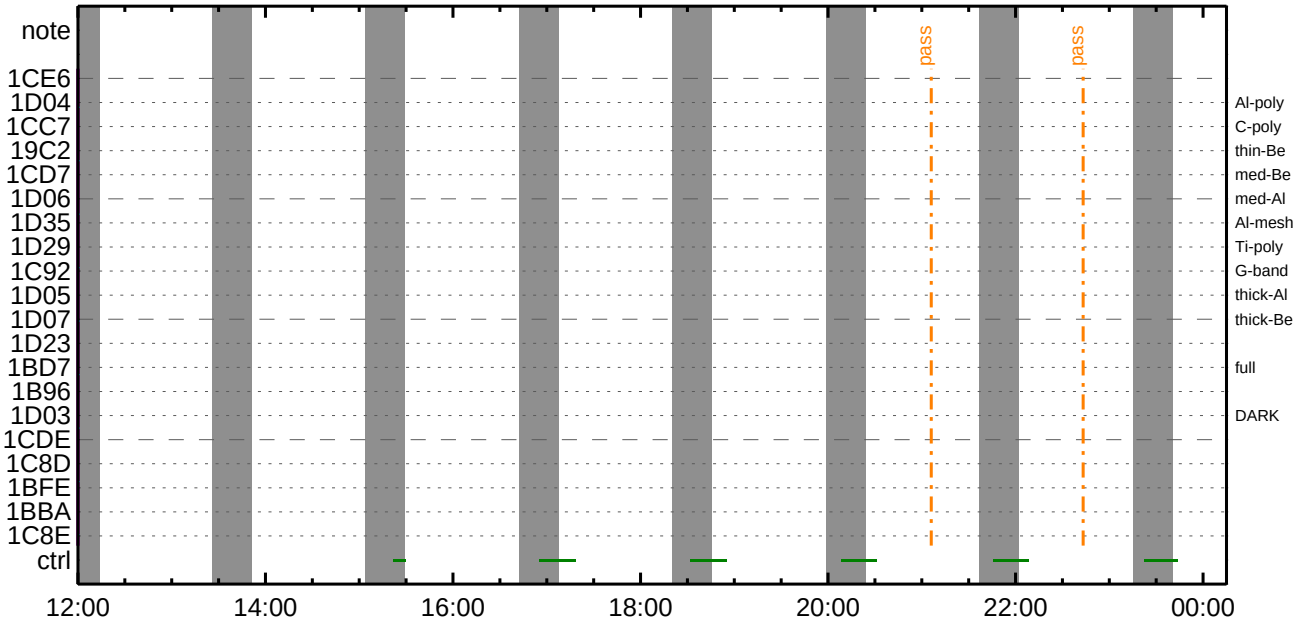
CMDI #0753 2025/08/06

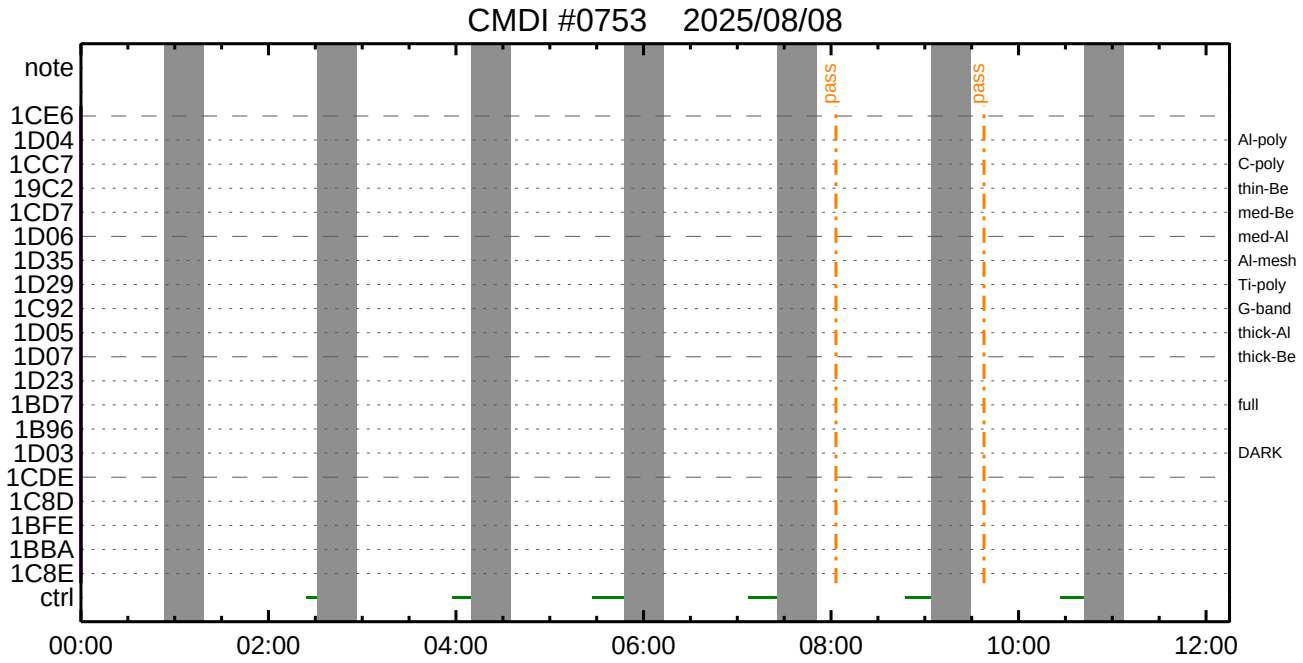


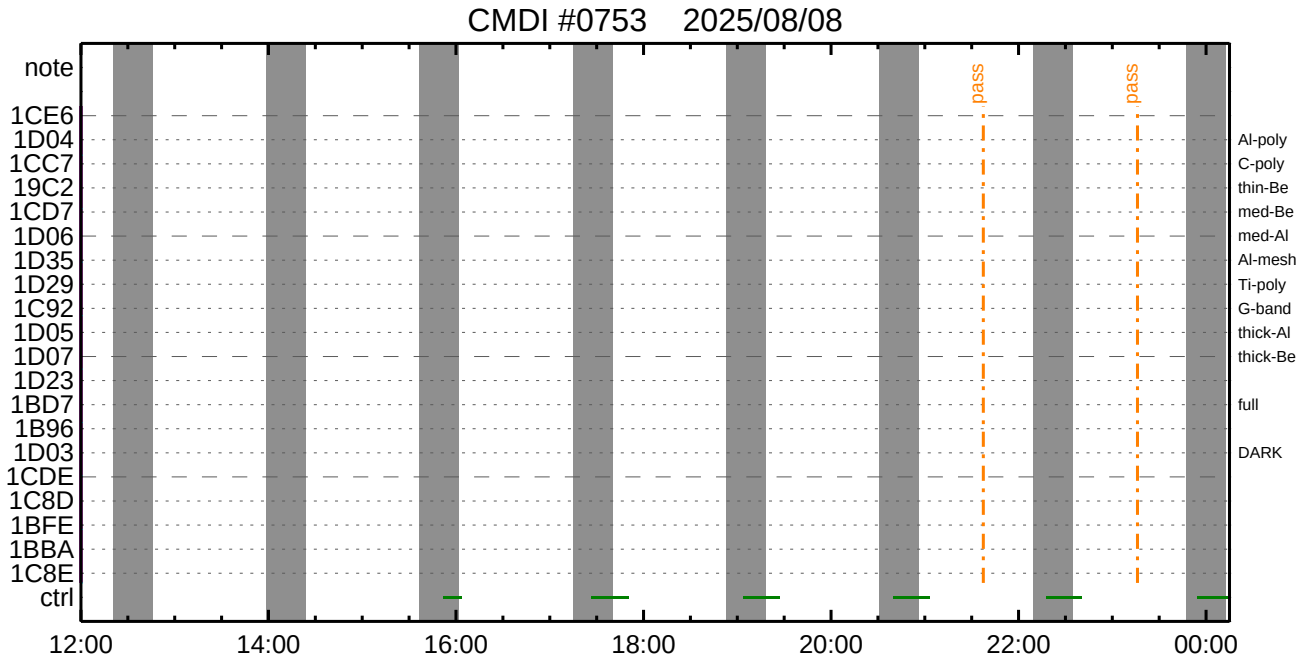

CMDI #0753 2025/08/07

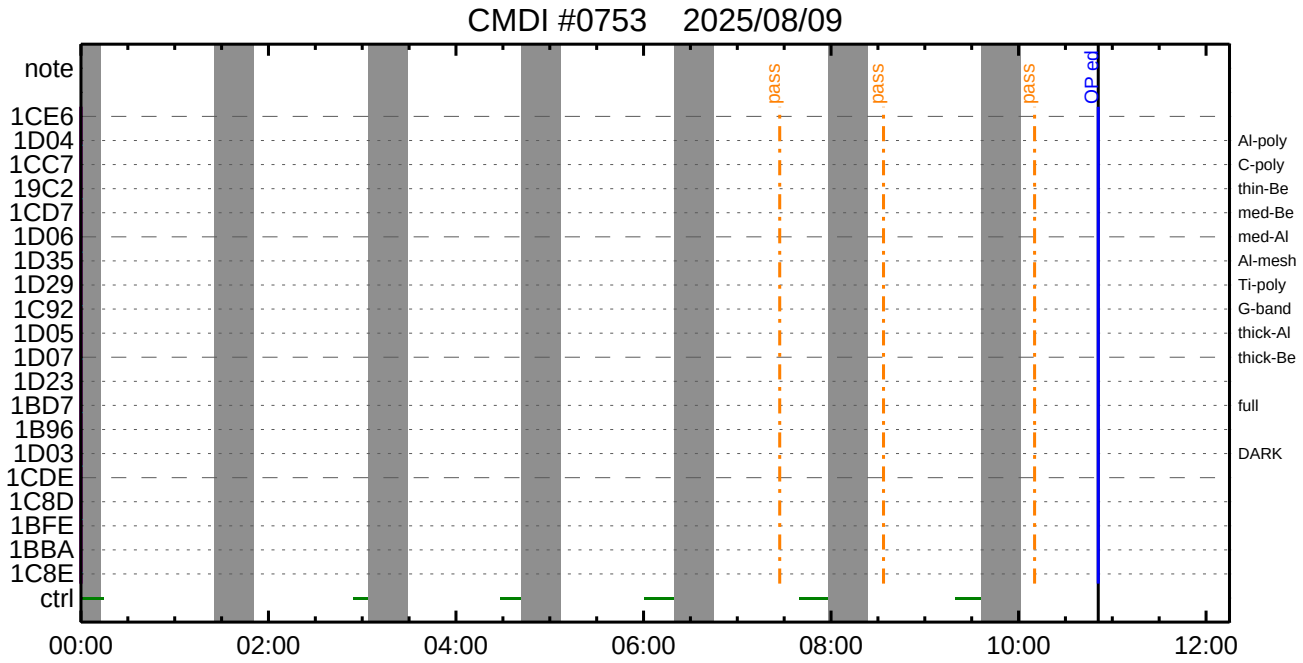


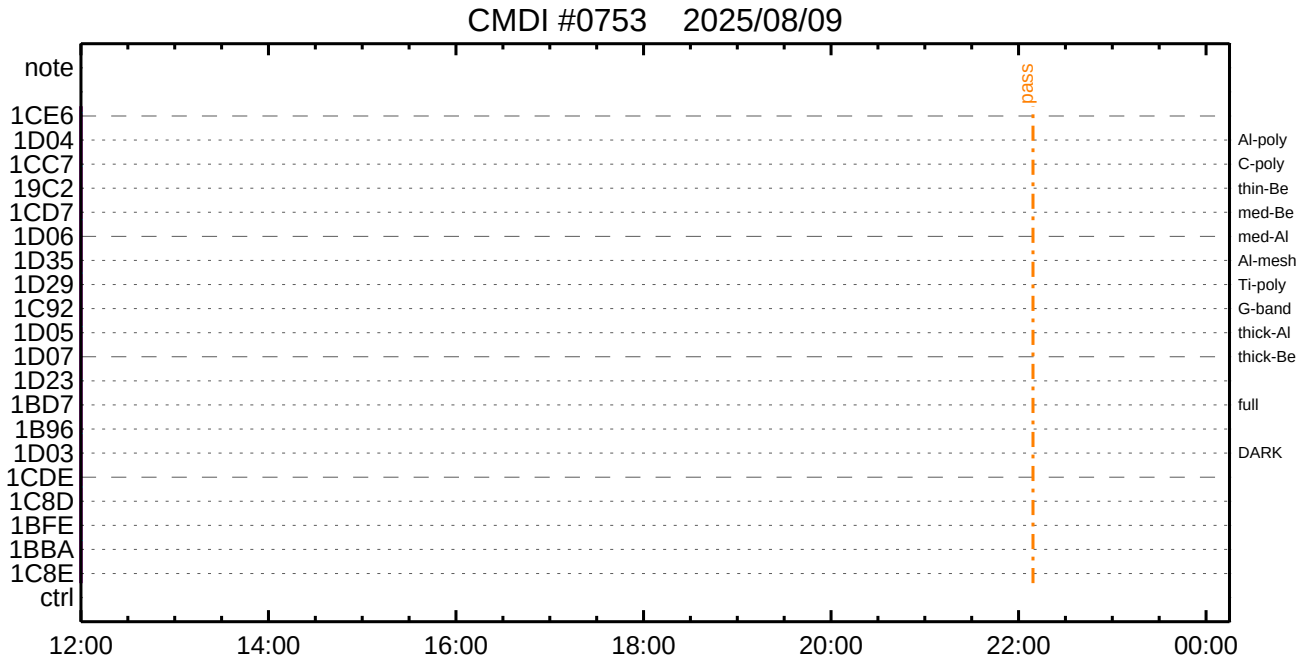

CMDI #0753 2025/08/07




CMDI #0753 2025/08/08
note
1CE6
1D04
1CC7
19C2
1CD7
1D06
1D35
1D29
1C92
1D05
1D07
1D23
1BD7
1B96
1D03
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0753 2025/08/08
note
1CE6
1D04
1CC7
19C2
1CD7
1D06
1D35
1D29
1C92
1D05
1D07
1D23
1BD7
1B96
1D03
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0753 2025/08/09
note
1CE6
1D04
1CC7
19C2
1CD7
1D06
1D35
1D29
1C92
1D05
1D07
1D23
1BD7
1B96
1D03
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0753 2025/08/09
note
1CE6
1D04
1CC7
19C2
1CD7
1D06
1D35
1D29
1C92
1D05
1D07
1D23
1BD7
1B96
1D03
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

(a) Spacecraft Operation Procedure (real-commands)

```
main-434 2025-08-05 15:27:08 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×ÈÝ¤äLOS¤Þ¤Ç¤Î»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021  +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023  +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                    ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                    ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040  +  DC 06-B3 DR_REP_START
0041  +  DC 01-32 DHU_X_VC4_ON
0042     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾¾Ú)
0043     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0044     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047  +. DC 06-B3 DR_REP_START
0048  +  DC 01-32 DHU_X_VC4_ON
0049     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾¾Ú)
0050     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0051     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066  +  DC 06-B3 DR_REP_START
0067  +  DC 01-32 DHU_X_VC4_ON
0068     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾¾Ú)
0069     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0070     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073  +. DC 06-B3 DR_REP_START
0074  +  DC 01-32 DHU_X_VC4_ON
0075     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾¾Ú)
0076     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0077     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                    ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                    ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093  +  DC 03-B5 TCIA_XPA_OFF
0094     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-434:OP
0104     ()
0105   . S. OG    og-434:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ-¥À¥ó¥×¼êº½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. *********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. *********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2025-08-05 10:16:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2025-08-05 10:16:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2025-08-05 10:16:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195  +. TI 2025-08-05 10:20:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                    ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                    ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                    ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205   . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238   . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244  +. TI 2025-08-05 10:20:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247  +. TI 2025-08-05 10:20:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250   . C.                     [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. *********** End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ***********
0256     C. Execute, after the success of OP upload.
0257  +. TI 2025-08-05 10:20:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260   . C.                     [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ***********
0263     C.
0264   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266   . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270   . C. ***** ¥Þ¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271   . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277   . C. *********** LOS ***********
0278     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-435 2025-08-05 15:27:08 94 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                     Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                        <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047  +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049  +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 7853668.0 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055  +. DC 07-FC EIS_MODE_CHG_ENA
0056     BC       (20)
0057   . C. Verify EIS_MODE_CHG_FLG is ENA
0058  +. DC 07-FC EIS_MODE_MANU
0059     BC       (21 02)
0060   . C. Verify EIS in MANUAL mode
0061   . C. Estimated OBSTBL upload time is 34s
0062     C. *********************************
0063     C. EIS START OBSTBL LOAD
0064     C. *********************************
0065   . S. RAM   ram-820:EIS_OBSTBL
0066     ()
0067  +. DC 07-FC EIS_DUMP_OBSTBL
0068     BC       (07 07 07 00 00 70 00)
0069     C.
0070     C. Execute, after the success of OBSTBL upload.
0071     C. Set EIS TI-commands
0072  +. TI 2025-08-05 10:20:50.0
0073     DC 07-FC EIS_MODE_CHG_ENA
0074     BC       (20)
0075   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0076     C. *********************************
0077     C. EIS END OBSTBL LOAD
0078     C. *********************************
0079     C.
0080   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0081     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0082   . S. DC-BC dcbc-402:DCBC
0083     (MDP_known_event)
0084     C.
0085     C.
0086   . C. ***** ¥Þ¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0087   . S. DC-BC dcbc-153:DCBC
0088     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089     C.
0090     C.
0091   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0092     C.
0093   . C. *********** LOS ***********
0094     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-436 2025-08-05 15:27:08 190 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×ÈÝ¤äLOS¤Þ¤Ç¤Î»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021  +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023  +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                    ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                    ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040  +  DC 06-B3 DR_REP_START
0041  +  DC 01-32 DHU_X_VC4_ON
0042     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047  +. DC 06-B3 DR_REP_START
0048  +  DC 01-32 DHU_X_VC4_ON
0049     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066  +  DC 06-B3 DR_REP_START
0067  +  DC 01-32 DHU_X_VC4_ON
0068     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073  +. DC 06-B3 DR_REP_START
0074  +  DC 01-32 DHU_X_VC4_ON
0075     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                    ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                    ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093  +  DC 03-B5 TCIA_XPA_OFF
0094     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +. DC 07-F0 MDP_XRT_CTRL_MANU
0104     BC       (c1)
0105  +  DC 07-F0 MDP_XRT_MODE_STBY
0106     BC       (c3)
0107   . C. ----------- Success Verify ?              OK / NG____
0108     C.
0109     C. XRT Obs. Table Upload
0110   . S. RAM   ram-291:MDP_OBS_X
0111     ()
0112     C.
0113  +. DC 07-F0 MDP_DUMP_XRTTBL
0114     BC       (84 07 00 00 00 3a d4)
0115   . C. ----------- Comparison Check ?                        OK / ERR ____
0116     C.
0117     C.
0118  +. DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 01 b1 b1 04 04)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 02 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 03 b1 b1 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 04 b1 b1 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 05 85 83 06 06)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 06 80 80 20 20)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 07 80 80 08 08)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 08 85 83 06 06)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 09 80 80 20 08)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0a 80 80 08 20)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0b 85 83 08 08)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 0c 80 60 20 18)
0142  +  DC 07-F0 MDP_XRT_ROI_SET
0143     BC       (cd 0d a0 80 18 20)
0144  +  DC 07-F0 MDP_XRT_ROI_SET
0145     BC       (cd 0f 80 80 06 06)
0146  +  DC 07-F0 MDP_XRT_ROI_SET
0147     BC       (cd 10 80 80 08 08)
0148  +  DC 07-F0 MDP_XRT_FLD_ENA
0149     BC       (d8)
0150  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151     BC       (c8)
0152  +  DC 07-F0 MDP_XRT_ARS_DIS
0153     BC       (d5)
0154  +  DC 07-F0 MDP_XRT_AEC_RESET
0155     BC       (d0)
0156  +  DC 07-F0 MDP_XRT_FLD_RESET
0157     BC       (da)
0158  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159     BC       (c4 09)
0160  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161     BC       (c5 0e)
0162   . C. ---------- Success Verify ?            OK / NG ____
0163     C.
0164     C.
0165   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0166     C.
0167  +. DC 07-F0 MDP_XRT_MODE_OBSV
0168     BC       (c2)
0169  +. TI 2025-08-05 10:20:02.0
0170     DC 07-F0 MDP_XRT_MODE_OBSV
0171     BC       (c2)
0172   . C. ---------- Success Verify ?            OK / NG ____
0173     C.
0174     C. ************ XRT END ***********
0175     C.
0176   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0177     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0178   . S. DC-BC dcbc-402:DCBC
0179     (MDP_known_event)
0180     C.
0181     C.
0182   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0183   . S. DC-BC dcbc-153:DCBC
0184     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0185     C.
0186     C.
0187   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0188     C.
0189   . C. *********** LOS ***********
0190     C.
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*** OP Sequence for XRT ***

2025/08/05  10:28:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  10:28:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  10:28:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  10:28:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  10:30:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  10:30:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  10:30:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/05  10:31:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 57 4b 01 b2
2025/08/05  10:31:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  10:31:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/05  10:31:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/05  10:31:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/05  10:31:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/05  10:31:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  10:33:56.0  XRT_QT_PROG_SET_436_OG [0x1b4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 14
2025/08/05  10:33:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/05  11:09:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  11:10:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  12:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  12:21:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  12:21:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  12:21:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  12:24:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  12:47:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  12:48:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  13:59:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  13:59:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  13:59:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  13:59:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  14:02:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  14:25:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  14:26:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  15:37:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  15:37:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  15:37:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  15:37:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  15:40:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  16:05:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  16:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  17:15:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  17:15:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  17:15:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  17:15:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  17:18:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  17:51:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  17:52:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  18:06:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  18:06:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
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2025/08/05  18:06:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/05  18:06:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/08/05  18:06:48.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/05  18:06:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/05  18:06:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/05  18:09:28.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2025/08/05  18:09:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  18:16:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  18:16:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  18:16:28.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/05  18:16:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 05 81 01 b2
2025/08/05  18:16:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/05  18:16:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/05  18:16:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/05  18:16:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/05  18:16:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  18:19:26.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2025/08/05  18:19:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/05  18:19:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  18:53:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  18:53:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  18:53:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  18:53:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  18:56:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  19:28:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  19:29:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  20:32:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  20:32:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  20:32:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  20:32:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  20:35:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  21:05:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  21:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  22:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  22:10:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  22:10:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  22:10:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  22:13:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/05  22:41:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/05  22:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/05  23:48:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  23:48:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/05  23:48:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/05  23:48:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/05  23:51:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  00:16:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  00:17:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  01:26:30.5  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  01:26:32.5  XRT_CTRL_MANU_402_OG [0x192]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  01:26:34.5  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  01:26:36.5  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  01:29:44.5  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  01:52:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  01:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  02:55:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  02:55:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  02:55:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  02:55:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  02:58:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  03:30:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  03:31:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  03:34:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  03:34:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  03:34:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/06  03:35:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/08/06  03:35:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/06  03:35:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/06  03:35:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/06  03:35:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  03:35:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  03:37:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2025/08/06  03:37:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/06  03:38:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  04:29:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  04:29:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  04:29:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  04:29:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  04:32:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  05:08:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  05:09:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  05:34:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  05:34:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  05:34:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/06  05:35:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/06  05:35:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/06  05:35:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  05:37:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2025/08/06  05:38:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  05:44:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  05:44:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  05:44:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/06  05:45:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 ad 59 00 00
2025/08/06  05:45:18.0  XRT_FLD_DIS_421_OG [0x1a5]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/06  05:59:54.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/06  05:59:56.0  XRT_ARS_DIS_427_OG [0x1ab]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  05:59:58.0  XRT_QT_PROG_SET_416_OG [0x1a0]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2025/08/06  06:00:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  07:44:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  07:44:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  07:44:58.0  XRT_FOCUS_POSITION_417_OG [0x1a1]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/06  07:45:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                        AOCU_NM              5 02-76 00 00 00 56 35
2025/08/06  07:45:18.0  XRT_FLD_DIS_421_OG [0x1a5]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/06  07:59:54.0  XRT_ARS_DIS_427_OG [0x1ab]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  07:59:56.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/06  07:59:58.0  XRT_QT_PROG_SET_420_OG [0x1a4]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2025/08/06  08:00:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  09:44:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  09:44:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  09:44:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/06  09:45:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 05 81 01 b2
2025/08/06  09:45:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/06  09:45:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/06  09:45:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/06  09:45:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  09:45:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  09:47:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2025/08/06  09:47:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/06  10:03:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  10:04:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  11:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  11:01:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  11:01:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  11:01:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  11:04:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  11:41:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  11:42:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  12:53:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  12:53:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  12:53:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  12:53:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  12:56:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  13:19:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  13:20:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  14:31:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  14:31:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  14:31:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  14:31:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  14:34:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  14:57:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  14:58:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  16:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  16:10:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  16:10:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  16:10:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  16:13:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
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2025/08/06  16:38:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  16:39:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                  MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  16:40:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  16:40:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  16:40:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                  MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  16:40:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                  MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  16:43:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                  MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  16:47:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  16:48:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                  MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  17:48:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  17:48:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  17:48:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                  MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  17:48:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                  MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  17:51:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                  MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  18:23:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  18:23:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  18:23:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                  XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/06  18:23:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                  AOCU_NM              5 02-76 00 00 00 00 00
2025/08/06  18:23:48.0  XRT_FLD_DIS_409_OG [0x199]
                                  MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/06  18:23:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                  MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/06  18:23:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                  MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  18:26:28.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                  MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2025/08/06  18:26:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                  MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  18:33:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  18:33:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  18:33:28.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                  XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/06  18:33:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                  AOCU_NM              5 02-76 01 05 81 01 b2
2025/08/06  18:33:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                  MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/06  18:33:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                  MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/06  18:33:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                  MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/06  18:33:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                  MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/06  18:33:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                  MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  18:36:26.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                  MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2025/08/06  18:36:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                  MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/06  18:36:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                  MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  19:26:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  19:26:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  19:26:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                  MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  19:26:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                  MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  19:29:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                  MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  20:00:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  20:01:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                  MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  21:04:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  21:04:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                  MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  21:04:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                  MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  21:04:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                  MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  21:07:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                  MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  21:37:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  21:38:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
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                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/06  22:42:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  22:42:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/06  22:42:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/06  22:42:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/06  22:45:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/06  23:13:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/06  23:14:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  00:20:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  00:20:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  00:20:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  00:20:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  00:23:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  00:46:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  00:47:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  01:56:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  01:56:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  01:56:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  01:56:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  01:59:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  02:24:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  02:25:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  03:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  03:26:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  03:26:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  03:26:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  03:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  04:02:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  04:03:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  04:05:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  04:05:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  04:05:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/07  04:06:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/08/07  04:06:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/07  04:06:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/07  04:06:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/07  04:06:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/07  04:06:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  04:08:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2025/08/07  04:08:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/07  04:09:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  04:54:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  04:54:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  04:54:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  04:54:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  04:57:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  05:40:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  05:41:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  06:05:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  06:05:56.0  XRT_CTRL_MANU_402_OG [0x192]
```

```
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  06:05:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/07  06:06:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/07  06:06:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/07  06:06:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/07  06:08:58.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0a
2025/08/07  06:09:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  06:15:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  06:15:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  06:15:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/07  06:16:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 05 81 01 b2
2025/08/07  06:16:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/07  06:16:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/07  06:16:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/07  06:16:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/07  06:16:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  06:18:56.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 10
2025/08/07  06:18:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/07  06:19:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  06:34:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  06:34:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  06:34:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  06:34:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  06:37:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  07:19:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  07:20:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  08:14:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  08:14:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  08:14:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  08:14:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  08:17:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  08:57:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  08:58:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  09:53:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  09:53:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  09:53:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/07  09:53:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/07  09:56:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/07  10:35:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/07  10:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/07  11:33:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/07  11:34:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
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