

XRT Timeline to be uploaded on 2025/08/16

Period: 2025/08/16 11:53:00 - 2025/08/21 10:46:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/16 12:09:30 - 08/16 17:59:54	Fixed (893.0, 65.2)	# OP start + 10min + AR 14172(Fix)
08/17 06:21:00 - 08/17 18:00:54	Track (-48.5, -221.9) @ 08/17 06:18:00	AR obs.
08/18 05:42:30 - 08/18 17:46:00	Track (165.5, -220.5) @ 08/18 05:39:30	AR obs.
08/19 06:13:30 - 08/19 11:45:30	Track (716.5, -303.9) @ 08/19 06:10:30	HOP507(AR 14179)

PROG= 02 Inf.-time(s)

Subr= 1		1-time(s)	2.0sec										
Seqn= 92		1-time(s)	2.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		5-time(s)	2.0sec										
Seqn= 47		1-time(s)	2.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96		4-time(s)	120.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795

Term	Pointing (x, y)	Comment
08/16 18:03:00 - 08/16 18:09:54	Fixed (0.0, 0.0)	synoptic
08/17 06:11:00 - 08/17 06:17:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 8.0 min
08/17 18:04:00 - 08/17 18:10:54	Fixed (0.0, 0.0)	synoptic, shifted 1.0 min
08/18 05:32:30 - 08/18 05:39:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -30.5 min
08/18 18:21:00 - 08/18 18:27:54	Fixed (0.0, 0.0)	synoptic, shifted 18.0 min
08/19 06:03:30 - 08/19 06:10:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 0.5 min

PROG= 10 1-time(s)

Subr= 1		1-time(s)	2.0sec										
Seqn= 5		1-time(s)	2.0sec										
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26		1-time(s)	2.0sec										
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99		1-time(s)	2.0sec										
Al-poly/Open		Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83		1-time(s)	2.0sec										
thin-Be/Open		thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23		1-time(s)	4.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec										
Seqn= 41		1-time(s)	2.0sec										
Open/thick-Be		Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17		1-time(s)	2.0sec										
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33		1-time(s)	2.0sec										
med-Be/Open		Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75		1-time(s)	2.0sec										
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/16 18:13:00 - 08/17 03:53:30	Track (-159.7, -220.3) @ 08/16 18:10:00	AR 14180 obs.
08/17 18:14:00 - 08/18 03:59:54	Track (60.9, -221.9) @ 08/17 18:11:00	AR obs.
08/18 18:35:00 - 08/19 03:21:00	Track (279.5, -217.2) @ 08/18 18:28:00	AR obs.
PROG= 03 Inf-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 92 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Subr= 2 4-time(s) 2.0sec		
Seqn= 47 1-time(s) 2.0sec		
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 2 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Seqn= 77 4-time(s) 300.0sec		
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 0 95.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 1 95.0sec
Al-poly/Open thin-Be/Open close	Safe Norm 250ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 1 2 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
08/17 04:30:30 - 08/17 05:23:00	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 8.0 min
08/18 04:03:00 - 08/18 05:29:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -30.5 min
08/19 04:03:00 - 08/19 06:00:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 0.5 min
PROG= 13 Inf-time(s)		
Subr= 1 1-time(s) 300.0sec		
Seqn=100 1-time(s) 2.0sec		
Open/Al-mesh Open/Al-mesh close	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 84 1-time(s) 2.0sec		
Al-poly/Open Al-poly/Open close	Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Al-poly/Open Al-poly/thick-Al close	Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 57 1-time(s) 2.0sec		
thin-Be/Open thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 11.3s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Seqn= 81 1-time(s) 2.0sec		
Open/G-band Open/G-band open	Safe Norm 1ms Obs 1x1 512x512 (1064, 1048)	Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 1ms Obs 1x1 512x512 (1064, 1048)	Q=95 0 0 2.0sec
Subr= 2 6-time(s) 1200.0sec		
Seqn= 8 1-time(s) 2.0sec		
thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024)	Q=98 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024)	DPCM 2 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Al-poly/Open Al-poly/Open close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 3 0 2.0sec
Al-poly/Open Al-poly/Open close	Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024)	DPCM 2 0 2.0sec
Seqn= 29 1-time(s) 2.0sec		
Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 3 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 250ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 2 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL

Term	Pointing (x, y)	Comment
08/16 12:09:30 - 08/16 17:59:54	Fixed (893.0, 65.2)	# OP start + 10min + AR 14172(Fix)
08/16 18:13:00 - 08/17 03:53:30	Track (-159.7, -220.3) @ 08/16 18:10:00	AR 14180 obs.
08/17 04:30:30 - 08/17 05:23:00	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 8.0 min
08/17 06:21:00 - 08/17 18:00:54	Track (-48.5, -221.9) @ 08/17 06:18:00	AR obs.
08/17 18:14:00 - 08/18 03:59:54	Track (60.9, -221.9) @ 08/17 18:11:00	AR obs.
08/18 04:03:00 - 08/18 05:29:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -30.5 min
08/18 05:42:30 - 08/18 17:46:00	Track (165.5, -220.5) @ 08/18 05:39:30	AR obs.
08/18 18:35:00 - 08/19 03:21:00	Track (279.5, -217.2) @ 08/18 18:28:00	AR obs.
08/19 04:03:00 - 08/19 06:00:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 0.5 min
08/19 06:13:30 - 08/19 11:45:30	Track (716.5, -303.9) @ 08/19 06:10:30	HOP507(AR 14179)
PROG= 14 30-time(s)		
Subr= 1 1-time(s) 2.0sec		

└─ Seqn= 68 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 2 20-time(s) 2.0sec														
└─ Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 73 1-time(s) 10.0sec		thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 3 1-time(s) 2.0sec														
└─ Seqn= 10 1-time(s) 2.0sec		med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 87 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

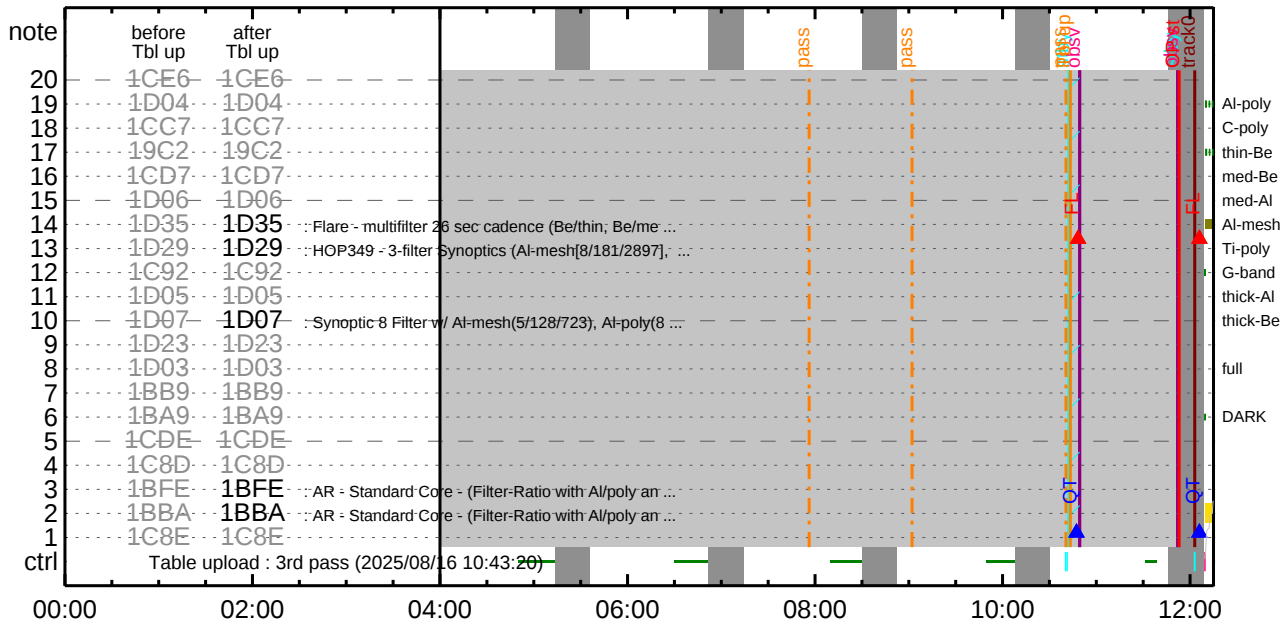
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Flare Detection

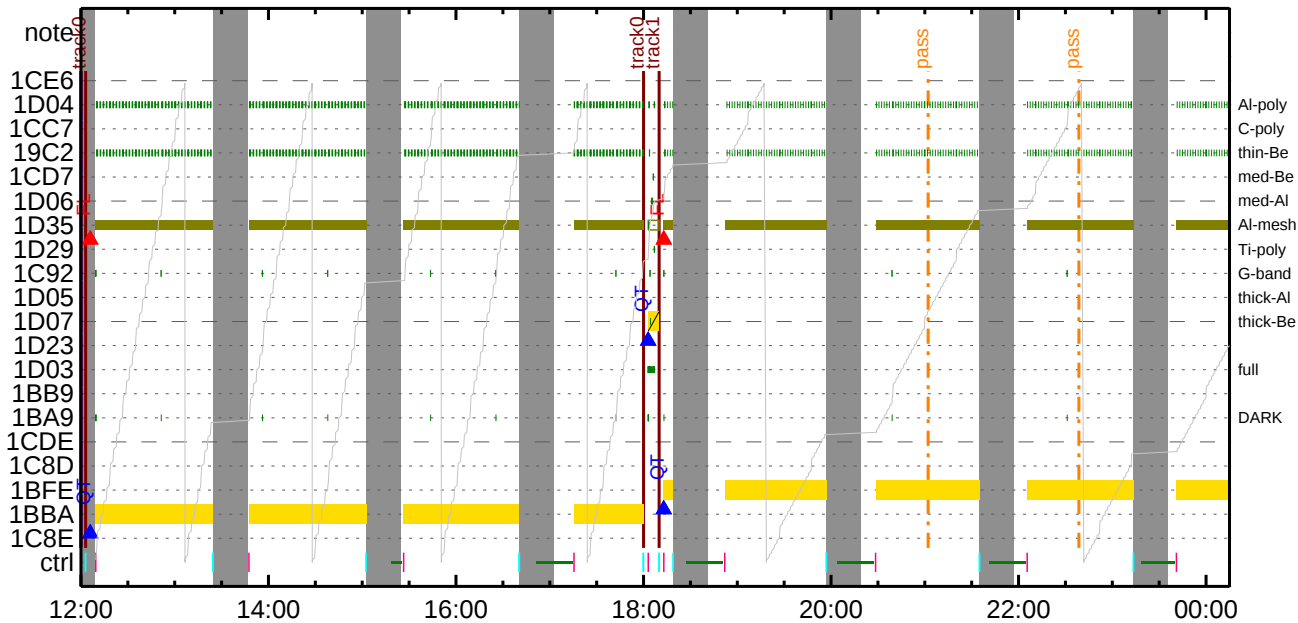
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FLD Patrol													
Term		Pointing (x, y)							Comment				
08/16 10:44:20 - 08/16 18:00:18		cannot be identified											
08/16 18:10:18 - 08/17 06:08:18		Track (	-159.7,	-220.3)	@ 08/16 18:10:00				AR 14180 obs.				
08/17 06:18:18 - 08/17 18:01:18		Track (	-48.5,	-221.9)	@ 08/17 06:18:00				AR obs.				
08/17 18:11:18 - 08/18 05:29:48		Track (	60.9,	-221.9)	@ 08/17 18:11:00				AR obs.				
08/18 05:39:48 - 08/18 18:18:18		Track (	165.5,	-220.5)	@ 08/18 05:39:30				AR obs.				
08/18 18:32:18 - 08/19 06:00:48		Track (	279.5,	-217.2)	@ 08/18 18:28:00				AR obs.				
08/19 06:10:48 - 08/21 10:46:00		Track (	716.5,	-303.9)	@ 08/19 06:10:30				HOP507(AR 14179)				
		Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec	
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

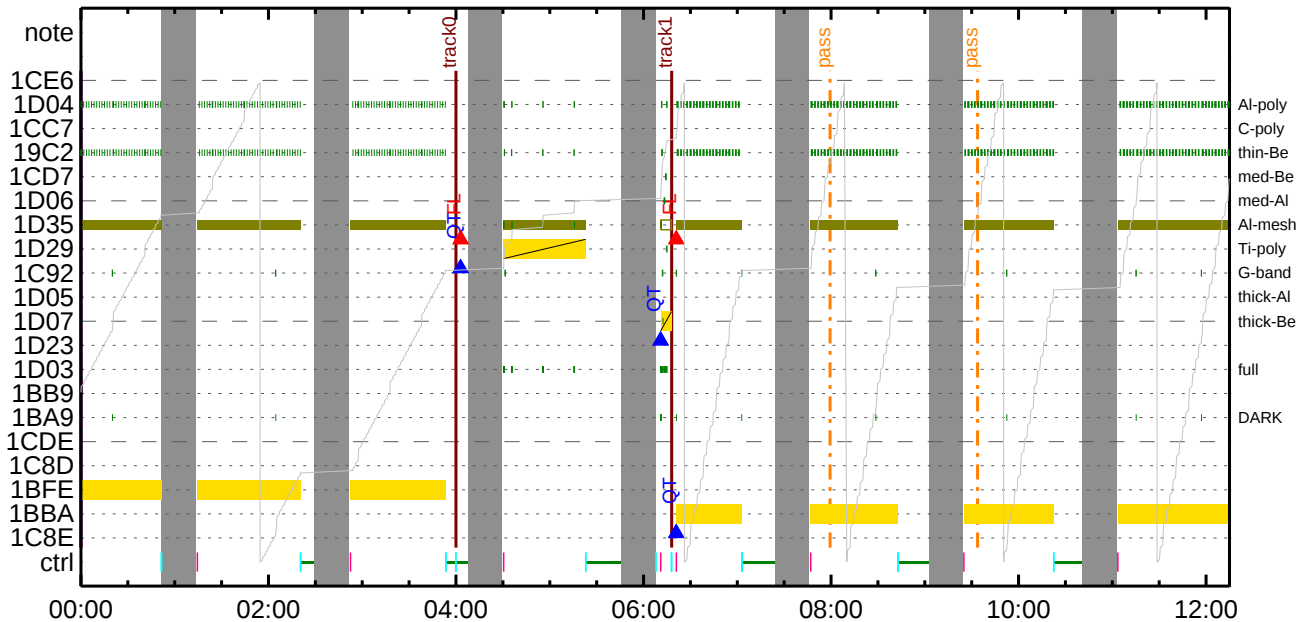
CMDI #0779 2025/08/16



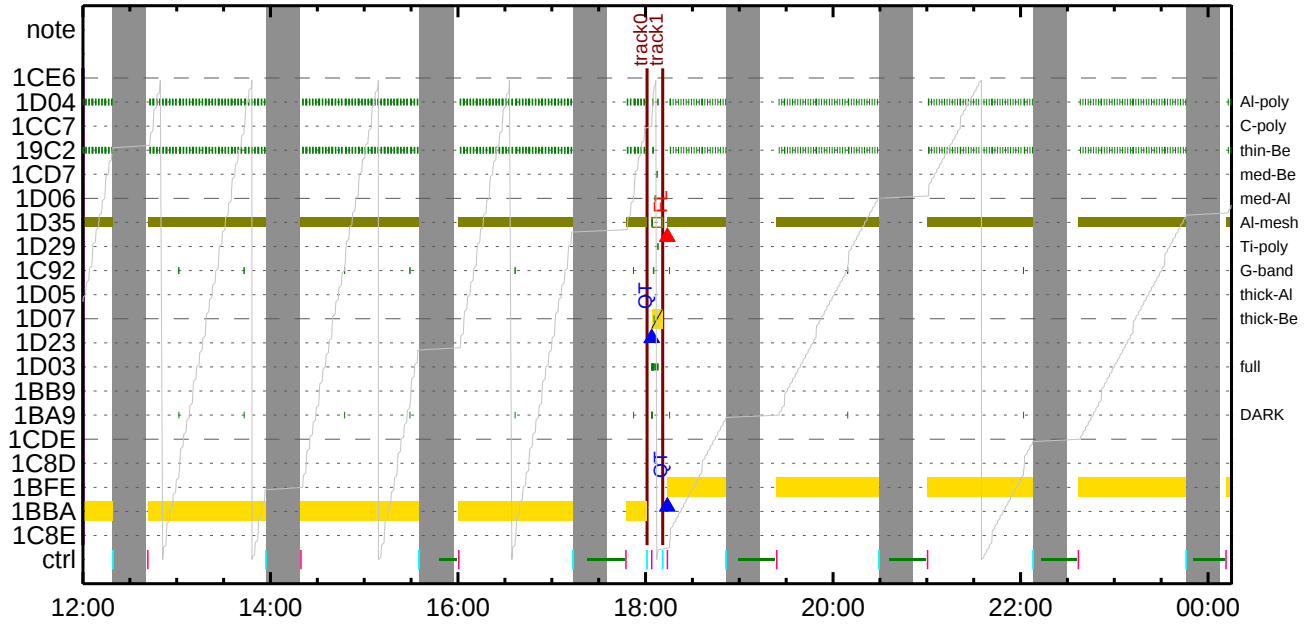
CMDI #0779 2025/08/16



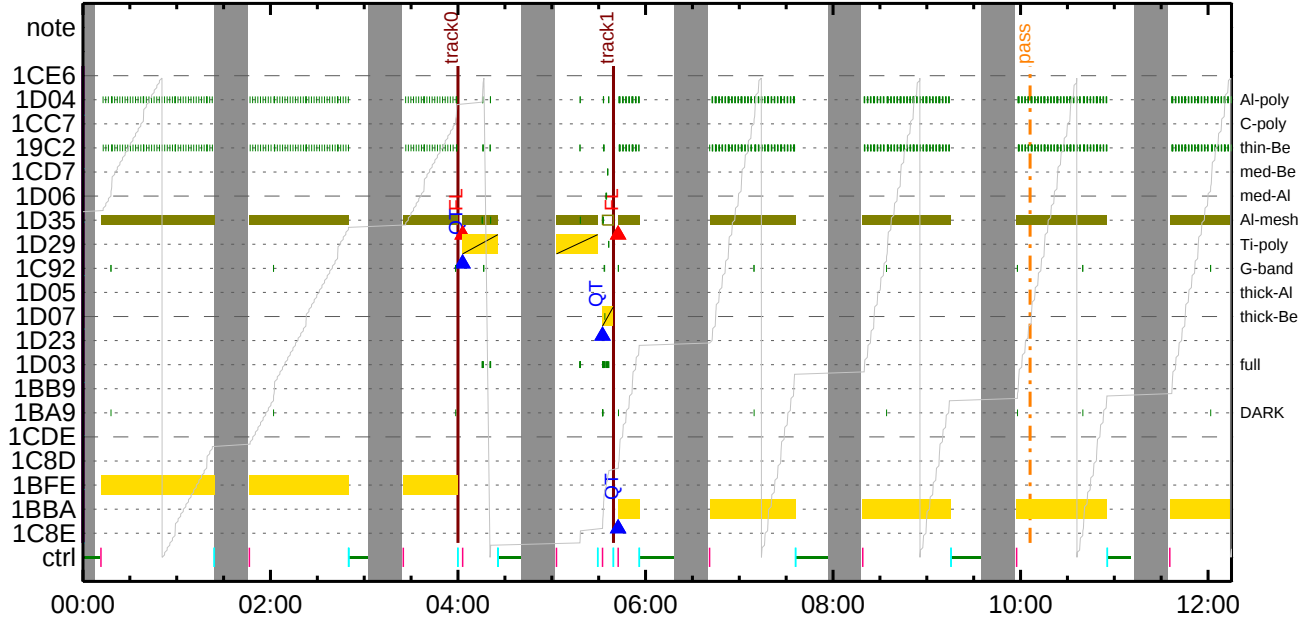
CMDI #0779 2025/08/17



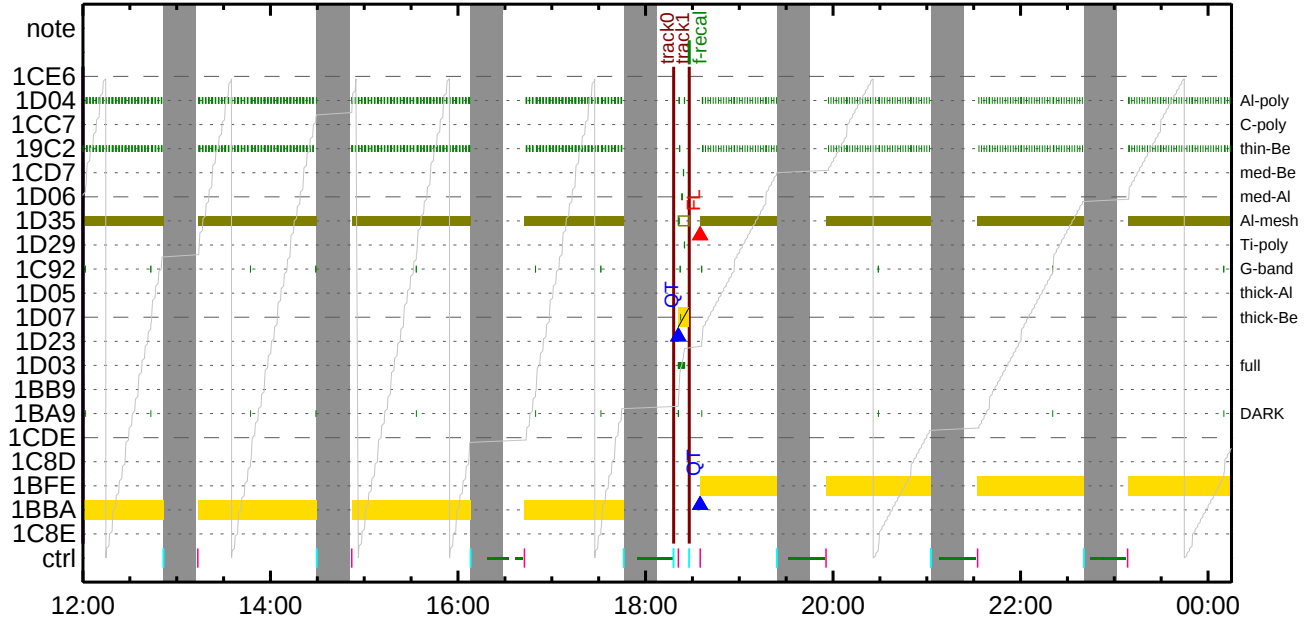
CMDI #0779 2025/08/17



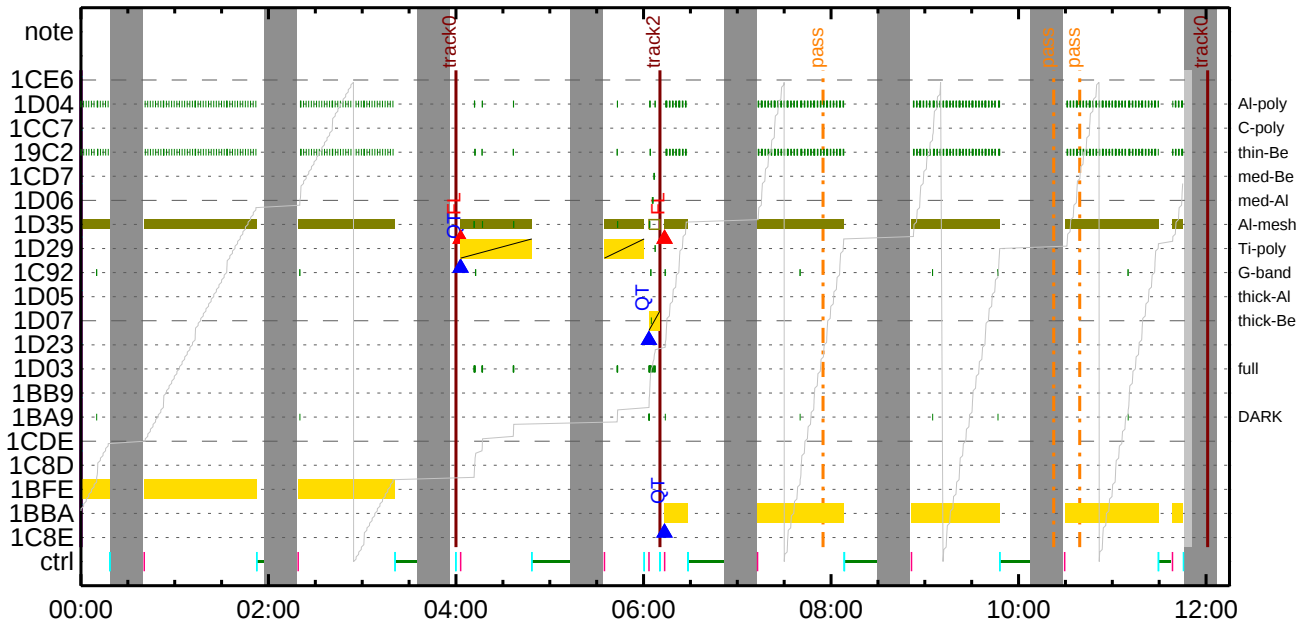
CMDI #0779 2025/08/18



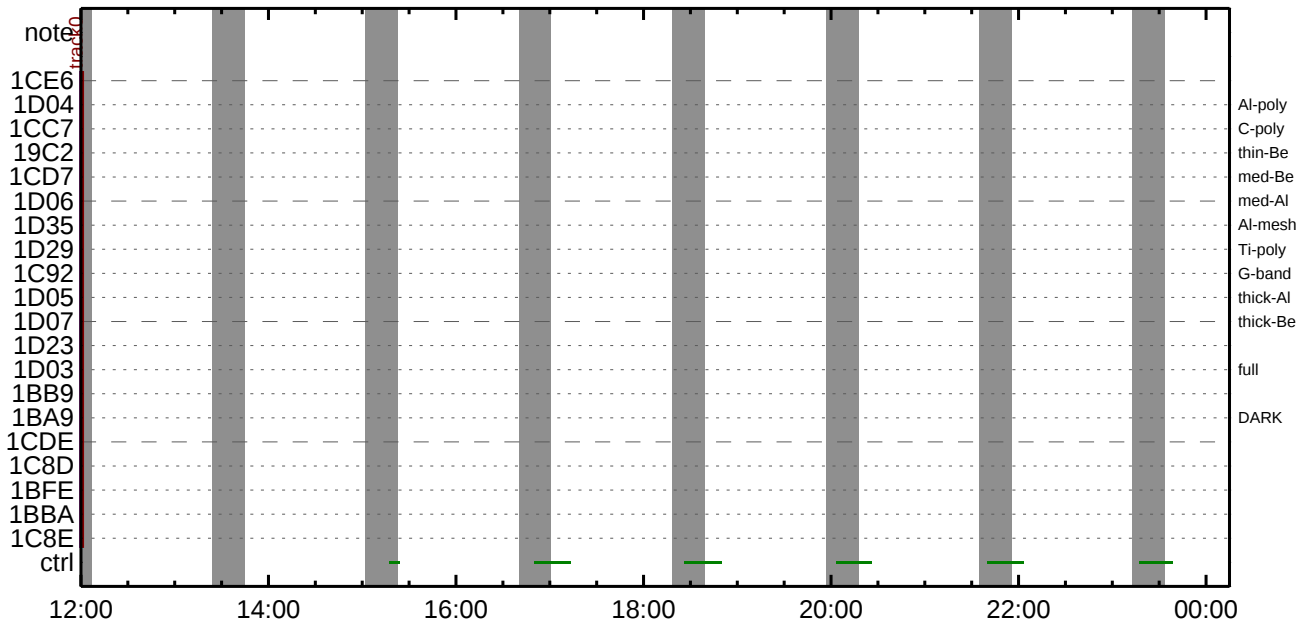
CMDI #0779 2025/08/18



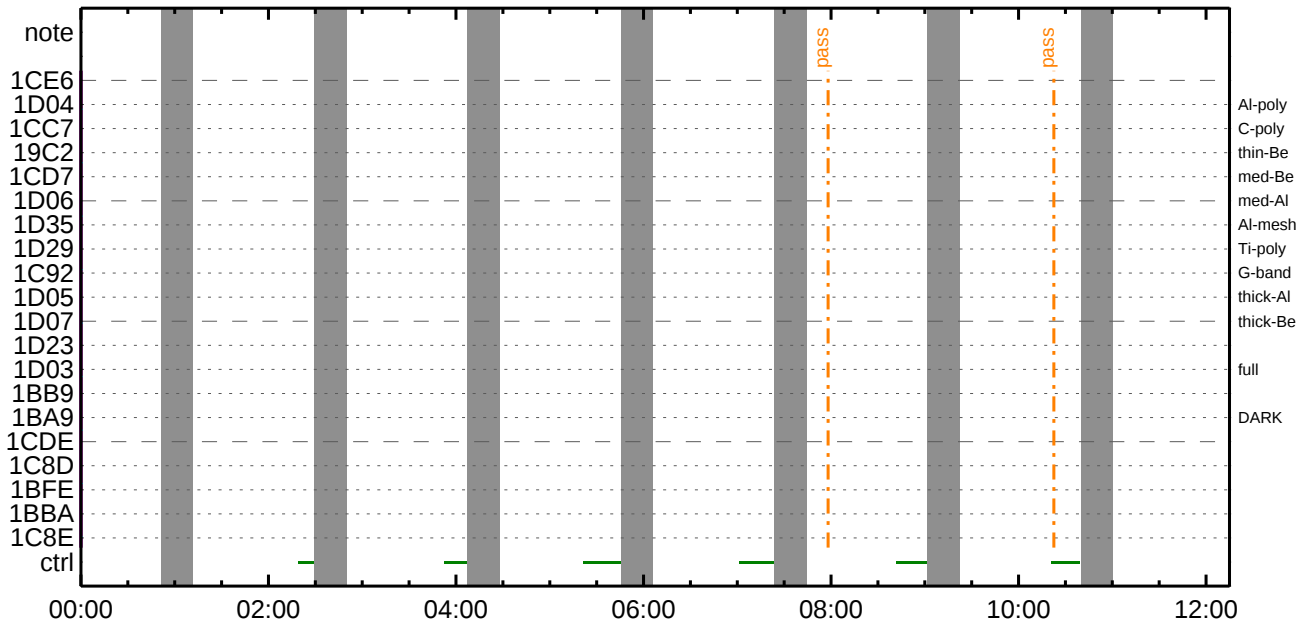
CMDI #0779 2025/08/19



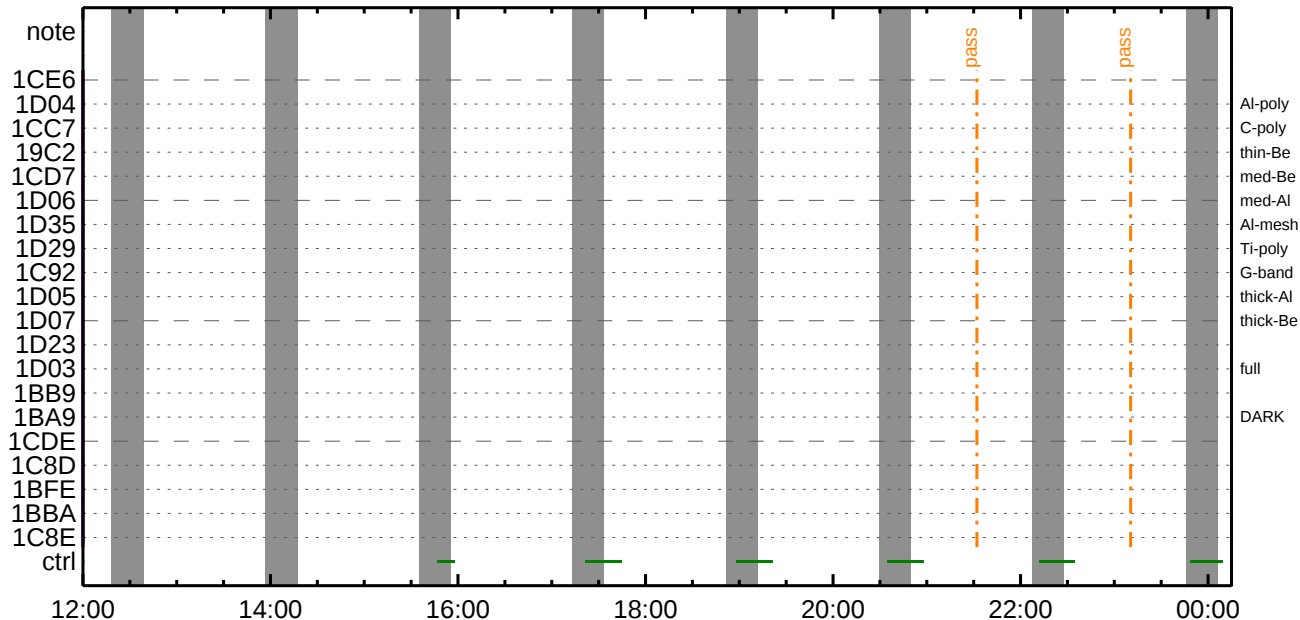
CMDI #0779 2025/08/19



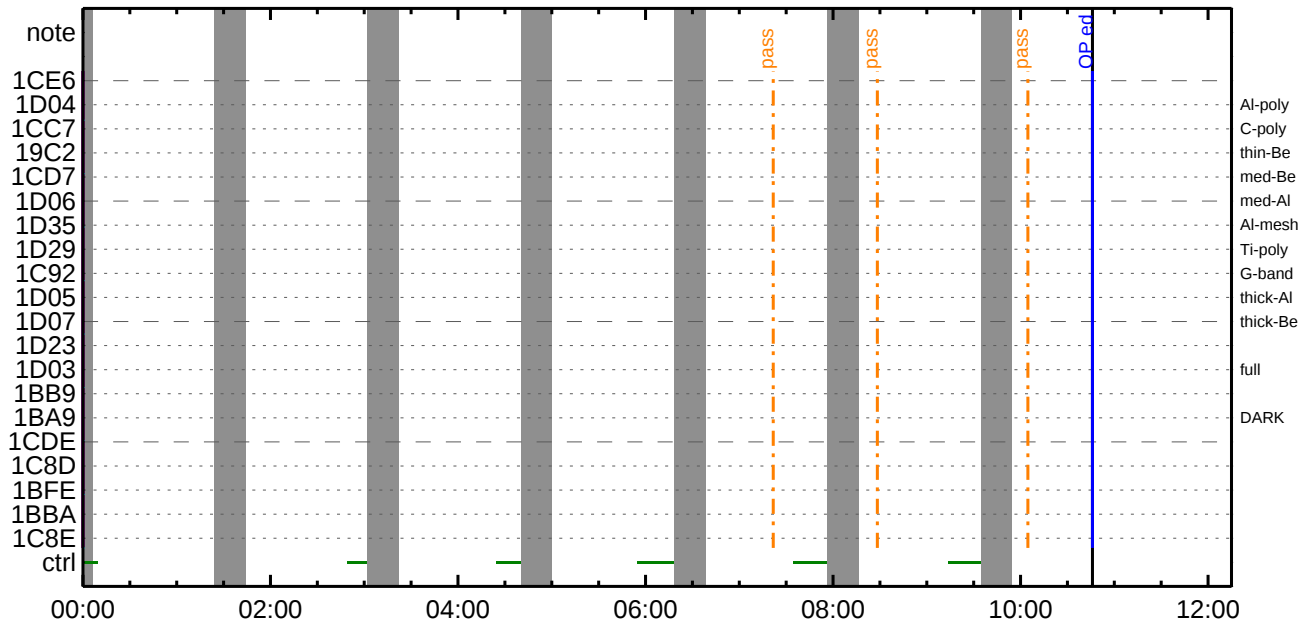
CMDI #0779 2025/08/20



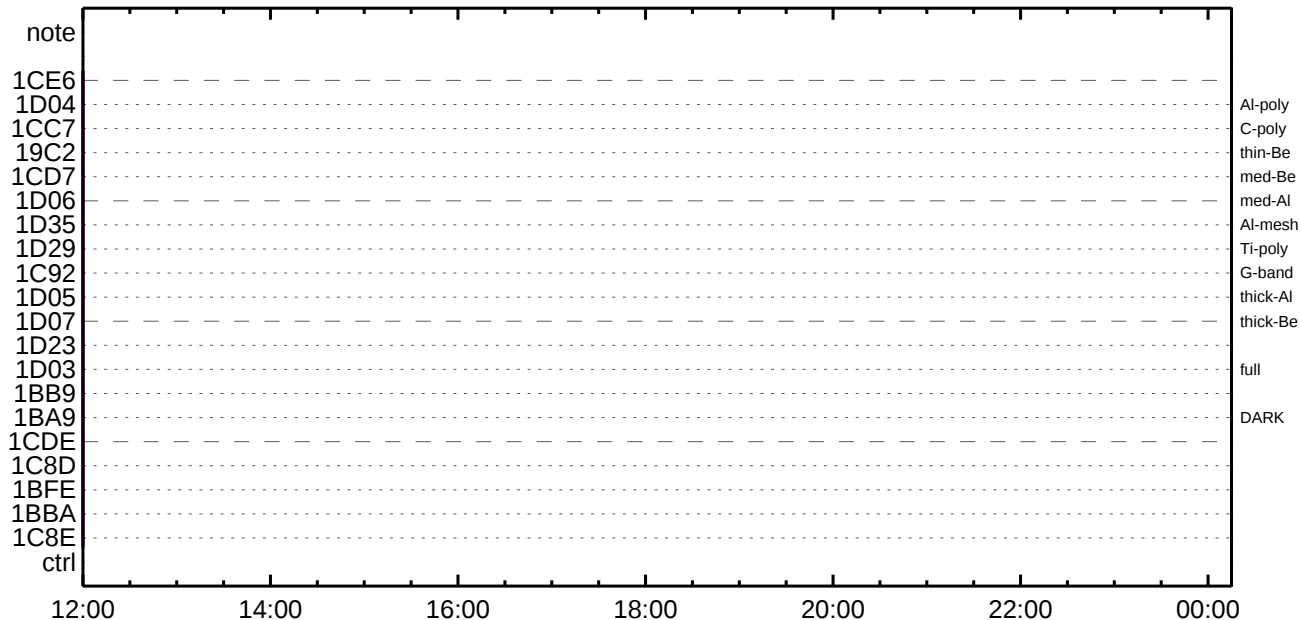
CMDI #0779 2025/08/20



CMDI #0779 2025/08/21



CMDI #0779 2025/08/21



(a) Spacecraft Operation Procedure (real-commands)

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main-458 2025-08-16 12:18:17 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥$¥Á¥~¼Ä»Ü;ä
0005 C.
0006 C. ¥À¥ß;¼¥³¥Ð¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ê¼¿¼Ä¼•µ°Æ»Í×ÁÇ¼Í¥Ç¥Á¥×¥í;¼¥É;ÊÊè¼µ•ííÊ;ÊÊÊ¼°ÇÔ¼•¼¿¼í¹Ç¼Í;ÇÀ®,ù¼¹¼è¼µ¼ÇÁ÷¿®¼•¼Ê¼µ¼³¼È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥í;¼¥É;¥Á¥ó¥×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OG¥í;¼¥É;ä
0019 . S. OP op-458:OP
0020 ()
0021 . S. OG og-458:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPíî°è¥Á¥ó¥×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0036 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥À¥ó¥×¼°áî»¼³íÇ$
0041 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¼í¾Ê¹Ç•è²íOK¼³íÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0055 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥À¥ó¥×¼°áî»¼³íÇ$
0060 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¼í¾Ê¹Ç•è²íOK¼³íÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0074 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥À¥ó¥×¼°áî»¼³íÇ$
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¼í¾Ê¹Ç•è²íOK¼³íÇ$
0081 C.
0082 . C. ***** °Ê²¼¼í¾Ä´¼Á°¼ÊÊ¬¼°Á÷¿® (¼ãµ-¥À¥ó¥×¼°è¼Ç¼¼ÁÖ¼µÇ¼°¼¬¼è¼í¹Ç¼Ç¼Ä) *****
0083 C. DHU¥ä;¼¥É;Ê¼ý½, ¥í;¼¥É;Ê¼òíá¼¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0087 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOAD¼¬Á÷¿®NG¼í¾í¹Ç;Ç°Ê²¼¼íTI-CMDÁ÷¿®¼í¾Á¹Ô¼•¼Ê¼µ¼³¼È;£
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0096	C.	SET EDUMP I± iYŃY¹ QÇ¹ Ô± ; ±³ ±Ē ; Ē		
0097	C.			
0098	C.	TIY³YDYOŸĒ òĀDİ; (UT)		
0099	+	TI 2025-08-16 11:48:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2025-08-16 11:48:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2025-08-16 11:48:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2025-08-16 11:52:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ē²¼± ĨĀē¾İİŃ±İŸĀŸ\$ŸĀŸ¹ àİÜ		
0116	C.	ÇÇ [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	4
0118	C.	ÇÇ [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	ÇÇ [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°ēŸĀŸóŸ×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF; \$ 1024byte)		
0126	+	DC 01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	ÇÇ [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	ÇÇ [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	ÇÇ [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	ÇÇ [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	ÇÇ [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	7
0136	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	ŸĀŸóŸ×½ªİ»±ò³İÇ\$		
0142	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL±İ¾Ē¹Ç•ē²İOK±ò³İÇ\$		
0145	C.			
0146	C.	DHUŸà;¼ŸĒ;Ē¼Ÿ½,Ÿİ;¼ŸĒ;Ē±òİá±¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	2
0150	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+	TI 2025-08-16 11:52:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+	TI 2025-08-16 11:52:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+	TI 2025-08-16 11:52:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP 'ĠĀİ±İ»ò¾Ÿ±ĒĀD±¹±ēDCBC•×²ē *****		
0181	C.	(¾Ā°İŸÓŸĀŸĒŸDŸĒŸĀŸÇŸē±Ē½¾±¼Ā»Ü±¹ē)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** ŸDŸ¹•İ Daily±İŃ±Ē'Ø±¹±ēDCBC•×²ē *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			

[illegible]

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 15s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-08-16 11:52:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÄîñî»ö¼ÝñÄðñ¹ñèDCBC•×²è *****
0156 C. (¼â°îÝÖÝÄÝÈÞÝÈÝÄÝÇÝèñÈ½¼ñ¼Ä»Üñ¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•İ Daily±;İññÈ'Øñ¹ñèDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;äLOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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main-460 2025-08-16 12:18:17 184 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŸçŸÃŸ×Ÿí;¼ŸÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÔñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,í×ÈÝñäLOSñÞñÇñÎ»Þ'Òðò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÖñ¬°ÄÄêñ•ñççé;ç°Ê²¼ñî°EÄ,¼ê½Çñðò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôñ»ñ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0045 C.
0046 . C. ;ãŸçŸóŸEŸËÄÚÂØ;ÊÄ•Ä°²óÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ã;ç°Ê²¼ñðò¼Ä¹Ôñ¹ñè;£
0055 C. ŸçŸóŸEŸËÄÚÂØñäÄ•Ä°²óÈòñ¬¼áñ¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôñ»ñ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0071 C.
0072 . C. ;ãŸçŸóŸEŸËÄÚÂØ;ÊÄ•Ä°²óÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 85 83 08 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0f 80 80 06 06)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 10 80 80 08 08)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 02)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 0e)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2025-08-16 11:52:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 C.
0170 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ëDCBC•x²è *****
0171 C. (%ã°îÿÓŸÄŸÊŸÐŸÊŸÄŸÇŸÊñÊ¼ñ¼Ä»Û¹ñê)
0172 . S. DC-BC dcbc-402:DCBC
0173 (MDP_known_event)
0174 C.
0175 C.
0176 . C. ***** ŸÐŸ¹•Ï Daily±çÍÑñÊ'Øñ¹ëDCBC•x²è *****
0177 . S. DC-BC dcbc-153:DCBC
0178 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0179 C.
0180 C.
0181 . C. ;ãLOSŸÁŸSŸÄŸ¹¼Ä»Ü;ä
0182 C.
0183 . C. ***** LOS *****
0184 C.
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*** OP Sequence for XRT ***

2025/08/16	12:02:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	12:02:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	12:02:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/16	12:03:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 fa 36 b0 9c
2025/08/16	12:03:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/16	12:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/16	12:03:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/16	12:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/16	12:03:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	12:05:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/08/16	12:05:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/16	12:08:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/16	12:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	13:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	13:24:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	13:24:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	13:24:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	13:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	13:46:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/16	13:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	15:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	15:02:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	15:02:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	15:02:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	15:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	15:25:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/16	15:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	16:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	16:40:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	16:40:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	16:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	16:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	17:14:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/16	17:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/16	18:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/08/16	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/08/16	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/16	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/16	18:02:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/08/16	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/16	18:10:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 04 e5 01 ca
2025/08/16	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			

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2025/08/16	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/16	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/16	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/16	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/16	18:12:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/08/16	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/16	18:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	18:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	18:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	18:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	18:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	18:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	18:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]			
2025/08/16	19:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	19:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	19:57:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	19:57:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	20:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	20:27:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	20:28:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]			
2025/08/16	21:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	21:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	21:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	21:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	21:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	22:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	22:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]			
2025/08/16	23:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/16	23:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	23:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/16	23:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/16	23:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/16	23:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/16	23:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]			
2025/08/17	00:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	00:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	00:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	00:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	00:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	01:13:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	01:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]			
2025/08/17	02:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	02:20:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	02:20:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	02:20:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	02:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	02:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	02:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG [0x1ae]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/08/17	03:53:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	03:53:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	03:53:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	03:53:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	03:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/17	04:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/08/17	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/17	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/17	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/17	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/17	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	04:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2025/08/17	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/17	04:29:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	04:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	05:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	05:23:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	05:23:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	05:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	05:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	06:07:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	06:07:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	06:07:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/17	06:08:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/08/17	06:08:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/17	06:08:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/17	06:10:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/08/17	06:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	06:17:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	06:17:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	06:17:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/17	06:18:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 04 e5 01 ca
2025/08/17	06:18:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/17	06:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/17	06:18:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/17	06:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/17	06:18:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	06:20:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/08/17	06:20:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/17	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	07:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	07:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	07:03:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	07:03:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2025/08/17	07:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	07:46:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	07:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	08:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	08:43:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	08:43:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	08:43:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	08:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	09:24:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	09:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	10:22:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	10:22:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	10:22:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	10:22:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	10:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	11:02:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	11:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	12:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	12:19:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	12:19:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	12:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	12:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	12:40:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	12:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	13:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	13:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	13:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	13:57:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	14:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	14:18:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	14:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	15:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	15:35:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	15:35:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	15:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	15:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	15:59:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	16:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	17:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	17:13:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	17:13:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	17:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	17:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	17:46:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	17:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	18:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	18:00:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	18:00:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/17	18:01:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/08/17	18:01:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9

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2025/08/17	18:01:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/17	18:01:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/17	18:03:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/08/17	18:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	18:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	18:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	18:10:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/17	18:11:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 04 e5 01 ca
2025/08/17	18:11:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/17	18:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/17	18:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/17	18:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/17	18:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	18:13:56.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/08/17	18:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/17	18:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	18:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	18:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	18:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	18:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	18:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	19:23:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	19:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	20:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	20:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	20:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	20:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	20:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	20:59:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	21:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	22:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	22:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	22:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	22:08:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	22:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/17	22:36:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/17	22:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/17	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	23:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/17	23:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/17	23:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/17	23:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	00:10:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/18	00:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	01:24:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	01:24:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	01:24:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	01:24:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	01:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

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2025/08/18	01:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/08/18	01:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/08/18	02:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/08/18	02:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	02:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/08/18	02:50:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/08/18	02:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/08/18	03:24:00.0	XRT_Custom_430_OG [0x1ae]					
2025/08/18	03:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/08/18	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/08/18	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	04:00:00.0	XRT_FOCUS_POSITION		4	07-F8	22 ff aa 00	
2025/08/18	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]					
2025/08/18	04:00:18.0	AOCU_NM		5	02-76	00 00 00 00 00	
2025/08/18	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]					
2025/08/18	04:00:20.0	XRT_FLD_ENA	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/08/18	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
2025/08/18	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/08/18	04:00:24.0	XRT_AEC_RESET	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/08/18	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
2025/08/18	04:00:26.0	XRT_ARS_DIS	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/08/18	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]					
2025/08/18	04:02:56.0	XRT_FLD_RESET	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/08/18	04:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]					
2025/08/18	04:02:58.0	XRT_QT_PROG_SET	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d	
2025/08/18	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]					
2025/08/18	04:03:00.0	XRT_FL_PROG_SET	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/08/18	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
2025/08/18	04:25:30.0	XRT_CTRL_AUTO	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/08/18	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]					
2025/08/18	04:25:32.0	XRT_CTRL_MANU	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	04:25:32.0	XRT_CTRL_MANU_402_OG [0x192]					
2025/08/18	04:25:34.0	XRT_CTRL_MANU	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	04:25:34.0	XRT_FLD_RESET_415_OG [0x19f]					
2025/08/18	04:25:36.0	XRT_FLD_RESET	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/08/18	04:25:36.0	XRT_PREFLR_STRT_404_OG [0x194]					
2025/08/18	04:28:44.0	XRT_PREFLR_STRT	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/08/18	04:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
2025/08/18	04:28:44.0	XRT_PREFLR_STOP	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/08/18	05:02:00.0	XRT_Custom_430_OG [0x1ae]					
2025/08/18	05:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/08/18	05:29:24.0	XRT_CTRL_AUTO	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/08/18	05:29:24.0	XRT_CTRL_MANU_402_OG [0x192]					
2025/08/18	05:29:26.0	XRT_CTRL_MANU	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	05:29:26.0	XRT_CTRL_MANU_402_OG [0x192]					
2025/08/18	05:29:28.0	XRT_CTRL_MANU	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	05:29:28.0	XRT_FOCUS_POSITION_406_OG [0x196]					
2025/08/18	05:29:48.0	XRT_FOCUS_POSITION		4	07-F8	22 ff aa 00	
2025/08/18	05:29:48.0	XRT_FLD_DIS_409_OG [0x199]					
2025/08/18	05:29:50.0	XRT_FLD_DIS	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/08/18	05:29:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]					
2025/08/18	05:29:52.0	XRT_FLRCTRL_DIS	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/08/18	05:29:52.0	XRT_ARS_DIS_435_OG [0x1b3]					
2025/08/18	05:32:28.0	XRT_ARS_DIS	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/08/18	05:32:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
2025/08/18	05:32:30.0	XRT_QT_PROG_SET	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2025/08/18	05:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
2025/08/18	05:39:24.0	XRT_CTRL_AUTO	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/08/18	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]					
2025/08/18	05:39:26.0	XRT_CTRL_MANU	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	05:39:26.0	XRT_CTRL_MANU_402_OG [0x192]					
2025/08/18	05:39:28.0	XRT_CTRL_MANU	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/08/18	05:39:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]					
2025/08/18	05:39:30.0	XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00	
2025/08/18	05:39:30.0	AOCs_Ore-point_Start_3_OG [0x099]					
2025/08/18	05:39:48.0	AOCU_NM		5	02-76	01 04 e5 01 ca	
2025/08/18	05:39:48.0	XRT_FLD_ENA_411_OG [0x19b]					
2025/08/18	05:39:50.0	XRT_FLD_ENA	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/08/18	05:39:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
2025/08/18	05:39:52.0	XRT_FLRCTRL_ENA	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/08/18	05:39:52.0	XRT_AEC_RESET_448_OG [0x1c0]					
2025/08/18	05:39:54.0	XRT_AEC_RESET	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/08/18	05:39:54.0	XRT_ARS_DIS_423_OG [0x1a7]					
2025/08/18	05:39:56.0	XRT_ARS_DIS	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/08/18	05:39:56.0	XRT_FLD_RESET_434_OG [0x1b2]					
2025/08/18	05:42:26.0	XRT_FLD_RESET	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/08/18	05:42:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
2025/08/18	05:42:28.0	XRT_QT_PROG_SET	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2025/08/18	05:42:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]					
2025/08/18	05:42:30.0	XRT_FL_PROG_SET	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/08/18	05:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]					

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2025/08/18	05:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	05:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	05:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	05:56:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	05:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	06:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	06:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	07:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	07:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	07:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	07:36:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	07:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	08:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	08:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	09:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	09:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	09:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	09:15:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	09:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	09:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	09:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	10:55:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	10:55:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	10:55:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	10:55:36.5	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	10:58:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	11:34:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	11:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	12:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	12:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	12:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	12:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	12:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	13:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	13:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	14:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	14:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	14:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	14:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	14:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	14:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	14:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	16:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	16:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	16:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	16:08:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	16:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	16:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	16:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/08/18	17:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/08/18	17:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	17:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	17:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	17:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	18:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	18:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	18:17:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	18:18:00.0	AOCs_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/18	18:18:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2025/08/18	18:18:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/08/18	18:18:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/18	18:20:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/18	18:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/08/18	18:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	18:27:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	18:27:58.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	18:28:00.5	AOCs_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_RECAL	2	07-F8	78 00
2025/08/18	18:31:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	AOCU_NM	5	02-76	01 04 e5 01 ca
2025/08/18	18:32:18.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/18	18:32:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/18	18:32:22.5	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/18	18:32:24.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/18	18:32:26.5	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/18	18:34:56.5	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	18:34:58.5	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/08/18	18:35:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/18	19:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	19:24:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	19:24:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	19:24:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	19:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	19:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	19:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/08/18	21:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	21:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	21:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	21:02:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	21:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	21:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	21:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/08/18	22:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/18	22:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	22:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/18	22:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/18	22:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/18	23:07:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/18	23:08:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/08/19	00:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2025/08/19	00:18:32.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	00:18:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/19	00:18:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/19	00:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/19	00:39:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/19	00:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/19	01:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	01:52:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	01:52:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/19	01:52:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/19	01:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/19	02:18:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/19	02:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/19	03:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	03:21:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	03:21:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/19	03:21:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/19	03:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/19	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/19	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/08/19	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/19	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/19	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/19	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/19	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/19	04:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2025/08/19	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/19	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/19	04:48:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	04:48:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	04:48:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/19	04:48:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/19	04:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/19	05:34:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/19	05:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/19	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	06:00:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	06:00:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/19	06:00:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/08/19	06:00:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/19	06:00:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/19	06:03:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/08/19	06:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/19	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	06:10:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/19	06:10:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			

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2025/08/19	06:10:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	02	04	e5	01 ca
2025/08/19	06:10:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/08/19	06:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/08/19	06:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/08/19	06:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/08/19	06:10:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/19	06:13:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2025/08/19	06:13:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/08/19	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/19	06:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	06:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	06:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/19	06:28:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/19	06:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/19	07:12:00.0	XRT_Custom_430_OG [0x1ae]							
2025/08/19	07:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/19	08:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	08:08:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	08:08:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/19	08:08:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/19	08:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/19	08:50:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/19	08:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/19	09:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	09:48:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	09:48:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/19	09:48:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/19	09:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/19	10:28:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/19	10:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/19	11:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	11:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	11:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/19	11:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/19	11:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/19	11:37:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/19	11:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/19	11:45:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/19	12:01:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00