

XRT Timeline to be uploaded on 2025/08/26

Period: 2025/08/26 10:44:00 - 2025/08/30 11:14:00

* * * * *

Normal mode

* * * * *

XOB #1B96: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)													
Term		Pointing (x, y)				Comment							
08/26 10:58:00 - 08/26 14:59:54		Fixed (-23.0, 715.4)				# OP start + 10min + HOP 485 (North 60)							
PROG= 06 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 30		1-time(s)		2.0sec									
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		10-time(s)		360.0sec									
Seqn= 8		1-time(s)		2.0sec									
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec									
Al-poly/Open		Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with													
Term		Pointing (x, y)		Comment									
08/26 15:03:00 - 08/26 17:58:24		Track (-680.6, -392.7) @ 08/26 15:00:00		AR 14196, 14197 obs.									
08/26 18:11:30 - 08/27 03:59:54		Track (-661.4, -395.2) @ 08/26 18:08:30		AR 14196, 14197 obs.									
08/27 05:38:30 - 08/27 09:59:54		Track (-586.6, -403.6) @ 08/27 05:35:30		AR 14196, 14197 obs.									
08/27 18:27:00 - 08/28 03:59:54		Track (-735.3, -54.2) @ 08/27 18:24:00		AR new E-Limb obs.									
08/28 06:10:30 - 08/28 09:44:00		Track (-659.8, -63.8) @ 08/28 06:07:30		AR new E-limb obs.									

PROG= 03 Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 92		1-time(s)		2.0sec									
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0 2.0sec
Subr= 2		4-time(s)		2.0sec									
Seqn= 47		1-time(s)		2.0sec									
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0 2.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0 2.0sec
Seqn= 77		4-time(s)		300.0sec									
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0 95.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1 95.0sec
Al-poly/Open		thin-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1CCC: Synoptic 7 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(64/1024/5795) - Thick-Be(32768), Al-poly+Ti-poly(128/2048), Med-Al(289													
Term		Pointing (x, y)		Comment									
08/26 18:01:30 - 08/26 18:08:24		Fixed (0.0, 0.0)		synoptic, shifted -1.5 min									
08/27 05:28:30 - 08/27 05:35:24		Fixed (0.0, 0.0)		HOP 349 + synoptic, shifted -34.5 min									
08/27 18:17:00 - 08/27 18:23:54		Fixed (0.0, 0.0)		synoptic, shifted 14.0 min									

PROG= 20 1-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 5		1-time(s)		2.0sec									
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0 2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0 2.0sec
Open/Ti-poly		Open/thick-Al		close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0 2.0sec
Seqn= 26		1-time(s)		2.0sec									
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 15		1-time(s)		2.0sec									
Al-poly/Open		Al-poly/Open		close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 45		1-time(s)		2.0sec									
thin-Be/Open		thin-Be/Open		close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 23		1-time(s)		4.0sec									
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0 2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 41	1-time(s)	2.0sec										
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 85	1-time(s)	2.0sec										
med-Al/Open	med-Al/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 4	1-time(s)	2.0sec										
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
08/27 04:03:00 - 08/27 05:25:24	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -34.5 min
08/28 04:03:00 - 08/28 05:57:24	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -2.5 min

PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 300.0sec												
Seqn=100	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 57	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
08/27 10:03:00 - 08/27 17:46:00	Fixed (-23.0, 856.0)	HOP 206 (North Pole)

PROG= 07 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 16	2-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90	1-time(s)	30.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 60-time(s) 60.0sec												
Seqn= 24	1-time(s)	30.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795

Term	Pointing (x, y)	Comment
08/28 06:00:30 - 08/28 06:07:24	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -2.5 min

PROG= 10 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 83 1-time(s) 2.0sec												
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 23 1-time(s) 4.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Subr= 2 1-time(s) 2.0sec												
	Seqn= 41 1-time(s) 2.0sec												
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Seqn= 17 1-time(s) 2.0sec												
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 33 1-time(s) 2.0sec												
	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 75 1-time(s) 2.0sec												
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Flare mode

* * * * *

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G														
Term		Pointing (x, y)				Comment								
08/26 10:58:00 - 08/26 14:59:54		Fixed (-23.0, 715.4)				# OP start + 10min + HOP 485 (North 60)								
08/26 15:03:00 - 08/26 17:58:24		Track (-680.6, -392.7) @ 08/26 15:00:00				AR 14196, 14197 obs.								
08/26 18:11:30 - 08/27 03:59:54		Track (-661.4, -395.2) @ 08/26 18:08:30				AR 14196, 14197 obs.								
08/27 04:03:00 - 08/27 05:25:24		Fixed (0.0, 0.0)				HOP 349 + synoptic, shifted -34.5 min								
08/27 05:38:30 - 08/27 09:59:54		Track (-586.6, -403.6) @ 08/27 05:35:30				AR 14196, 14197 obs.								
08/27 10:03:00 - 08/27 17:46:00		Fixed (-23.0, 856.0)				HOP 206 (North Pole)								
08/27 18:27:00 - 08/28 03:59:54		Track (-735.3, -54.2) @ 08/27 18:24:00				AR new E-Limb obs.								
08/28 04:03:00 - 08/28 05:57:24		Fixed (0.0, 0.0)				HOP 349 + synoptic, shifted -2.5 min								
08/28 06:10:30 - 08/28 09:44:00		Track (-659.8, -63.8) @ 08/28 06:07:30				AR new E-limb obs.								
PROG= 14 30-time(s)														
Subr= 1		1-time(s)		2.0sec										
└ Seqn= 68		1-time(s)		2.0sec										
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		20-time(s)		2.0sec										
└ Seqn= 11		1-time(s)		2.0sec										
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└ Seqn= 73		1-time(s)		10.0sec										
		thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3		1-time(s)		2.0sec										
└ Seqn= 10		1-time(s)		2.0sec										
		med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└ Seqn= 11		1-time(s)		2.0sec										
		Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└ Seqn= 87		1-time(s)		2.0sec										
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

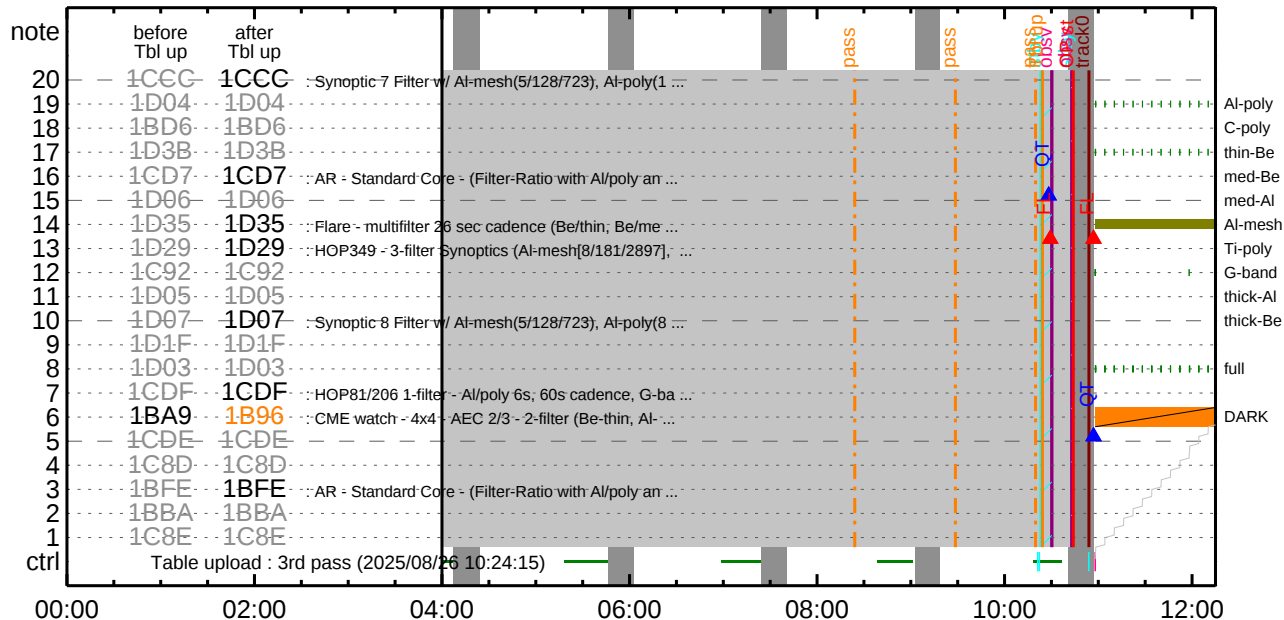
* * * * *

Flare Detection

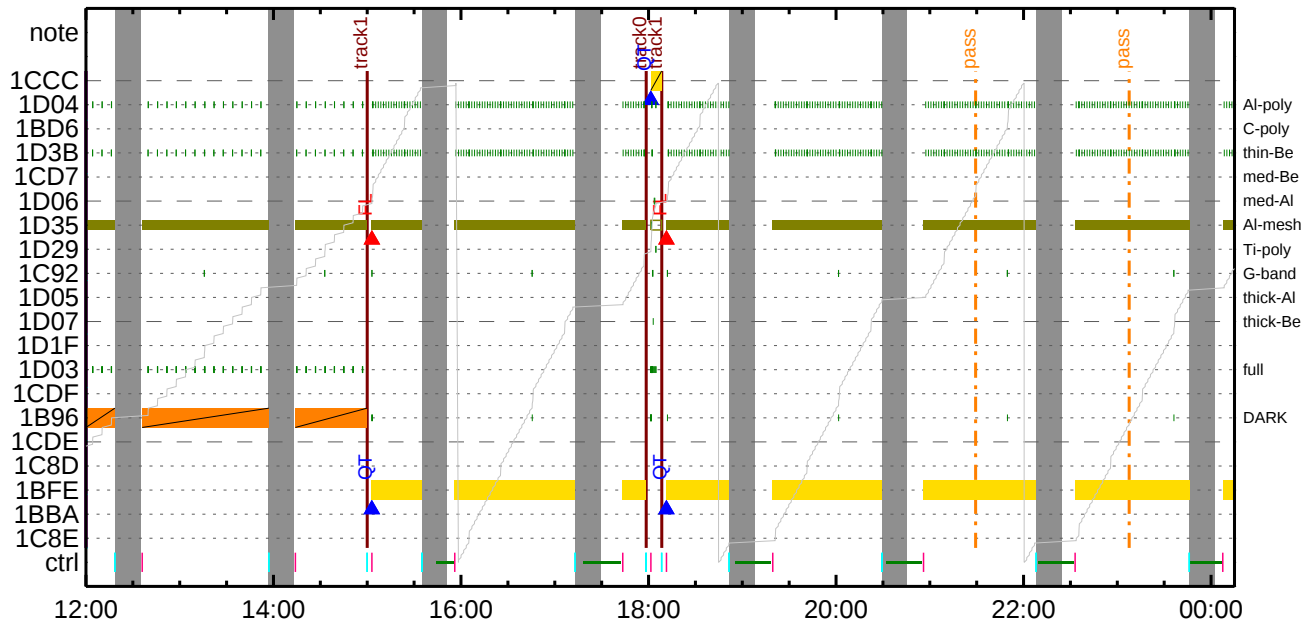
* * * * *

FLD Patrol													
Term		Pointing (x, y)		Comment									
08/26 10:25:15 - 08/26 17:58:48		cannot be identified											
08/26 18:08:48 - 08/27 05:25:48		Track (-661.4, -395.2) @ 08/26 18:08:30		AR 14196, 14197 obs.									
08/27 05:35:48 - 08/27 18:14:18		Track (-586.6, -403.6) @ 08/27 05:35:30		AR 14196, 14197 obs.									
08/27 18:24:18 - 08/28 05:57:48		Track (-735.3, -54.2) @ 08/27 18:24:00		AR new E-Limb obs.									
08/28 06:07:48 - 08/30 11:14:00		Track (-659.8, -63.8) @ 08/28 06:07:30		AR new E-limb obs.									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

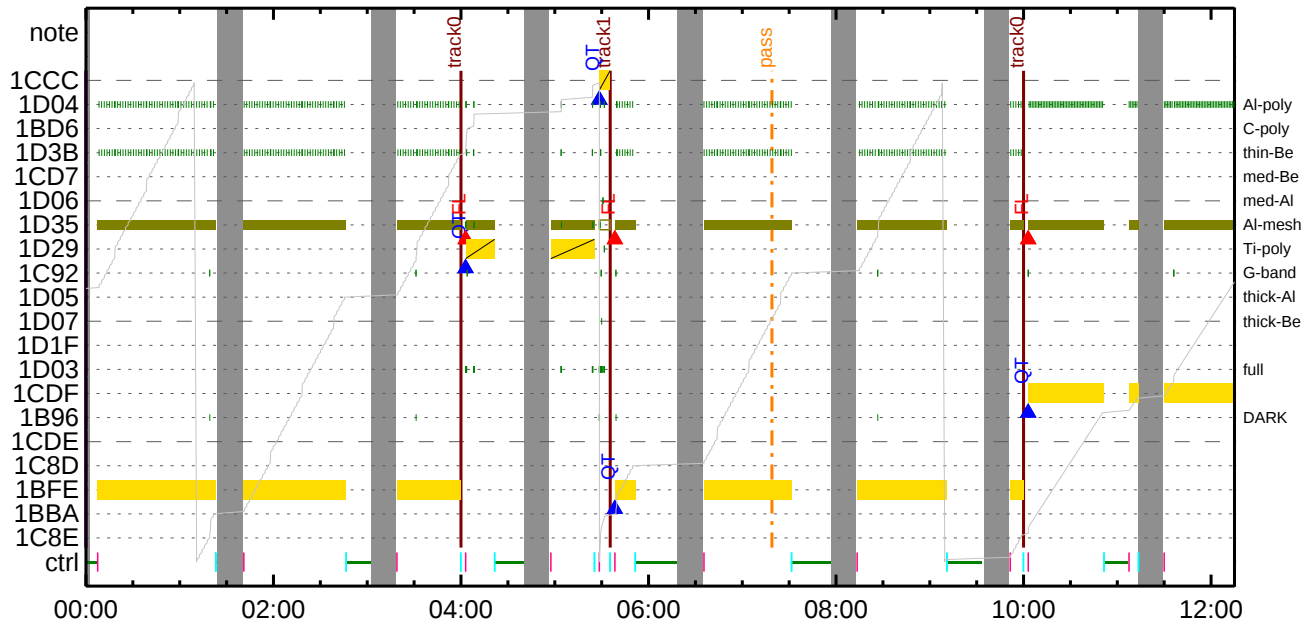
CMDI #0804 2025/08/26



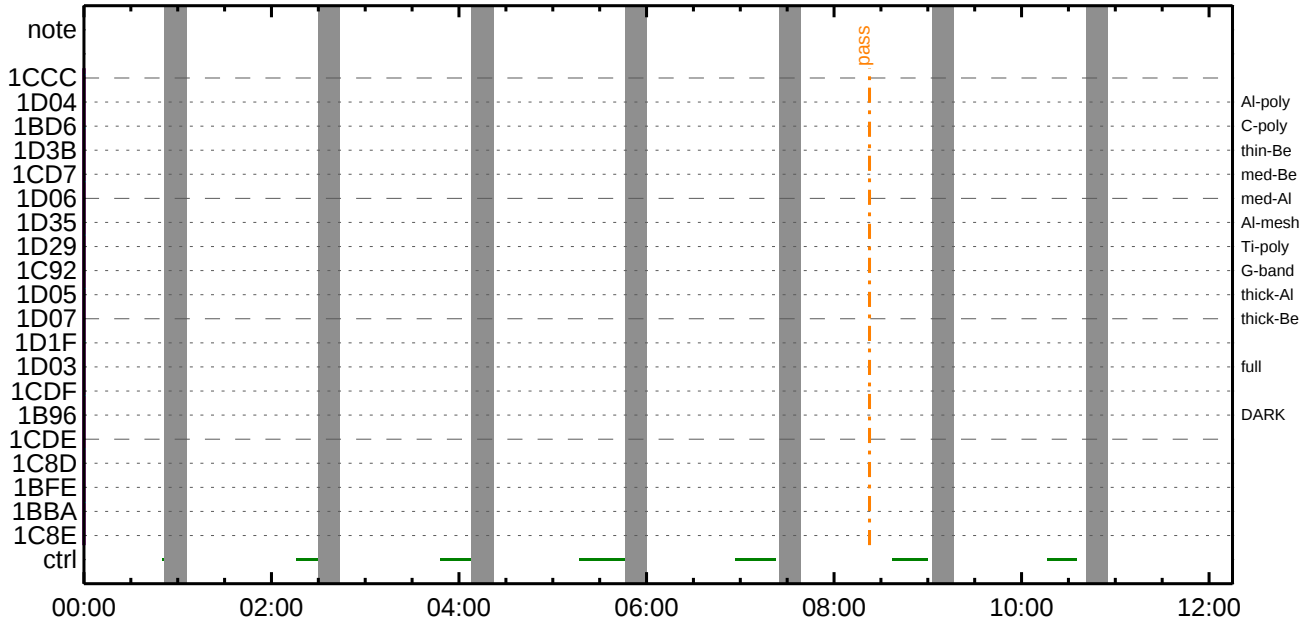
CMDI #0804 2025/08/26



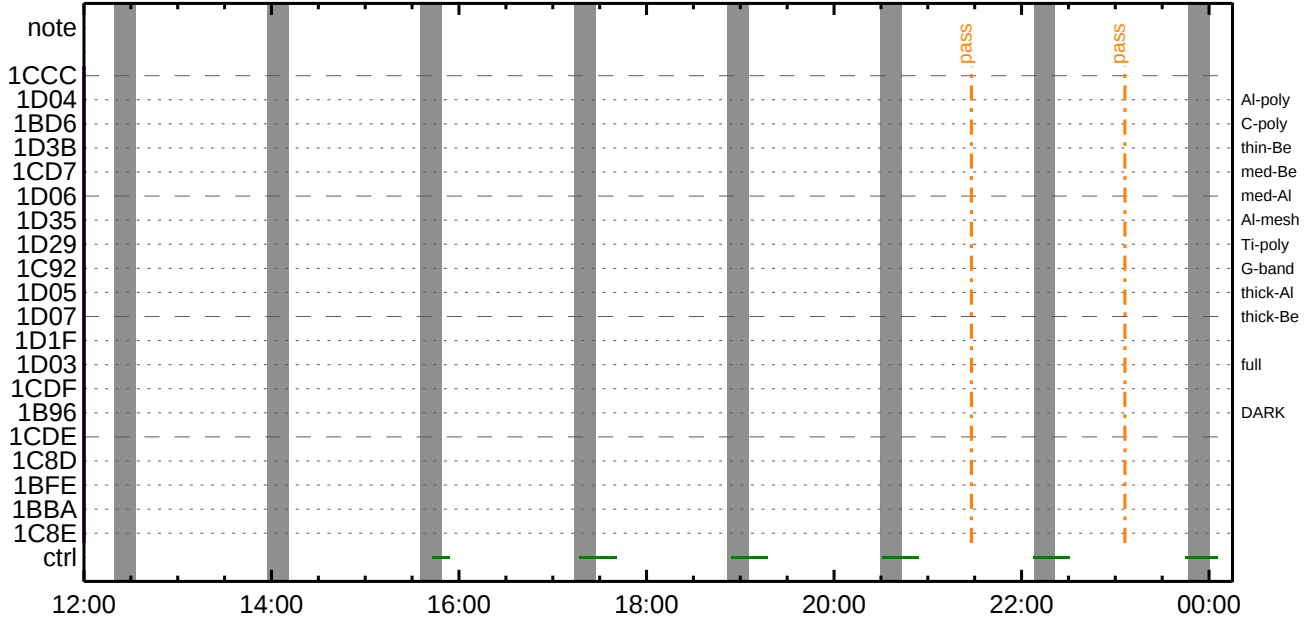
CMDI #0804 2025/08/27



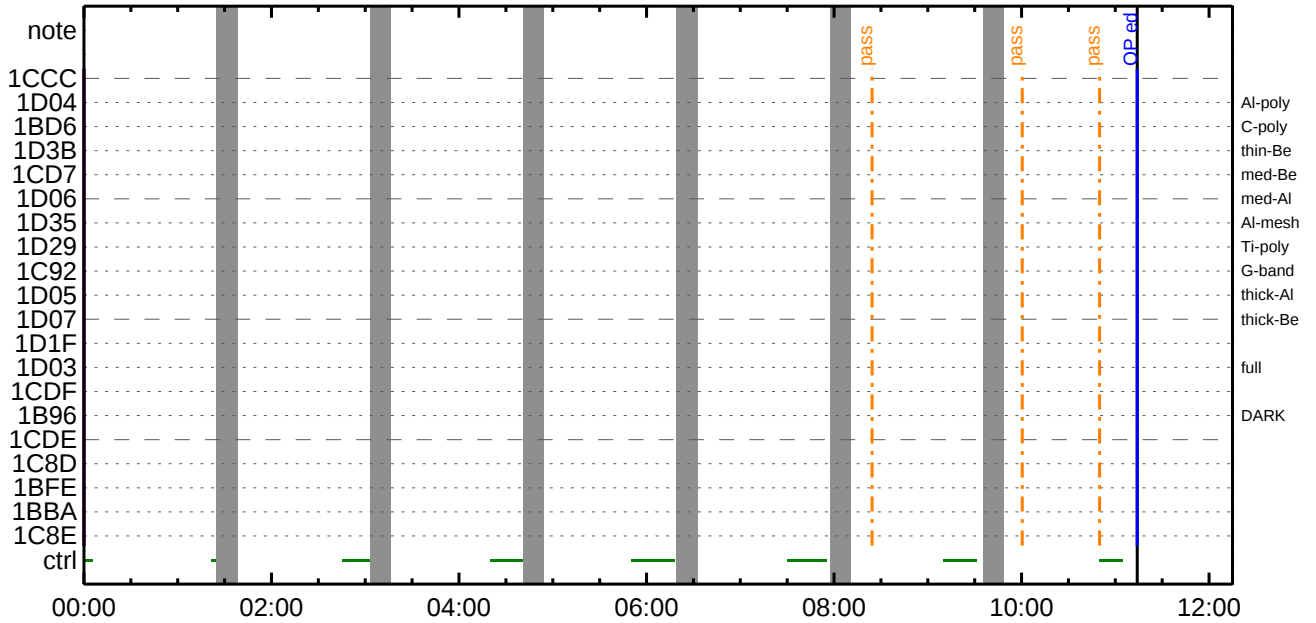
CMDI #0804 2025/08/29

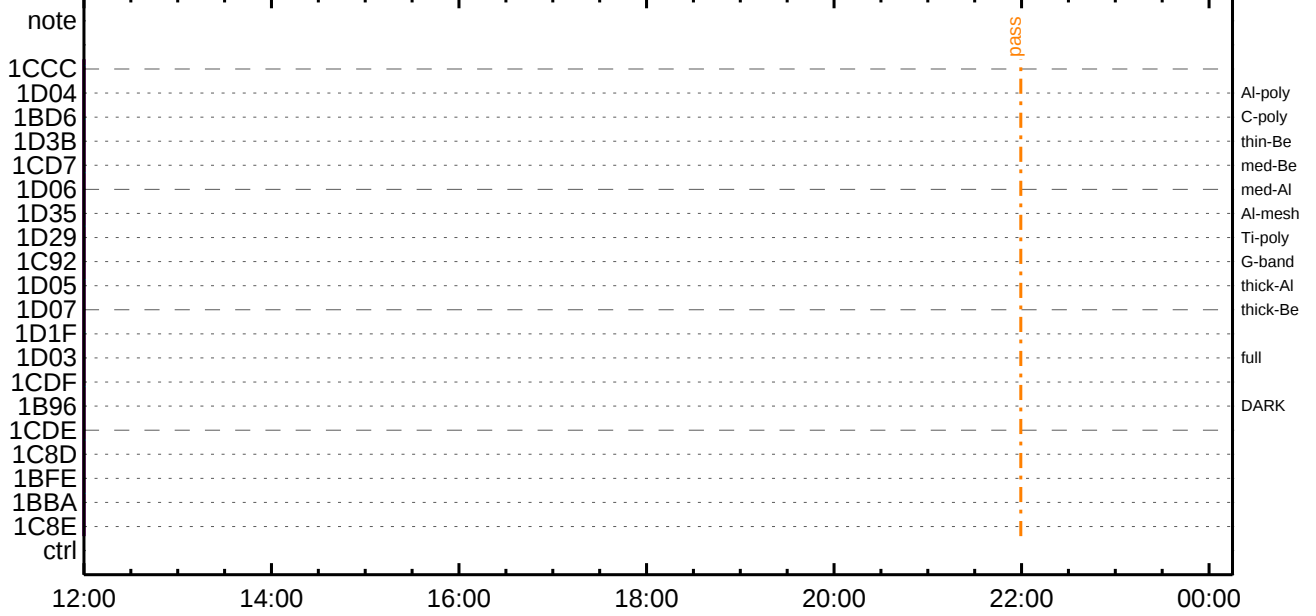


CMDI #0804 2025/08/29



CMDI #0804 2025/08/30





```

main-477 2025-08-26 13:43:28 194 333 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ã»Ü;ä
0005 C.
0006 C. ŲÃŲß;¼Ų³ŲÐŲóŲÉÃ÷:ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āī;ĒñçĀñ•µ°E»İ×AÇñİŲçŲÃŲ×Ųī;¼ŲÉ;ĒĒèµ•īİĒ;ĒñĒ¼°ÇÖñ•ñç¼ī¹çñİ;çÀ®,ùñ¹ñĒñÐñÇÃ÷:ç®ñ•ñĒñññ³ñĒ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŲī;¼ŲÉ;|ŲÃŲóŲ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŲī;¼ŲÉ;ä
0019 . S. OP op-477:OP
0020 ()
0021 . S. OG og-477:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPİİ°èŲÃŲóŲ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŲÃŲóŲ×½ªİ»ñò³İÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñİ¼Ē¹ç•Ē²İOKñò³İÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŲÃŲóŲ×½ªİ»ñò³İÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñİ¼Ē¹ç•Ē²İOKñò³İÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŲÃŲóŲ×½ªİ»ñò³İÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPñİ¼Ē¹ç•Ē²İOKñò³İÇ$
0081 C.
0082 . C. ***** °Ē²¼ñİ¼Ã`¶Ā°ñĒĒ¬ñ°Ã÷:ç® (¼µ-ŲÃŲóŲ×¼Ē½çñòĀŌĀñÇ½ñ•ñĒ¼ī¹çñÇñâ) *****
0083 C. DHUŲâ;¼ŲÉ;Ē¼Ų½,Ųī;¼ŲÉ;Ēñòİāñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Ã÷:ç®NGñİ¼ī¹ç;ç°Ē²¼ñİTI-CMDÃ÷:ç®ñİ¼Ā¹Ōñ•ñĒñññ³ñĒ;£

```

0096	C.	*****		
0097	C.	*****		
0098	C.	*****		
0099	+. TI	2025-08-26 10:39:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.	*****		
0103	+. TI	2025-08-26 10:39:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.	*****		
0107	+. TI	2025-08-26 10:39:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.	*****		
0111	+. TI	2025-08-26 10:43:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.	*****		
0115	C.	*****		
0116	C.	[] [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	[] [HK1_TI_CMD_NUM]	EQ	4
0118	C.	[] [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	[] [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.	*****		
0121	C.	*****		
0122	C.	*****		
0123	C.	*****		
0124	C.	*****		
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	[] [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	[] [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	[] [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	[] [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	[] [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+. DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	[] [HK1_PKT_FORM_NO]	EQ	7
0136	C.	[] [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	[] [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	[] [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	[] [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.	*****		
0141	C.	*****		
0142	C.	[] [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.	*****		
0144	C.	*****		
0145	C.	*****		
0146	C.	*****		
0147	+. DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	[] [HK1_PKT_FORM_NO]	EQ	2
0150	C.	[] [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	[] [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	[] [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.	*****		
0154	C.	*****		
0155	C.	*****		
0156	C.	*****		
0157	+. TI	2025-08-26 10:43:00.0		
0158	DC	07-F0 MDP_XRT_MODE_STBY		
0159	BC	(c3)		
0160	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0161	C.	*****		
0162	C.	*****		
0163	C.	*****		
0164	C.	*****		
0165	C.	*****		
0166	C.	*****		
0167	C.	*****		
0168	C.	*****		
0169	+. TI	2025-08-26 10:43:30.0		
0170	DC	07-FC EIS_MODE_MANU		
0171	BC	(21 02)		
0172	+. TI	2025-08-26 10:43:40.0		
0173	DC	07-FC EIS_MODE_CHG_DIS		
0174	BC	(22)		
0175	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0176	C.	*****		
0177	C.	*****		
0178	C.	*****		
0179	C.	*****		
0180	C.	*****		
0181	C.	*****		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.	*****		
0185	C.	*****		
0186	C.	*****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.	*****		
0190	C.	*****		
0191	C.	*****		
0192	C.	*****		
0193	C.	*****		


```
main-478      2025-08-26 13:43:28 94 33 SOLAR-B MAIN //
0001        C.
0002    .   C. ***** AOS *****
0003        C.
0004    .   C. ;ãAOSŲÁŷ$ŲÃŶ~¼Ä»Ü;ä
0005        C.
0006    .   C. ŲÀŲß;¼Ų³ŲÐŲóŲÉĂ÷¿®
0007 +     DC 00-00 NULL_DUMMY_CMD
0008        C.
0009    .   C. ***** AOCS : Reload orbital element (send every contact) *****
0010    .   C. Āí;Ē¤¿¤Å¤•µ°E¤İ×Ȧ¤ıȤ¥Ȣ¥×ŵı;¼ŴE;ĔĖè½µ•īĬĚ;Ĕ¤Ĕ¼°CÔ¤•¤¿¼ı¹Ç¤İ;çÀ®,ù¤¹¤ē¤Đ¤ČĄ÷¿®¤•¤ĕ¤¤¤¤³¤Ĕ;£
0011 +     DC 02-8E AOCU_ORB_UPD
0012        C.
0013        C.
0014    .   C. ***** AOCS Commands (Tracking Curve Upload) *****
0015    .   C. Upload the Orbit Element and the Target Attitude
0016    .   C. RAM-ID:TARGET_ATT
0017    .   S. RAM ram-150:TARGET_ATT
0018        ()
0019        C.
0020        C.
0021    .   C. Set the dump memory area of TARGET_ATT
0022 +     DC 02-48 AOCU_DUMP_SET
0023        BC          (07 00 00 00 18 00)
0024        C.
0025    .       <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026        C.
0027        C.
0028    .   C. Change the TLMFormatNo for the AOCS Dump Format
0029 +     DC 01-22 DHU_MODE_CHNG
0030        BC          (04 0b f8)
0031        C.
0032    .   C. Wait for AOCS DUMP to end
0033        C.
0034    .   C. Check the dump memory
0035        C.
0036    .                               Result = OK [ ]
0037        C.
0038 +     DC 01-22 DHU_MODE_CHNG
0039        BC          (02 0a f8)
0040        C.
0041    .                                <A_***>[TLM STS] FMT = 2 [ ]
0042        C.
0043 +     DC 02-8E AOCU_ORB_UPD
0044    .   C.
0045    .   C. ***** AOCS Commands (Orbital Element Update) *****
0046    .   C. Update the orbital element
0047 +     DC 02-50 AOCU_ORB_PRPGT_START
0048        BC          (16)
0049 +     DC 02-8E AOCU_ORB_UPD
0050        C.
0051    .           <A_ORB>[ORBIT] EPC = 1286673.1 +- 1.0 (s) [ ]
0052        C.
0053    .   C.
0054    .   C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +     DC 07-FC EIS_MODE_CHG_ENA
0056        BC          (20)
0057    .   C. Verify EIS_MODE_CHG_FLG is ENA
0058 +     DC 07-FC EIS_MODE_MANU
0059        BC          (21 02)
0060    .   C. Verify EIS in MANUAL mode
0061    .   C. Estimated OBSTBL upload time is 12s
0062    .   C. *****
0063    .   C. EIS START OBSTBL LOAD
0064    .   C. *****
0065    .   S. RAM ram-820:EIS_OBSTBL
0066        ()
0067 +     DC 07-FC EIS_DUMP_OBSTBL
0068        BC          (07 07 07 00 00 70 00)
0069        C.
0070    .   C. Execute, after the success of OBSTBL upload.
0071    .   C. Set EIS TI-commands
0072 +     TI 2025-08-26 10:43:50.0
0073    .   DC 07-FC EIS_MODE_CHG_ENA
0074        BC          (20)
0075    .   C.                  [ ] [HK1_TI_CMD_NUM]            EQ         1 COUNTUP
0076    .   C. *****
0077    .   C. EIS END OBSTBL LOAD
0078    .   C. *****
0079        C.
0080    .   C. ***** MDP ‘ûĀÎ¤İ›ô¾Ÿ¤ĔĄ¤¤¹¤ēDCBC•x²ë *****
0081    .   C. (%â°îŴÖŲĀŲĔŲPŴĔŲāŲčŲé¤Ĕ¾¤¤¼Ä»Ü¤¹¤ē)
0082    .   S. DC-BC dcbc-402:DCBC
0083        (MDP_known_event)
0084        C.
0085        C.
0086    .   C. ***** ŲᎠŴ!•İ Daily±¿ÍÑ¤Ĕ’Ø¤¹¤ēDCBC•x²ë *****
0087    .   S. DC-BC dcbc-153:DCBC
0088        (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089        C.
0090        C.
0091    .   C. ;ãLOSŲÁŷ$ŲÃŶ~¼Ä»Ü;ä
0092        C.
0093    .   C. ***** LOS *****
0094        C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-479 2025-08-26 13:43:28 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë»¿»Ä»•µ°»»Í×ÄÇ»ÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèµ•ííË;Ë»Ë¼°ÇÖ»•»¿¼¹Ç»Í;çÄ®,ù»¹»è»»»»ÇÄ÷¿®»•»Ë»»»»»»»Ë;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 80 80 08 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 20 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 20)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b 85 83 08 08)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 +. DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 +. DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 +. DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 10)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 0e)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2025-08-26 10:43:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP 'ûÄî»Í»ö¼Ÿ»ËÄ»¹»èDCBC•x²è *****
0089 C. (¾ä°íŸóŸÄŸËŸÐŸËŸÄŸçŸË»Ë¼»»¼Ä»Ü»¹»è)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** ŸÐŸ¹•í Daily÷¿íÑ»Ë'Ø»¹»èDCBC•x²è *****
0095 . S. DC-BC dcbc-153:DCBC
```

0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÃŸ-¼Â»Ü;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 1/8

*** OP Sequence for XRT ***

2025/08/26	10:53:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	10:53:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/26	10:54:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 c0 6b 02 0c
2025/08/26	10:54:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/26	10:54:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/26	10:54:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/26	10:54:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/26	10:54:24.0	XRT_FLD_RESET_446_OG [0x1be]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	10:56:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/08/26	10:56:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/26	10:57:00.5	XRT_Custom_430_OG [0x1ae]			
2025/08/26	10:58:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	12:18:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	12:18:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	12:18:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	12:18:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	12:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	12:35:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/26	12:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	13:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	13:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	13:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	13:57:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	14:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	14:13:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/26	14:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	14:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	14:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/26	15:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 03 e7 02 0c
2025/08/26	15:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/26	15:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/26	15:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/26	15:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/26	15:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	15:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/08/26	15:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/26	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	15:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	15:35:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	15:35:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	15:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	15:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	15:55:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/26	15:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	17:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	17:13:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	17:13:04.0	XRT_FLD_RESET_415_OG [0x19f]			

Tuesday August 26, 2025

1/8

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 2/8

2025/08/26	17:13:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	17:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	17:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	17:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	17:58:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	17:58:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	17:58:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/26	17:58:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2025/08/26	17:58:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/08/26	17:58:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/26	17:58:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/26	18:01:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2025/08/26	18:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	18:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	18:08:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	18:08:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/26	18:08:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 03 e7 02 0c
2025/08/26	18:08:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/26	18:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/26	18:08:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/26	18:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/26	18:08:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	18:11:26.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/08/26	18:11:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/26	18:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	18:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	18:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	18:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	18:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	18:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	19:18:30.0	XRT_Custom_430_OG [0x1ae]				
2025/08/26	19:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	20:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	20:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	20:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	20:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	20:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	20:55:00.0	XRT_Custom_430_OG [0x1ae]				
2025/08/26	20:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	22:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	22:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	22:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/26	22:08:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/26	22:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/26	22:32:00.0	XRT_Custom_430_OG [0x1ae]				
2025/08/26	22:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/26	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/26	23:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 3/8

2025/08/26	23:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/08/26	23:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/08/26	23:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/08/27	00:06:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	00:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/08/27	01:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	01:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	01:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/08/27	01:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/08/27	01:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/08/27	01:40:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	01:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/08/27	02:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	02:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	02:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/08/27	02:46:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/08/27	02:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/08/27	03:18:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	03:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/08/27	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22 ff aa 00
		XRT_FOCUS_POSITION			
2025/08/27	04:00:00.0	AOCs_Ore-point_Start_3_OG [0x099]	5	02-76	00 00 00 00 00
		AOCU_NM			
2025/08/27	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8
		MDP_XRT_FLD_ENA			
2025/08/27	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8
		MDP_XRT_FLRCTRL_ENA			
2025/08/27	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0
		MDP_XRT_AEC_RESET			
2025/08/27	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
		MDP_XRT_ARS_DIS			
2025/08/27	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/08/27	04:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	2	07-F0	c4 0d
		MDP_XRT_QT_PROG_SET			
2025/08/27	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0	c5 0e
		MDP_XRT_FL_PROG_SET			
2025/08/27	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/08/27	04:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	04:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	04:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
		MDP_XRT_FLD_RESET			
2025/08/27	04:21:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			
2025/08/27	04:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
		MDP_XRT_PREFLR_STOP			
2025/08/27	04:56:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	04:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/08/27	05:25:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	05:25:26.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	05:25:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22 ff aa 00
		XRT_FOCUS_POSITION			
2025/08/27	05:25:48.0	XRT_FLD_DIS_409_OG [0x199]	1	07-F0	d9
		MDP_XRT_FLD_DIS			
2025/08/27	05:25:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	1	07-F0	c9
		MDP_XRT_FLRCTRL_DIS			
2025/08/27	05:25:52.0	XRT_ARS_DIS_435_OG [0x1b3]	1	07-F0	d5
		MDP_XRT_ARS_DIS			
2025/08/27	05:28:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	2	07-F0	c4 14
		MDP_XRT_QT_PROG_SET			
2025/08/27	05:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
		MDP_XRT_CTRL_AUTO			
2025/08/27	05:35:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	05:35:26.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
		MDP_XRT_CTRL_MANU			
2025/08/27	05:35:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 4/8

2025/08/27	05:35:30.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	01	03	e7	02 0c
2025/08/27	05:35:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/08/27	05:35:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/08/27	05:35:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/08/27	05:35:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/08/27	05:35:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/27	05:38:26.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2025/08/27	05:38:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/08/27	05:38:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/27	05:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	05:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	05:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/27	05:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/27	05:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/27	06:34:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/27	06:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/27	07:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	07:31:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	07:31:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/27	07:31:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/27	07:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/27	08:12:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/27	08:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/27	09:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	09:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	09:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/27	09:11:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/27	09:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/27	09:50:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/27	09:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/27	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	09:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/08/27	10:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	b3	e8	02 0c
2025/08/27	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/08/27	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/08/27	10:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/08/27	10:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/08/27	10:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/27	10:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2025/08/27	10:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/08/27	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/08/27	10:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	10:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/08/27	10:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/08/27	10:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/08/27	10:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/08/27	11:06:30.0	XRT_Custom_430_OG [0x1ae]							

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 5/8

2025/08/27	11:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	11:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	11:13:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	11:13:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	11:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	11:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	11:29:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	11:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	12:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	12:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	12:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	12:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	12:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	13:07:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	13:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	14:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	14:29:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	14:29:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	14:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	14:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	14:45:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	14:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	16:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	16:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	16:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	16:08:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	16:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	16:28:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	16:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	16:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	16:34:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	16:34:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	16:34:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	16:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	16:37:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	16:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	17:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	17:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	17:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	17:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	17:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	18:13:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	18:13:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	18:13:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/08/27	18:14:00.0	AOCs_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/08/27	18:14:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/08/27	18:14:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/08/27	18:14:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/27	18:16:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14

Tuesday August 26, 2025

5/8

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 6/8

2025/08/27	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	18:23:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	18:23:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	18:23:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/08/27	18:24:00.0	AOCs_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	02 03 e7 02 0c
2025/08/27	18:24:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/08/27	18:24:20.5	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/08/27	18:24:22.5	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/08/27	18:24:24.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/08/27	18:24:26.5	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	18:26:56.5	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/08/27	18:26:58.5	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/08/27	18:27:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	19:24:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	19:24:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	19:24:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	19:24:06.5	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	19:27:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	19:50:30.5	XRT_Custom_430_OG [0x1ae]			
2025/08/27	19:51:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	21:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	21:02:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	21:02:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	21:02:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	21:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	21:27:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	21:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/27	22:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	22:40:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/27	22:40:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/27	22:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/27	22:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/27	23:03:30.0	XRT_Custom_430_OG [0x1ae]			
2025/08/27	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/28	00:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	00:18:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	00:18:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/28	00:18:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/28	00:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/28	00:34:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/28	00:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/28	01:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	01:50:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	01:50:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/28	01:50:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/28	01:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/28	02:12:00.0	XRT_Custom_430_OG [0x1ae]			
2025/08/28	02:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/28	03:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 7/8

2025/08/28	03:17:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	03:17:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/08/28	03:17:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/08/28	03:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/08/28	03:50:00.0	XRT_Custom_430_OG [0x1ae]							
2025/08/28	03:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/08/28	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/08/28	04:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/08/28	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/08/28	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/08/28	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/08/28	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/08/28	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/08/28	04:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2025/08/28	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/08/28	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/08/28	04:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	04:44:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	04:44:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/08/28	04:44:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/08/28	04:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/08/28	05:28:30.0	XRT_Custom_430_OG [0x1ae]							
2025/08/28	05:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/08/28	05:57:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	05:57:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	05:57:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/08/28	05:57:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/08/28	05:57:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/08/28	05:57:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/08/28	06:00:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2025/08/28	06:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/08/28	06:07:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	06:07:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	06:07:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/08/28	06:07:30.0	AOCs_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02 03 e7 02 0c				
2025/08/28	06:07:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/08/28	06:07:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/08/28	06:07:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/08/28	06:07:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/08/28	06:07:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/08/28	06:10:26.0	XRT_QT_PROG_SET_403_OG [0x193]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2025/08/28	06:10:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/08/28	06:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/08/28	06:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	06:24:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/08/28	06:24:34.0	XRT_FLD_RESET_415_OG [0x19f]							

Tuesday August 26, 2025

7/8

Aug 26, 25 13:43

XRT_OGLIST_0804.chk

Page 8/8

2025/08/28	06:24:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/28	06:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/28	07:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/28	07:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/28	08:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	08:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	08:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/28	08:04:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/28	08:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/28	08:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/08/28	08:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/08/28	09:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	09:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	09:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/28	09:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/28	09:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/28	10:08:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	10:08:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/08/28	10:08:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/08/28	10:08:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/08/28	10:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/08/28	11:25:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00