XRT Timeline to be uploaded on 2025/08/30

Period: 2025/08/30 11:14:00 - 2025/09/04 10:39:00

✲ ✲ ✲ ✲ ✲ **Normal mode** ✲ ✲ ✲ ✲ ✲

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
08/30 11:27:00 - 08/30 17:46:30	Fixed (-23.0, 859.0)	# OP start + 10min +HOP 206

PROG= 07 Inf.-time(s)

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 16 2-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 90 1-time(s) 30.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

- **Subr= 3 60-time(s) 60.0sec**
  - **Seqn= 24 1-time(s) 30.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
08/30 18:15:30 - 08/30 18:22:24	Fixed (0.0, 0.0)	synoptic, shifted 12.5 min
08/31 05:59:00 - 08/31 06:16:00	Fixed (0.0, 0.0)	HOP 349 + synoptic

PROG= 08 1-time(s)

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 5 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

  - **Seqn= 55 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 98 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 76 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 23 1-time(s) 4.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 41 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

  - **Seqn= 18 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 86 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 54 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

XOB #1CA6: AR (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 72s

Term	Pointing (x, y)	Comment
08/30 18:25:30 - 08/31 03:59:54	Track (109.0, -425.3) @ 08/30 18:22:30	AR14197 obs.

PROG= 15 Inf.-time(s)

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 92 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

  - **Seqn= 71 3-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec

- **Subr= 2 75-time(s) 72.0sec**
  - **Seqn= 94 1-time(s) 34.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 58 1-time(s) 34.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Seqn= 48 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wat

Term	Pointing (x, y)	Comment
08/31 04:03:00 - 08/31 05:55:54	Fixed (0.0, 0.0)	HOP 349 + synoptic

PROG= 11 Inf.-time(s)												
Subr= 1 1-time(s) 600.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 7-time(s) 600.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ **Flare mode** ✲ ✲ ✲ ✲ ✲

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
08/30 11:27:00 - 08/30 17:46:30	Fixed (-23.0, 859.0)	# OP start + 10min +HOP 206
08/30 18:25:30 - 08/31 03:59:54	Track (109.0, -425.3) @ 08/30 18:22:30	AR14197 obs.
08/31 04:03:00 - 08/31 05:55:54	Fixed (0.0, 0.0)	HOP 349 + synoptic

PROG= 14 30-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 68 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ **Active Region Search** ✲ ✲ ✲ ✲ ✲

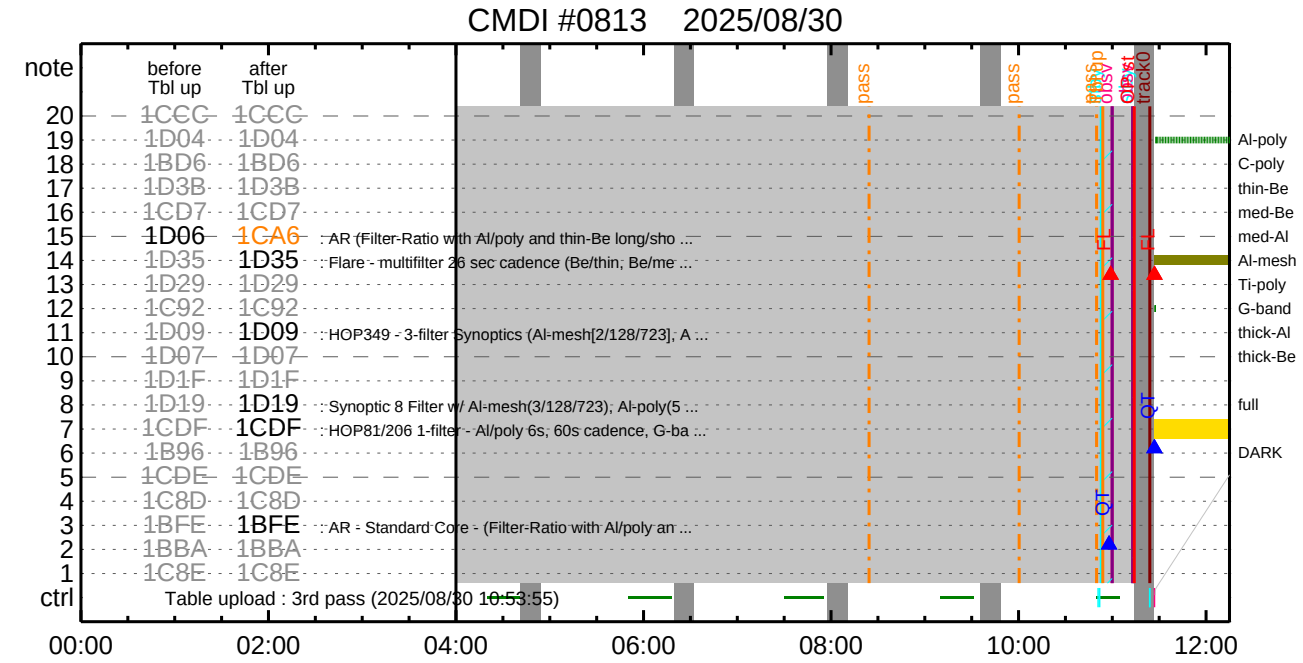
NOT USED

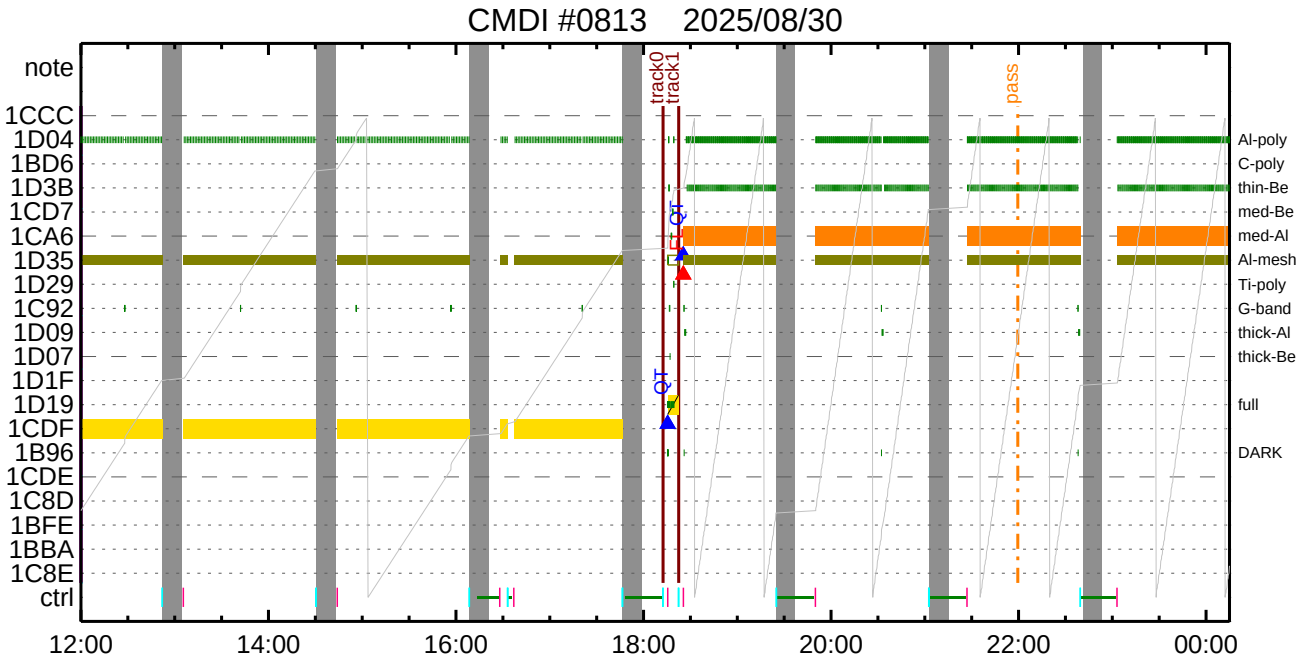
✲ ✲ ✲ ✲ ✲ **Flare Detection** ✲ ✲ ✲ ✲ ✲

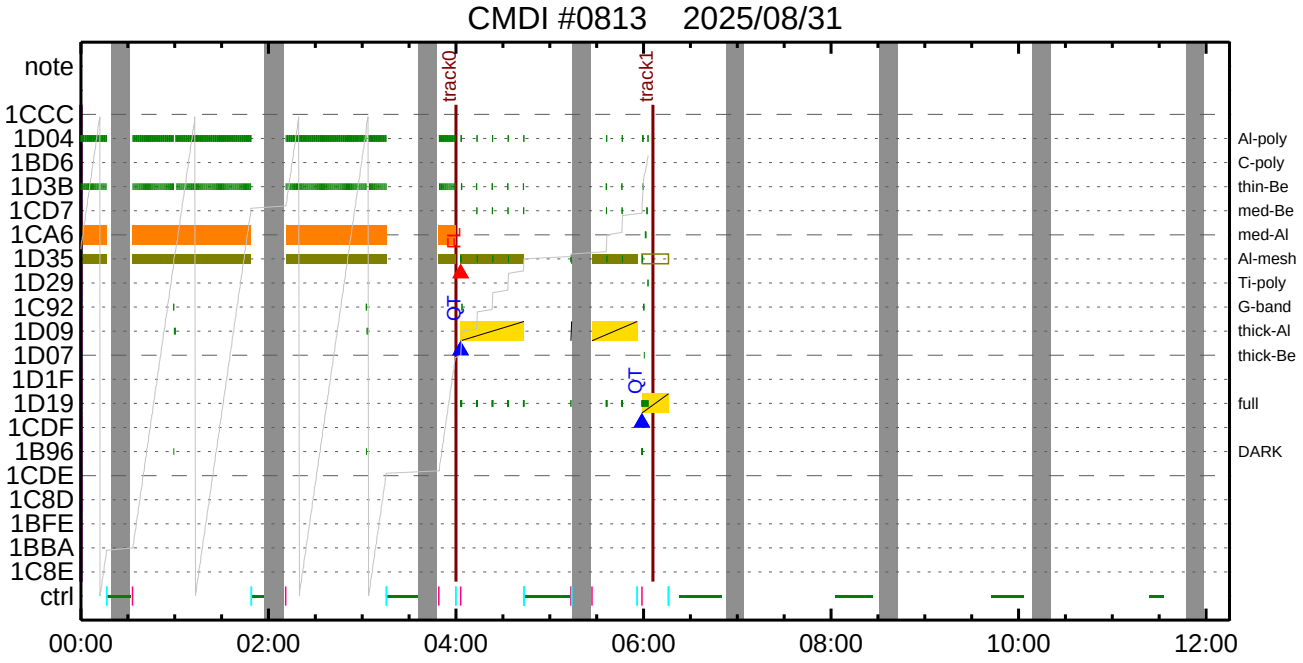
FLD Patrol

Term	Pointing (x, y)	Comment
08/30 10:54:55 - 08/30 18:12:48	cannot be identified	
08/30 18:22:48 - 08/31 05:56:18	Track (109.0, -425.3) @ 08/30 18:22:30	AR14197 obs.

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval


CMDI #0813 2025/08/30
note
before Tbl up
after Tbl up
20 1CCC 1CCC
19 1D04 1D04
18 1BD6 1BD6
17 1D3B 1D3B
16 1CD7 1CD7
15 1D06 1CA6 : AR (Filter-Ratio with Al/poly and thin-Be long/sho ...
14 1D35 1D35 : Flare - multifilter 26 sec cadence (Be/thin; Be/me ...
13 1D29 1D29
12 1C92 1C92
11 1D09 1D09 : HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], A ...
10 1D07 1D07
9 1D1F 1D1F
8 1D19 1D19 : Synoptic 8 Filter w/ Al-mesh(3/128/723); Al-poly(5 ...
7 1CDF 1CDF : HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-ba ...
6 1B96 1B96
5 1CDE 1CDE
4 1C8D 1C8D
3 1BFE 1BFE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
2 1BBA 1BBA
1 1C8E 1C8E
ctrl
Table upload : 3rd pass (2025/08/30 10:53:55)
pass
pass
track0
track1
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0813 2025/08/30
note
1CCC
1D04
1BD6
1D3B
1CD7
1CA6
1D35
1D29
1C92
1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
track0
track1
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0813 2025/08/31
note
1CCC
1D04
1BD6
1D3B
1CD7
1CA6
1D35
1D29
1C92
1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
track0
track1
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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full
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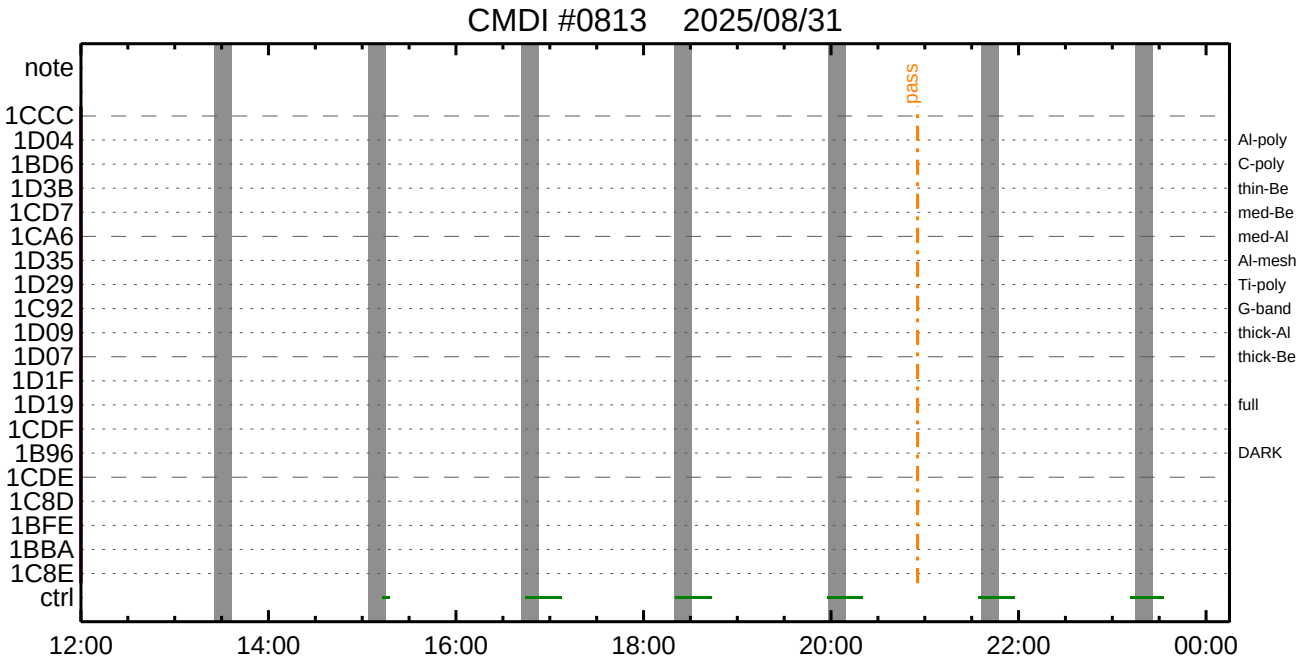
08:00
10:00
12:00


CMDI #0813 2025/08/31
note
1CCC
1D04
1BD6
1D3B
1CD7
1CA6
1D35
1D29
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1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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16:00
18:00
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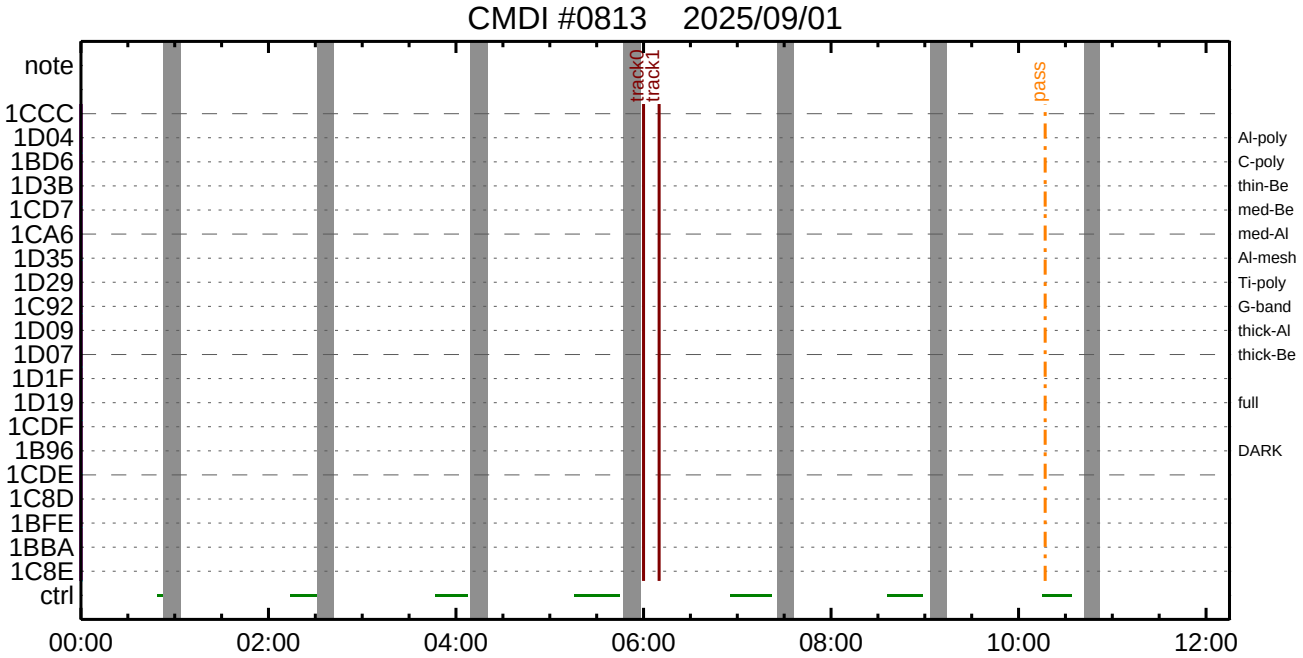
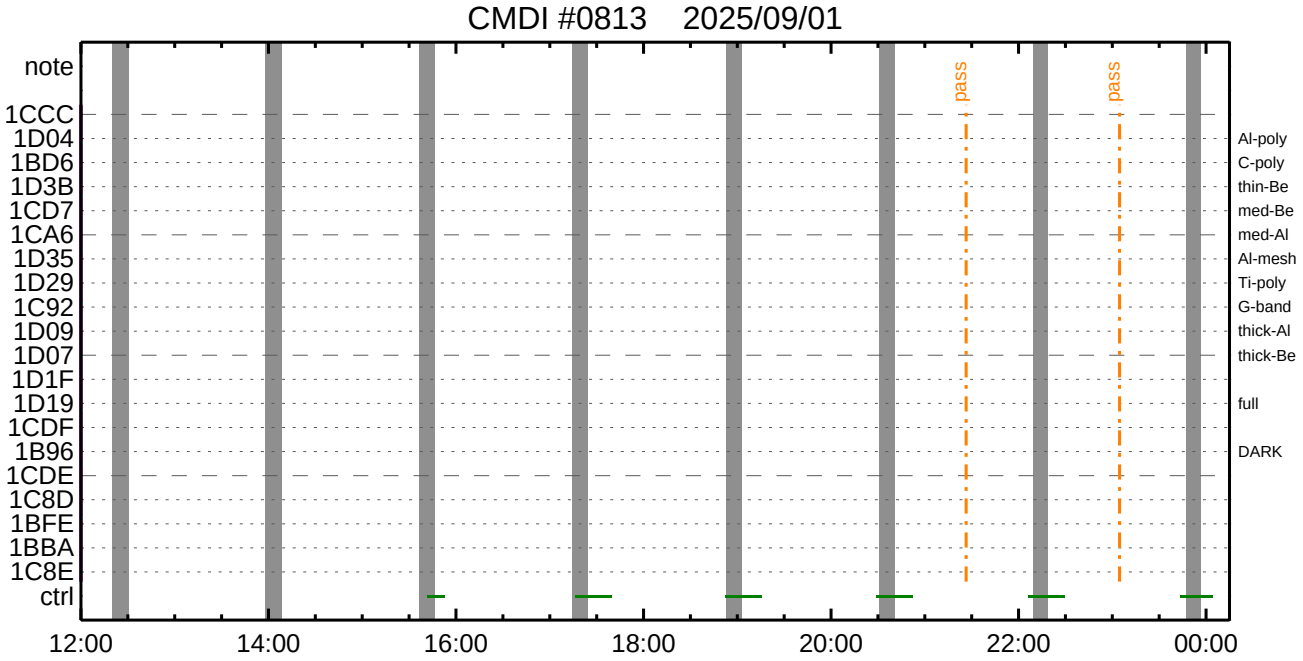
22:00
00:00


CMDI #0813 2025/09/01
note
1CCC
1D04
1BD6
1D3B
1CD7
1CA6
1D35
1D29
1C92
1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
track0
track1
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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note
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1CD7
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1D29
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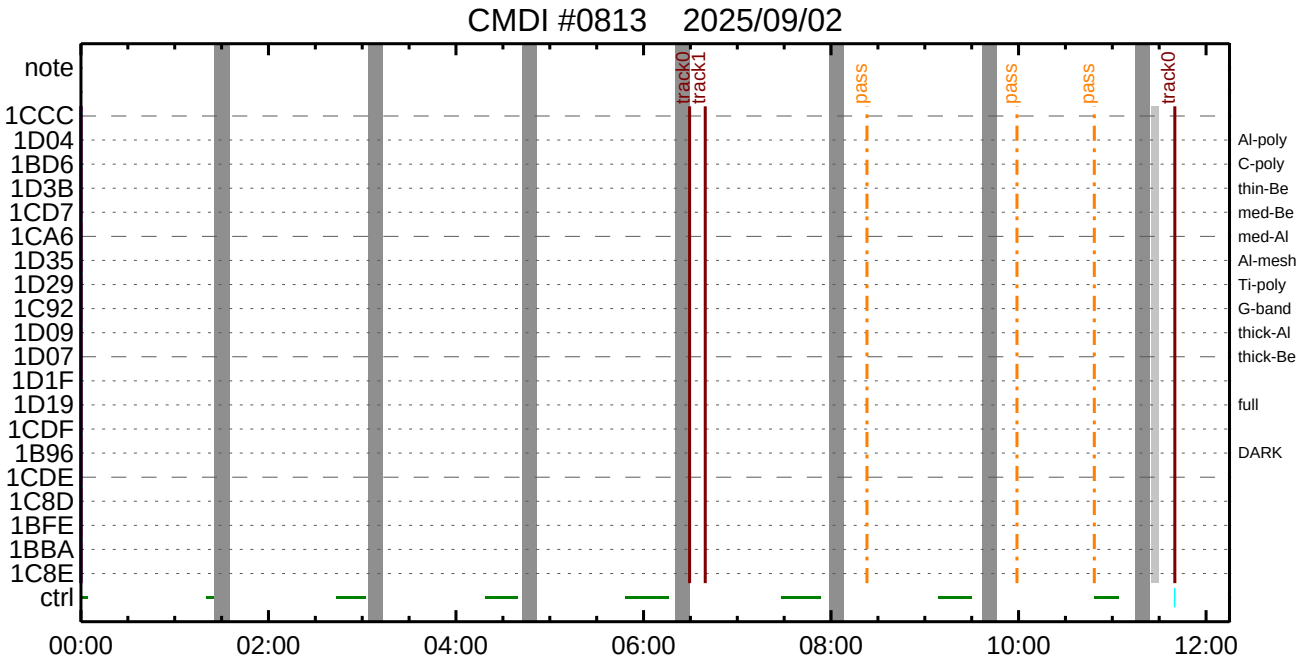
1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00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note
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1D3B
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1CA6
1D35
1D29
1C92
1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
track0
track1
pass
pass
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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thick-Be
full
DARK
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02:00
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06:00
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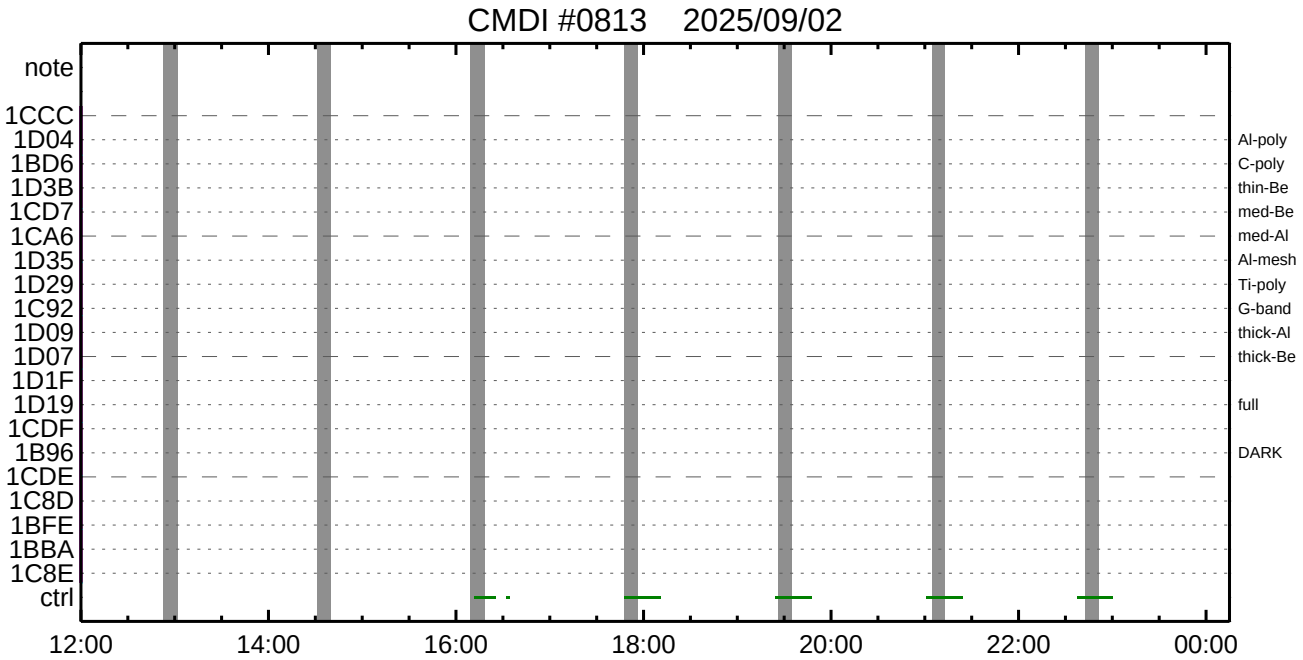
10:00
12:00


CMDI #0813 2025/09/02
note
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1D3B
1CD7
1CA6
1D35
1D29
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1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
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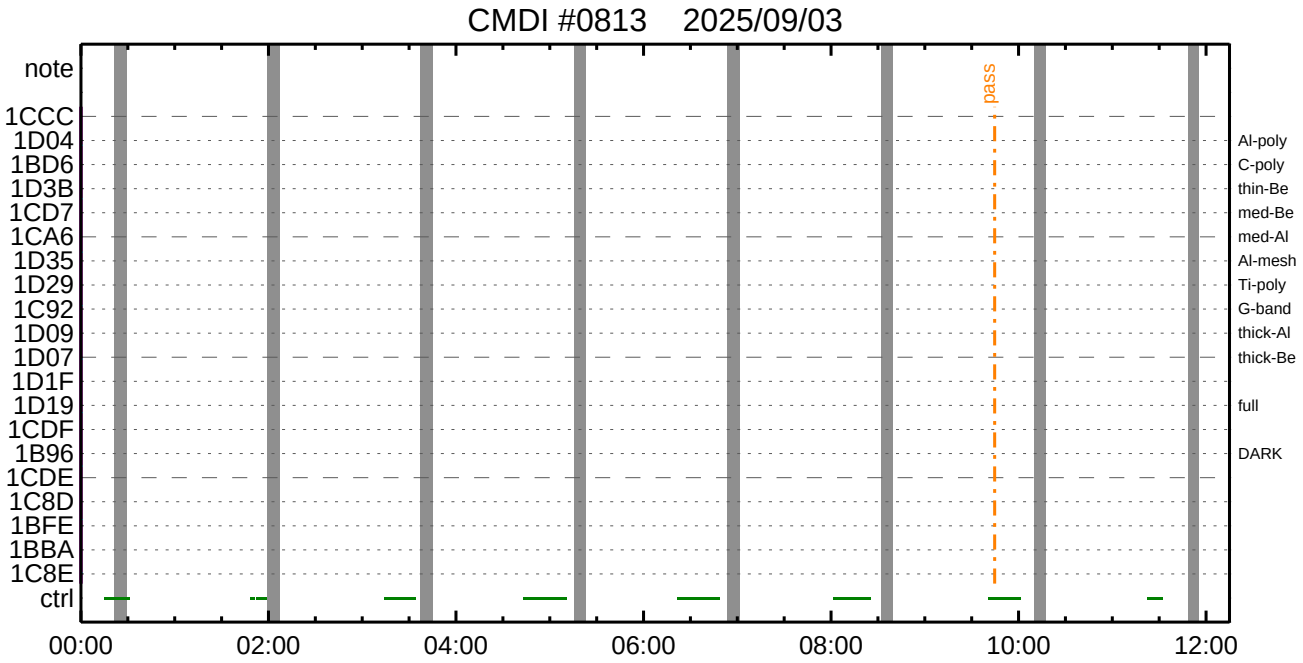
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1D3B
1CD7
1CA6
1D35
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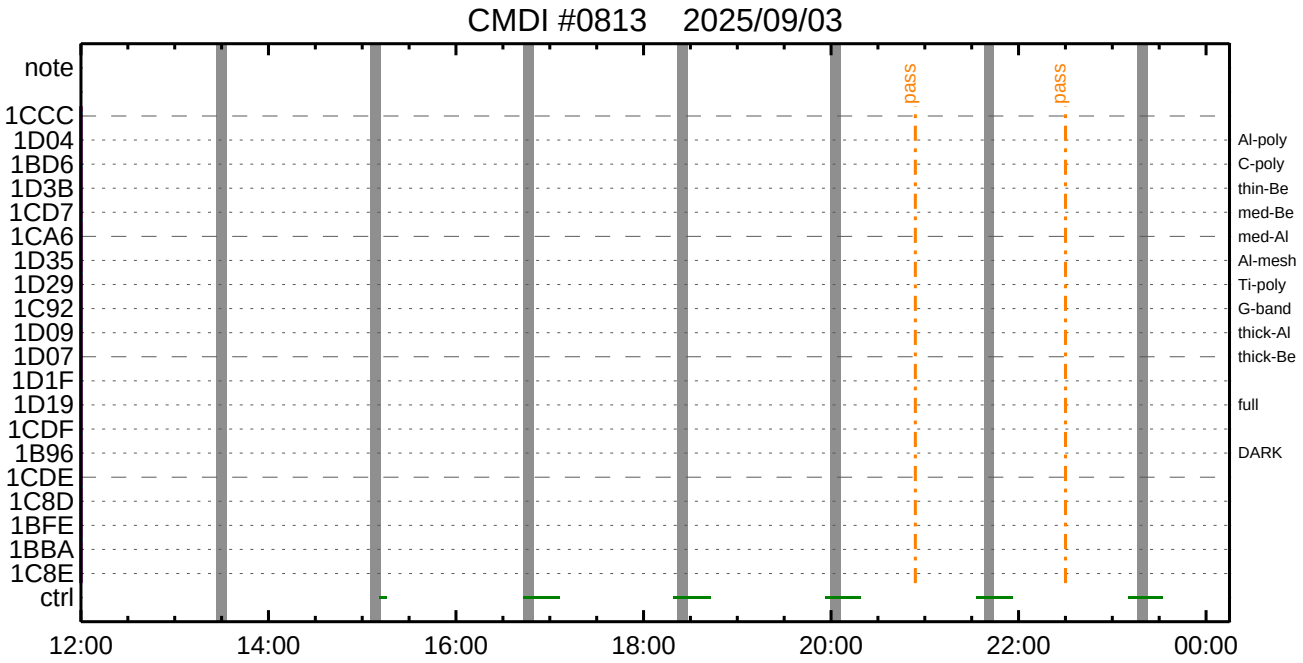
1D09
1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00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1BD6
1D3B
1CD7
1CA6
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1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
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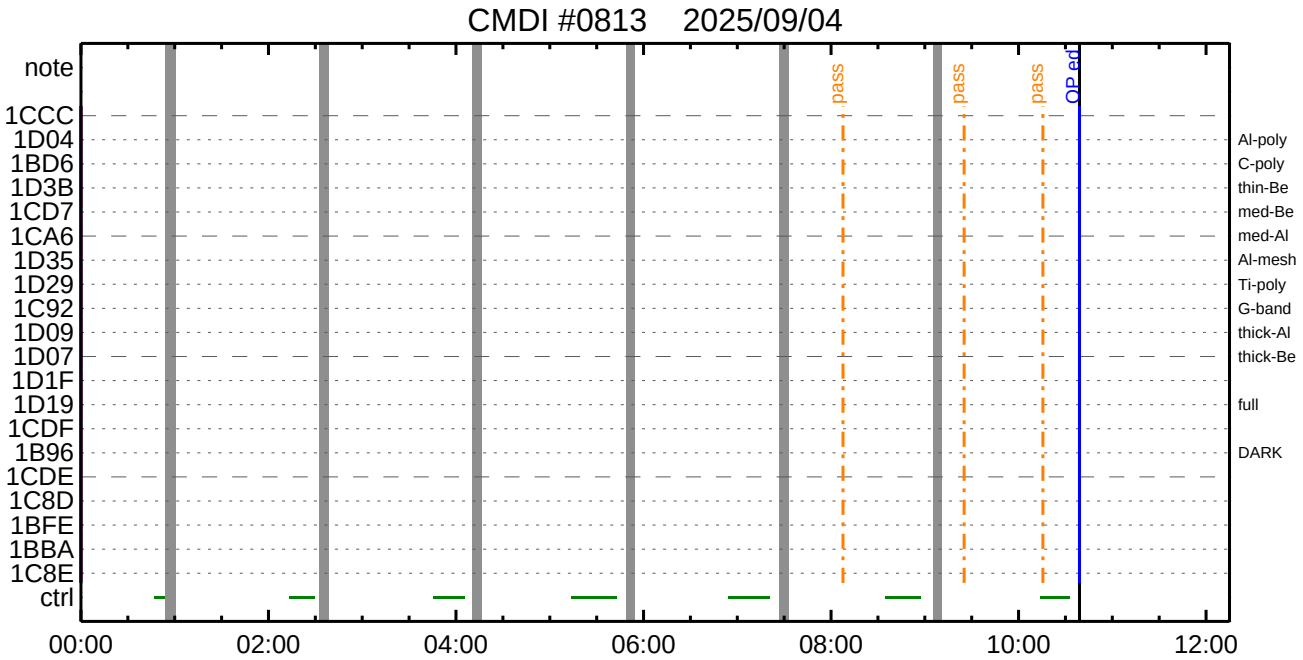
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1D3B
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1CA6
1D35
1D29
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1D19
1CDF
1B96
1CDE
1C8D
1BFE
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1C8E
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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thick-Be
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DARK
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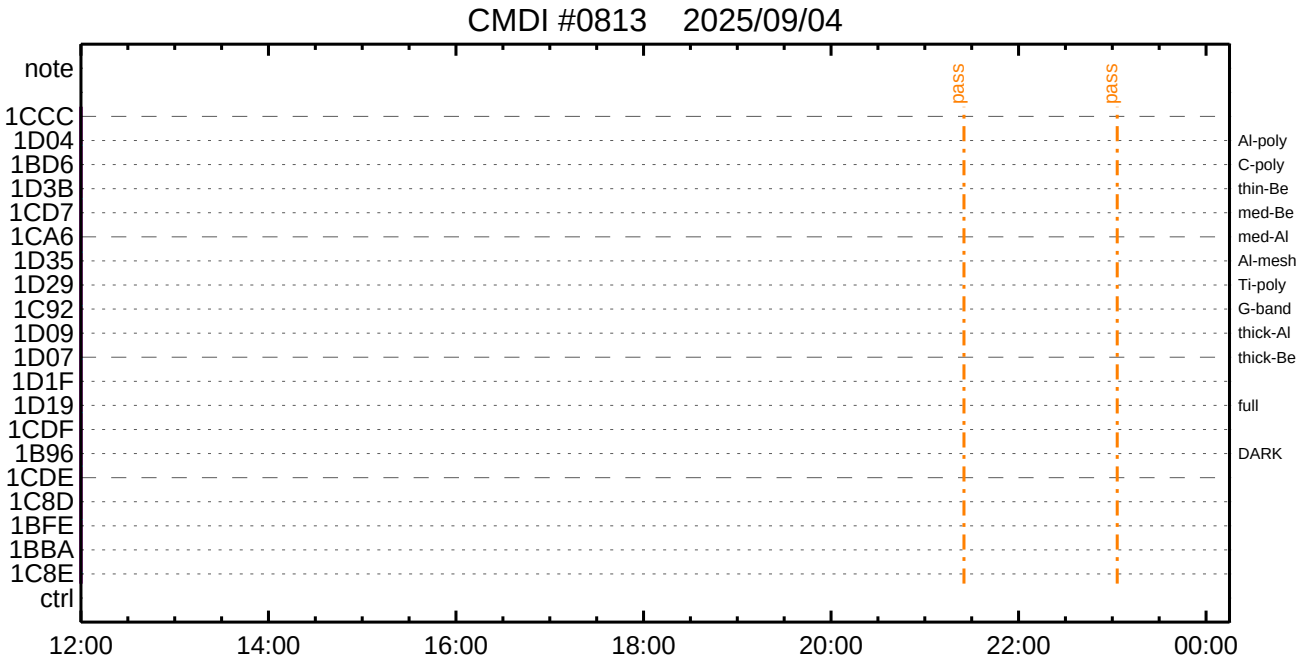
12:00


CMDI #0813 2025/09/04
note
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1D07
1D1F
1D19
1CDF
1B96
1CDE
1C8D
1BFE
1BBA
1C8E
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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(a) Spacecraft Operation Procedure (real-commands)

```
main-485 2025-08-30 11:23:55 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×ÈÝ¤äLOS¤Þ¤ÇÎ»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021  +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023  +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                    ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                    ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040  +  DC 06-B3 DR_REP_START
0041  +  DC 01-32 DHU_X_VC4_ON
0042     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾¾Ú)
0043     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0044     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047  +. DC 06-B3 DR_REP_START
0048  +  DC 01-32 DHU_X_VC4_ON
0049     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾¾Ú)
0050     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0051     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066  +  DC 06-B3 DR_REP_START
0067  +  DC 01-32 DHU_X_VC4_ON
0068     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾¾Ú)
0069     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0070     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073  +. DC 06-B3 DR_REP_START
0074  +  DC 01-32 DHU_X_VC4_ON
0075     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾¾Ú)
0076     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0077     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                    ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                    ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093  +  DC 03-B5 TCIA_XPA_OFF
0094     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-485:OP
0104     ()
0105   . S. OG    og-485:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ-¥À¥ó¥×¼êÇ¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. *********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. *********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2025-08-30 11:09:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2025-08-30 11:09:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2025-08-30 11:09:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195  +. TI 2025-08-30 11:13:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                    ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                    ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                    ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205   . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238   . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244  +. TI 2025-08-30 11:13:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247  +. TI 2025-08-30 11:13:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250   . C.                     [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. *********** End EIS operation (TI set) ***********
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ***********
0256     C. Execute, after the success of OP upload.
0257  +. TI 2025-08-30 11:13:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260   . C.                     [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ***********
0263     C.
0264   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266   . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270   . C. ***** ¥Þ¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271   . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277   . C. *********** LOS ***********
0278     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-486 2025-08-30 11:23:55 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×ÈÝ¤äLOS¤Þ¤Ç¤Î»þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021  +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023  +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                    ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                    ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040  +  DC 06-B3 DR_REP_START
0041  +  DC 01-32 DHU_X_VC4_ON
0042     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾¾Ú)
0043     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0044     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²ÓÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047  +. DC 06-B3 DR_REP_START
0048  +  DC 01-32 DHU_X_VC4_ON
0049     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾¾Ú)
0050     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0051     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066  +  DC 06-B3 DR_REP_START
0067  +  DC 01-32 DHU_X_VC4_ON
0068     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾¾Ú)
0069     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0070     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073  +. DC 06-B3 DR_REP_START
0074  +  DC 01-32 DHU_X_VC4_ON
0075     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾¾Ú)
0076     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾¾Ú)
0077     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                    ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                    ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093  +  DC 03-B5 TCIA_XPA_OFF
0094     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106  +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113  +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                     Result = OK [ ]
0121     C.
0122  +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                        <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127  +. DC 02-8E AOCU_ORB_UPD
0128   . C.
0129   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130  +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132   . C. Verify EIS_MODE_CHG_FLG is ENA
0133  +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135   . C. Verify EIS in MANUAL mode
0136   . C. Estimated OBSTBL upload time is 17s
0137     C. *********************************
0138     C. EIS START OBSTBL LOAD
0139     C. *********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142  +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147  +. TI 2025-08-30 11:13:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. *********************************
0152     C. EIS END OBSTBL LOAD
0153     C. *********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. *********** LOS ***********
0169     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-487 2025-08-30 11:23:55 102 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014     C.
0015     C. ************ XRT START ************
0016     C.
0017  +. DC 07-F0 MDP_XRT_CTRL_MANU
0018     BC       (c1)
0019  +. DC 07-F0 MDP_XRT_CTRL_MANU
0020     BC       (c1)
0021  +  DC 07-F0 MDP_XRT_MODE_STBY
0022     BC       (c3)
0023   . C. ----------- Success Verify ?              OK / NG____
0024     C.
0025     C. XRT Obs. Table Upload
0026   . S. RAM   ram-291:MDP_OBS_X
0027     ()
0028     C.
0029  +. DC 07-F0 MDP_DUMP_XRTTBL
0030     BC       (84 07 00 00 00 3a d4)
0031   . C. ----------- Comparison Check ?                        OK / ERR ____
0032     C.
0033     C.
0034  +. DC 07-F0 MDP_XRT_ROI_SET
0035     BC       (cd 01 b1 b1 04 04)
0036  +  DC 07-F0 MDP_XRT_ROI_SET
0037     BC       (cd 02 b1 b1 08 08)
0038  +  DC 07-F0 MDP_XRT_ROI_SET
0039     BC       (cd 03 b1 b1 08 08)
0040  +  DC 07-F0 MDP_XRT_ROI_SET
0041     BC       (cd 04 b1 b1 06 06)
0042  +  DC 07-F0 MDP_XRT_ROI_SET
0043     BC       (cd 05 85 83 06 06)
0044  +  DC 07-F0 MDP_XRT_ROI_SET
0045     BC       (cd 06 85 83 06 06)
0046  +  DC 07-F0 MDP_XRT_ROI_SET
0047     BC       (cd 07 80 80 20 20)
0048  +  DC 07-F0 MDP_XRT_ROI_SET
0049     BC       (cd 08 80 80 20 08)
0050  +  DC 07-F0 MDP_XRT_ROI_SET
0051     BC       (cd 09 80 80 08 20)
0052  +  DC 07-F0 MDP_XRT_ROI_SET
0053     BC       (cd 0a 85 83 08 08)
0054  +  DC 07-F0 MDP_XRT_ROI_SET
0055     BC       (cd 0b 80 80 08 08)
0056  +  DC 07-F0 MDP_XRT_ROI_SET
0057     BC       (cd 0f 80 80 06 06)
0058  +  DC 07-F0 MDP_XRT_ROI_SET
0059     BC       (cd 10 80 80 08 08)
0060  +  DC 07-F0 MDP_XRT_FLD_ENA
0061     BC       (d8)
0062  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063     BC       (c8)
0064  +  DC 07-F0 MDP_XRT_ARS_DIS
0065     BC       (d5)
0066  +  DC 07-F0 MDP_XRT_AEC_RESET
0067     BC       (d0)
0068  +  DC 07-F0 MDP_XRT_FLD_RESET
0069     BC       (da)
0070  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071     BC       (c4 03)
0072  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073     BC       (c5 0e)
0074   . C. ---------- Success Verify ?            OK / NG ____
0075     C.
0076     C.
0077   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0078     C.
0079  +. DC 07-F0 MDP_XRT_MODE_OBSV
0080     BC       (c2)
0081  +. TI 2025-08-30 11:13:02.0
0082     DC 07-F0 MDP_XRT_MODE_OBSV
0083     BC       (c2)
0084   . C. ---------- Success Verify ?            OK / NG ____
0085     C.
0086     C. ************ XRT END ***********
0087     C.
0088   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0089     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0090   . S. DC-BC dcbc-402:DCBC
0091     (MDP_known_event)
0092     C.
0093     C.
0094   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0095   . S. DC-BC dcbc-153:DCBC
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```
0096     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097     C.
0098     C.
0099   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0100     C.
0101   . C. *********** LOS ***********
0102     C.
```

```
*** OP Sequence for XRT ***

2025/08/30  11:23:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  11:23:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  11:23:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/30  11:24:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 00 b3 a6 02 0c
2025/08/30  11:24:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/30  11:24:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/30  11:24:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/30  11:24:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/30  11:24:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  11:26:56.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2025/08/30  11:26:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/30  11:27:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  12:52:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  12:52:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  12:52:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  12:52:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  12:55:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  13:04:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  13:05:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  14:30:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  14:30:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  14:30:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  14:30:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  14:33:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  14:43:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  14:44:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  16:08:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  16:08:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  16:08:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  16:08:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  16:11:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  16:27:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  16:28:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  16:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  16:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  16:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  16:33:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  16:36:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  16:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  16:37:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  17:46:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  17:46:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  17:46:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  17:46:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  17:49:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  18:12:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  18:12:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  18:12:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
```

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                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/30  18:12:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/08/30  18:12:48.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/30  18:12:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/30  18:12:52.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/30  18:15:28.0  XRT_QT_PROG_SET_401_OG [0x191]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2025/08/30  18:15:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  18:22:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  18:22:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  18:22:28.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2025/08/30  18:22:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 a5 02 0c
2025/08/30  18:22:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/30  18:22:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/30  18:22:52.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/30  18:22:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/30  18:22:56.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  18:25:26.0  XRT_QT_PROG_SET_426_OG [0x1aa]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2025/08/30  18:25:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/30  18:25:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  19:25:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  19:25:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  19:25:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  19:25:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  19:28:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  19:49:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  19:50:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  21:02:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  21:02:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  21:02:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  21:02:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  21:05:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  21:26:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  21:27:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/30  22:39:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  22:39:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/30  22:39:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/30  22:39:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/30  22:42:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/30  23:02:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/30  23:03:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  00:16:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  00:16:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  00:16:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  00:16:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/31  00:19:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/31  00:32:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/31  00:33:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  01:49:00.5  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  01:49:02.5  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
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2025/08/31  01:49:04.5  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  01:49:06.5  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/31  01:52:14.5  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/31  02:10:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/31  02:11:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  03:15:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  03:15:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  03:15:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  03:15:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/31  03:18:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/31  03:48:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/31  03:49:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/31  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/08/31  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2025/08/31  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2025/08/31  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2025/08/31  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/31  04:00:26.0  XRT_FLD_RESET_434_OG [0x1b2]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  04:02:56.0  XRT_QT_PROG_SET_425_OG [0x1a9]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2025/08/31  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2025/08/31  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  04:43:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  04:43:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  04:43:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  04:43:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/31  04:46:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/31  05:12:30.0  XRT_Custom_430_OG [0x1ae]
2025/08/31  05:13:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  05:14:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  05:14:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  05:14:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  05:14:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/31  05:17:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/08/31  05:26:00.0  XRT_Custom_430_OG [0x1ae]
2025/08/31  05:27:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  05:55:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  05:55:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  05:55:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2025/08/31  05:56:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2025/08/31  05:56:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2025/08/31  05:56:22.0  XRT_ARS_DIS_435_OG [0x1b3]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2025/08/31  05:58:58.0  XRT_QT_PROG_SET_401_OG [0x191]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2025/08/31  05:59:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2025/08/31  06:06:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 a5 02 0c
2025/08/31  06:16:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  06:16:00.5  XRT_CTRL_MANU_402_OG [0x192]
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                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  06:16:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/08/31  06:16:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2025/08/31  06:16:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2025/08/31  06:16:30.0  XRT_TCIB_XRT_S_HTR_A_ENA_418_OG [0x1a2]
                                        TCIB_XRT_S_HTR_A_ENA 0 04-BC
2025/08/31  06:19:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2025/09/01  06:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/09/01  06:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 a5 02 0c
2025/09/02  06:29:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2025/09/02  06:39:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 a5 02 0c
2025/09/02  11:39:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2025/09/02  11:40:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```