

Period: 2025/09/02 11:30:00 - 2025/09/06 11:43:00

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XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -All/mesh(2048ms) - 1x1, All/Poly(1443ms) - 2x2 - w leak image-1msCCD														
Term		Pointing (x, y)					Comment							
09/03 12:33:00 - 09/03 12:39:54		Fixed (-528.4, 528.4)					Post bakeout Q4							
PROG= 01 1-time(s) Subr= 1 1-time(s) 2.0sec Seqn= 14 1-time(s) 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec Subr= 2 1-time(s) 120.0sec														

└	Seqn= 93	2-time(s)	2.0sec										
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Subr= 3	2-time(s)	2.0sec										
└	Seqn= 34	1-time(s)	60.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096)

Term		Pointing (x, y)		Comment									
09/03 12:43:00 - 09/03 12:49:54		Fixed (0.0, 0.0)		Post bakeout synoptics									
PROG= 08		1-time(s)											
└	Subr= 1	1-time(s)	2.0sec										
	Seqn= 5	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Seqn= 55	1-time(s)	2.0sec										
└	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 98	1-time(s)	2.0sec										
└	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 76	1-time(s)	2.0sec										
└	thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 23	1-time(s)	4.0sec										
└	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Subr= 2	1-time(s)	2.0sec										
	Seqn= 41	1-time(s)	2.0sec										
└	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Seqn= 18	1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 86	1-time(s)	2.0sec										
	med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 54	1-time(s)	2.0sec										
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term		Pointing (x, y)		Comment									
09/03 12:57:00 - 09/03 17:54:54		Track (750.1, -373.2) @ 09/03 12:50:00		AR 14197									
09/03 18:08:00 - 09/04 03:45:30		Track (773.4, -368.2) @ 09/03 18:05:00		AR 14197									
09/04 06:13:00 - 09/04 10:13:30		Track (819.2, -356.2) @ 09/04 06:10:00		AR 14197									
PROG= 03		Inf.-time(s)											
└	Subr= 1	1-time(s)	2.0sec										
	Seqn= 92	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Subr= 2	4-time(s)	2.0sec										
	Seqn= 47	1-time(s)	2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Seqn= 77	4-time(s)	300.0sec										
└	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
└	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D08: Synoptic for HOP448 w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/

Term	Pointing (x, y)				Comment								
09/03 17:58:00 - 09/03 18:04:54	Fixed (0.0, 0.0)				synoptic, shifted -5.0 min								
09/04 06:03:00 - 09/04 06:09:54	Fixed (0.0, 0.0)				HOP 349 + synoptic								
PROG= 04 1-time(s)													
Subr= 1 1-time(s)		2.0sec											
Seqn= 5 1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)		DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)		DPCM	0	0	2.0sec
Seqn= 55 1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 98 1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 76 1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 23 1-time(s)		4.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2 1-time(s)		2.0sec											
Seqn= 41 1-time(s)		2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=98	0	0	2.0sec
Seqn= 18 1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 86 1-time(s)		2.0sec											
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D09: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 90min cad) + CME wat

Term			Pointing (x, y)				Comment								
09/04 04:07:00 - 09/04 05:59:54			Fixed (0.0, 0.0)				HOP 349 + synoptic								
PROG= 11 Inf.-time(s)															
Subr= 1		1-time(s)		600.0sec											
Seqn= 55		1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 98		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/Open		close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 79		1-time(s)		2.0sec											
thin-Be/Open		thin-Be/Open		close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 30		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		7-time(s)		600.0sec											
Seqn= 8		1-time(s)		2.0sec											
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 74		1-time(s)		2.0sec											
med-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
med-Be/Open		med-Be/Open		close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 29		1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)				Comment								
09/03 12:57:00 - 09/03 17:54:54	Track (750.1, -373.2) @ 09/03 12:50:00				AR 14197								
09/03 18:08:00 - 09/04 03:45:30	Track (773.4, -368.2) @ 09/03 18:05:00				AR 14197								
09/04 04:07:00 - 09/04 05:59:54	Fixed (0.0, 0.0)				HOP 349 + synoptic								
09/04 06:13:00 - 09/04 10:13:30	Track (819.2, -356.2) @ 09/04 06:10:00				AR 14197								
PROG= 14 30-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 68 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 73 1-time(s) 10.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Subr= 3 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 87 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

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Active Region Search

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NOT USED

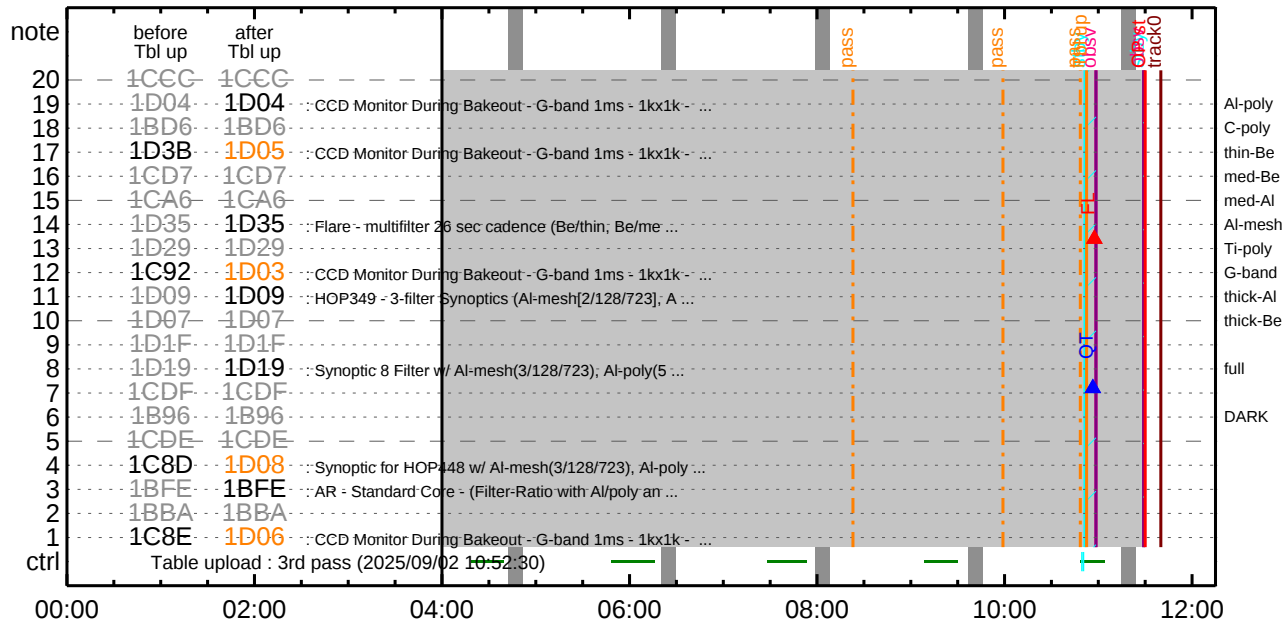
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Flare Detection

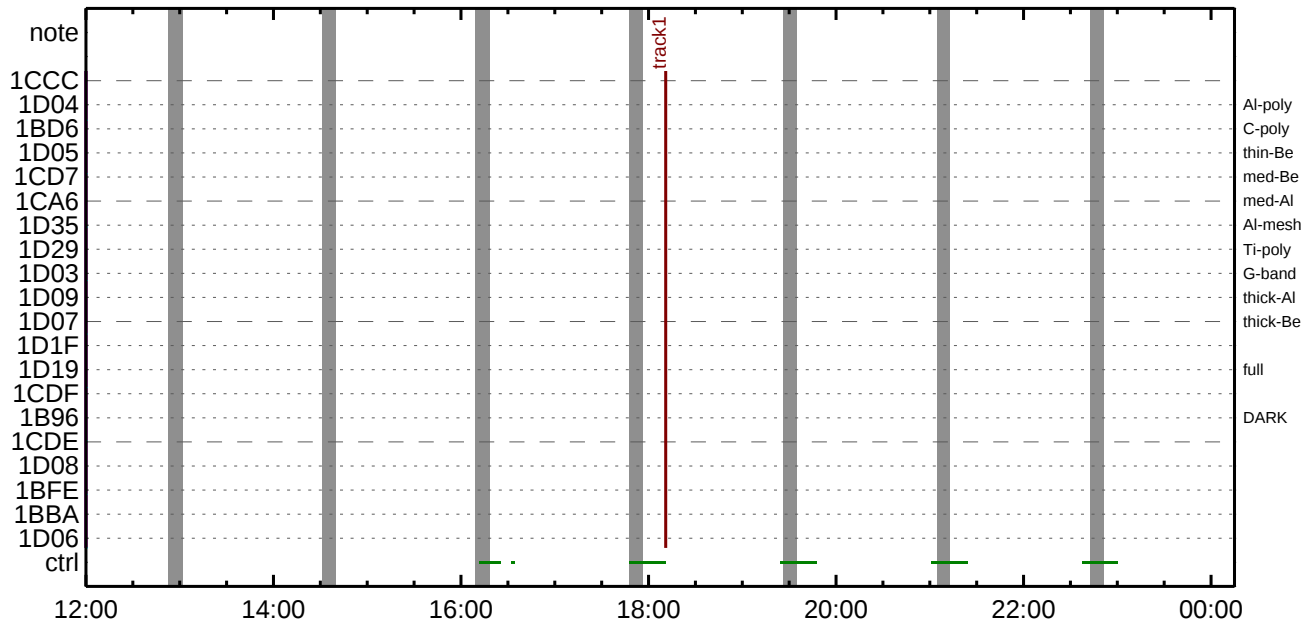
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FLD Patrol												
Term	Pointing (x, y)		Comment									
09/02 10:53:30 - 09/03 12:02:56	cannot be identified											
09/03 12:54:18 - 09/03 17:55:18	Track (750.1, -373.2) @ 09/03 12:50:00		AR 14197									
09/03 18:05:18 - 09/04 06:00:18	Track (773.4, -368.2) @ 09/03 18:05:00		AR 14197									
09/04 06:10:18 - 09/06 11:43:00	Track (819.2, -356.2) @ 09/04 06:10:00		AR 14197									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

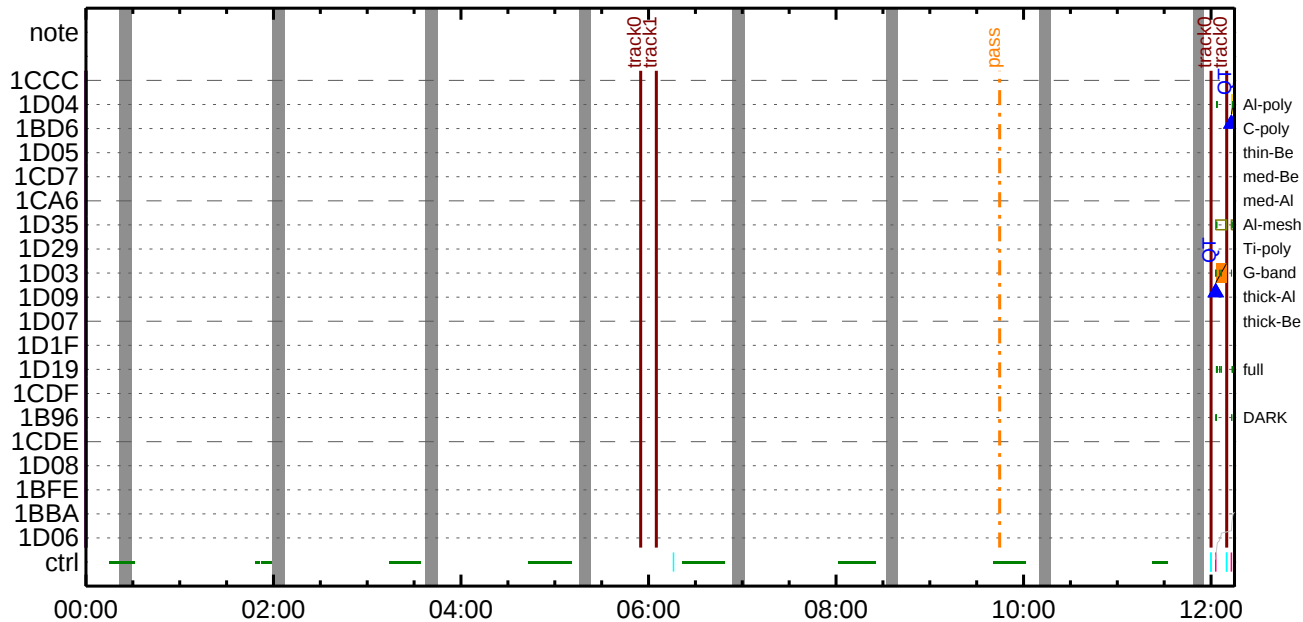
CMDI #0817 2025/09/02



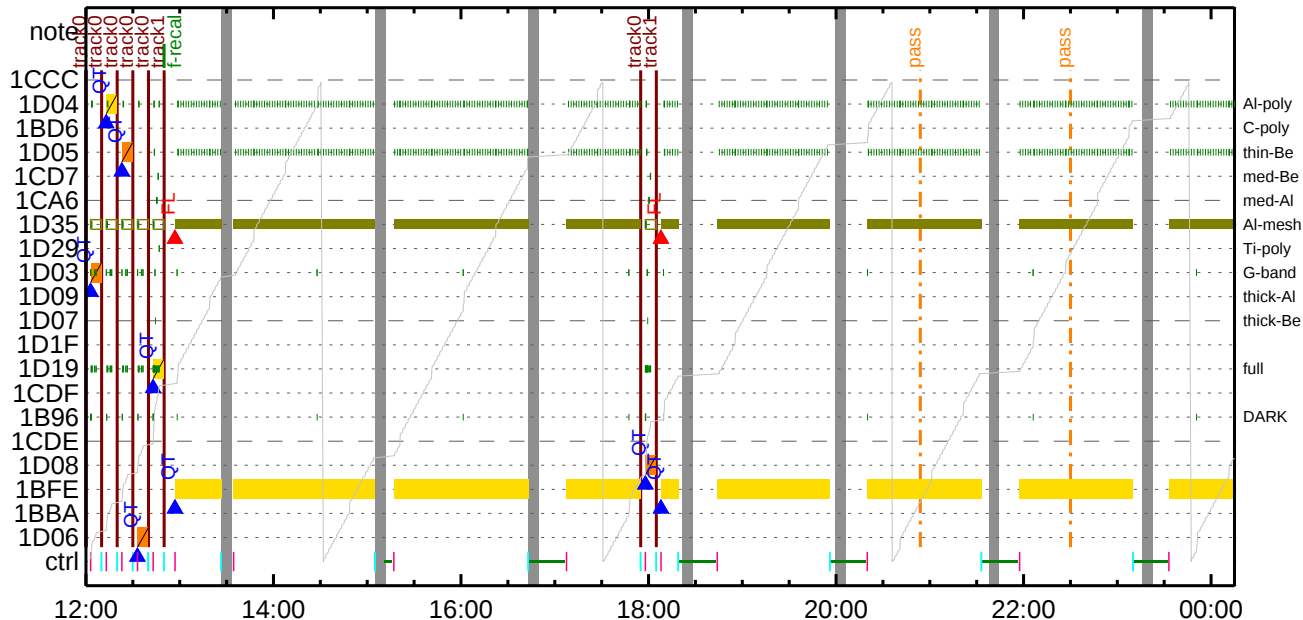
CMDI #0817 2025/09/02



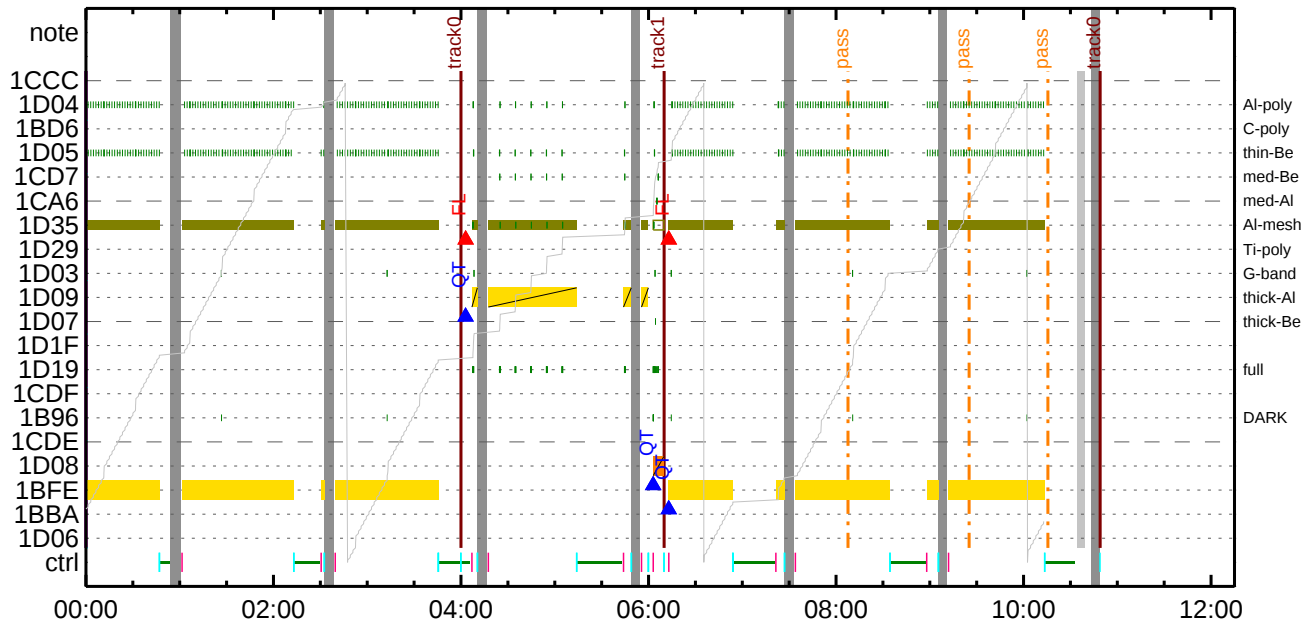
CMDI #0817 2025/09/03



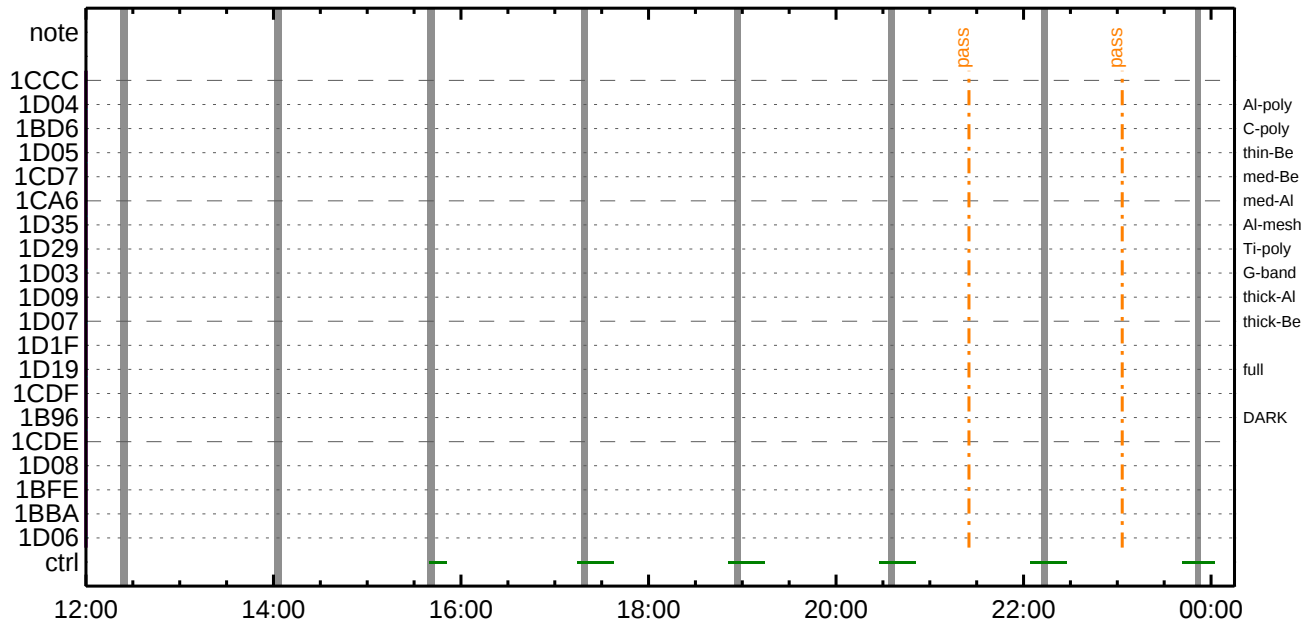
CMDI #0817 2025/09/03



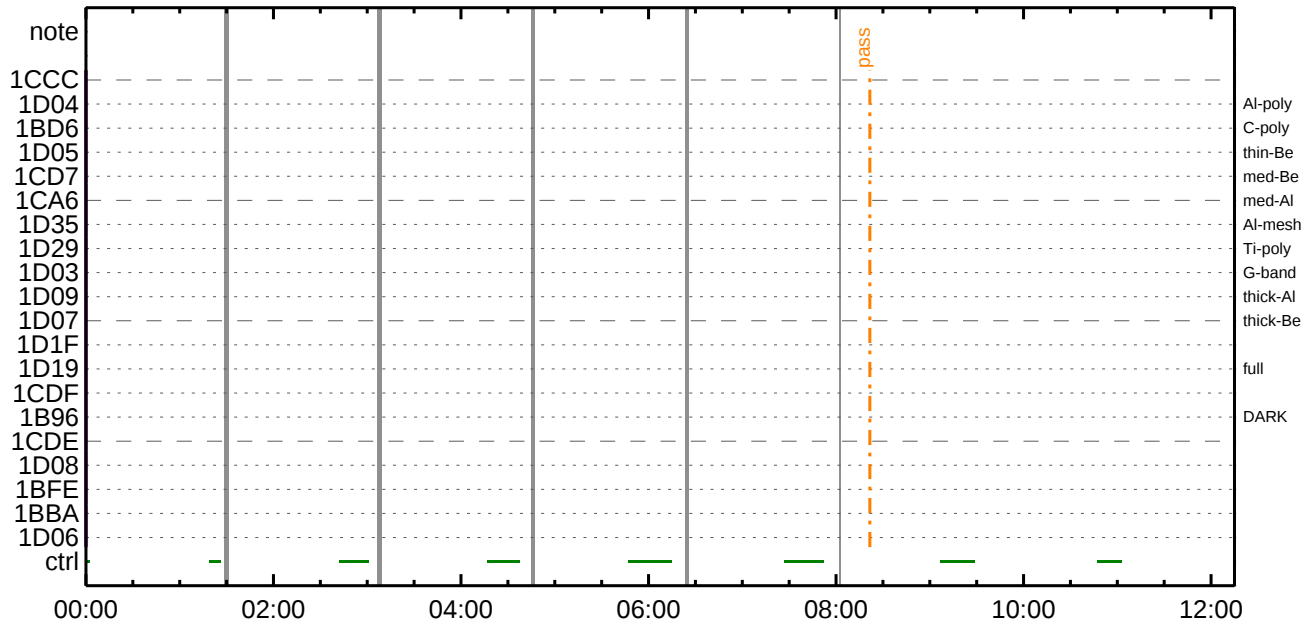
CMDI #0817 2025/09/04



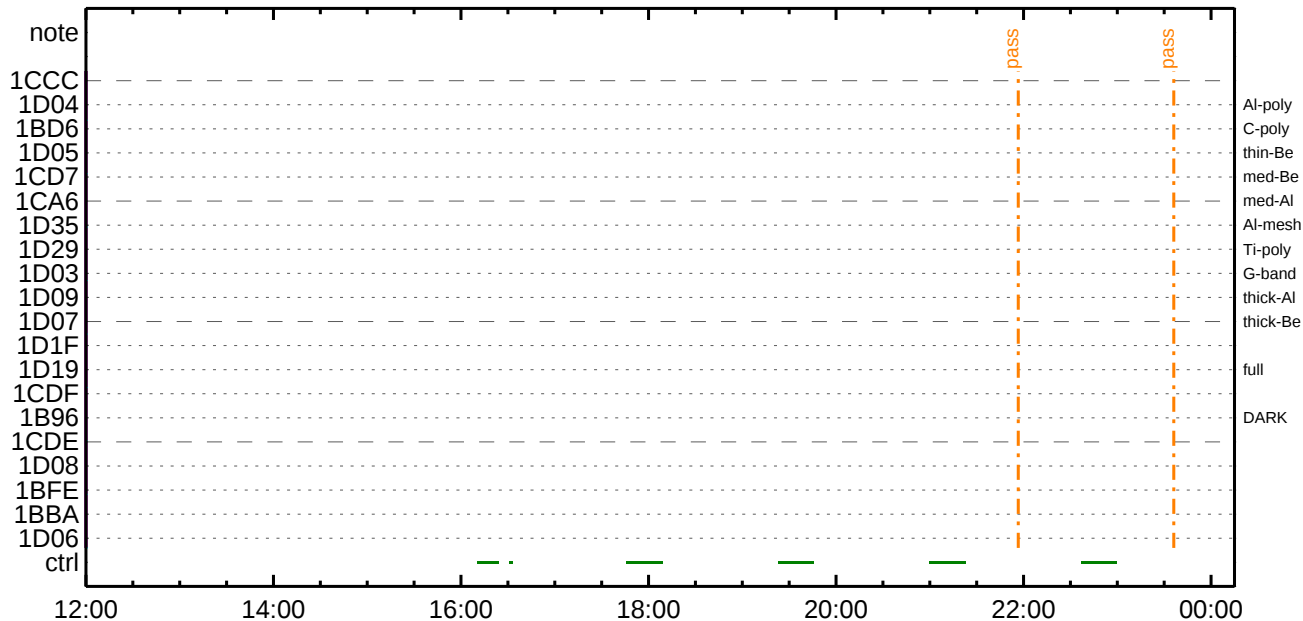
CMDI #0817 2025/09/04



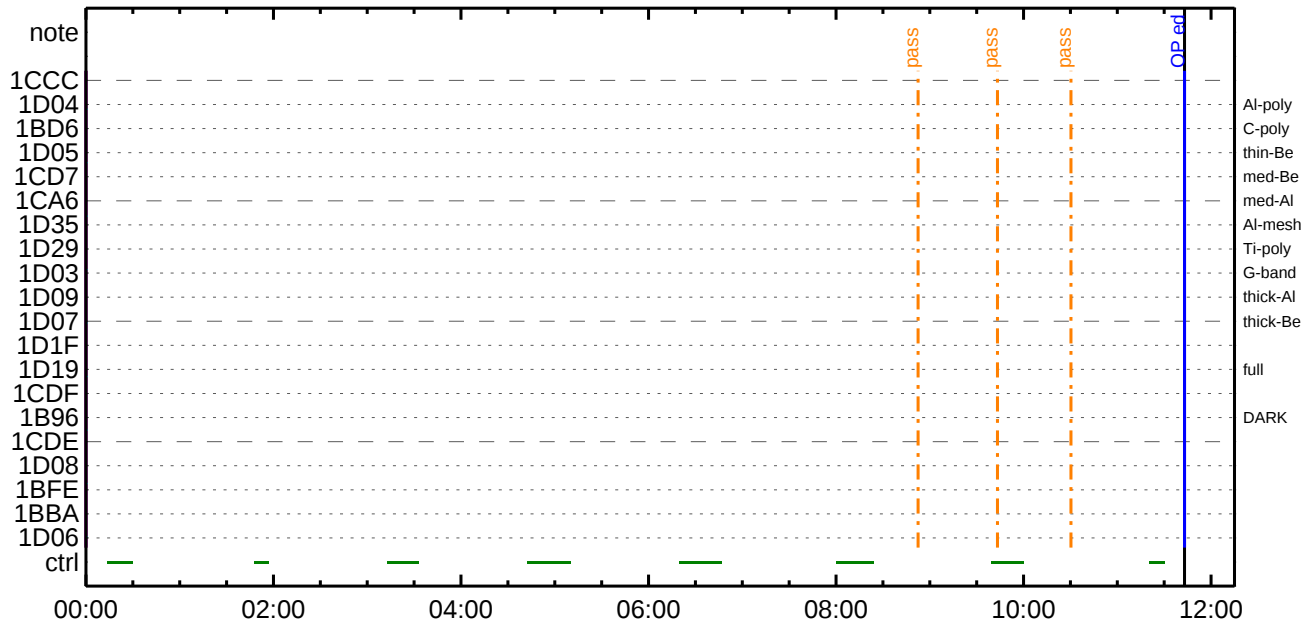
CMDI #0817 2025/09/05

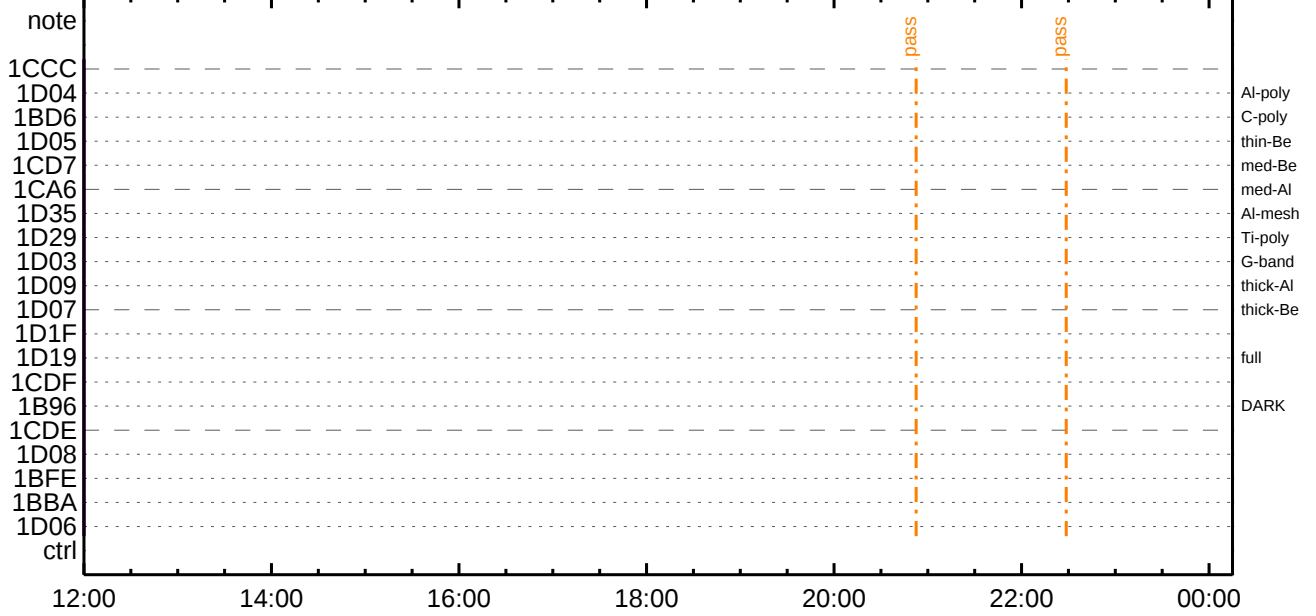


CMDI #0817 2025/09/05



CMDI #0817 2025/09/06





```
main-489 2025-09-02 11:11:39 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËñÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÐñÇñÎ»p´Öòò¹íî,ñ•;çÉÖÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲ~¾ÔÖñ¬°ÄÄèñ•ñççé;ç°Ê²¼ñî°EÄ,¼è½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öñññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲEŲÄÚÂØñÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ´°î»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öñññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGŵı;¼ŶÉ;¼ŶŶóŶ×
0100 C. *****
0101 C.
0102 C. ;ãOP/OGŵı;¼ŶÉ;ã
0103 S. OP op-489:OP
0104 (
0105 S. OG og-489:OG
0106 (
0107 C.
0108 C. ;ãNMOG&OPîî°èŶŶŶóŶ×;ã
0109 C. NMOG(0x200000-0x207FFF;\$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ŶŶŶóŶ×½ªî»¤ð³îÇ\$
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ\$
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;\$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ŶŶŶóŶ×½ªî»¤ð³îÇ\$
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ\$
0146 C.
0147 C. NMOG(0x210000-0x2100FF;\$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ŶŶŶóŶ×½ªî»¤ð³îÇ\$
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ\$
0165 C.
0166 C. ***** °Ē²¼¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ\$ *****
0167 C. DHUŶã;¼ŶÉ;Ē¼Ŷ½,Ŷı;¼ŶÉ;Ē¤ðİã¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;\$ OPOG UPLOAD¤¬Á÷Ĳ@NG¤İ¼ı¹Ç;Ĳ°Ē²¼¤î¼Ē¹Ç÷Ĳ@¤İ¼Ē¹Ç•Ē²İOK¤ð³îÇ\$
0180 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIŶ³ŶŶŶóŶÉ¤ðİãİĲ (UT)
0183 +. TI 2025-09-02 11:25:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2025-09-02 11:25:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2025-09-02 11:25:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2025-09-02 11:29:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê¼¤İÄê¼İİñİŸÄŸ$ŸÄŸ~¹aiÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]         EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]      EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]      EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]       EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]         EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]       EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]       EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¼È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHUŸâ;¼ŸÉ;Ê¼Ÿ½,Ÿİ;¼ŸÈ;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]         EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]       EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]       EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2025-09-02 11:29:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2025-09-02 11:29:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2025-09-02 11:29:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÄİ¤İ»ö¼Ÿ¤ÈÄ¤¤¹¤èDCBC•×²è *****
0265 C. (¼å°İŸÖŸÄŸÈŸŸŸÈŸâŸÇŸë¤È½¼¤¤¼Ä»Ü¤¹¤è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŸĐŸ¹•İ Daily±;İİñ¤È´Ø¤¹¤èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOSŸÄŸ$ŸÄŸ~¼Ä»Ü;ä
0276 C.
0277 C. ***** LOS *****
0278 C.
```

[illegible]

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 1833877.1 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 9s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-09-02 11:29:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0165 C. (¾å°î¥Ó¥Å¥Ê¥Ð¥Ê¥Å¥Ç¥ÊñÊ¼¾ñ¼Å»Û¹ñë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±¿ÍÑñÊ'Ø¹ñëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥Á¥S¥Å¥¹¼Å»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

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[illegible]

0096 C.
0097 C.
0098 . C. ***** ¥Ð¥¹•ï Daily±¿íÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0099 . S. DC-BC dcbc-153;DCBC
0100 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0101 C.
0102 C.
0103 . C. ;ãLOS¥Á¥\$¥Ã¥¬¼Ã»Ü;ä
0104 C.
0105 . C. ***** LOS *****
0106 C.

Sep 02, 25 11:11

XRT_OGLIST_0817.chk

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*** OP Sequence for XRT ***

2025/09/02	11:40:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	b3	a6	02	0c
2025/09/02	18:11:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	03	a5	02	0c
2025/09/03	05:55:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2025/09/03	06:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01	03	a5	02	0c
2025/09/03	06:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	06:16:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_445_OG [0x1bd]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2025/09/03	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	11:59:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/09/03	12:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2025/09/03	12:02:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/09/03	12:02:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/09/03	12:02:56.0	XRT_FLD_DIS_432_OG [0x1b0]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/09/03	12:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2025/09/03	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/09/03	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:09:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/09/03	12:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2025/09/03	12:12:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/09/03	12:12:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/09/03	12:12:56.0	XRT_FLD_DIS_432_OG [0x1b0]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/09/03	12:12:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13			
2025/09/03	12:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/09/03	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:19:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/09/03	12:20:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2025/09/03	12:22:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/09/03	12:22:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/09/03	12:22:56.0	XRT_FLD_DIS_432_OG [0x1b0]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/09/03	12:22:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2025/09/03	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/09/03	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:29:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/09/03	12:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2025/09/03	12:32:52.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/09/03	12:32:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/09/03	12:32:56.0	XRT_FLD_DIS_432_OG [0x1b0]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/09/03	12:32:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2025/09/03	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/09/03	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/09/03	12:39:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/09/03	12:40:00.5	AOCS_ORe-point_Start_3_OG [0x099]							

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2025/09/03	12:40:18.0	XRT_FLD_DIS_409_OG [0x199]	5	02-76 00 00 00 00 00
		MDP_XRT_FLD_DIS	1	07-F0 d9
2025/09/03	12:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	1	07-F0 c9
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2025/09/03	12:40:22.0	XRT_ARS_DIS_435_OG [0x1b3]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/09/03	12:42:58.0	XRT_QT_PROG_SET_401_OG [0x191]	2	07-F0 c4 08
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 08
2025/09/03	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/09/03	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	12:49:58.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]	2	07-F8 78 00
		XRT_FOCUS_RECAL	2	07-F8 78 00
2025/09/03	12:50:00.0	AOCs_ORe-point_Start_2_OG [0x098]	5	02-76 01 03 a5 02 0c
		AOCU_NM	5	02-76 01 03 a5 02 0c
2025/09/03	12:53:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	4	07-F8 22 fe 97 00
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2025/09/03	12:54:18.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0 d8
		MDP_XRT_FLD_ENA	1	07-F0 d8
2025/09/03	12:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2025/09/03	12:54:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2025/09/03	12:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/09/03	12:54:26.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/09/03	12:56:56.0	XRT_QT_PROG_SET_403_OG [0x193]	2	07-F0 c4 03
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2025/09/03	12:56:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0 c5 0e
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0e
2025/09/03	12:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/09/03	13:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	13:26:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	13:26:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/09/03	13:26:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/09/03	13:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/09/03	13:33:30.0	XRT_Custom_430_OG [0x1ae]	1	07-F0 c0
2025/09/03	13:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/09/03	15:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	15:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	15:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/09/03	15:05:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/09/03	15:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/09/03	15:16:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0 c0
2025/09/03	15:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/09/03	16:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	16:43:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	16:43:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/09/03	16:43:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/09/03	16:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/09/03	17:06:30.0	XRT_Custom_430_OG [0x1ae]	1	07-F0 c0
2025/09/03	17:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/09/03	17:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	17:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/03	17:54:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8 22 ff aa 00
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2025/09/03	17:55:00.0	AOCs_ORe-point_Start_3_OG [0x099]	5	02-76 00 00 00 00 00
		AOCU_NM	5	02-76 00 00 00 00 00
2025/09/03	17:55:18.0	XRT_FLD_DIS_409_OG [0x199]	1	07-F0 d9
		MDP_XRT_FLD_DIS	1	07-F0 d9
2025/09/03	17:55:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	1	07-F0 c9
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2025/09/03	17:55:22.0	XRT_ARS_DIS_435_OG [0x1b3]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/09/03	17:57:58.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0 c4 04
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2025/09/03	17:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0

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2025/09/03	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/03	18:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	18:04:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	18:05:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/03	18:05:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 a5 02 0c
2025/09/03	18:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/03	18:05:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/03	18:05:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/03	18:05:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/03	18:07:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/03	18:07:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/09/03	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/03	18:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/03	18:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	18:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	18:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/03	18:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/03	18:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/03	18:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/03	19:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/03	19:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	19:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	19:56:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/03	19:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/03	20:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/03	20:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/03	21:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/03	21:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	21:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	21:33:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/03	21:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/03	21:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/03	21:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/03	23:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/03	23:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	23:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/03	23:10:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/03	23:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/03	23:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/03	23:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/04	00:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	00:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	00:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	00:47:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	00:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	01:00:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/04	01:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/04	02:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	02:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/09/04	02:13:04.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	02:13:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	02:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/04	02:29:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/04	02:30:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	02:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	02:32:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	02:32:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	02:32:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	02:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/04	02:38:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/04	02:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	03:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	03:45:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	03:45:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	03:45:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	03:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/04	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/04	04:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/04	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/04	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/04	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/04	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/04	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	04:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/09/04	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/04	04:06:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/04	04:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	04:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	04:10:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	04:10:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	04:10:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	04:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/04	04:16:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/04	04:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	05:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	05:14:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	05:14:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	05:14:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	05:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/04	05:43:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/04	05:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/04	05:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	05:49:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/04	05:49:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/04	05:49:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/04	05:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

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2025/09/04	09:11:00.0	XRT_Custom_430_OG [0x1ae]		
2025/09/04	09:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/09/04	10:13:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/04	10:13:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/04	10:13:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/09/04	10:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/09/04	10:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/09/04	10:48:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/09/04	10:49:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00