

XRT Timeline to be uploaded on 2025/09/11

Period: 2025/09/11 11:09:00 - 2025/09/16 10:50:00

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Normal mode

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XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms													
Term		Pointing (x, y)				Comment							
09/11 11:22:00 - 09/11 18:18:54		Fixed (-21.0, 857.0)				# OP start + 10min + HOP206 N pole							
PROG= 07 Inf.-time(s)													
[	Subr= 1 1-time(s) 2.0sec												
	Seqn= 16 2-time(s) 2.0sec												
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Subr= 2 1-time(s) 2.0sec												
	Seqn= 90 1-time(s) 30.0sec												
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Subr= 3 60-time(s) 60.0sec												
[	Seqn= 24 1-time(s) 30.0sec												
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CEE: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(512/8192/32768), Med-Be(128/576/2304)													
Term		Pointing (x, y)				Comment							
09/11 18:22:00 - 09/11 18:28:54		Fixed (0.0, 0.0)				synoptic, shifted							
09/12 05:53:32 - 09/12 05:59:54		Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -10.0 min							
09/12 17:53:30 - 09/12 18:00:24		Fixed (0.0, 0.0)				synoptic, shifted -9.5 min							
09/13 06:03:32 - 09/13 06:09:54		Fixed (0.0, 0.0)				HOP349 + synoptic							

PROG= 02 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26 1-time(s) 2.0sec													
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 15 1-time(s) 2.0sec													
Al-poly/Open		Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83 1-time(s) 2.0sec													
thin-Be/Open		thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
Seqn= 41 1-time(s) 2.0sec													
Open/thick-Be		Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec													
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec													
med-Be/Open		Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 36 1-time(s) 2.0sec													
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C94: AR (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 512x512 at 1064 1048, thick-Al context, with G-band (1ms/1ms leak), 300s													
Term		Pointing (x, y)				Comment							
09/11 18:32:00 - 09/12 03:59:54		Track (523.4, -372.5) @ 09/11 18:29:00				AR 14213							

PROG= 09 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 81 1-time(s) 2.0sec													
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Seqn= 82 1-time(s) 2.0sec													
Al-poly/Open		Al-poly/Open	close	Safe	Dark	1.00s	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 71 3-time(s) 2.0sec													
Open/thick-Al		Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	3	0	2.0sec

Subr= 2 20-time(s) 300.0sec													
Seqn= 94 1-time(s) 100.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 58 1-time(s) 100.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Seqn= 48 1-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C9B: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term			Pointing (x, y)				Comment							
09/12 04:03:00 - 09/12 05:53:00			Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -10.0 min							
PROG= 11 Inf.-time(s)														
Subr= 1		1-time(s)		300.0sec										
Seqn=100		1-time(s)		2.0sec										
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 84		1-time(s)		2.0sec										
└─ Al-poly/Open		Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 57		1-time(s)		2.0sec										
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
└─ thin-Be/Open		thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec
Seqn= 81		1-time(s)		2.0sec										
└─ Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=90	0	0	2.0sec
└─ Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=95	0	0	2.0sec
Subr= 2		9-time(s)		360.0sec										
Seqn= 8		1-time(s)		2.0sec										
└─ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└─ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec										
└─ Al-poly/Open		Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└─ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 29		1-time(s)		2.0sec										
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
└─ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D1F: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term			Pointing (x, y)					Comment									
09/12 06:03:00 - 09/12 17:50:24			Track (602.7, -364.2) @ 09/12 06:00:00					AR 14213									
09/12 18:03:30 - 09/12 23:59:54			Track (677.1, -354.5) @ 09/12 18:00:30					AR 14213									
09/13 06:13:00 - 09/13 10:09:00			Track (743.0, -343.6) @ 09/13 06:10:00					AR 14213									
PROG= 20 Inf.-time(s)																	
Subr= 1		1-time(s)		2.0sec													
Seqn= 92		1-time(s)		2.0sec													
├		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)			DPCM	0	0	2.0sec	
├		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)			DPCM	0	0	2.0sec	
└		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)			Q=98	0	0	2.0sec	
Subr= 2		4-time(s)		2.0sec													
Seqn= 47		1-time(s)		2.0sec													
├		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)			Q=95	2	0	2.0sec	
├		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	0	2.0sec	
├		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	2	0	2.0sec	
└		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	3	0	2.0sec	
Seqn= 77		4-time(s)		600.0sec													
├		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)			Q=95	1	0	2.0sec	
├		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	1	0	95.0sec	
├		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)			Q=95	1	1	2.0sec	
├		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	1	1	95.0sec	
├		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)			Q=95	1	2	2.0sec	
└		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)			Q=95	1	2	2.0sec	
Default Filter		Thicker Filter		VLS	mode	image	Exo.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval		

XOB #1CD9: HOP393 - 4x4 - Full Sun double long/short pair AEC 2/3 - Al-poly - Dark (512ms) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s

Term		Pointing (x, y)										Comment			
09/13 00:03:00 - 09/13 03:41:00		Track (-21.0, -163.0) @ 09/13 00:00:00										HOP393			
PROG= 05 Inf.-time(s)															
└ Subr= 1		1-time(s)		2.0sec											
└└ Seqn= 30		1-time(s)		2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec		

	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 52	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Subr= 2	30-time(s)	360.0sec										
	Seqn= 97	2-time(s)	2.0sec										
	Al-poly/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D28: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)							Comment						
09/13 04:03:00 - 09/13 06:03:00	Fixed (0.0, 0.0)			HOP349 + synoptic										
PROG= 18 Inf.-time(s)														
Subr= 1	1-time(s)		300.0sec											
Seqn=100	1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 84	1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 57	1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)			Q=95	0	0	2.0sec
Seqn= 81	1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)			Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)			Q=95	0	0	2.0sec
Subr= 2	10-time(s)		720.0sec											
Seqn= 8	1-time(s)		2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)			Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)			DPCM	2	0	2.0sec
Seqn= 6	1-time(s)		2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)			DPCM	2	0	2.0sec
Seqn= 29	1-time(s)		2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)			Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)				Comment										
09/11 11:22:00 - 09/11 18:18:54	Fixed (-21.0, 857.0)				# OP start + 10min + HOP206 N pole										
09/11 18:32:00 - 09/12 03:59:54	Track (523.4, -372.5) @ 09/11 18:29:00				AR 14213										
09/12 04:03:00 - 09/12 05:53:00	Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -10.0 min										
09/12 06:03:00 - 09/12 17:50:24	Track (602.7, -364.2) @ 09/12 06:00:00				AR 14213										
09/12 18:03:30 - 09/12 23:59:54	Track (677.1, -354.5) @ 09/12 18:00:30				AR 14213										
09/13 00:03:00 - 09/13 03:41:00	Track (-21.0, -163.0) @ 09/13 00:00:00				HOP393										
09/13 04:03:00 - 09/13 06:03:00	Fixed (0.0, 0.0)				HOP349 + synoptic										
09/13 06:13:00 - 09/13 10:09:00	Track (743.0, -343.6) @ 09/13 06:10:00				AR 14213										
PROG= 14 30-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 68		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		20-time(s)		2.0sec											
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 73		1-time(s)		10.0sec											
thin-Be/Open		med-Be/Open		close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
med-Be/Open		Open/thick-Al		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Subr= 3		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open		med-Al/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)		Q=95	2	0	2.0sec
Seqn= 87		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

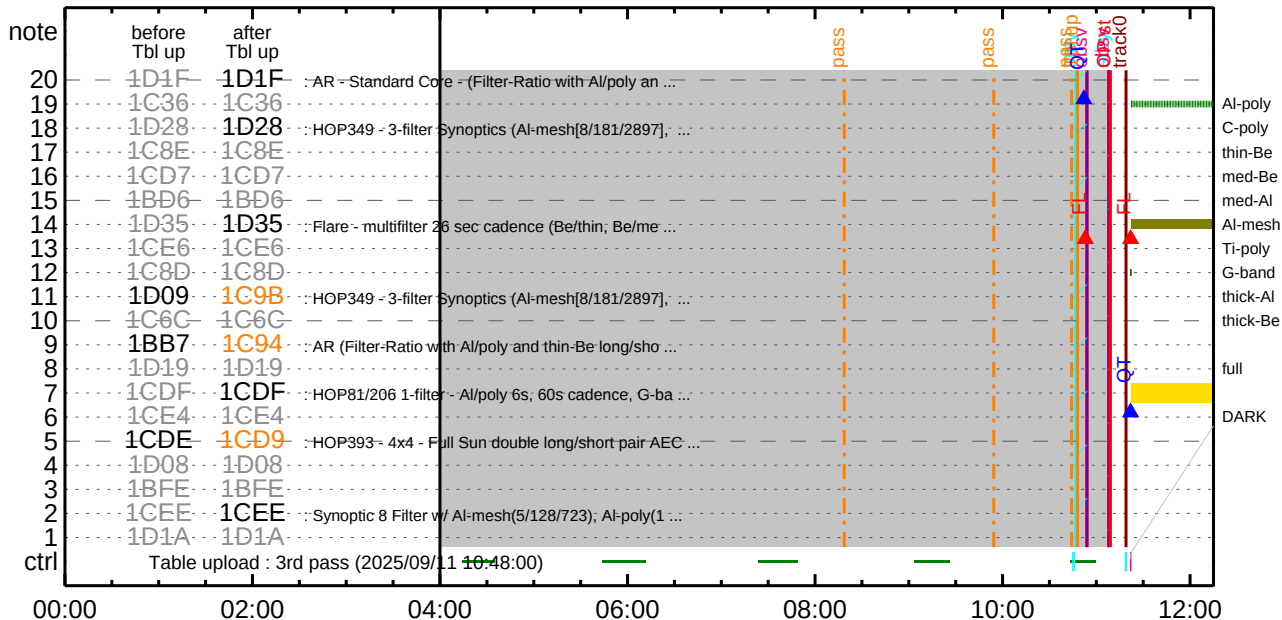
* * * * *

Flare Detection

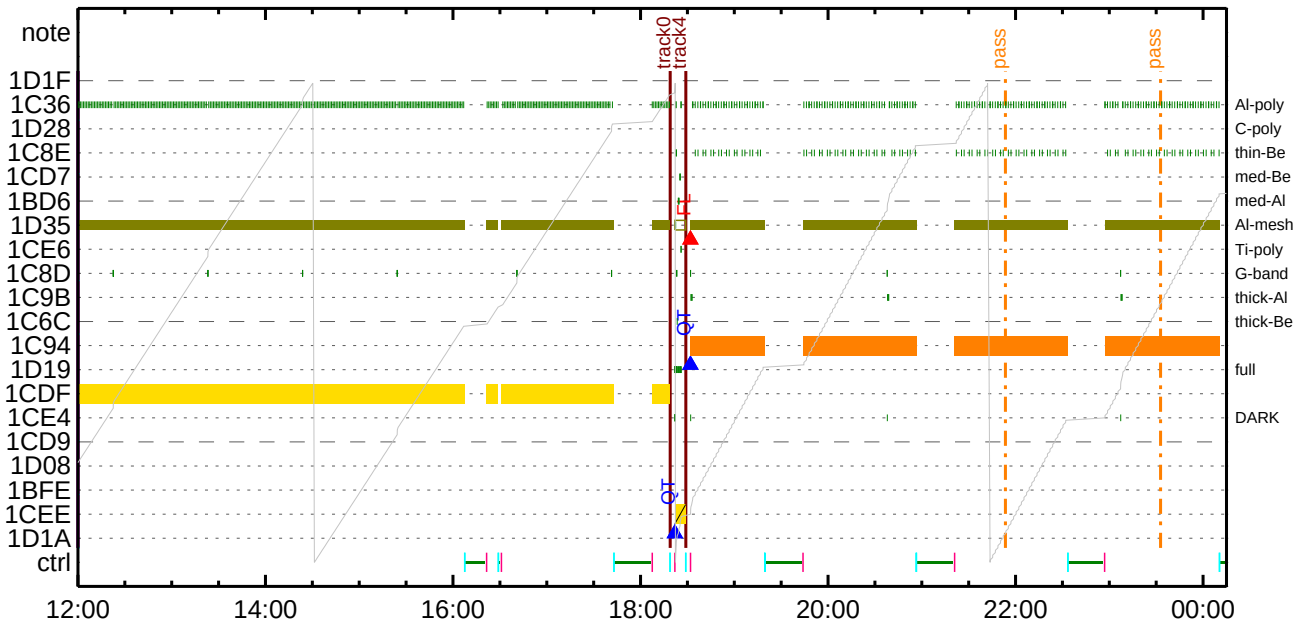
* * * * *

FLD Patrol												
Term			Pointing (x, y)					Comment				
09/11 10:49:00 - 09/11 18:19:18			cannot be identified									
09/11 18:29:18 - 09/12 05:53:24			Track (523.4, -372.5) @ 09/11 18:29:00					AR 14213				
09/12 06:00:18 - 09/12 17:50:48			Track (602.7, -364.2) @ 09/12 06:00:00					AR 14213				
09/12 18:00:48 - 09/13 06:03:24			Track (677.1, -354.5) @ 09/12 18:00:30					AR 14213				
09/13 06:10:18 - 09/16 10:50:00			Track (743.0, -343.6) @ 09/13 06:10:00					AR 14213				
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

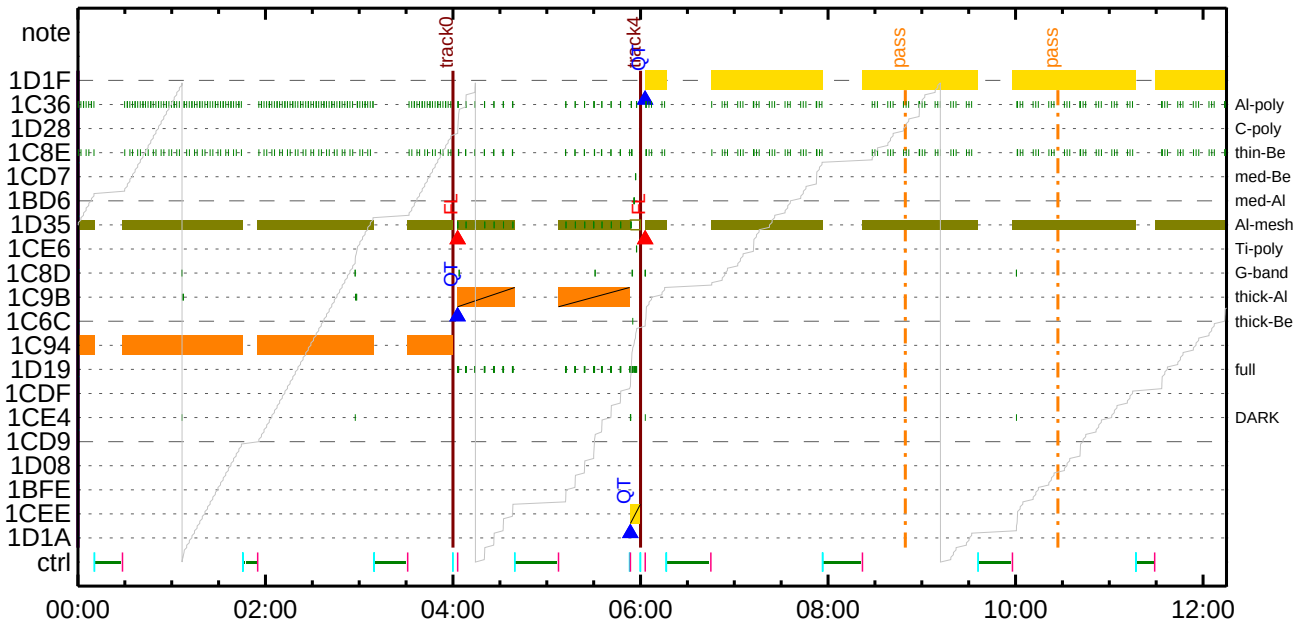
CMDI #0833 2025/09/11



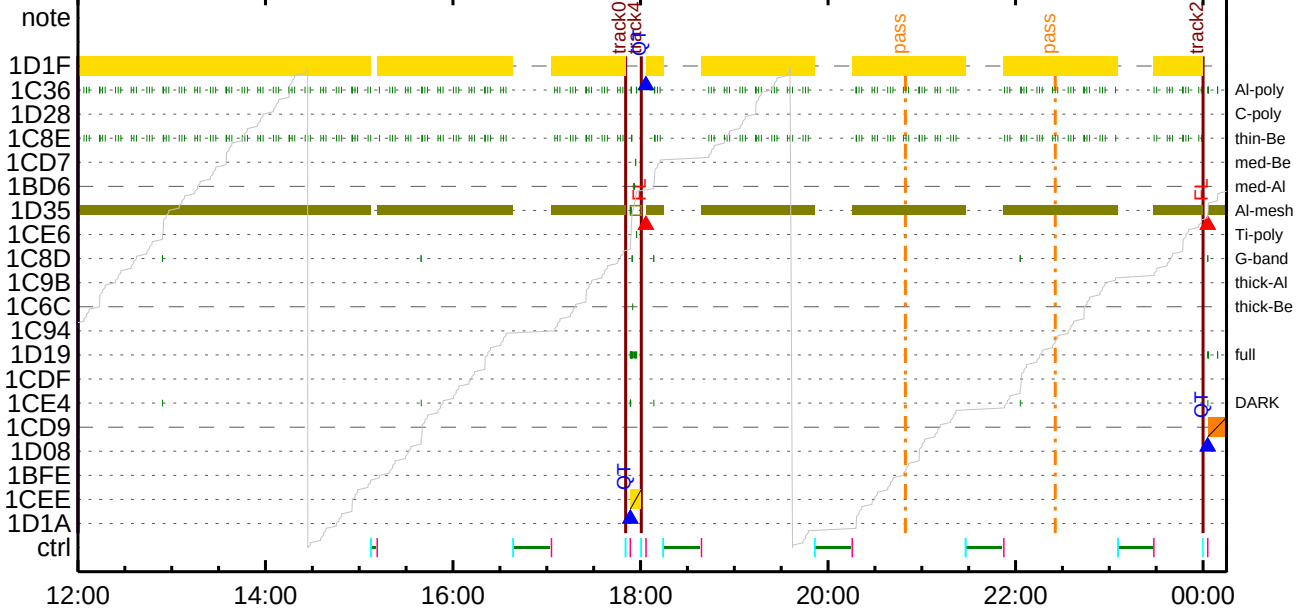
CMDI #0833 2025/09/11



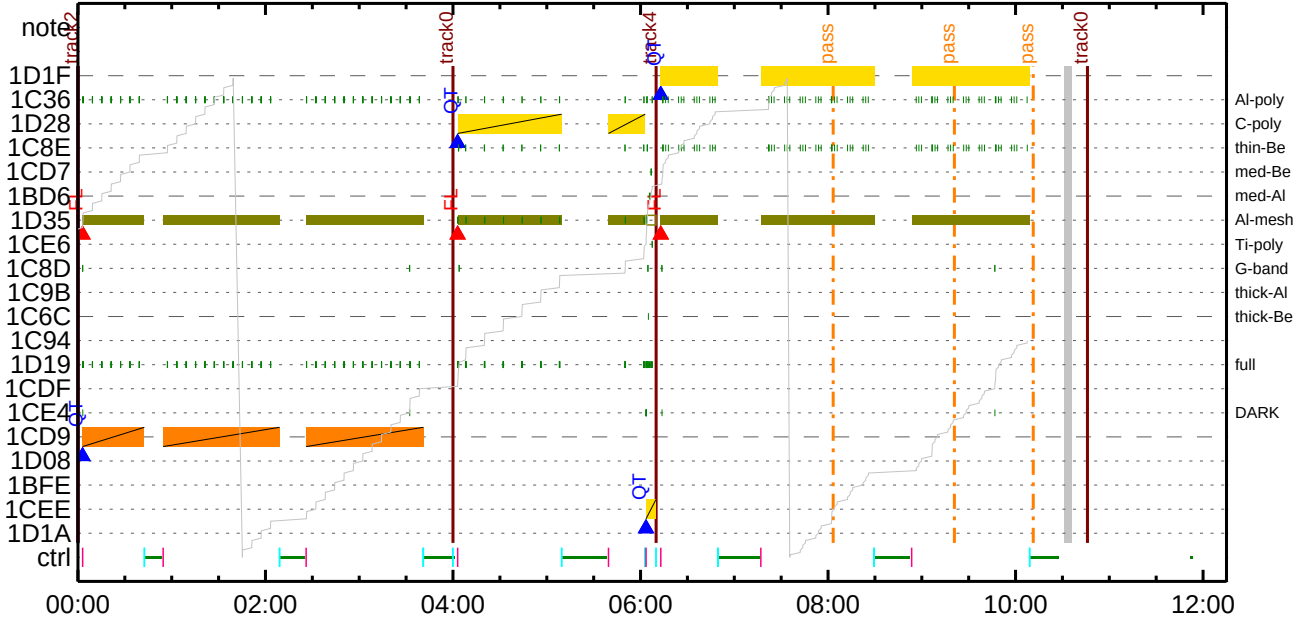
CMDI #0833 2025/09/12



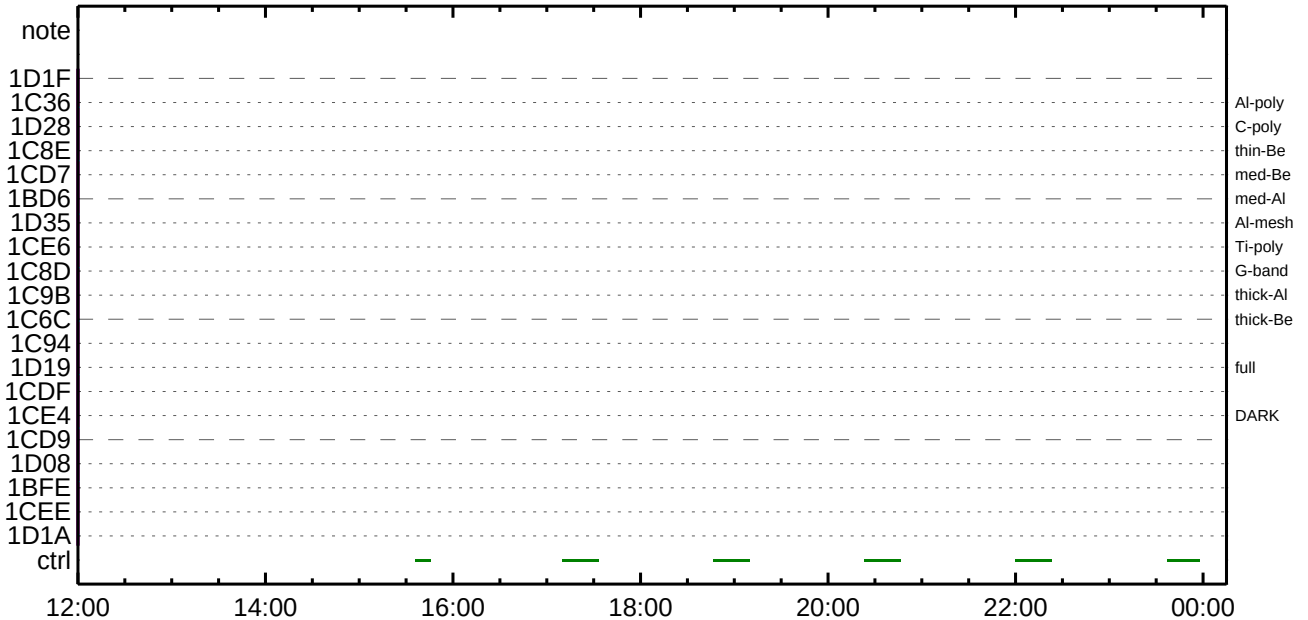
CMDI #0833 2025/09/12



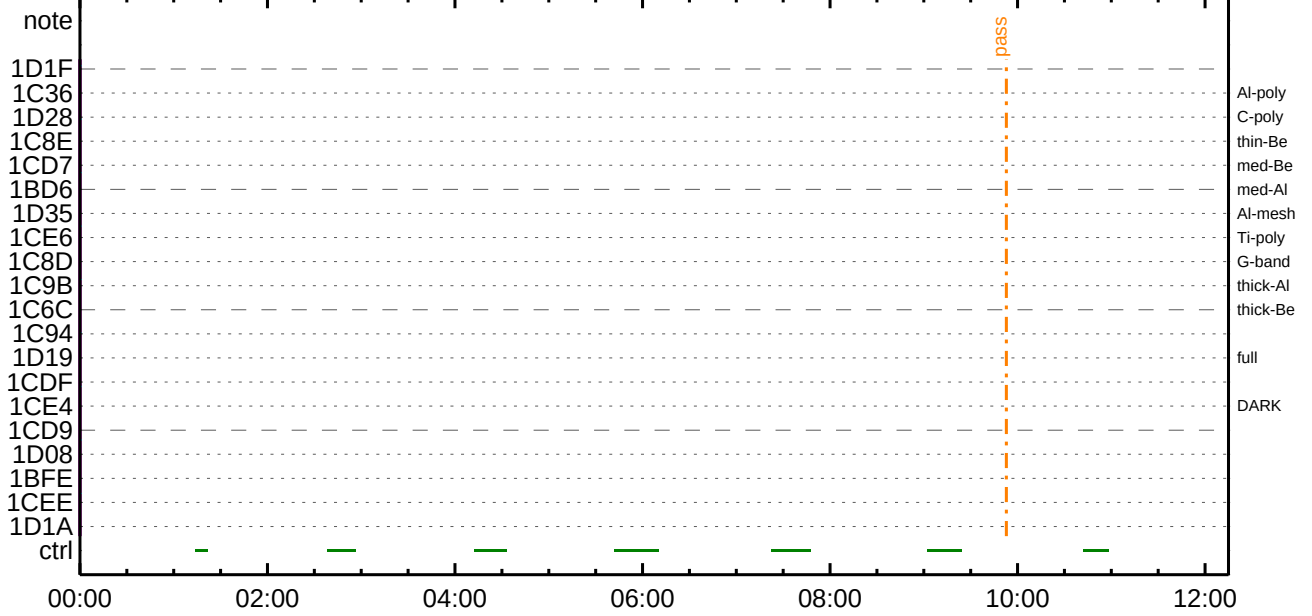
CMDI #0833 2025/09/13



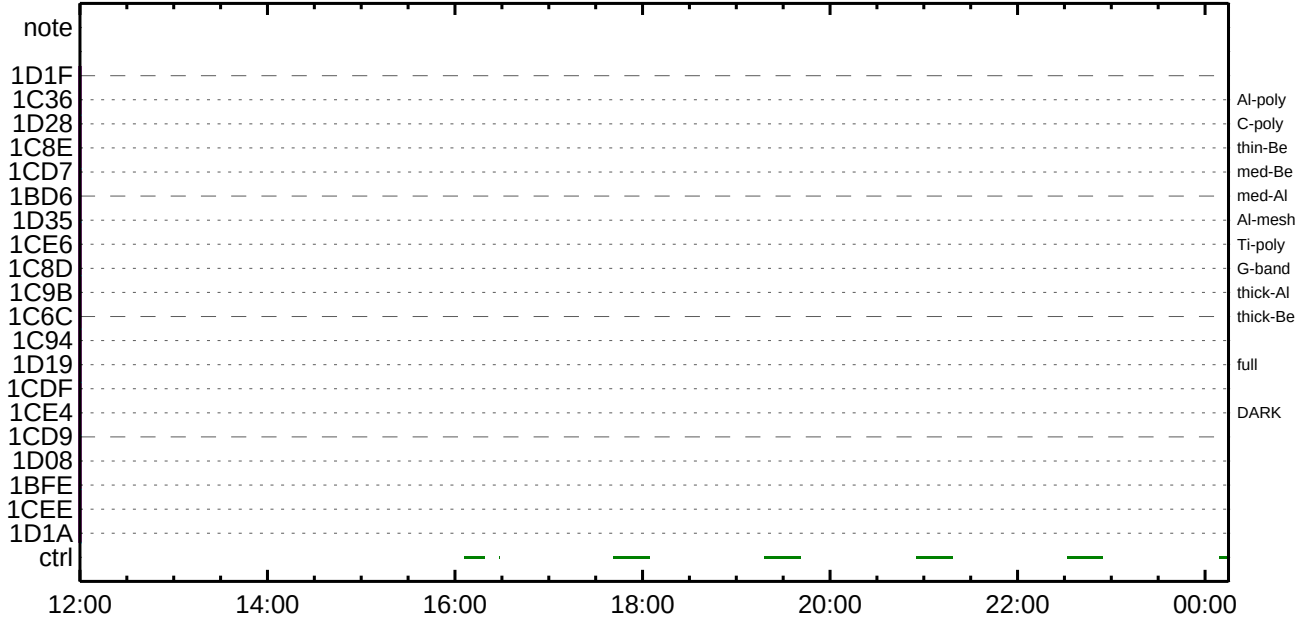
CMDI #0833 2025/09/13



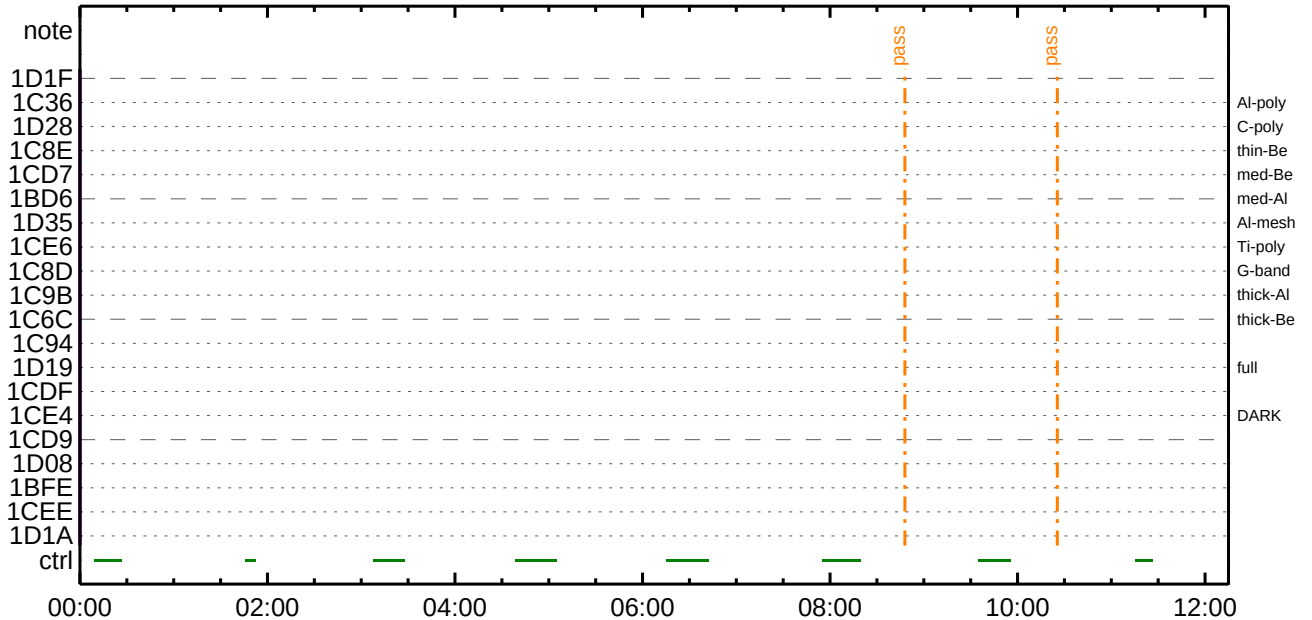
CMDI #0833 2025/09/14



CMDI #0833 2025/09/14



CMDI #0833 2025/09/15



[illegible]

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGŵı;¼ŵÉ;¼ŵŶóŵ×
0100 C. *****
0101 C.
0102 C. ;ãOP/OGŵı;¼ŵÉ;ã
0103 S. OP op-505:OP
0104 ()
0105 S. OG og-505:OG
0106 ()
0107 C.
0108 C. ;ãNMOG&OPîî°èŵŶŵŶóŵ×;ã
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ŵŶŵŶóŵ×½ªî»¤ò³îÇ$
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î¼Ē¹Ç•Ē²İOK¤ò³îÇ$
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ŵŶŵŶóŵ×½ªî»¤ò³îÇ$
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î¼Ē¹Ç•Ē²İOK¤ò³îÇ$
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ŵŶŵŶóŵ×½ªî»¤ò³îÇ$
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP¤î¼Ē¹Ç•Ē²İOK¤ò³îÇ$
0165 C.
0166 C. ***** °Ē²¼¤î¼Ē¹Ç•Ē²İOK¤ò³îÇ$ *****
0167 C. DHUŵŶŵŶóŵ×½ªî»¤ò³îÇ$ *****
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;$ OPOG UPLOAD¤î¼Ē¹Ç•Ē²İOK¤ò³îÇ$ *****
0180 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0181 C.
0182 C. TIŵŶŵŶóŵ×½ªî»¤ò³îÇ$ (UT)
0183 +. TI 2025-09-11 11:04:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2025-09-11 11:04:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2025-09-11 11:04:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2025-09-11 11:08:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê¼¤İÄê¼İİñ¤İ¥Á¥$¥Ä¥~¹äiÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]      EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]      EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]      EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¼È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHU¥â;¼¥É;Ê¼ý½,¥İ;¼¥È;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2025-09-11 11:08:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2025-09-11 11:08:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2025-09-11 11:08:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÄİ¤İ»ö¼Ý¤ÈÄ¤¹¤èDCBC•×²è *****
0265 C. (¼å°İ¥Ö¥Ä¥È¥Ð¥È¥â¥Ç¥ë¤È½¼¤¼Ä»Ü¤¹¤è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ¥Ð¥¹•İ Daily±;İİñ¤İ'Ø¤¹¤èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOS¥Ä¥$¥Ä¥~¼Ä»Ü;ä
0276 C.
0277 C. ***** LOS *****
0278 C.
```

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main-506 2025-09-11 11:54:36 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Öòò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÒÄÖñ¬°ÄÄèñ•ñç,ç°Ê²¼ñî°EÄ,¼è½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñç¼ñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲEŲÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñ¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñç¼ñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 7s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-09-11 11:08:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0156 C. (¼å°îÿÖÿÄÿËÿÐÿËÿÄÿÇÿËÿËÿ¼ñ¼Ä»Û¹ñë)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¼Ðÿ¹•Ï Daily±¿íññÊ'Øñ¹ñëDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOSÿÄÿSÿÄÿ¬¼Ä»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-507 2025-09-11 11:54:36 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¼¿¼Ä¼•µ°Ë»Í×ÄÇ¼ÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËè¼µ•ííË;Ë¼Ë¼°ÇÖ¼¼•¼¿¼¹Ç¼Í;ÇÄ®,ù¼¹¼è¼D¼ÇÄ÷¿®¼•¼Ë¼¼¼³¼Ë;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 80 80 20 20)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 08)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 08 20)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 85 83 08 08)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0b 80 80 08 08)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 +. DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 +. DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 +. DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 14)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 0e)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2025-09-11 11:08:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP 'ûÄîñí»ö¼Ÿ¼ËÄD¼¹¼èDCBC•x²è *****
0089 C. (¾ä°íŸÓŸÄŸËŸDŸËŸÄŸçŸËè¼¼¼Ä»Ü¼¹¼è)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** ŸDŸ¹•í Daily÷¿íñ¼Ë'Ø¼¹¼èDCBC•x²è *****
0095 . S. DC-BC dcbc-153:DCBC
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0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÃŸ-¼Â»Ü;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.

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*** OP Sequence for XRT ***

2025/09/11	11:18:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	11:18:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	11:18:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/11	11:19:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 b3 cf 01 db
2025/09/11	11:19:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/11	11:19:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/11	11:19:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/11	11:19:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/11	11:19:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	11:21:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2025/09/11	11:21:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/11	11:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	16:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	16:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	16:07:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	16:07:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/11	16:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/11	16:20:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/11	16:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	16:29:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	16:29:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	16:29:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	16:29:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/11	16:30:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/11	16:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	16:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/11	17:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	17:43:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	17:43:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	17:43:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/11	17:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/11	18:06:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/11	18:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	18:18:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	18:18:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	18:18:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/11	18:19:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/11	18:19:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/11	18:19:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/11	18:19:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/11	18:21:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/09/11	18:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	18:28:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	18:28:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	18:28:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/11	18:29:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 03 ce 01 db
2025/09/11	18:29:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8

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2025/09/11	18:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/11	18:29:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/11	18:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/11	18:29:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	18:31:56.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2025/09/11	18:31:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/11	18:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	19:19:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	19:19:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	19:19:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	19:19:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/11	19:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/11	19:43:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/11	19:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	20:56:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	20:56:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	20:56:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	20:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/11	20:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/11	21:20:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/11	21:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/11	22:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	22:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/11	22:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/11	22:33:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/11	22:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/11	22:56:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/11	22:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	00:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	00:10:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	00:10:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	00:10:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	00:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	00:27:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	00:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	01:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	01:45:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	01:45:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	01:45:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	01:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	01:54:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	01:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	03:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	03:09:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	03:09:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	03:09:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	03:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	03:30:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	03:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			

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2025/09/12	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	04:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/12	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00
2025/09/12	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/12	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/12	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/12	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/12	04:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/09/12	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/12	04:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	04:39:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	04:39:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	04:39:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	04:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	05:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	05:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/12	05:53:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	05:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	05:53:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	05:53:24.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/12	05:53:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/12	05:53:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/12	05:53:30.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/12	05:53:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/09/12	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	05:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	06:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/12	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 03 ce 01 db
2025/09/12	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/12	06:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/12	06:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/12	06:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/12	06:02:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	06:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2025/09/12	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/12	06:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	06:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	06:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	06:16:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	06:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	06:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	06:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2025/09/12	07:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	07:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12			MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/09/12	07:56:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	07:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	07:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	08:21:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	08:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	09:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	09:36:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	09:36:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	09:36:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	09:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	09:57:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	09:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	11:17:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	11:17:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	11:17:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	11:17:06.5	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	11:20:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	11:28:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	11:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	15:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	15:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	15:07:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	15:07:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	15:10:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	15:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	15:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	16:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	16:38:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	16:38:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	16:38:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	16:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	17:02:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	17:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	17:50:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	17:50:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	17:50:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/12	17:50:30.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/12	17:50:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/12	17:50:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/12	17:50:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/12	17:53:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/09/12	17:53:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	18:00:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/12	18:00:30.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 03 ce 01 db
2025/09/12	18:00:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/12	18:00:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/12	18:00:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0

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2025/09/12	18:00:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/12	18:00:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	18:03:26.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2025/09/12	18:03:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/12	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	18:14:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	18:14:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	18:14:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	18:14:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	18:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	18:38:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	18:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	19:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	19:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	19:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	19:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	19:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	20:14:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	20:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	21:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	21:28:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	21:28:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	21:28:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	21:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	21:51:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	21:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	23:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	23:05:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	23:05:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/12	23:05:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/12	23:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/12	23:27:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/12	23:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/12	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/12	23:59:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/13	00:00:00.0	AOCs_OrE-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	02 03 ce 01 db
2025/09/13	00:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/13	00:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/13	00:00:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/13	00:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	00:00:24.0	XRT_FLD_RESET_446_OG [0x1be]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	00:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2025/09/13	00:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/13	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	00:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	00:42:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	00:42:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	00:42:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	00:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

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2025/09/13	00:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	00:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	02:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	02:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	02:09:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	02:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	02:25:00.0	XRT_Custom_430_OG [0x1ae]				
2025/09/13	02:26:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	03:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	03:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	03:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	03:41:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	03:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/13	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2025/09/13	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/13	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/13	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/13	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	04:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/09/13	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/13	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	05:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	05:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	05:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	05:09:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	05:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	05:38:30.0	XRT_Custom_430_OG [0x1ae]				
2025/09/13	05:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	06:03:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	06:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	06:03:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/13	06:03:24.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/13	06:03:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/13	06:03:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	06:03:30.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/09/13	06:03:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	06:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/13	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 03 ce 01 db
2025/09/13	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/13	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/13	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]				

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2025/09/13	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/13	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	06:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2025/09/13	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/13	06:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	06:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	06:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	06:49:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	06:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	07:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	07:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	08:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	08:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	08:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	08:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	08:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	08:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	08:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	10:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	10:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	10:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	10:09:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	10:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	10:46:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00