

XRT Timeline to be uploaded on 2025/09/13

Period: 2025/09/13 10:36:00 - 2025/09/18 11:04:00

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Normal mode

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XOB #1D0D: HOP476_SOOP_AR_Be_Thin - (Med-Be) - Med-Be (Al-Thick) - AEC3 - PFB - Be-Thick- Be-Med 384X384 - AEC3														
Term			Pointing (x, y)					Comment						
09/13 10:49:00 - 09/13 18:03:24			Track (-788.9, 65.1) @ 09/13 10:46:00					# OP start + 10min + AR 14216						
09/13 18:16:30 - 09/14 03:59:54			Track (-748.3, 58.4) @ 09/13 18:13:30					HOP 515(18:30-23:15) + AR 14216						
PROG= 04 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 92		1-time(s)		2.0sec										
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		180-time(s)		2.0sec										
Seqn= 3		1-time(s)		2.0sec										
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2	2.0sec
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2	2.0sec
Seqn= 39		1-time(s)		2.0sec										
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1	2.0sec
Seqn= 80		1-time(s)		2.0sec										
		med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
		thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1CEE: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(512/8192/32768), Med-Be(128/5768)														
Term			Pointing (x, y)			Comment								
09/13 18:06:30 - 09/13 18:13:24			Fixed (0.0, 0.0)			synoptic, shifted 3.5 min								
09/14 06:13:30 - 09/14 06:30:00			Fixed (0.0, 0.0)			HOP349 + synoptic, shifted 20.0 min								

PROG= 02 1-time(s)														
Subr= 1			1-time(s)			2.0sec								
Seqn= 5			1-time(s)			2.0sec								
Open/Ti-poly			Open/thick-Al			close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
Open/Ti-poly			Open/thick-Al			close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
Open/Ti-poly			Open/thick-Al			close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
Open/Ti-poly			Open/thick-Al			close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0 0 2.0sec
Open/Ti-poly			Open/thick-Al			close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0 0 2.0sec
Seqn= 26			1-time(s)			2.0sec								
Open/Al-mesh			Open/Al-mesh			close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh			Open/Al-mesh			close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh			Open/Al-mesh			close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 15			1-time(s)			2.0sec								
Al-poly/Open			Al-poly/Open			close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Al-poly/Open			Al-poly/Open			close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Al-poly/Open			Al-poly/thick-Al			close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 83			1-time(s)			2.0sec								
thin-Be/Open			thin-Be/Open			close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
thin-Be/Open			thin-Be/Open			close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
thin-Be/Open			thin-Be/Open			close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 23			1-time(s)			4.0sec								
Open/G-band			Open/G-band			open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0 0 2.0sec
Open/G-band			Open/G-band			close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Subr= 2			1-time(s)			2.0sec								
Seqn= 41			1-time(s)			2.0sec								
Open/thick-Be			Open/thick-Be			close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
Seqn= 17			1-time(s)			2.0sec								
med-Al/Open			med-Al/thick-Al			close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
med-Al/Open			med-Al/thick-Al			close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
med-Al/Open			med-Al/Open			close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 33			1-time(s)			2.0sec								
med-Be/Open			Open/thick-Al			close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
med-Be/Open			med-Be/Open			close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
med-Be/Open			med-Be/Open			close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 36			1-time(s)			2.0sec								
Al-poly/Ti-poly			Al-poly/thick-Al			close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
Al-poly/Ti-poly			Al-poly/thick-Al			close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp.	AEC Buffer Interval

XOB #1D28: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min														
Term			Pointing (x, y)			Comment								
09/14 04:34:30 - 09/14 05:42:30			Fixed (0.0, 0.0)			HOP349 + synoptic, shifted 20.0 min								

PROG= 18 Inf.-time(s)														
Subr= 1			1-time(s)			300.0sec								
Seqn=100			1-time(s)			2.0sec								
Open/Al-mesh			Open/Al-mesh			close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec

	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 84	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 57	1-time(s)	2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Seqn= 81	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
	Subr= 2	10-time(s)	720.0sec										
	Seqn= 8	1-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Seqn= 6	1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
	Seqn= 29	1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D27: HOP500_SOOP_FLARE_MODE Be_Thick_2s_Fixed_Exposure														
Term		Pointing (x, y)				Comment								
09/13 10:49:00 - 09/13 18:03:24		Track (-788.9, 65.1) @ 09/13 10:46:00				# OP start + 10min + AR 14216								
09/13 18:16:30 - 09/14 03:59:54		Track (-748.3, 58.4) @ 09/13 18:13:30				HOP 515(18:30-23:15) + AR 14216								
09/14 04:34:30 - 09/14 05:42:30		Fixed (0.0, 0.0)				HOP349 + synoptic, shifted 20.0 min								
PROG= 03 30-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 87		1-time(s)		2.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0	0	2.0sec
Subr= 2		120-time(s)		2.0sec										
Seqn= 31		1-time(s)		2.0sec										
med-Be/Open		Open/thick-Al	close	Safe	Norm	16ms	Obs	1x1	384x384 (1024, 1024)		DPCM	3	0	2.0sec
Seqn= 20		10-time(s)		2.0sec										
Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		DPCM	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

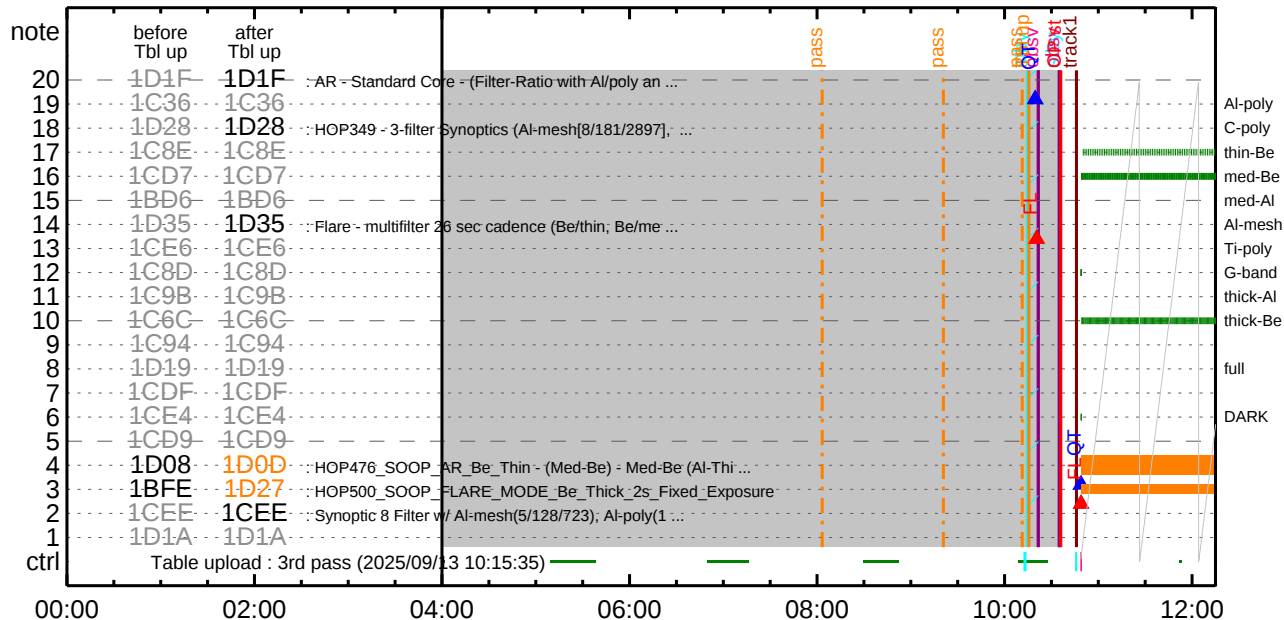
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Flare Detection

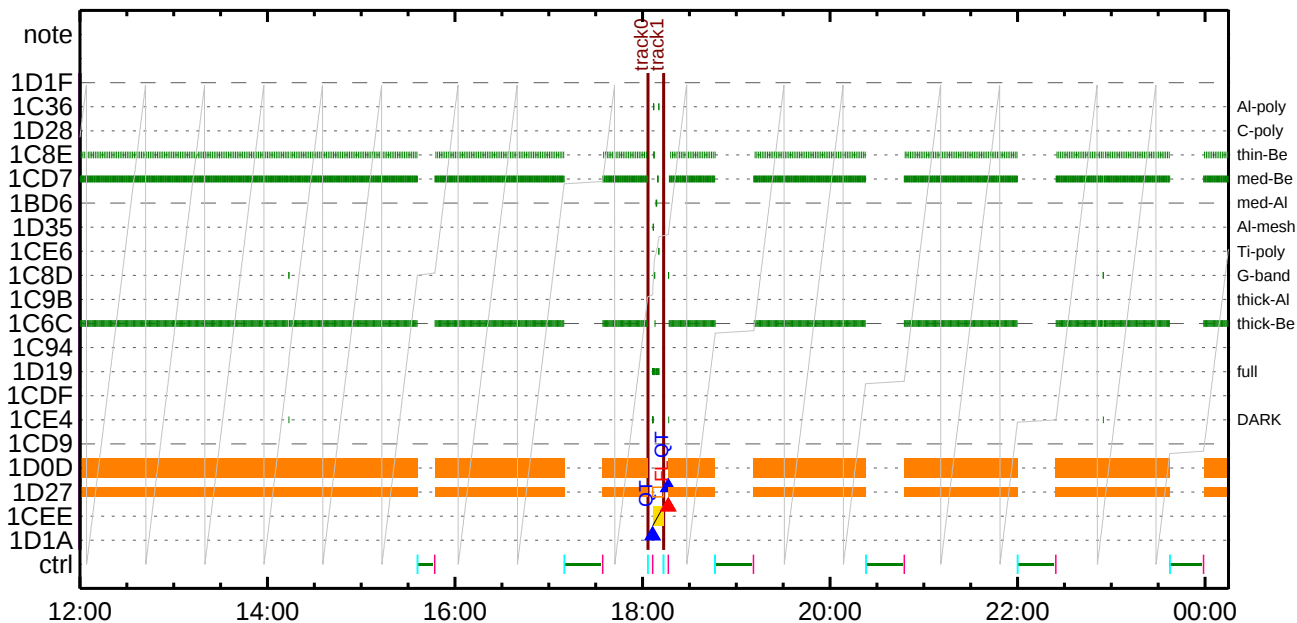
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FLD Patrol													
Term		Pointing (x, y)				Comment							
09/13 10:16:35 - 09/13 18:03:48		cannot be identified											
09/13 18:13:48 - 09/14 06:10:48		Track (-748.3, 58.4) @ 09/13 18:13:30				HOP 515(18:30-23:15) + AR 14216							
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

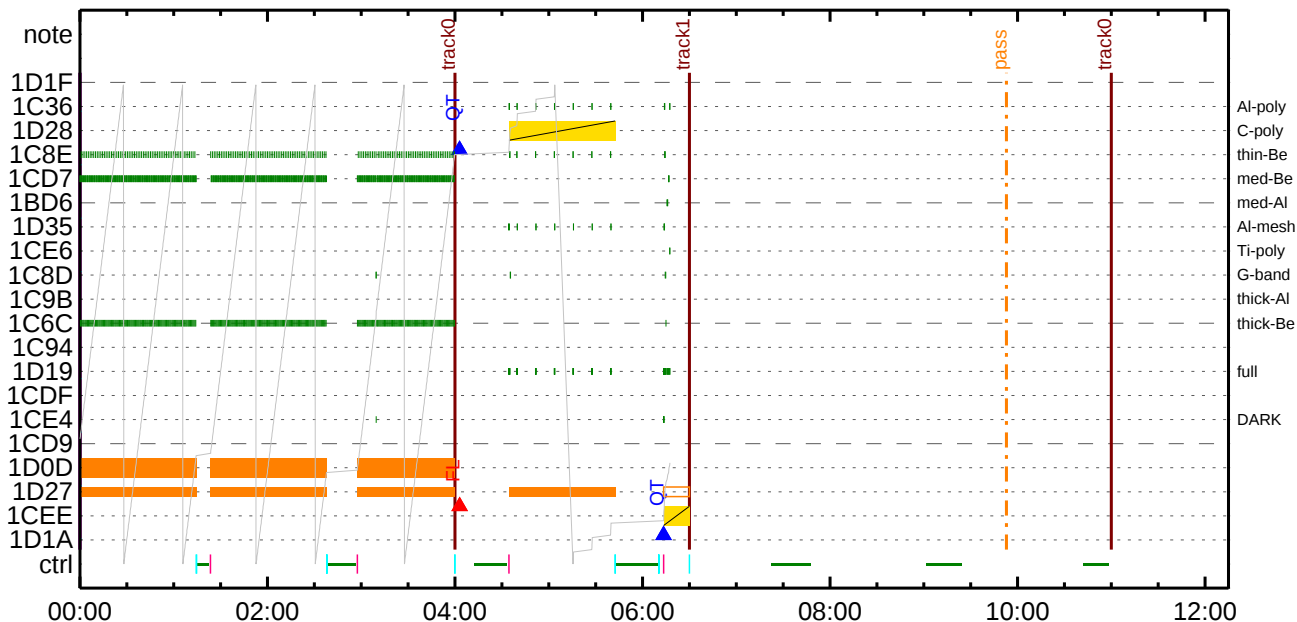
CMDI #0839 2025/09/13



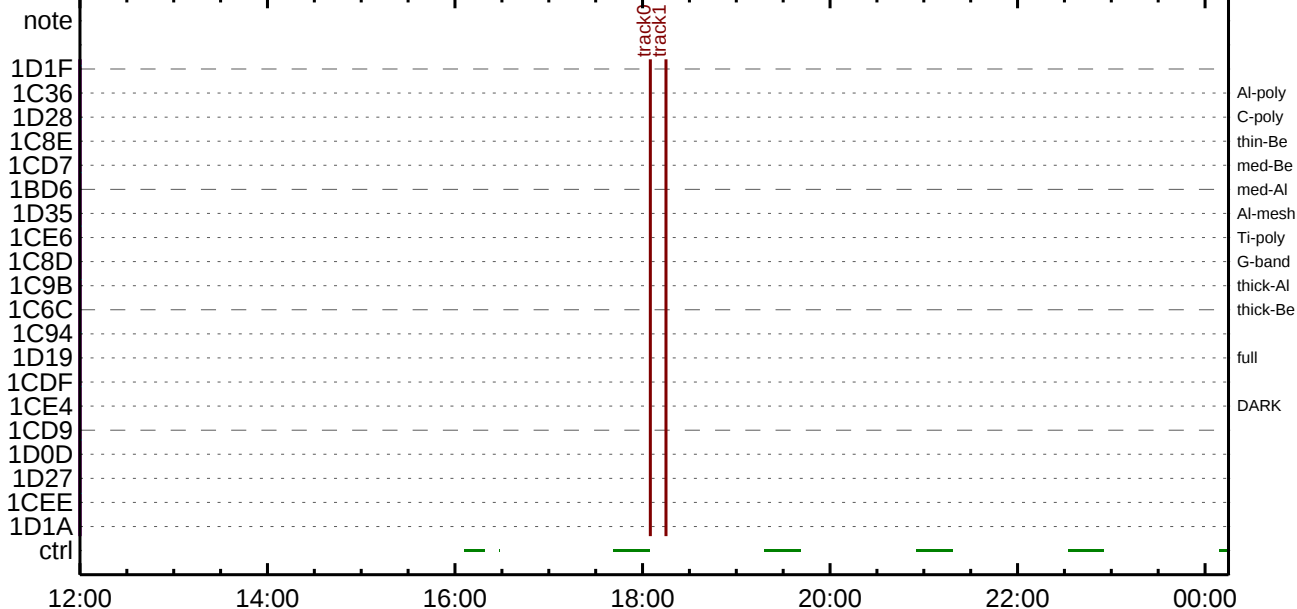
CMDI #0839 2025/09/13



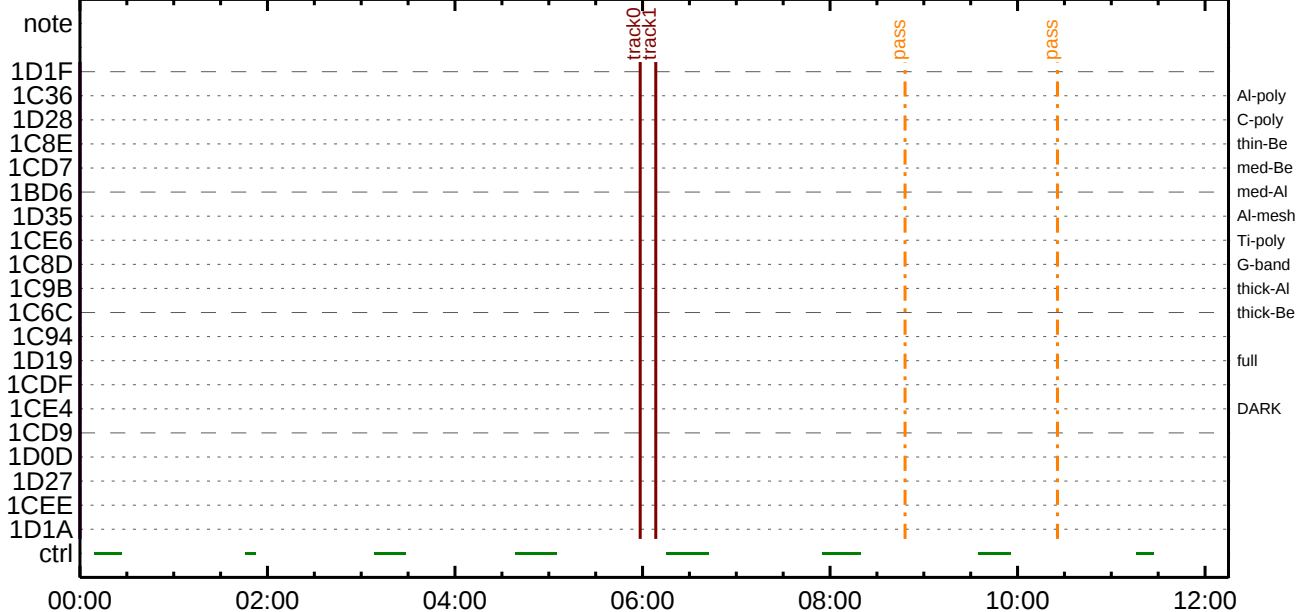
CMDI #0839 2025/09/14



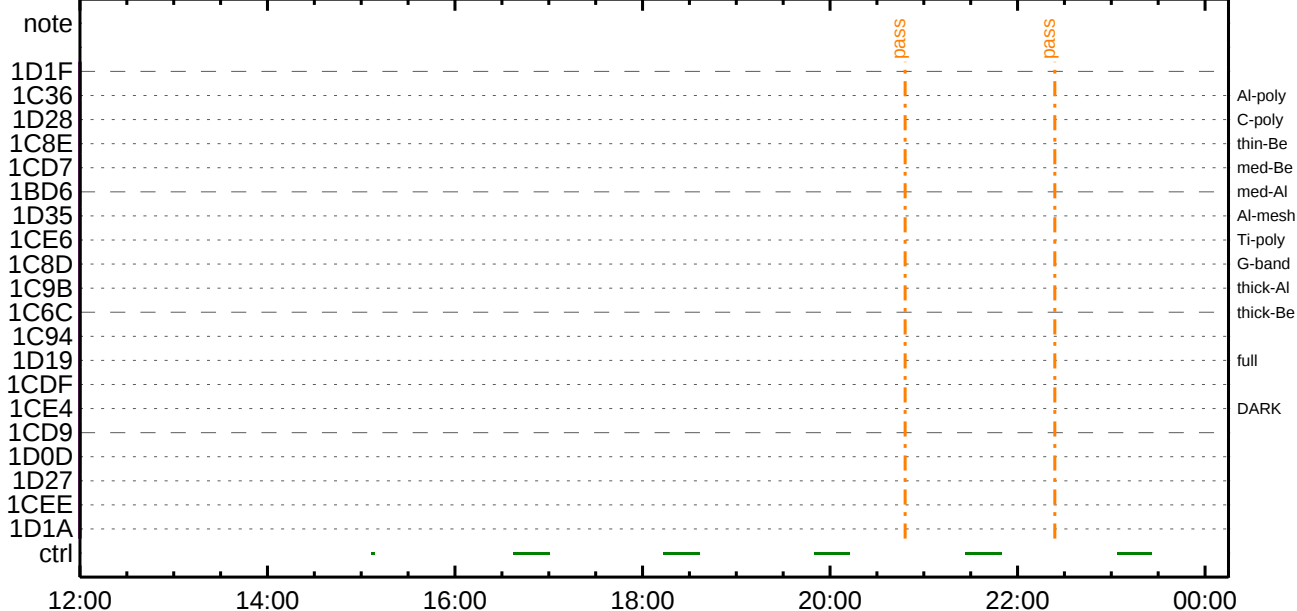
CMDI #0839 2025/09/14



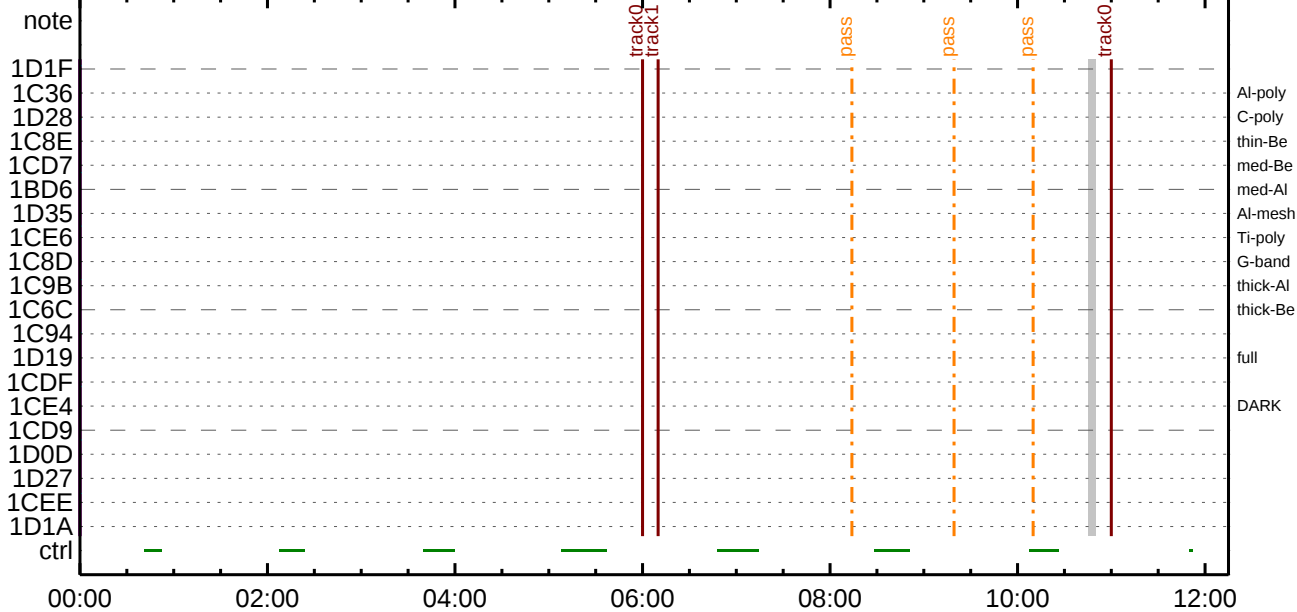
CMDI #0839 2025/09/15



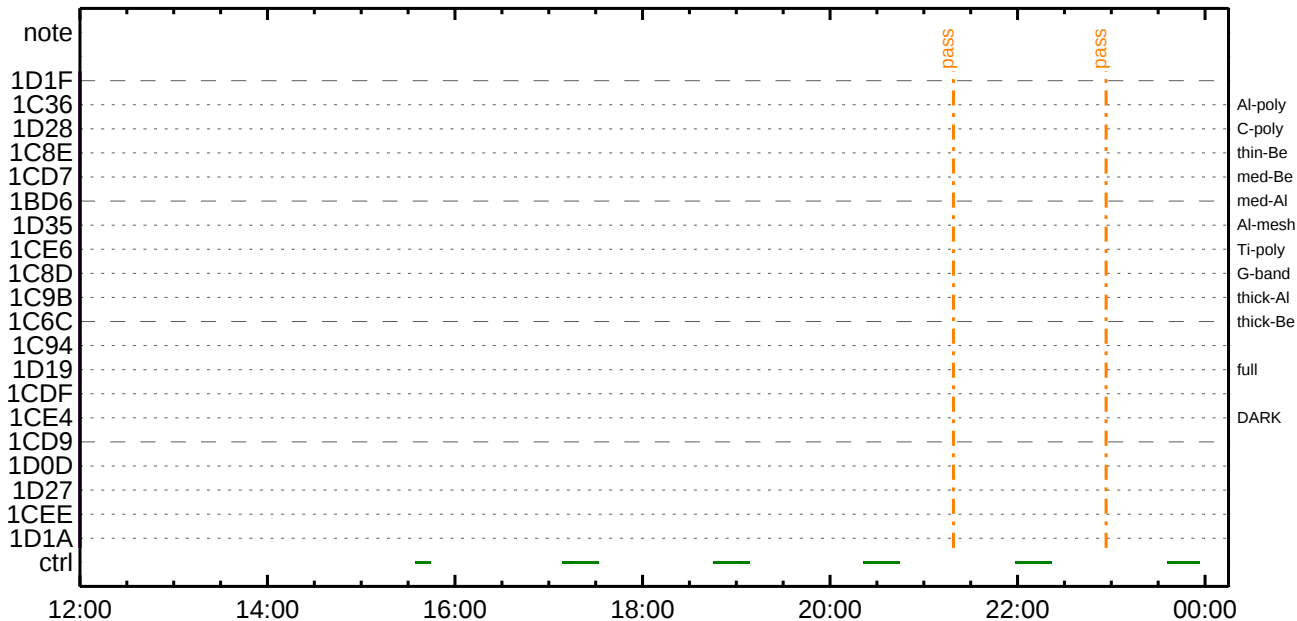
CMDI #0839 2025/09/15



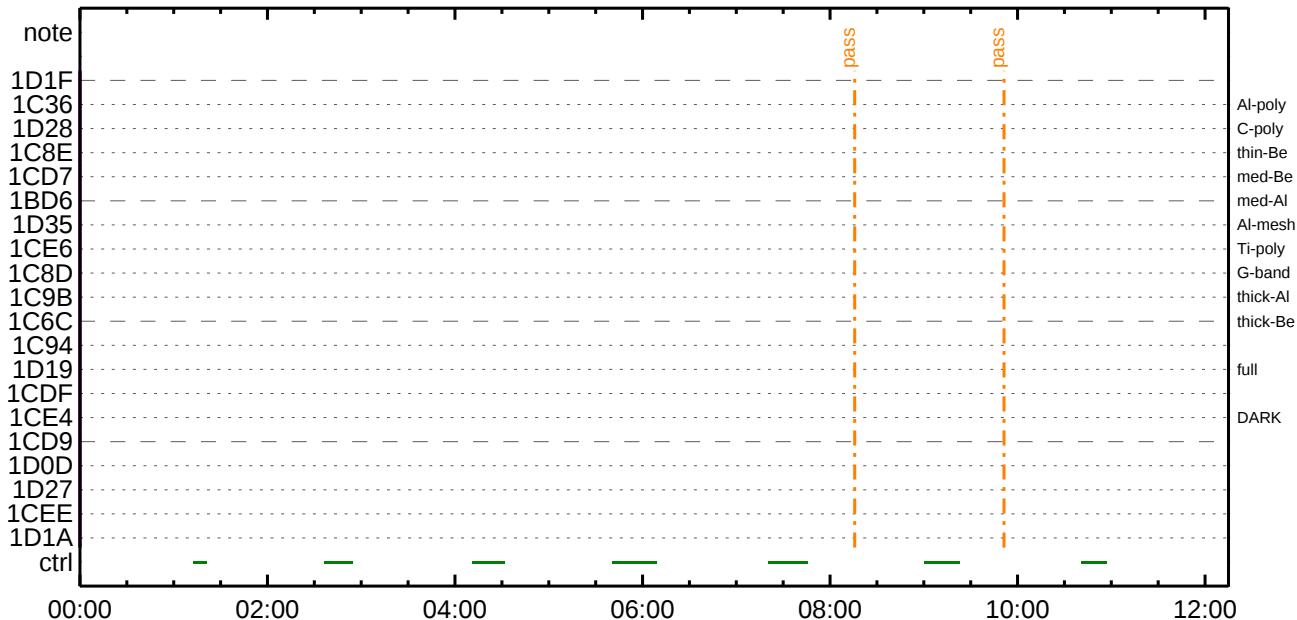
CMDI #0839 2025/09/16



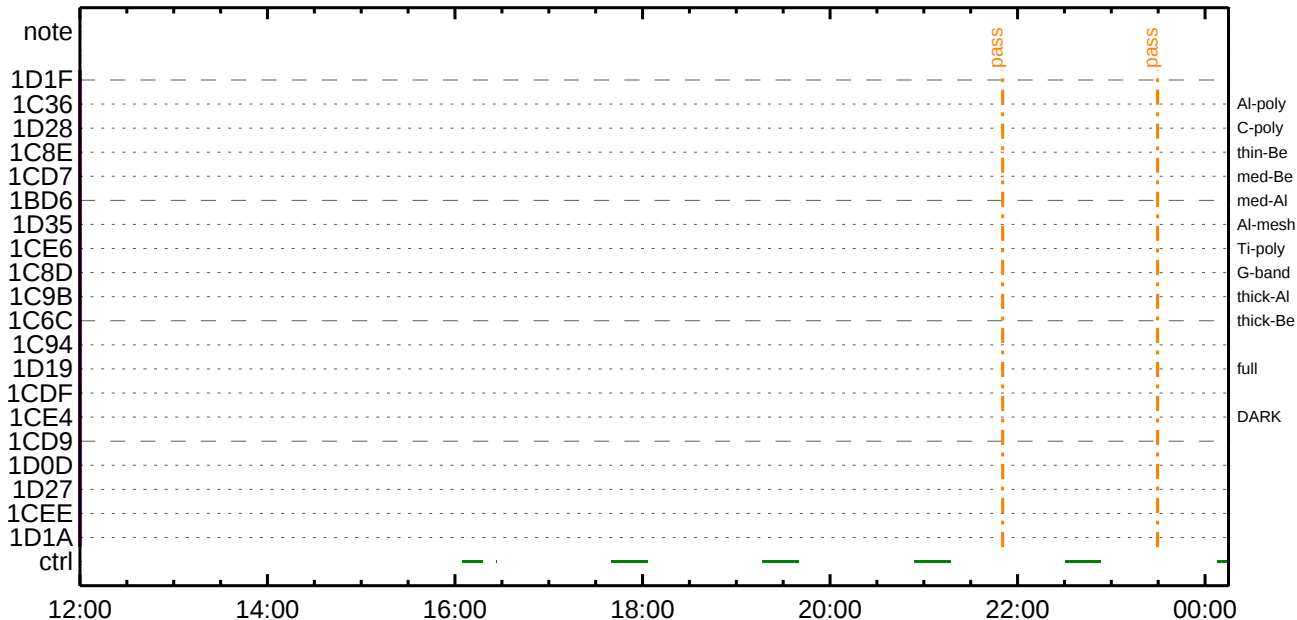
CMDI #0839 2025/09/16



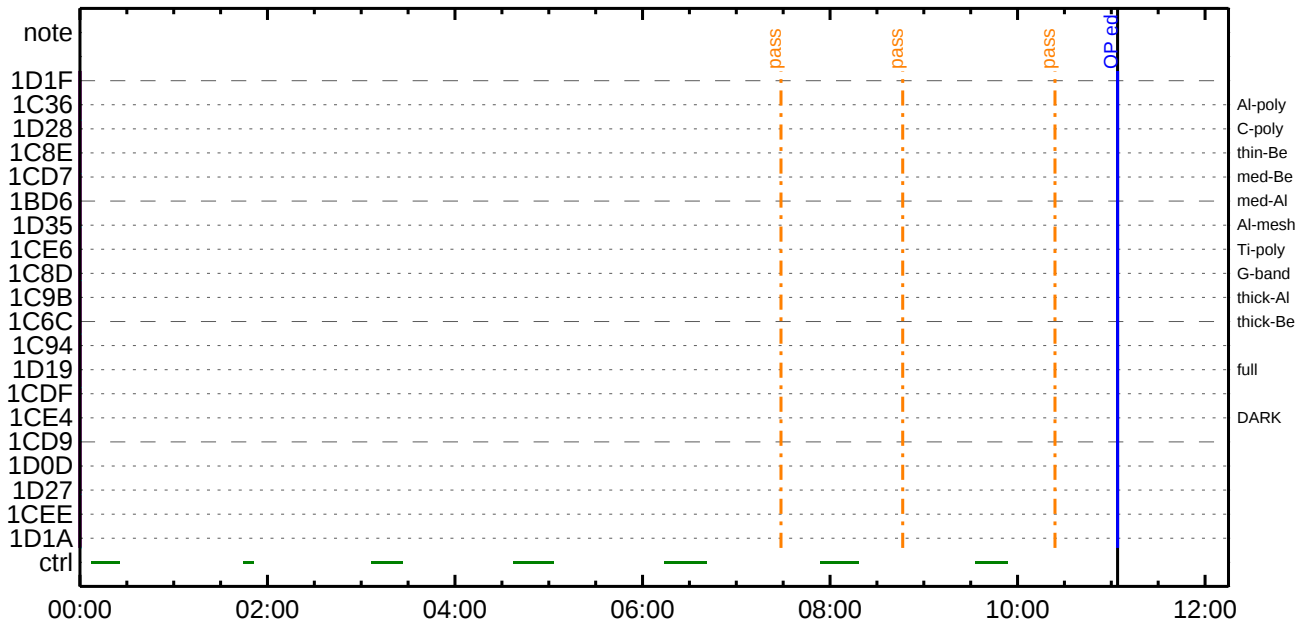
CMDI #0839 2025/09/17



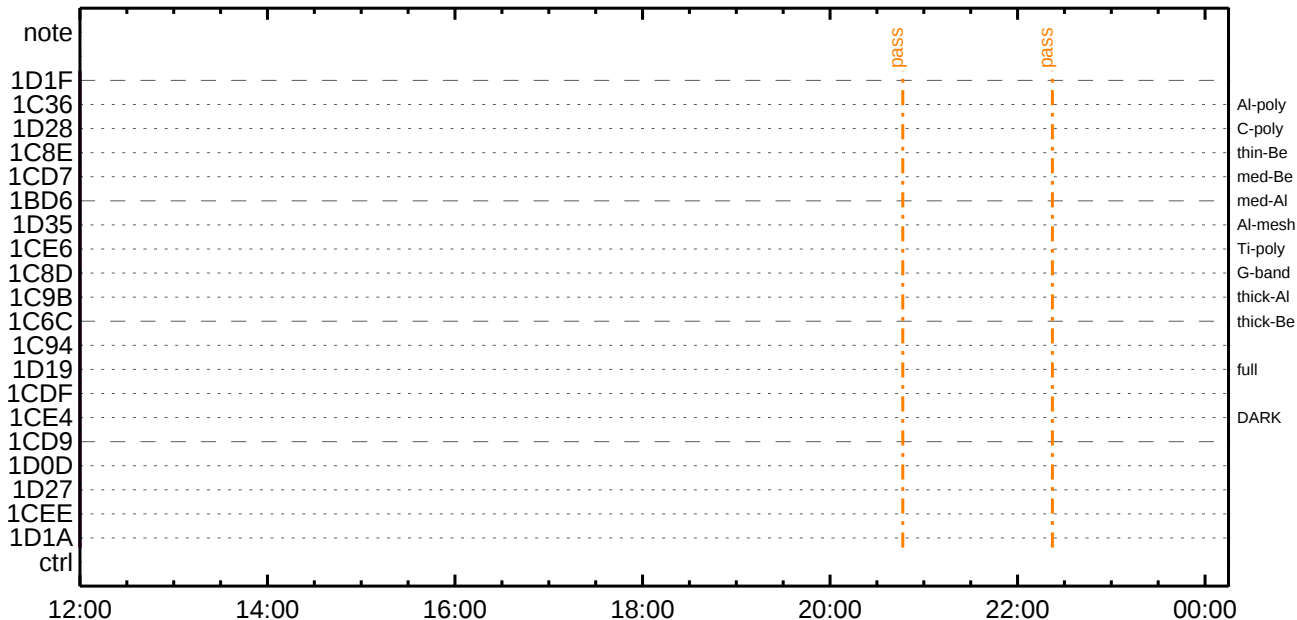
CMDI #0839 2025/09/17



CMDI #0839 2025/09/18



CMDI #0839 2025/09/18



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main-510 2025-09-13 14:21:01 194 33 SOLAR-B MAIN /\n0001 C.\n0002 . C. ***** AOS *****\n0003 C.\n0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä\n0005 C.\n0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®\n0007 +. DC 00-00 NULL_DUMMY_CMD\n0008 C.\n0009 . C. ***** AOCS : Reload orbital element (send every contact) *****\n0010 C. Äí;Ëñç²Äñ•µ°E»Í×ÅÇñÎŲçŲÄŲ×Ųí;¼ŲÉ;ËËè²µ•îÍÊ;ËñÊ¼°ÇÖñ•ñç¼ì¹çñİ;çÀ®,ùñ¹ñÈñÐñÇÄ÷ç®ñ•ñÊñññ³ñÈ;£\n0011 +. DC 02-8E AOCU_ORB_UPD\n0012 C.\n0013 C.\n0014 . C. *****\n0015 C. OP/OGŲí;¼ŲÉ;|ŲÄŲóŲ×\n0016 C. *****\n0017 C.\n0018 . C. ;ãOP/OGŲí;¼ŲÉ;ä\n0019 . S. OP op-510:OP\n0020 ()\n0021 . S. OG og-510:OG\n0022 ()\n0023 C.\n0024 . C. ;ãNMOG&OPîî°èŲÄŲóŲ×;ä\n0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)\n0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET\n0027 BC (20 00 7f 01 02)\n0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40\n0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0\n0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127\n0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0\n0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU\n0033 +. DC 01-22 DHU_MODE_CHNG\n0034 BC (07 0b f8)\n0035 C. çç[HK1_PKT_FORM_NO] EQ 7\n0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s\n0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k\n0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M\n0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC\n0040 . C. ŲÄŲóŲ×½ªî»ñò³ÎÇ$ \n0041 C. çç[HK1_DMP_CHK_FLG] EQ NON\n0042 . C. RAM ID=NMOGñî¾Ê¹ç•ë²ïOKñò³ÎÇ$ \n0043 C.\n0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)\n0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET\n0046 BC (20 80 7f 01 02)\n0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41\n0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0\n0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127\n0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0\n0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU\n0052 +. DC 01-22 DHU_MODE_CHNG\n0053 BC (07 0b f8)\n0054 C. çç[HK1_PKT_FORM_NO] EQ 7\n0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s\n0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k\n0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M\n0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC\n0059 . C. ŲÄŲóŲ×½ªî»ñò³ÎÇ$ \n0060 C. çç[HK1_DMP_CHK_FLG] EQ NON\n0061 . C. RAM ID=NMOGñî¾Ê¹ç•ë²ïOKñò³ÎÇ$ \n0062 C.\n0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)\n0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET\n0065 BC (21 00 41 01 02)\n0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42\n0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0\n0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65\n0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0\n0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU\n0071 +. DC 01-22 DHU_MODE_CHNG\n0072 BC (07 0b f8)\n0073 C. çç[HK1_PKT_FORM_NO] EQ 7\n0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s\n0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k\n0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M\n0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC\n0078 . C. ŲÄŲóŲ×½ªî»ñò³ÎÇ$ \n0079 C. çç[HK1_DMP_CHK_FLG] EQ NON\n0080 . C. RAM ID=NMOG,RAM ID=OPñî¾Ê¹ç•ë²ïOKñò³ÎÇ$ \n0081 C.\n0082 . C. ***** °Ê²¼ñî¾Ä´¶¹°ñÊÉ¬ñ°Ä÷ç® (¾µ-ŲÄŲóŲ×¼ê½çñòÄÓÄæñÇ½ñ•ñë¾ì¹çñÇñâ) *****\n0083 C. DHUŲâ;¼ŲÉ;Ë¼ý½,Ųí;¼ŲÉ;Ëñòîáñ¹\n0084 +. DC 01-22 DHU_MODE_CHNG\n0085 BC (02 0a f8)\n0086 C. çç[HK1_PKT_FORM_NO] EQ 2\n0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S\n0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K\n0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M\n0090 C.\n0091 . C. *****\n0092 C. TI-CMD SET (OPOG STOP/COPY/START)\n0093 C. *****\n0094 C.\n0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñî¾ì¹ç;ç°Ê²¼ñîTI-CMDÄ÷ç®ñî¾Ä¹Ôñ•ñÊñññ³ñÈ;£
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	C.	␣P␣;çSET␣ÈDUMP␣ÏÆ±°ìŸÑ¹␣Ç¹Ô␣ ␣³␣Ê;£					
0097	C.						
0098	. C.	TI¥³ŸP¥óYÉ␣òÃÐİ¿(UT)					
0099	+ . TI	2025-09-13 10:31:00.0					
0100	DC	01-B3 DHU_OP_STOP					
0101	C.	¢¢[HK1_TI_CMD_NUM]		EQ	1COUNTUP		
0102	C.						
0103	+ . TI	2025-09-13 10:31:01.0					
0104	DC	01-B4 DHU_OP_COPY					
0105	C.	¢¢[HK1_TI_CMD_NUM]		EQ	1COUNTUP		
0106	C.						
0107	+ . TI	2025-09-13 10:31:01.0					
0108	DC	01-B5 DHU_OPOG_COPY					
0109	C.	¢¢[HK1_TI_CMD_NUM]		EQ	1COUNTUP		
0110	C.						
0111	+ . TI	2025-09-13 10:35:59.5					
0112	DC	01-B2 DHU_OP_START					
0113	C.	¢¢[HK1_TI_CMD_NUM]		EQ	1COUNTUP		
0114	C.						
0115	C.	°Ê²¼␣îÄê¾íÍñ␣îŸÁŸSŸÅŸ~¹àïÜ					
0116	C.	¢¢[HK1_TI_CMD_ENA/DIS]		EQ	ENA		
0117	C.	¢¢[HK1_TI_CMD_NUM]		EQ	4		
0118	C.	¢¢[HK1_NEXT_EXEC_PIM]		EQ	DHU		
0119	C.	¢¢[HK1_NEXT_EXEC_DC]		EQ	0xB3		
0120	C.						
0121	. C.	*****					
0122	C.	TIIÎ°è¥À¥Ó¥×					
0123	C.	*****					
0124	C.						
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)					
0126	+ . DC	01-23 DHU_DMA_DMP_PRM_SET					
0127	BC	(03 ab 03 01 02)					
0128	C.	¢¢[HK1_DMP_TOP_ADRS_1]		EQ	07		
0129	C.	¢¢[HK1_DMP_TOP_ADRS_0]		EQ	2B		
0130	C.	¢¢[HK1_DMP_BLOCK_NUM]		EQ	3		
0131	C.	¢¢[HK1_DMP_REPEAT_NUM]		EQ	0		
0132	C.	¢¢[HK1_DMA_DMP_PIM]		EQ	DHU		
0133	+ . DC	01-22 DHU_MODE_CHNG					
0134	BC	(07 0b f8)					
0135	C.	¢¢[HK1_PKT_FORM_NO]		EQ	7		
0136	C.	¢¢[HK1_PKT_GEN_TIME]		EQ	0.25 s		
0137	C.	¢¢[HK1_S_TLM_BIT_RATE]		EQ	32k		
0138	C.	¢¢[HK1_X_TLM_BIT_RATE]		EQ	4M		
0139	C.	¢¢[HK1_DMP_CHK_FLG]		EQ	EXEC		
0140	C.						
0141	. C.	¥À¥Ó¥×½ªî»␣ð³İÇ\$					
0142	C.	¢¢[HK1_DMP_CHK_FLG]		EQ	NON		
0143	C.						
0144	. C.	RAM ID=TI_TBL␣î¾È¹Ç•ë²ÌOK␣ð³İÇ\$					
0145	C.						
0146	. C.	DHU¥â;¼¥É;Ê¼ý½, ¥ì;¼¥Ê;Ë␣ðİá␣¹					
0147	+ . DC	01-22 DHU_MODE_CHNG					
0148	BC	(02 0a f8)					
0149	C.	¢¢[HK1_PKT_FORM_NO]		EQ	2		
0150	C.	¢¢[HK1_PKT_GEN_TIME]		EQ	0.5S		
0151	C.	¢¢[HK1_S_TLM_BIT_RATE]		EQ	32K		
0152	C.	¢¢[HK1_X_TLM_BIT_RATE]		EQ	4M		
0153	C.						
0154	. C.	Stop EIS observation and temporarily disable EIS mode changes					
0155	C.						
0156	C.						
0157	C.	***** Start EIS operation (TI set) *****					
0158	C.	Execute, after the success of OP upload.					
0159	C.	Set EIS TI-commands					
0160	+ . TI	2025-09-13 10:35:30.0					
0161	DC	07-FC EIS_MODE_MANU					
0162	BC	(21 02)					
0163	+ . TI	2025-09-13 10:35:40.0					
0164	DC	07-FC EIS_MODE_CHG_DIS					
0165	BC	(22)					
0166	. C.	[] [HK1_TI_CMD_NUM]		EQ	2 COUNTUP		
0167	C.	***** End EIS operation (TI set) *****					
0168	C.						
0169	C.						
0170	C.						
0171	C.	***** XRT START *****					
0172	C.	Execute, after the success of OP upload.					
0173	+ . TI	2025-09-13 10:35:00.0					
0174	DC	07-F0 MDP_XRT_MODE_STBY					
0175	BC	(c3)					
0176	. C.	[] [HK1_TI_CMD_NUM]		EQ	1COUNTUP		
0177	C.						
0178	C.	***** XRT END *****					
0179	C.						
0180	. C.	***** MDP `üÃî␣î»ö¾Ý␣ÈÂÞ␣¹␣èDCBC•×²è *****					
0181	C.	(¾å°İŸÖŸÅŸÈŸPŸEŸÅŸÇŸÈ␣È¾¾␣¾Å»Û␣¹␣é)					
0182	. S.	DC-BC dcbc-402:DCBC (MDP_known_event)					
0183	C.						
0184	C.						
0185	C.						
0186	. C.	***** ŸĐŸ¹•İ Daily±¿İñ␣È´Ø␣¹␣èDCBC•×²è *****					
0187	. S.	DC-BC dcbc-153:DCBC (SPECIAL-CMD_DAILY_OPERATIN_DCB)					
0188	C.						
0189	C.						
0190	C.						
0191	. C.	;ãLOSŸÁŸSŸÅŸ~¼Ã»Û;ä					
0192	C.						
0193	. C.	***** LOS *****					


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main-511 2025-09-13 14:21:01 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñİ»Þ'Öòò¹íî,ñ•;ÇÉÔÍ×ñÈXÄÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲ~¾ÒÄÖñ¬°ÄÄèñ•ñççé;Ç°Ê²¼ñî°EÄ,¼è½Çñò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôñ»ñ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲÈŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ã;Ç°Ê²¼ñò¼Ä¹Ôñ¹ñè;£
0055 C. ŲÇŲóŲÉŲÈŲÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôñ»ñ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0071 C.
0072 . C. ;ãŲÇŲóŲÉŲÈŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1 XPA ON/OFF] EO OFF
```

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 19s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-09-13 10:35:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÄîñî»ö¼ÝñÄðñ¹ñèDCBC×²è *****
0156 C. (¼â°îÝÖÝÄÝÐÝËÝÇÝèñÈ½¼ñ¼Ä»Üñ¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•İ Daily±;İññÈ'Øñ¹ñèDCBC×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;äLOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

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main-513 2025-09-13 14:21:01 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¼¿¼Ä¼•µ°Æ»Í×ÄÇ¼ÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËè¼µ•ííË;Ë¼Ë¼°ÇÖ¼¼•¼¿¼¹Ç¼Í;ÇÄ®,ù¼¹¼è¼¼Ð¼ÇÄ÷¿®¼¼•¼Ë¼¼¼³¼Ë;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 06 06)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 85 83 08 08)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 +. DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 +. DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 +. DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 +. DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 14)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 0e)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2025-09-13 10:35:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP ´ûÄí¼Í»ö¼Ÿ¼ËÄÐ¼¹¼èDCBC•×²è *****
0087 C. (¼ä°íŸóŸÄŸËŸÐŸËŸäŸçŸË¼Ë¼°Ë¼¼¼¼Ä»Ü¼¹¼è)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** ŸÐŸ¹•İ Daily÷¿İŸ¼Ë´Ø¼¹¼èDCBC•×²è *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
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0096 C.
0097 . C. ;ãLOS¥Á¥\$¥Ã¥~¼Ã»Ü;ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

Sep 13, 25 14:21

XRT_OGLIST_0839.chk

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*** OP Sequence for XRT ***

2025/09/13	10:45:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	10:45:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	10:45:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/13	10:46:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 ce 01 db
2025/09/13	10:46:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/13	10:46:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/13	10:46:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/13	10:46:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	10:46:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	10:48:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2025/09/13	10:48:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2025/09/13	10:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	15:36:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	15:36:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	15:36:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	15:46:00.0	XRT_Custom_430_OG [0x1ae]			
2025/09/13	15:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	17:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	17:10:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	17:10:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	17:10:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	17:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	17:33:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/13	17:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	18:03:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/13	18:03:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/13	18:03:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/13	18:03:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/13	18:03:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	18:06:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/09/13	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	18:13:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	18:13:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/13	18:13:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 ce 01 db
2025/09/13	18:13:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/13	18:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/13	18:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/13	18:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/13	18:13:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	18:16:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2025/09/13	18:16:28.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2025/09/13	18:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]			

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2025/09/13	18:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	18:46:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	18:46:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	18:46:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	18:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	19:10:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	19:11:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	20:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	20:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	20:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	20:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	20:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	20:46:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	20:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	22:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	22:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	22:00:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	22:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	22:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	22:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/13	23:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/13	23:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	23:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/13	23:37:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/13	23:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/13	23:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/13	23:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/14	01:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/14	01:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	01:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	01:14:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/14	01:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/14	01:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/14	01:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/14	02:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/14	02:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	02:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	02:38:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/14	02:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/14	02:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/14	02:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/09/14	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/14	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/14	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00
2025/09/14	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/14	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
			MDP_XRT_AEC_RESET	1	07-F0	d0

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2025/09/14	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/14	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/14	04:02:56.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2025/09/14	04:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2025/09/14	04:33:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/14	04:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/14	05:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	05:42:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	05:42:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/14	05:42:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/14	05:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/14	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	06:10:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	06:10:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/14	06:10:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/14	06:10:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/14	06:10:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/14	06:13:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/09/14	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/14	06:30:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 ce 01 db
2025/09/14	06:30:00.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/14	06:32:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_418_OG [0x1a2]			
		TCIB_XRT_S_HTR_A_ENA	0	04-BC	
2025/09/14	11:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 b3 cf 01 db
2025/09/14	18:05:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/14	18:15:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 ce 01 db
2025/09/15	05:58:30.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/15	06:08:30.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 ce 01 db
2025/09/16	06:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/16	06:10:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 ce 01 db
2025/09/16	11:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00