

Period: 2025/09/23 11:04:00 - 2025/09/30 11:33:00

✱ ✱ ✱ ✱ ✱

Term	Pointing (x, y)	Comment
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PROG= 06													
1-time(s)													
Subr= 1													
1-time(s) 2.0sec													
Seqn= 5													
1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26													
1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99													
1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83													
1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23													
1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2													
1-time(s) 2.0sec													
Seqn= 41													
1-time(s) 2.0sec													
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17													
1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33													
1-time(s) 2.0sec													
	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75													
1-time(s) 2.0sec													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

Term	Pointing (x, y)	Comment
09/24 04:03:00 - 09/24 05:43:54	Fixed (0.0, 0.0)	HOP 349

PROG= 17														
Inf.-time(s)														
Subr= 1	1-time(s)		300.0sec											
	Seqn=100		1-time(s)		2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Seqn= 84		1-time(s)		2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Seqn= 57		1-time(s)		2.0sec									
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Seqn= 81		1-time(s)		2.0sec									
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec		
Subr= 2	6-time(s)		1200.0sec											
	Seqn= 8		1-time(s)		2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec	
	Seqn= 6		1-time(s)		2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec	
	Seqn= 29		1-time(s)		2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec	
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval														

Term	Pointing (x, y)	Comment
09/23 11:17:00 - 09/23 15:13:54	Fixed (-21.0, 722.0)	# OP start + 10min + HOP 485 N pole

09/23 15:17:00 - 09/23 16:59:54 Track (586.4, -400.1) @ 09/23 15:14:00 AR 14217
09/23 17:03:00 - 09/23 17:37:00 Track (252.3, -457.3) @ 09/23 17:00:00 HOP 514
09/23 18:13:30 - 09/23 20:09:54 Track (262.0, -456.9) @ 09/23 18:10:30 HOP 514
09/23 20:13:00 - 09/23 23:09:54 Fixed (866.6, -356.2) AR 14220 fixed
09/23 23:13:00 - 09/24 03:59:54 Track (636.8, -394.0) @ 09/23 23:10:00 AR 14217
09/24 04:03:00 - 09/24 05:43:54 Fixed (0.0, 0.0) HOP 349
09/24 05:57:00 - 09/24 07:24:45 Track (676.6, -388.6) @ 09/24 05:54:00 AR 14217

PROG= 14 30-time(s)												
Subr= 1		1-time(s)		2.0sec								
Seqn= 68		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95 0 0 2.0sec
Subr= 2		20-time(s)		2.0sec								
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95 2 0 2.0sec
Seqn= 73		1-time(s)		10.0sec								
thin-Be/Open		med-Be/Open		close		Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
med-Be/Open		Open/thick-Al		close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
Open/thick-Be		Open/thick-Be		close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
Subr= 3		1-time(s)		2.0sec								
Seqn= 10		1-time(s)		2.0sec								
med-Al/Open		med-Al/thick-Al		close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
Open/thick-Be		Open/thick-Be		close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95 3 0 2.0sec
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close		Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95 2 0 2.0sec
Seqn= 87		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/G-band		Open/G-band		close		Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al		Open/thick-Al		close		Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al		Open/thick-Al		close		Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98 0 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

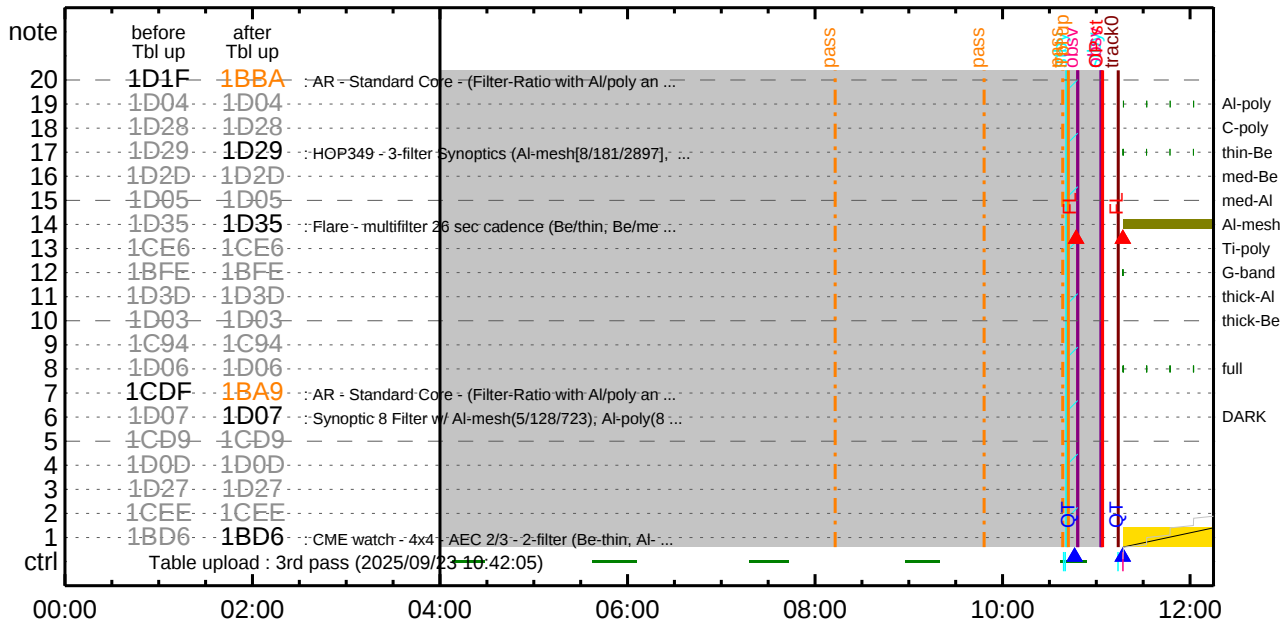
* * * * *

Flare Detection

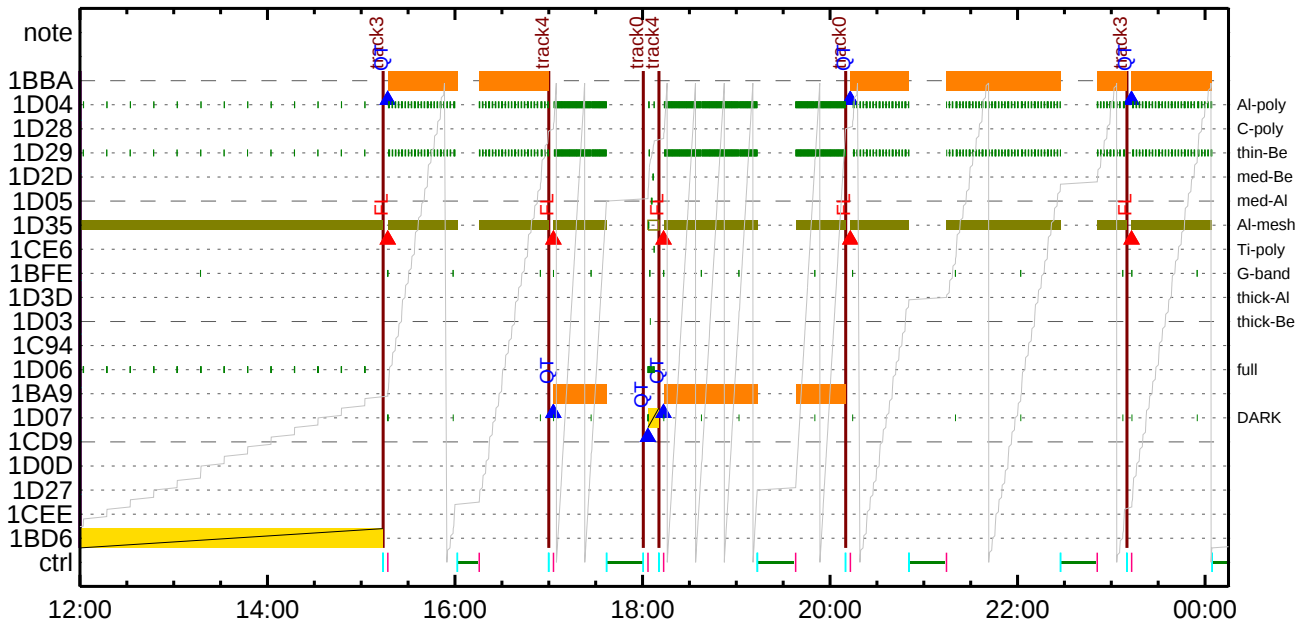
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FLD Patrol												
Term		Pointing (x, y)						Comment				
09/23 10:43:05 - 09/23 18:00:48		cannot be identified										
09/23 18:10:48 - 09/24 05:44:18		Track (262.0, -456.9)		@ 09/23 18:10:30		HOP 514						
09/24 05:54:18 - 09/30 11:33:00		Track (676.6, -388.6)		@ 09/24 05:54:00		AR 14217						
Al-poly/Open		Al-poly/Open		close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp. AEC Buffer Interval

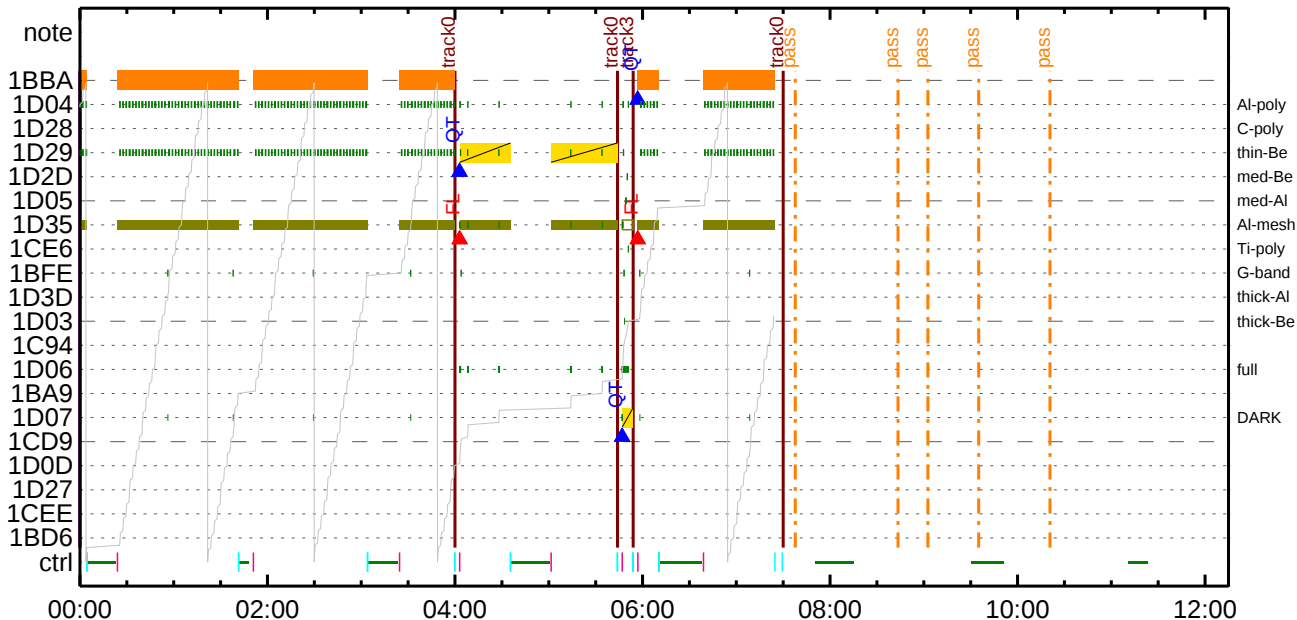
CMDI #0856 2025/09/23



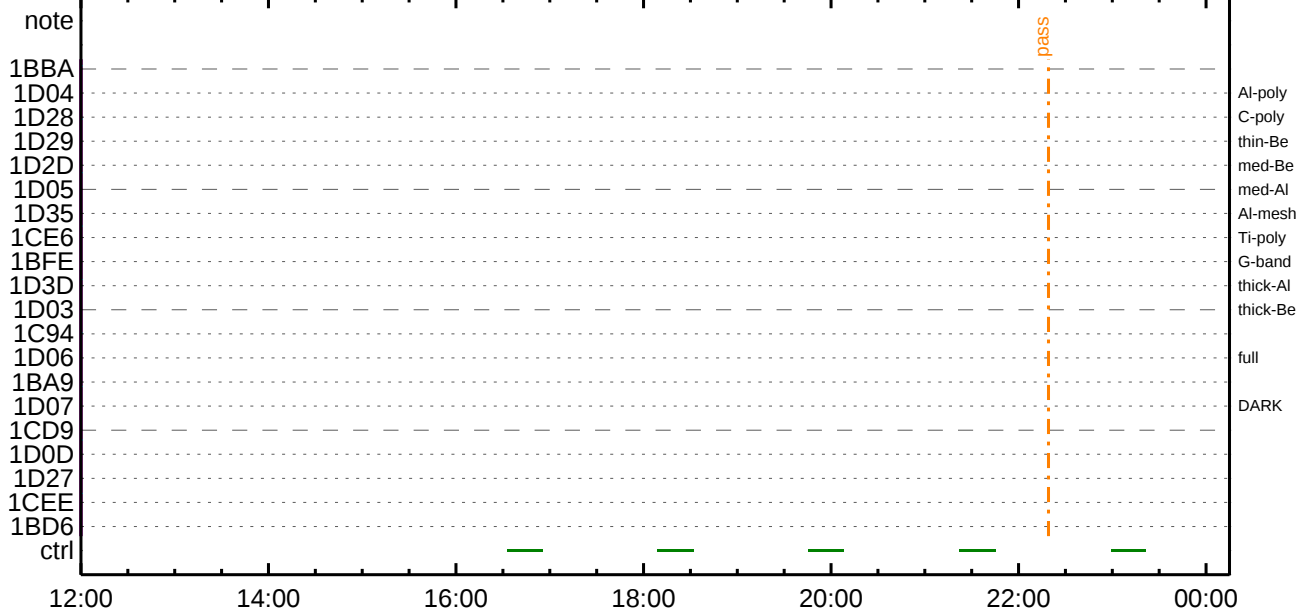
CMDI #0856 2025/09/23



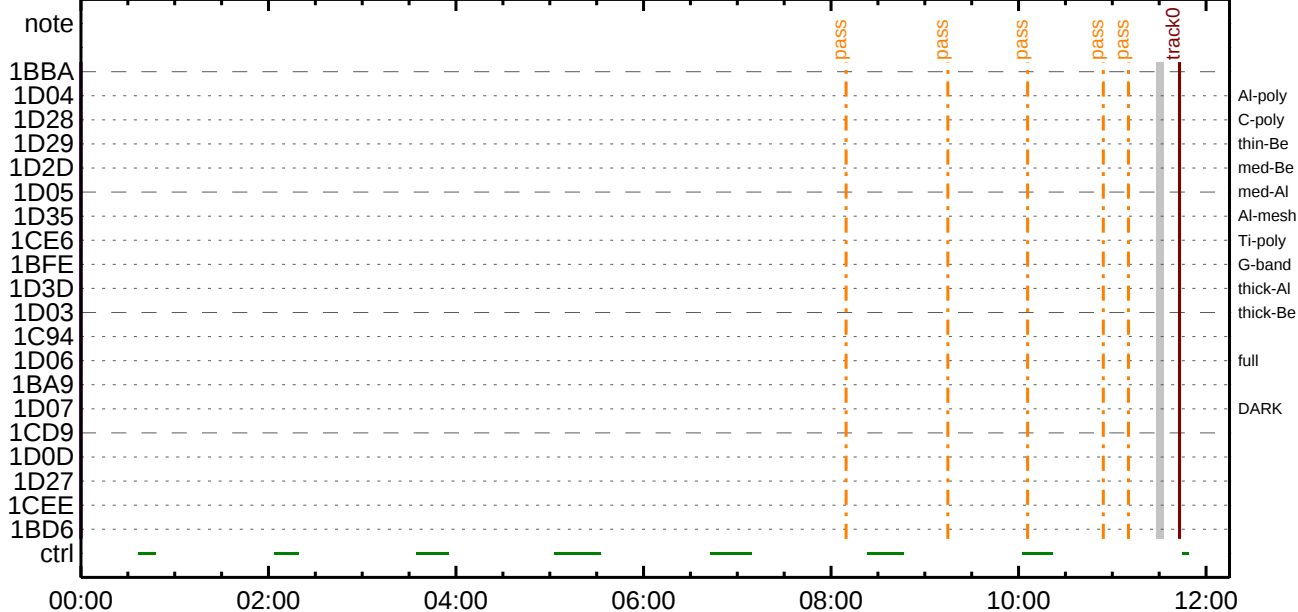
CMDI #0856 2025/09/24



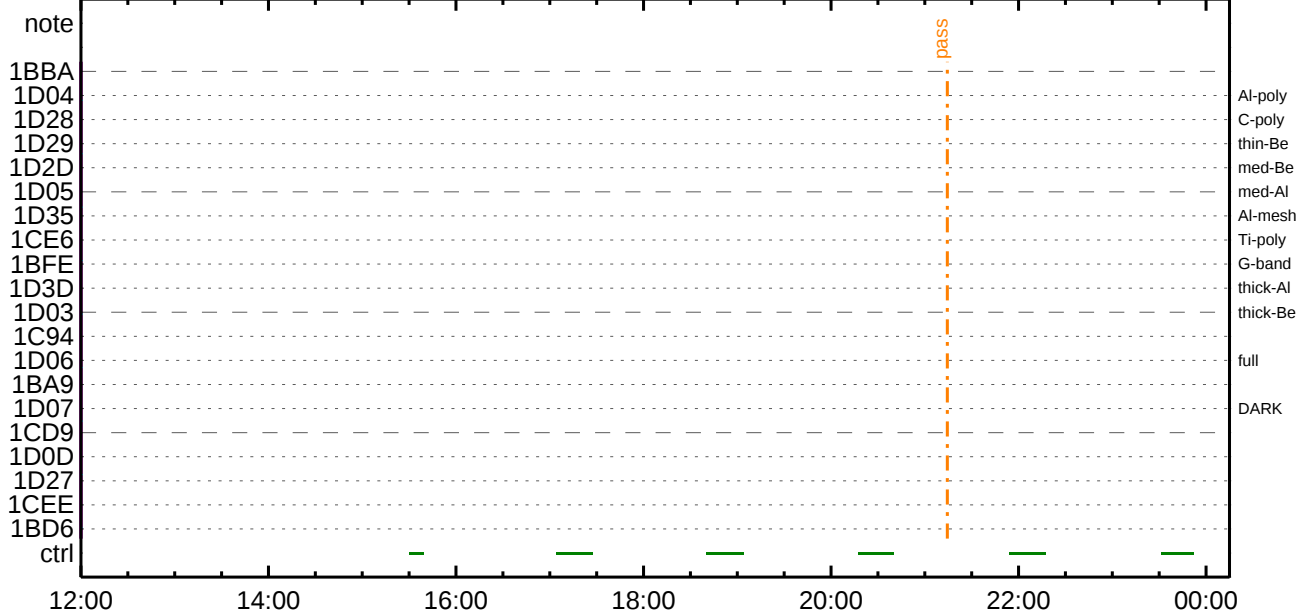
CMDI #0856 2025/09/24



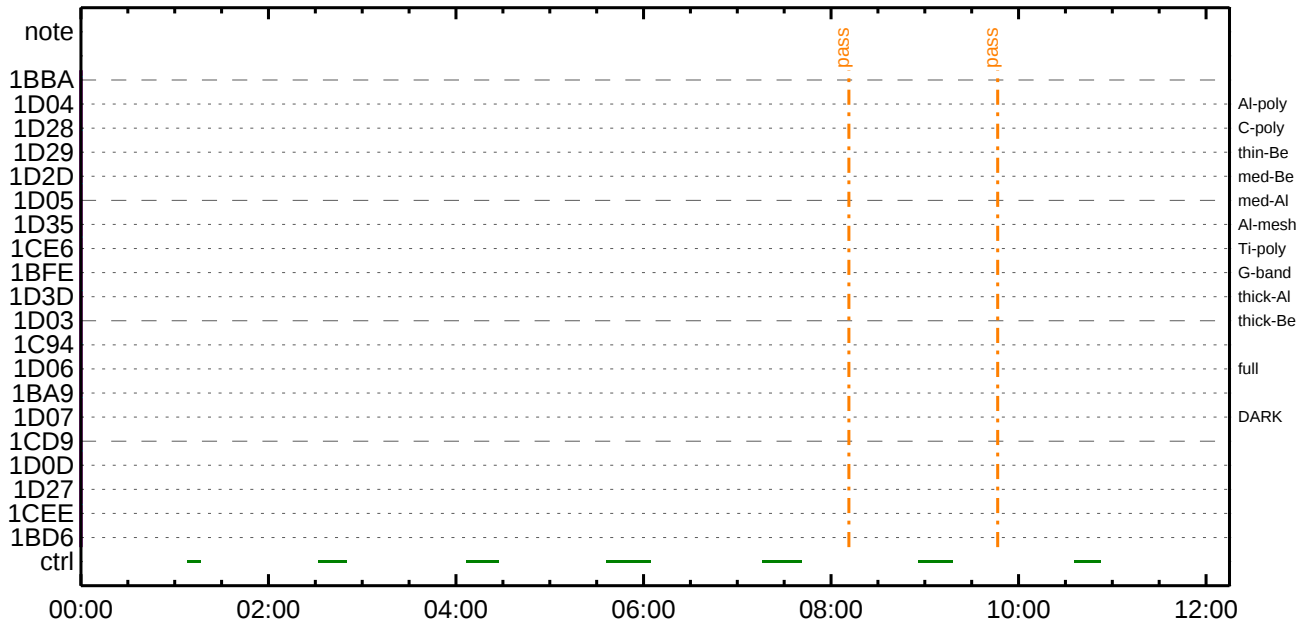
CMDI #0856 2025/09/25



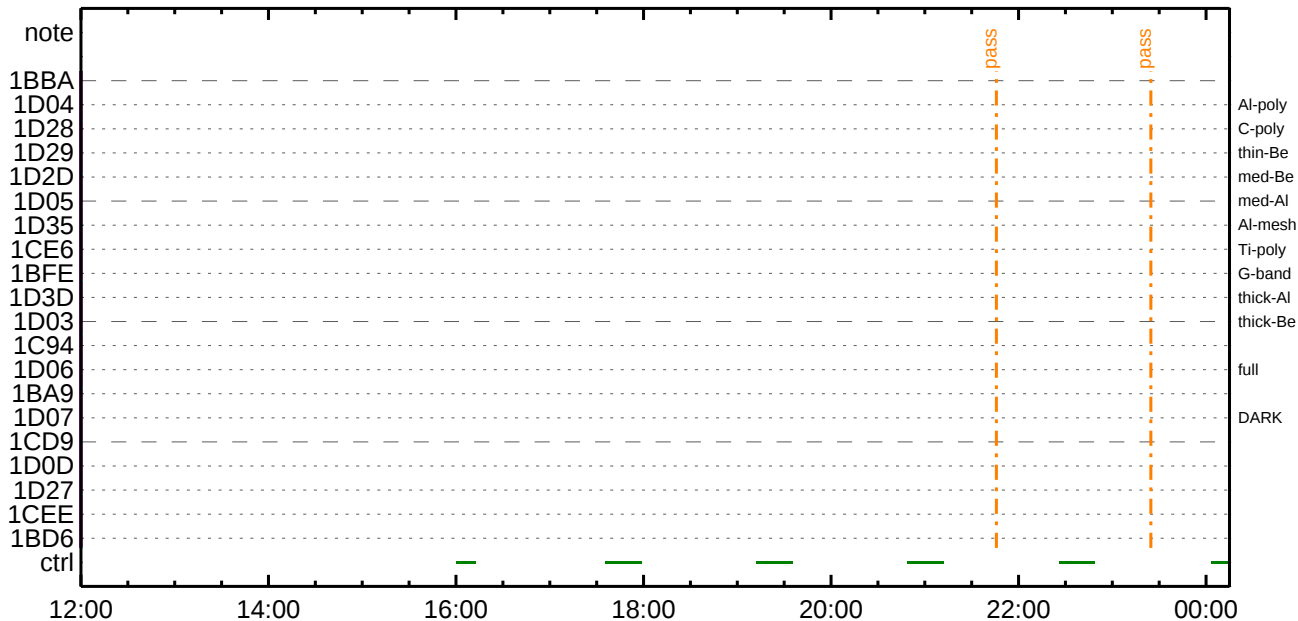
CMDI #0856 2025/09/25



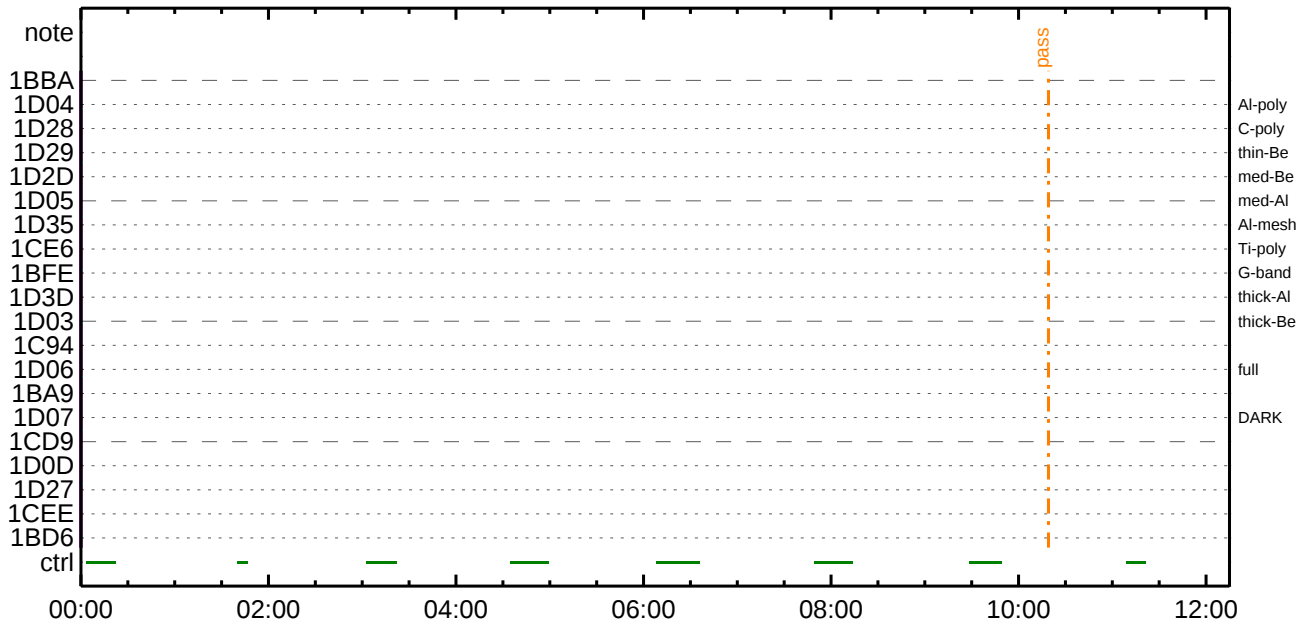
CMDI #0856 2025/09/26



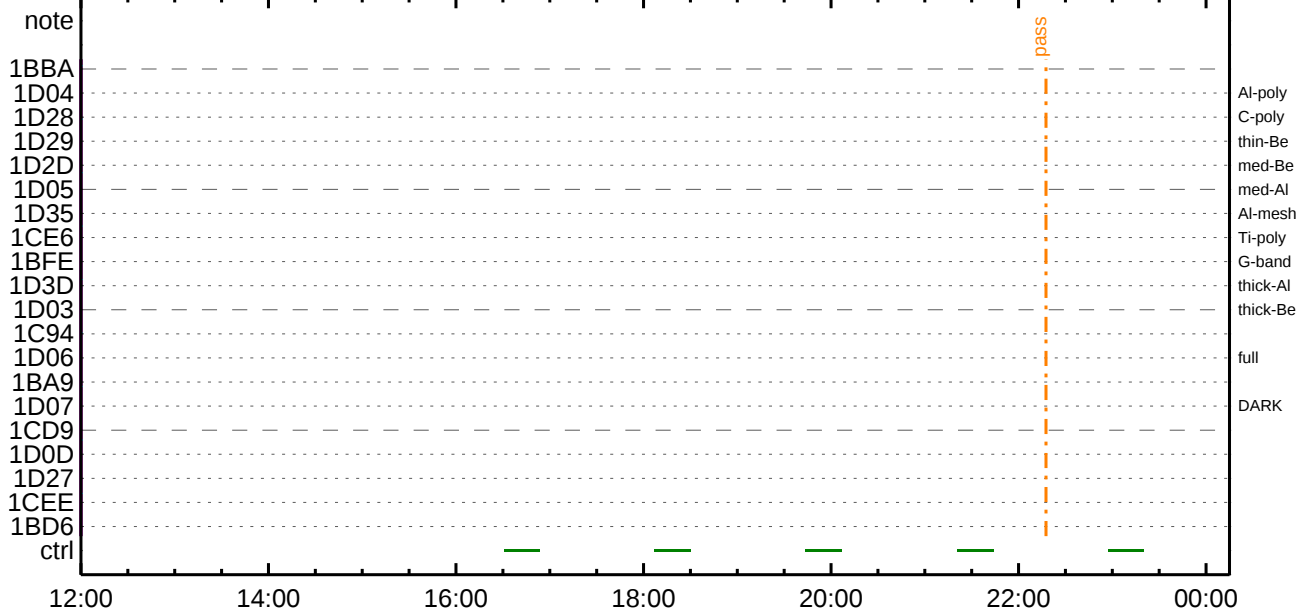
CMDI #0856 2025/09/26



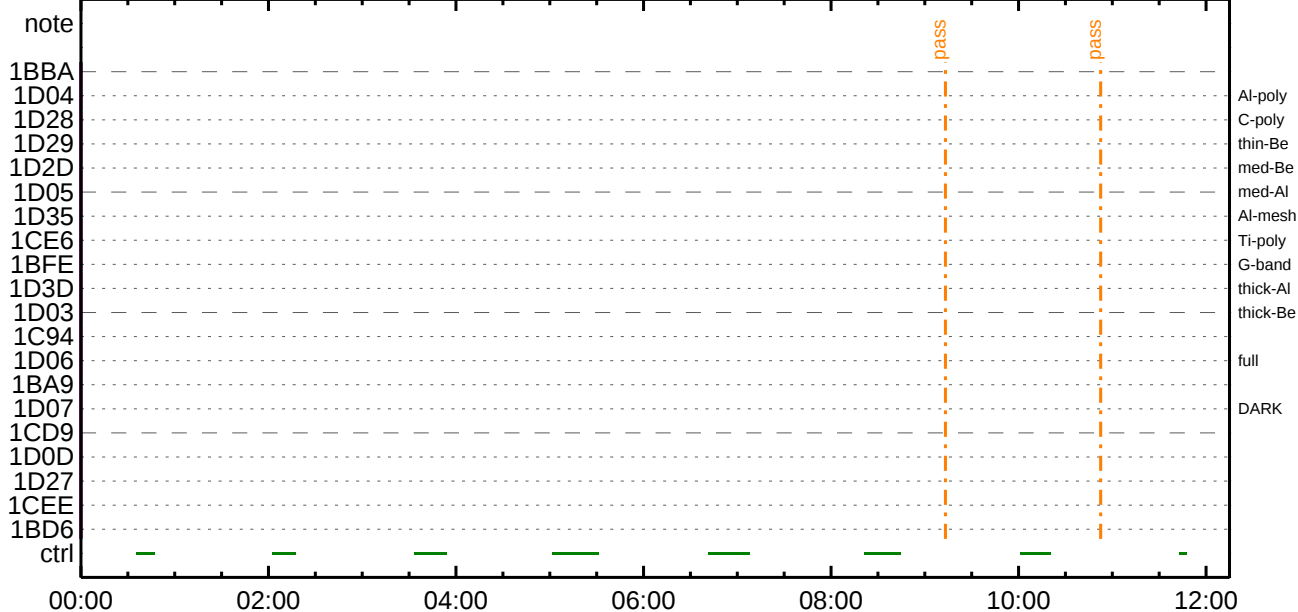
CMDI #0856 2025/09/27



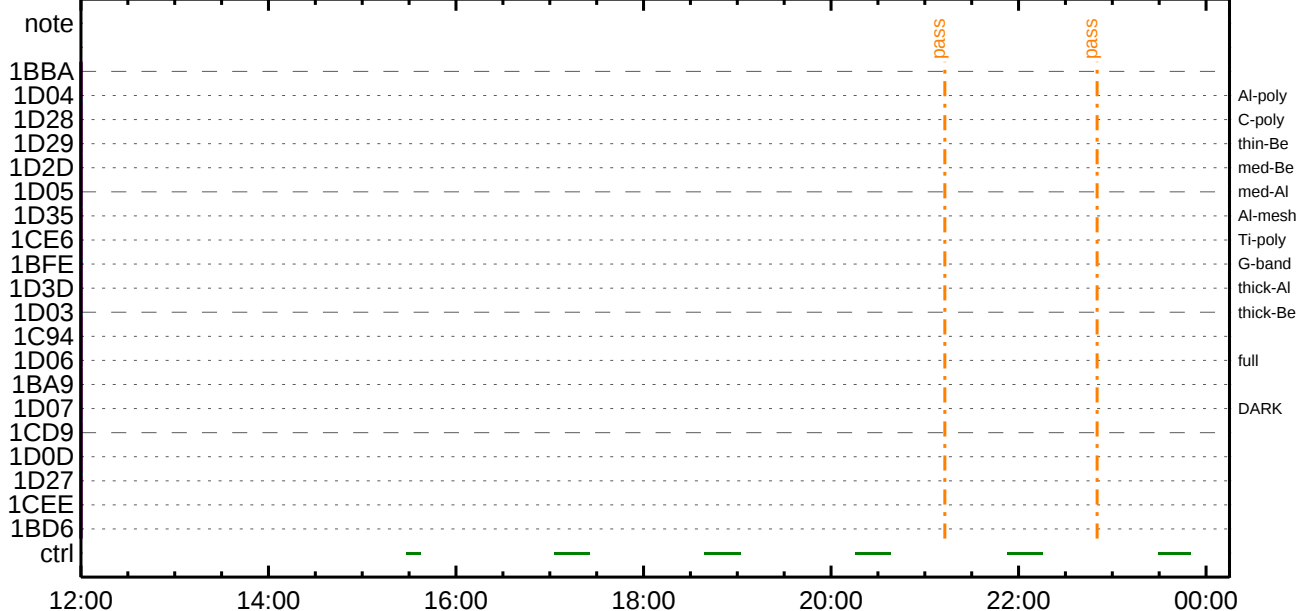
CMDI #0856 2025/09/27



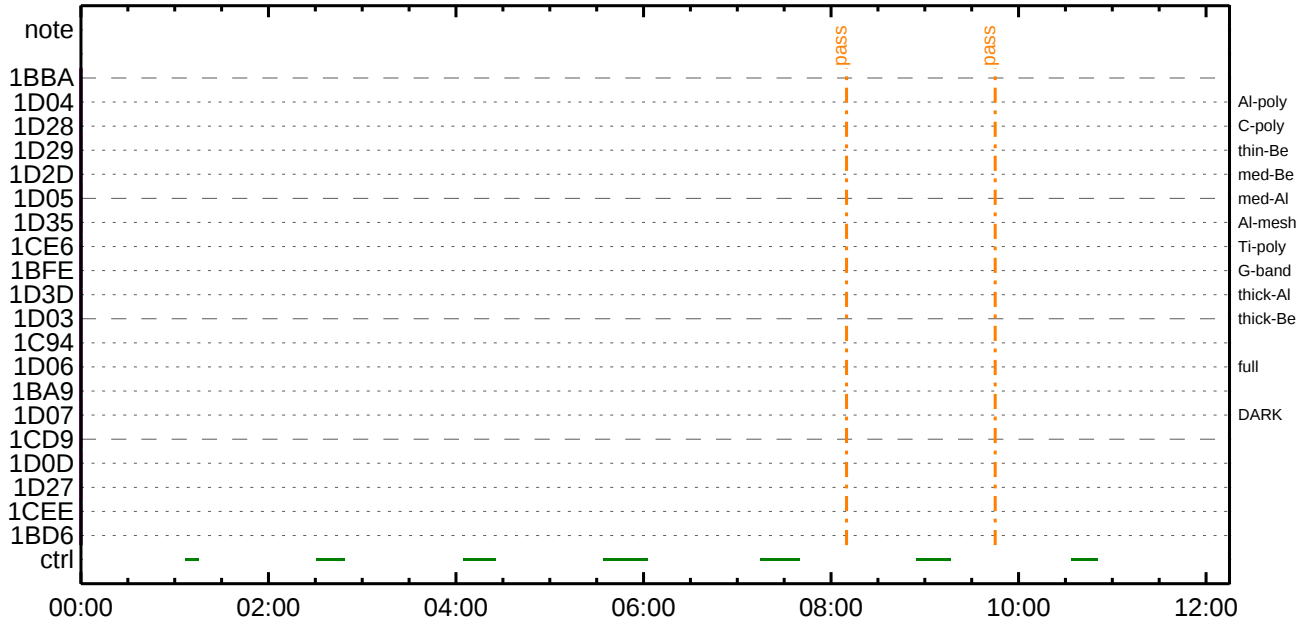
CMDI #0856 2025/09/28



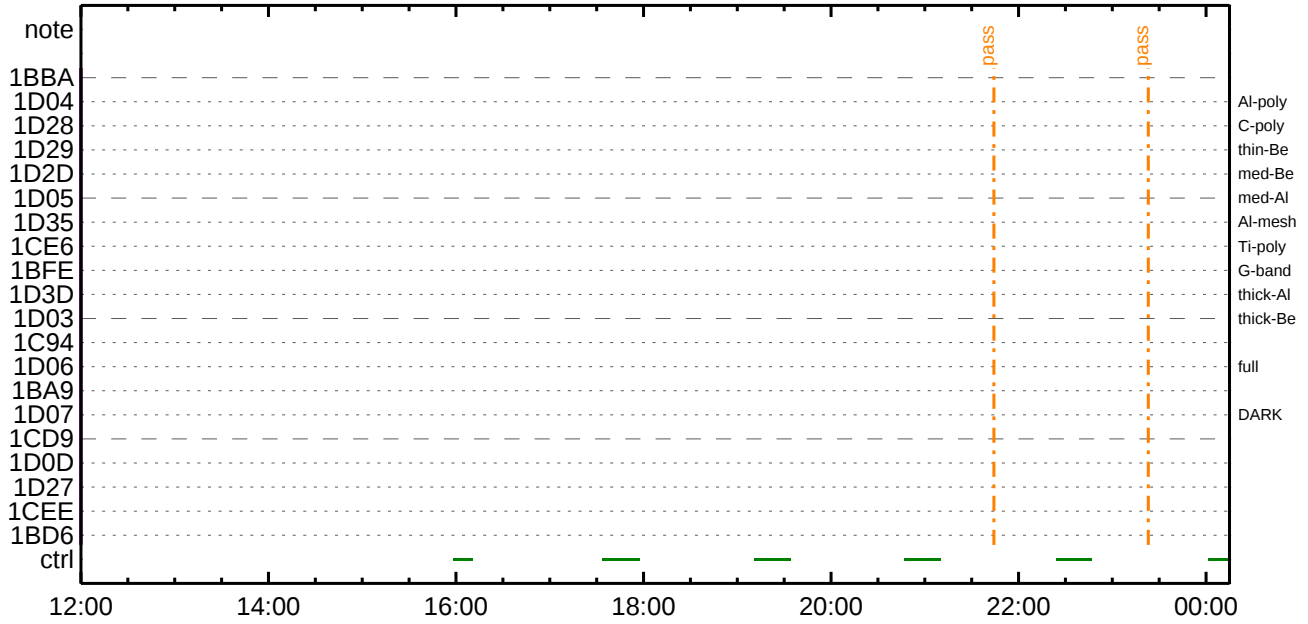
CMDI #0856 2025/09/28



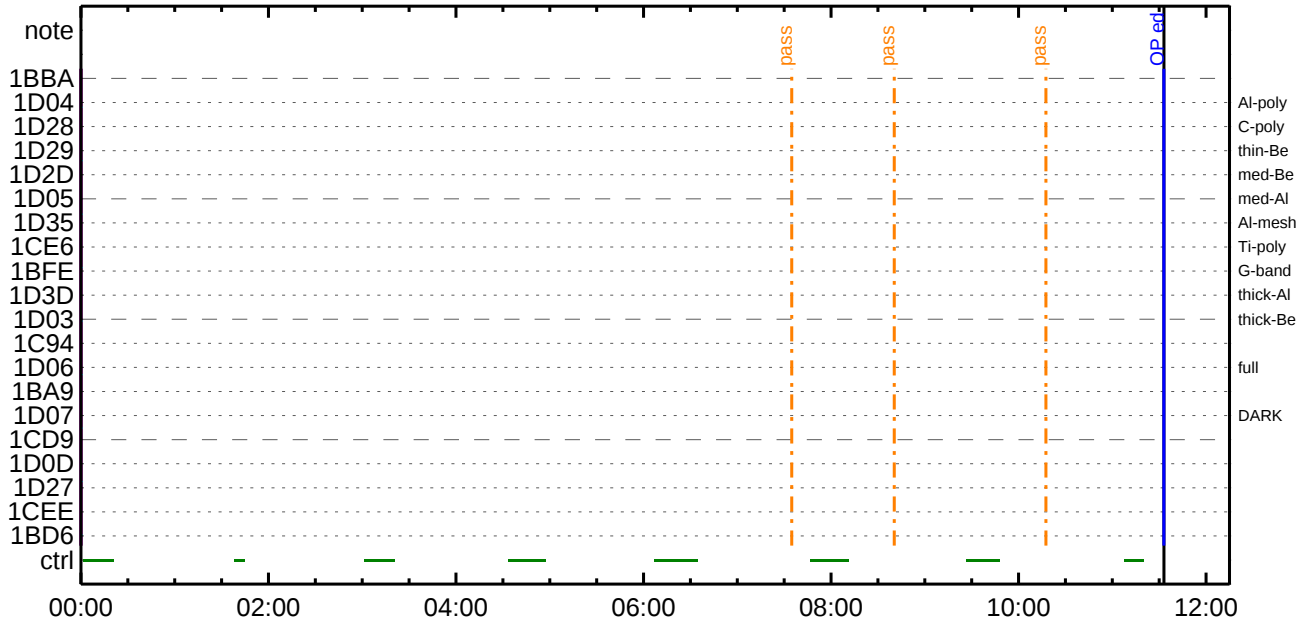
CMDI #0856 2025/09/29

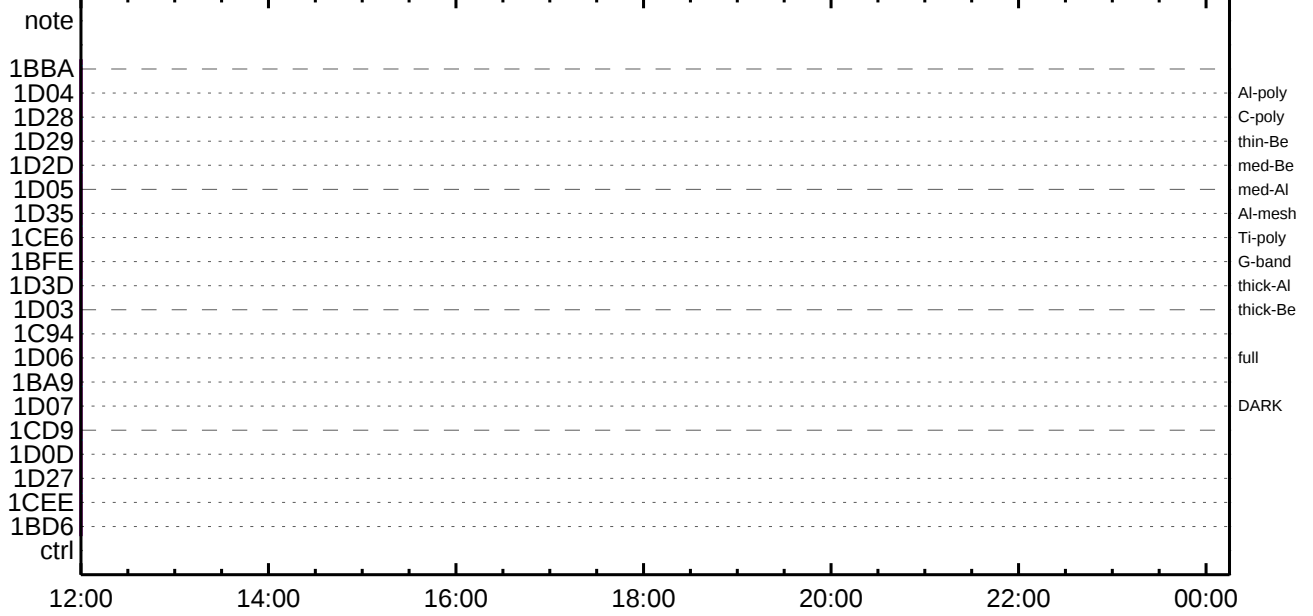


CMDI #0856 2025/09/29



CMDI #0856 2025/09/30






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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OG¥1;¼¥É;|¥À¥ó¥×
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OG¥1;¼¥É;ä
0103 . S. OP      op-529:OP
0104 . ()
0105 . S. OG      og-529:OG
0106 . ()
0107 . C.
0108 . C. ;ãNMOG&OPîî°è¥À¥ó¥×;ä
0109 . C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 . C.           ¢¢[HK1_DMP_TOP_ADRS_1]              EQ      40
0113 . C.           ¢¢[HK1_DMP_TOP_ADRS_0]              EQ      0
0114 . C.           ¢¢[HK1_DMP_BLOCK_NUM]               EQ     127
0115 . C.           ¢¢[HK1_DMP_REPEAT_NUM]              EQ      0
0116 . C.           ¢¢[HK1_DMA_DMP_PIM]                 EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 . C.           ¢¢[HK1_PKT_FORM_NO]                  EQ      7
0120 . C.           ¢¢[HK1_PKT_GEN_TIME]                 EQ    0.25 s
0121 . C.           ¢¢[HK1_S_TLM_BIT_RATE]               EQ    32k
0122 . C.           ¢¢[HK1_X_TLM_BIT_RATE]               EQ     4M
0123 . C.           ¢¢[HK1_DMP_CHK_FLG]                  EQ    EXEC
0124 . C. ¥À¥ó¥×½ªî»¤ð³îÇ$
0125 . C.           ¢¢[HK1_DMP_CHK_FLG]                  EQ    NON
0126 . C. RAM ID=NMOG¤î¾È¹Ç•ë²ïOK¤ð³îÇ$
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 . C.           ¢¢[HK1_DMP_TOP_ADRS_1]              EQ     41
0132 . C.           ¢¢[HK1_DMP_TOP_ADRS_0]              EQ      0
0133 . C.           ¢¢[HK1_DMP_BLOCK_NUM]                EQ    127
0134 . C.           ¢¢[HK1_DMP_REPEAT_NUM]               EQ      0
0135 . C.           ¢¢[HK1_DMA_DMP_PIM]                 EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 . C.           ¢¢[HK1_PKT_FORM_NO]                  EQ      7
0139 . C.           ¢¢[HK1_PKT_GEN_TIME]                 EQ    0.25 s
0140 . C.           ¢¢[HK1_S_TLM_BIT_RATE]               EQ    32k
0141 . C.           ¢¢[HK1_X_TLM_BIT_RATE]               EQ     4M
0142 . C.           ¢¢[HK1_DMP_CHK_FLG]                  EQ    EXEC
0143 . C. ¥À¥ó¥×½ªî»¤ð³îÇ$
0144 . C.           ¢¢[HK1_DMP_CHK_FLG]                  EQ    NON
0145 . C. RAM ID=NMOG¤î¾È¹Ç•ë²ïOK¤ð³îÇ$
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 . C.           ¢¢[HK1_DMP_TOP_ADRS_1]              EQ     42
0151 . C.           ¢¢[HK1_DMP_TOP_ADRS_0]              EQ      0
0152 . C.           ¢¢[HK1_DMP_BLOCK_NUM]                EQ     65
0153 . C.           ¢¢[HK1_DMP_REPEAT_NUM]               EQ      0
0154 . C.           ¢¢[HK1_DMA_DMP_PIM]                 EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 . C.           ¢¢[HK1_PKT_FORM_NO]                  EQ      7
0158 . C.           ¢¢[HK1_PKT_GEN_TIME]                 EQ    0.25 s
0159 . C.           ¢¢[HK1_S_TLM_BIT_RATE]               EQ    32k
0160 . C.           ¢¢[HK1_X_TLM_BIT_RATE]               EQ     4M
0161 . C.           ¢¢[HK1_DMP_CHK_FLG]                  EQ    EXEC
0162 . C. ¥À¥ó¥×½ªî»¤ð³îÇ$
0163 . C.           ¢¢[HK1_DMP_CHK_FLG]                  EQ    NON
0164 . C. RAM ID=NMOG,RAM ID=OP¤î¾È¹Ç•ë²ïOK¤ð³îÇ$
0165 . C.
0166 . C. ***** °Ê²¼¤î¾Ä´¶Á¤¤ËÉ¬¤ºÁ÷¿@ (¾âµ-¥À¥ó¥×¼ê½Ç¤ðÃÓÃæ¤Ç½ª¤¤¬ë¾ì¹Ç¤Ç¤â) *****
0167 . C. DHU¥â;¼¥É;Ê¼ý½,¥ì;¼¥É;Ê¤ðÌá¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 . C.           ¢¢[HK1_PKT_FORM_NO]                  EQ      2
0171 . C.           ¢¢[HK1_PKT_GEN_TIME]                 EQ    0.5S
0172 . C.           ¢¢[HK1_S_TLM_BIT_RATE]               EQ    32K
0173 . C.           ¢¢[HK1_X_TLM_BIT_RATE]               EQ     4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ;$ OPOG UPLOAD¤¬Á÷¿ONG¤î¾ì¹Ç;¢°Ê²¼¤î¾TI-CMDÁ÷¿@¤î¾Ä¹Ô¤¤¤Ê¤¤¤¤¤¤;£
0180 . C.           ¤¤¤¿;¢SET¤EDUMP¤îÆ±°ì¾Ñ¥¹¤Ç¹Ô¤¤¤¤¤¤;£
0181 . C.
0182 . C. TII¥³¥P¥ó¥É¤ðÂBî¿(UT)
0183 +. TI 2025-09-23 10:59:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C.           ¢¢[HK1_TI_CMD_NUM]                     EQ    1COUNTUP
0186 . C.
0187 +. TI 2025-09-23 10:59:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C.           ¢¢[HK1_TI_CMD_NUM]                     EQ    1COUNTUP
0190 . C.
0191 +. TI 2025-09-23 10:59:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C.           ¢¢[HK1_TI_CMD_NUM]                     EQ    1COUNTUP
```

```
0194 C.
0195 +. TI 2025-09-23 11:03:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼¤İÄê¼İİÑ¤İ¥Á¥$¥Ä¥~¹àİÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]          EQ          ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ          4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]          EQ          DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]          EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ          07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ          2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]          EQ          3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]          EQ          0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ          7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ          0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]          EQ          32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ          EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ          NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¾È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHU¥â;¼¥É;Ê¼Ý½,¥İ;¼¥È;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ          2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ          0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]          EQ          32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]          EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2025-09-23 11:03:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2025-09-23 11:03:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2025-09-23 11:03:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]          EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2025-09-23 11:03:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]          EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃİ¤İ»ö¼Ý¤ÈÂ¤¤¹¤èDCBC•×²è *****
0276 C. (¼å°İ¥Ö¥Ä¥È¥Ð¥È¥Ä¥Ç¥è¤È¼¤¤¼Ä»Ü¤¹¤è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•İ Daily±çİÑ¤È´Ø¤¹¤èDCBC•×²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;äLOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0287 C.
0288 C. ***** LOS *****
0289 C.
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 3702291.0 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 10s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-09-23 11:03:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîÎ»ö¼Ý¤ÊÃ¤¤¹¤èDCBC•×²è *****
0165 C. (¾â°îÝÔÝÄÝÊÝÐÝËÝÄÝ¢ÝË¤Ê½¾¤¤¼Ã»Û¤¹¤è)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±;İŃ¤Ê'Ø¤¹¤èDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 C. ;ãLOS¥Ä¥S¥Ä¥~¼Ã»Û;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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main-531 2025-09-23 12:13:28 134 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŷ$ŷÃŷ~¼Ä»Ü;ä
0005 C.
0006 C. ŴÄŴß;¼Ŵ³ŴÐŴóŴÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¤¿¤¤•µ°E»Í×ÅÇ¤ÎŴ¿Ŵ×Ŵí;¼ŴÉ;ËËè½µ•íîË;Ë¤È¼°ÇÔ¤•¤¿¾í¹Ç¤İ;¿À®,ù¤¹¤Ë¤¤¤ÇÄ÷¿®¤•¤Ë¤¤¤³¤Ë;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-287:MDP_OBS_S
0026 ()
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2025-09-23 11:03:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_CTRL_MANU
0052 BC (c1)
0053 +. DC 07-F0 MDP_XRT_MODE_STBY
0054 BC (c3)
0055 . C. ----- Success Verify ? OK / NG_____
0056 C.
0057 C. XRT Obs. Table Upload
0058 . S. RAM ram-291:MDP_OBS_X
0059 ()
0060 C.
0061 +. DC 07-F0 MDP_DUMP_XRTTBL
0062 BC (84 07 00 00 00 3a d4)
0063 . C. ----- Comparison Check ? OK / ERR _____
0064 C.
0065 C.
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 01 b1 b1 04 04)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 02 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 03 b1 b1 08 08)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 04 b1 b1 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 05 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 06 80 80 08 08)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 07 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 08 85 83 06 06)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 09 80 80 20 08)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0a 80 80 08 20)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0b 85 83 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0f 80 80 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 10 80 80 08 08)
0092 +. DC 07-F0 MDP_XRT_FLD_ENA
0093 BC (d8)
0094 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095 BC (c8)
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0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 + DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 01)
0104 + DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 0e)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 + DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 + TI 2025-09-23 11:03:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP 'ûÃîñî»ö¼ýñÊÂðñ¹ñÐDCBC•x²è *****
0121 C. (¼å°îÿÓÿÃÿÊÿÐÿËÿÂÿÇÿËñÊ¼¼¼¼»Ûñ¹ñÐ)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ¥Ðÿ¹•Ï Daily±¿ÍÑñÊ´Øñ¹ñÐDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ;ãLOS¥Á¥$¥Ã¥´¼Â»Û;ä
0132 C.
0133 . C. ***** LOS *****
0134 C.
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*** OP Sequence for XRT ***

2025/09/23	11:13:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	11:13:56.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/23	11:14:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 bf cf 01 db
2025/09/23	11:14:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/23	11:14:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/23	11:14:20.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/23	11:14:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/23	11:14:24.0	XRT_FLD_RESET_446_OG [0x1be]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/23	11:16:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/23	11:16:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/23	11:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/23	15:13:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	15:13:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	15:13:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/23	15:14:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 03 ce 01 db
2025/09/23	15:14:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/23	15:14:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/23	15:14:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/23	15:14:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/23	15:14:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/23	15:16:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2025/09/23	15:16:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/23	15:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/23	16:01:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	16:01:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	16:01:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/23	16:01:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/09/23	16:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/09/23	16:14:30.0	XRT_Custom_430_OG [0x1ae]			
2025/09/23	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/23	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	16:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	16:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/09/23	17:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	04 03 ce 01 db
2025/09/23	17:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/23	17:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/23	17:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/23	17:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/23	17:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/23	17:02:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2025/09/23	17:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/23	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/23	17:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	17:37:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/23	17:37:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/23	17:37:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2025/09/23	22:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/23	22:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/23	22:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/09/23	22:27:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/09/23	22:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/09/23	22:50:00.0	XRT_Custom_430_OG [0x1ae]							
2025/09/23	22:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/09/23	23:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/23	23:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/23	23:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2025/09/23	23:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 ce 01 db			
2025/09/23	23:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/09/23	23:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/09/23	23:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/09/23	23:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/09/23	23:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/09/23	23:12:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2025/09/23	23:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e			
2025/09/23	23:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/09/24	00:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	00:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	00:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/09/24	00:04:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/09/24	00:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/09/24	00:23:01.0	XRT_Custom_430_OG [0x1ae]							
2025/09/24	00:24:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/09/24	01:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	01:41:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	01:41:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/09/24	01:41:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/09/24	01:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/09/24	01:50:00.0	XRT_Custom_430_OG [0x1ae]							
2025/09/24	01:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/09/24	03:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	03:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	03:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/09/24	03:04:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/09/24	03:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/09/24	03:23:30.0	XRT_Custom_430_OG [0x1ae]							
2025/09/24	03:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/09/24	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/09/24	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2025/09/24	04:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00			
2025/09/24	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/09/24	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/09/24	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/09/24	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2025/09/24	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/24	04:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2025/09/24	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/24	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/24	04:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	04:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	04:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/24	04:35:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/09/24	04:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/09/24	05:00:30.5	XRT_Custom_430_OG [0x1ae]					
2025/09/24	05:01:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/24	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	05:43:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	05:43:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/09/24	05:44:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00 00 00 00
2025/09/24	05:44:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/09/24	05:44:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/09/24	05:44:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/24	05:46:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2025/09/24	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/24	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	05:53:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	05:53:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2025/09/24	05:54:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03	03 ce 01 db
2025/09/24	05:54:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/24	05:54:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/24	05:54:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/24	05:54:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/24	05:54:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/24	05:56:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2025/09/24	05:56:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/24	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/24	06:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	06:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	06:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/24	06:10:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/09/24	06:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/09/24	06:38:00.0	XRT_Custom_430_OG [0x1ae]					
2025/09/24	06:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/24	07:24:45.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	07:29:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/24	07:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00 00 00 00
2025/09/25	11:43:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00 00 00 00