

Period: 2025/09/30 11:33:00 - 2025/10/04 10:26:00

PROG= 01 Inf.-time(s)

XOB #1CCC: Synoptic 7 Filter w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(64/1024/5795) - Thick-Be(32768), Al-poly+Ti-poly(128/2048), Med-Al(289

PROG= 10	1-time(s)
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XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term			Pointing (x, y)					Comment						
09/30 15:21:00 - 09/30 17:40:54			Track (		-317.0,	21.8)	@ 09/30 15:18:00	End of HOP130 -> AR14236 obs						
09/30 17:54:00 - 10/01 03:59:54			Track (		-294.8,	21.0)	@ 09/30 17:51:00	AR obs						
10/01 06:10:00 - 10/01 17:59:54			Track (		-185.9,	18.4)	@ 10/01 06:07:00	AR obs						
10/01 18:13:00 - 10/02 03:59:54			Track (		-76.5,	17.3)	@ 10/01 18:10:00	AR obs						
PROG= 12 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 92		1-time(s)		2.0sec										
├──		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
├──		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└──		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		4-time(s)		2.0sec										
Seqn= 47		1-time(s)		2.0sec										
├──		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
├──		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
├──		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└──		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77		4-time(s)		300.0sec										
├──		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
├──		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
├──		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
├──		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
├──		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
└──		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)		Comment									
10/01 04:03:00 - 10/01 05:56:54	Fixed (	0.0, 0.0)		HOP349 + synoptic, shifted -3.0 min								
10/02 04:25:00 - 10/02 06:10:24	Fixed (	0.0, 0.0)		HOP349 + synoptic, shifted 10.5 min								

PROG= 17														Inf.-time(s)									
Subr= 1		1-time(s)		300.0sec																			
Seqn=100		1-time(s)		2.0sec																			
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
Seqn= 84		1-time(s)		2.0sec																			
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
Seqn= 57		1-time(s)		2.0sec																			
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec									
Seqn= 81		1-time(s)		2.0sec																			
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=90	0	0	2.0sec									
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=95	0	0	2.0sec									
Subr= 2		6-time(s)		1200.0sec																			
Seqn= 8		1-time(s)		2.0sec																			
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec									
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec									
Seqn= 6		1-time(s)		2.0sec																			
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec									
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec									
Seqn= 29		1-time(s)		2.0sec																			
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec									
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec									
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval										

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)		Comment									
10/02 06:23:30 - 10/02 10:32:00	Track (	34.7, 17.8)	@ 10/02 06:20:30	AR obs								

PROG= 08														
Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 92		1-time(s)		2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec
Subr= 2		5-time(s)		2.0sec										
Seqn= 47		1-time(s)		2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec
Seqn= 96		4-time(s)		180.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	1	1	2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL

Term	Pointing (x, y)	Comment
09/30 11:46:00 - 09/30 11:58:54	Fixed (936.0, 0.0)	# OP start + 10min + HOP130 01/15
09/30 12:02:00 - 09/30 12:14:54	Fixed (468.0, 0.0)	HOP130 02/15
09/30 12:34:00 - 09/30 12:46:54	Fixed (-468.0, 0.0)	HOP130 04/15
09/30 12:50:00 - 09/30 13:02:54	Fixed (-936.0, 0.0)	HOP130 05/15
09/30 13:06:00 - 09/30 13:12:54	Fixed (815.0, 470.0)	HOP130 06/15
09/30 13:16:00 - 09/30 13:28:54	Fixed (411.0, 847.0)	HOP130 07/15
09/30 13:32:00 - 09/30 13:44:54	Fixed (0.0, 941.0)	HOP130 08/15
09/30 13:48:00 - 09/30 14:00:54	Fixed (-411.0, 847.0)	HOP130 09/15
09/30 14:04:00 - 09/30 14:10:54	Fixed (-815.0, 470.0)	HOP130 10/15
09/30 14:14:00 - 09/30 14:20:54	Fixed (815.0, -470.0)	HOP130 11/15
09/30 14:24:00 - 09/30 14:36:54	Fixed (411.0, -847.0)	HOP130 12/15
09/30 14:40:00 - 09/30 14:52:54	Fixed (0.0, -941.0)	HOP130 13/15
09/30 14:56:00 - 09/30 15:08:54	Fixed (-411.0, -847.0)	HOP130 14/15
09/30 15:12:00 - 09/30 15:17:54	Fixed (-815.0, -471.0)	HOP130 15/15
09/30 15:21:00 - 09/30 17:40:54	Track (-317.0, 21.8) @ 09/30 15:18:00	End of HOP130 -> AR14236 obs
09/30 17:54:00 - 10/01 03:59:54	Track (-294.8, 21.0) @ 09/30 17:51:00	AR obs
10/01 04:03:00 - 10/01 05:56:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -3.0 min
10/01 06:10:00 - 10/01 17:59:54	Track (-185.9, 18.4) @ 10/01 06:07:00	AR obs
10/01 18:13:00 - 10/02 03:59:54	Track (-76.5, 17.3) @ 10/01 18:10:00	AR obs
10/02 04:25:00 - 10/02 06:10:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 10.5 min
10/02 06:23:30 - 10/02 10:32:00	Track (34.7, 17.8) @ 10/02 06:20:30	AR obs

PROG= 14 30-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 68	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	20-time(s)	2.0sec										
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73	1-time(s)	10.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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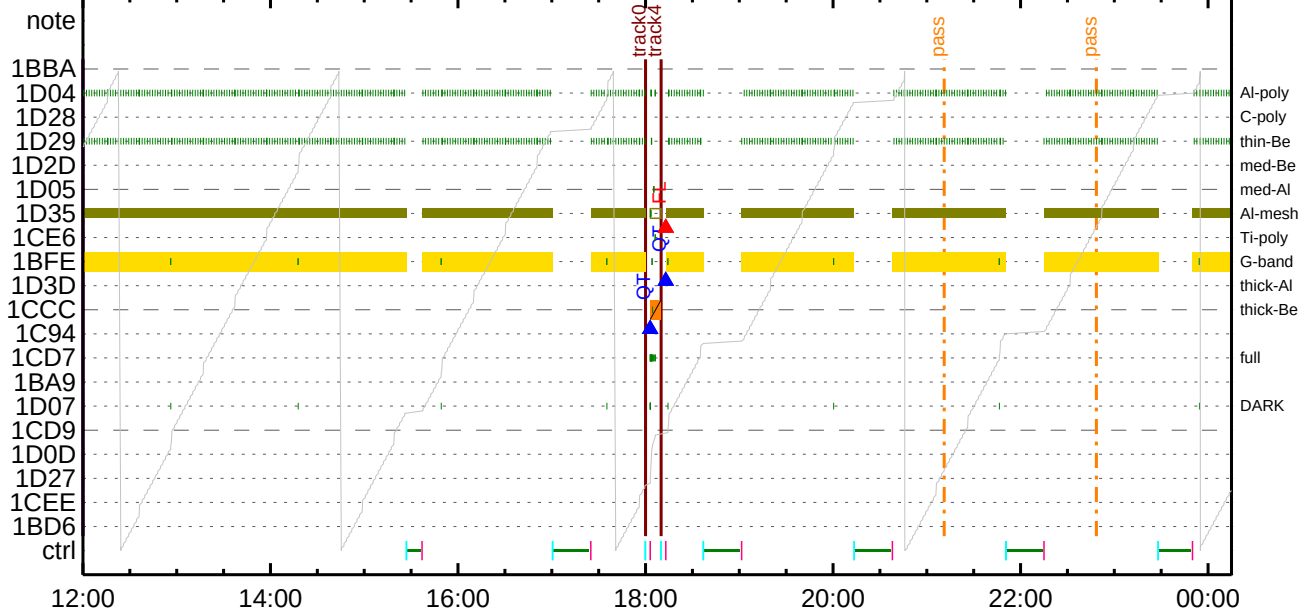
Flare Detection

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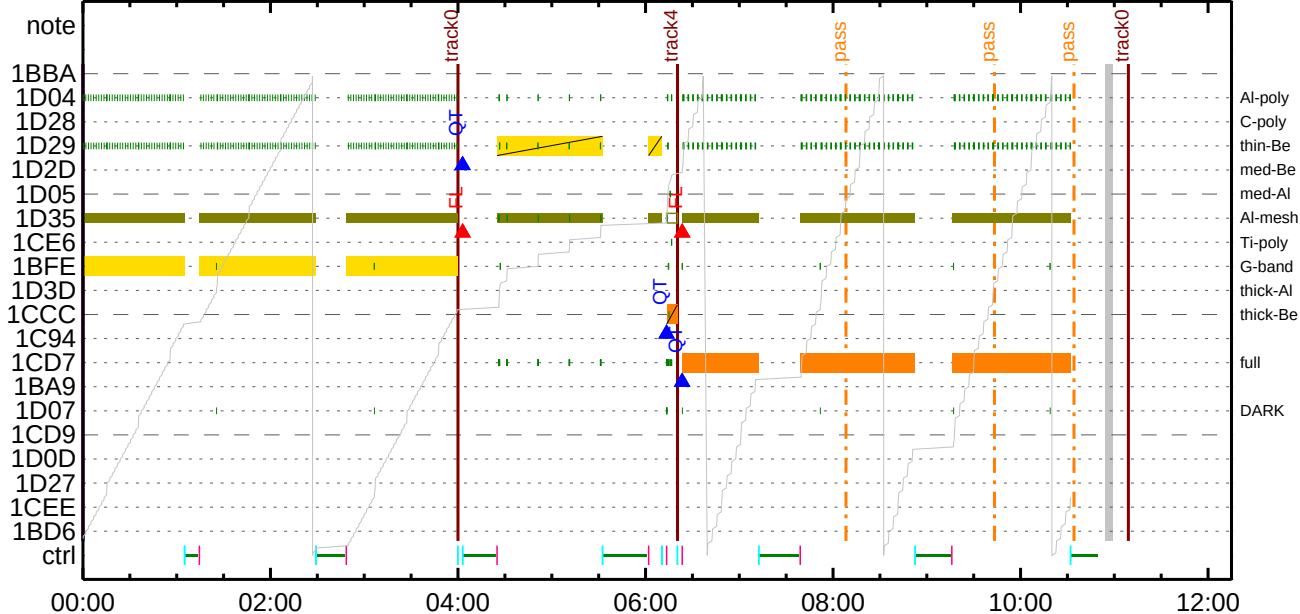
FLD Patrol												
Term	Pointing (x, y)	Comment										
09/30 10:21:20 - 09/30 12:15:18	cannot be identified											
09/30 12:31:18 - 09/30 17:41:18	Fixed (-468.0, 0.0)	HOP130 04/15										
09/30 17:51:18 - 10/01 05:57:18	Track (-294.8, 21.0) @ 09/30 17:51:00	AR obs										
10/01 06:07:18 - 10/01 18:00:18	Track (-185.9, 18.4) @ 10/01 06:07:00	AR obs										
10/01 18:10:18 - 10/02 06:10:48	Track (-76.5, 17.3) @ 10/01 18:10:00	AR obs										
10/02 06:20:48 - 10/04 10:26:00	Track (34.7, 17.8) @ 10/02 06:20:30	AR obs										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

A horizontal number line is shown with tick marks at every integer from 0 to 10. A red arrow points to the tick mark for the number 7.

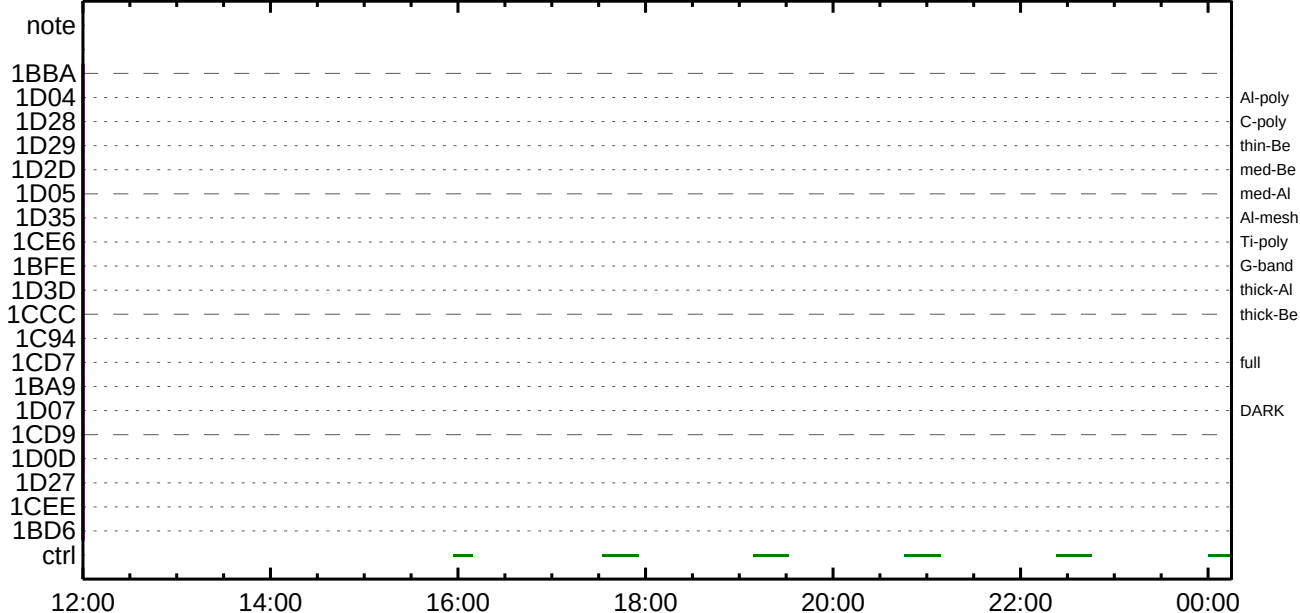
CMDI #0869 2025/10/01



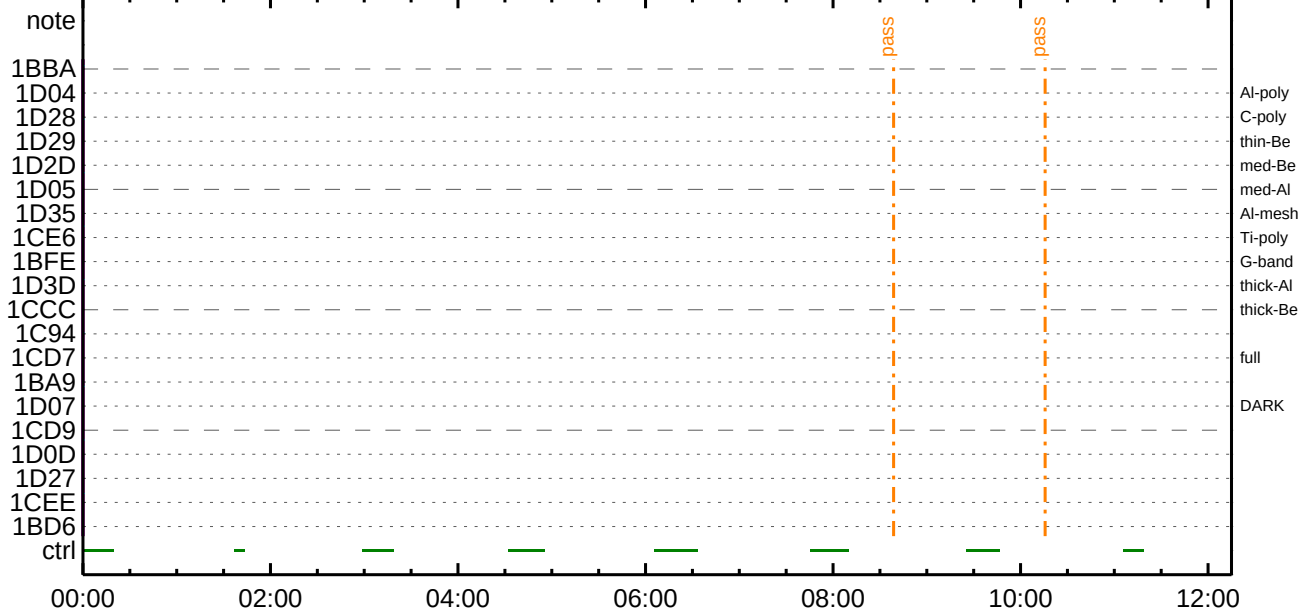
CMDI #0869 2025/10/02



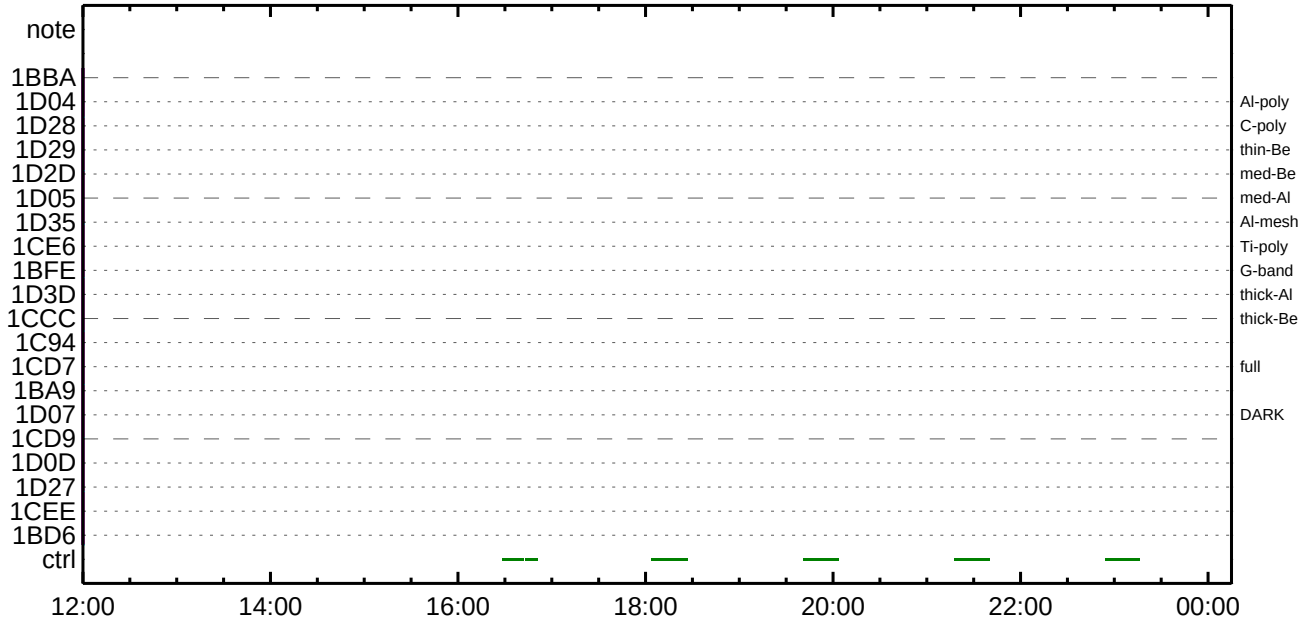
CMDI #0869 2025/10/02



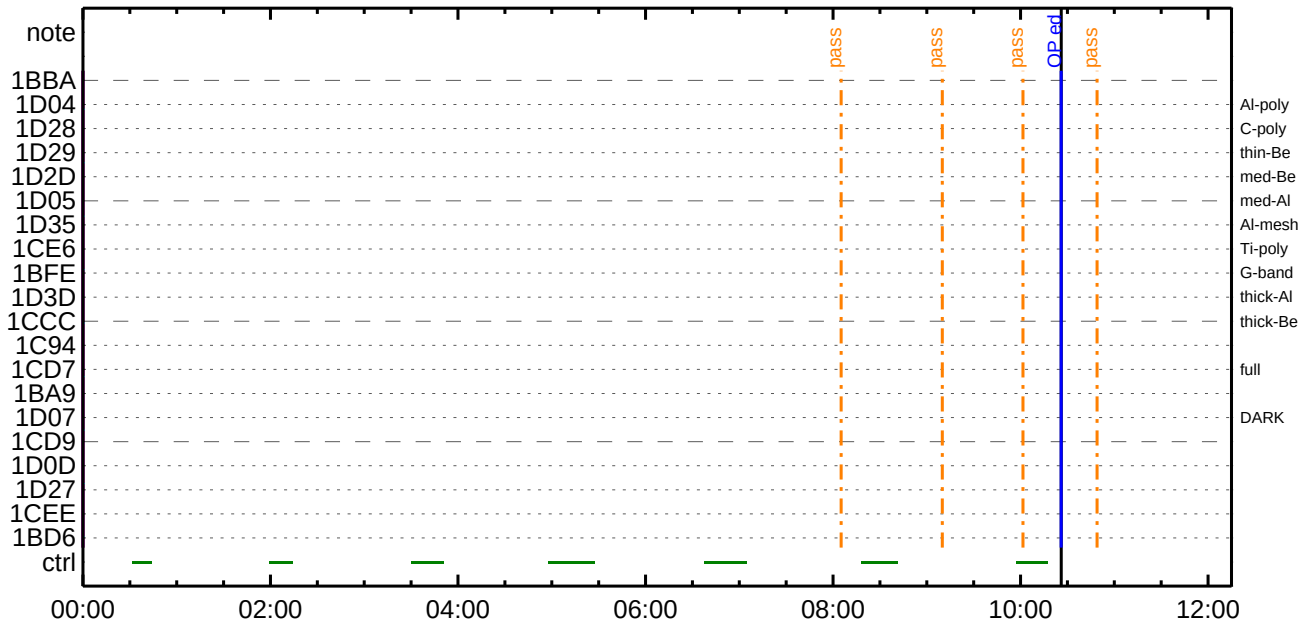
CMDI #0869 2025/10/03

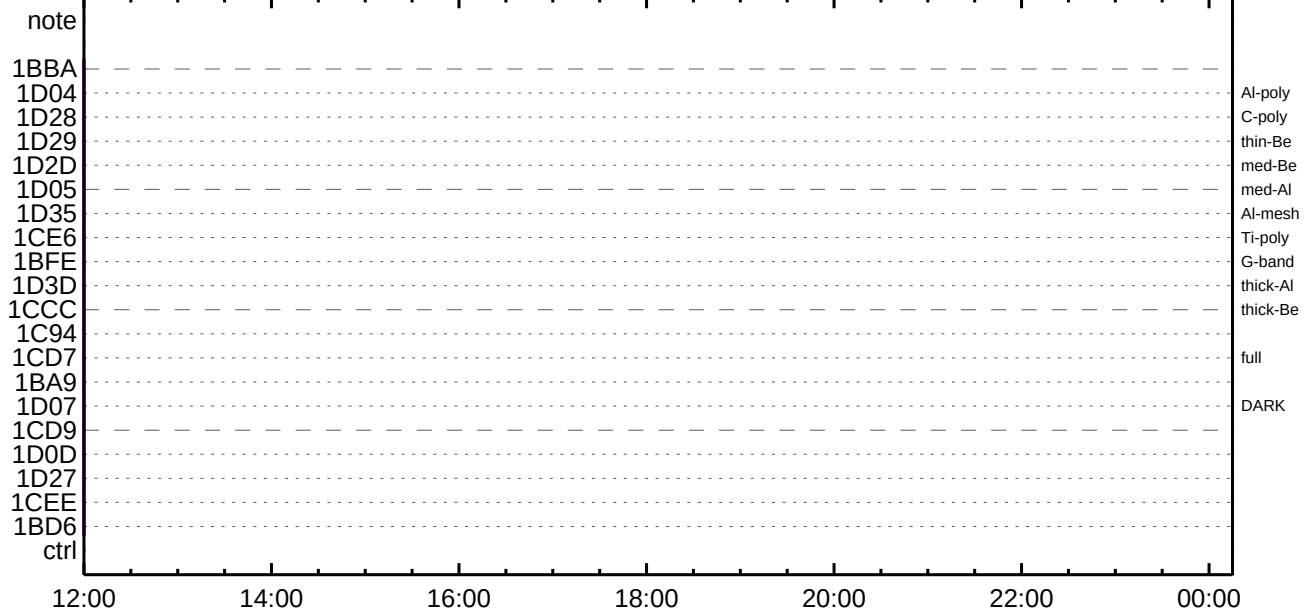


CMDI #0869 2025/10/03



CMDI #0869 2025/10/04





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main-546 2025-09-30 14:30:04 194 33 SOLAR-B MAIN //
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0095 . C. NOTICE ;\$ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾¼¹Ç;ç°Ê²¼¤ÎTI-CMDÁ÷¿®¤Î¼Â¹Ô¤•¤¤¤¤³¤È;£

0096	C.	SET EDUMP			
0097	C.				
0098	C.	TI 2025-09-30 11:28:00.0			
0099	+	TI 2025-09-30 11:28:00.0			
0100	DC	01-B3 DHU_OP_STOP			
0101	C.	[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0102	C.				
0103	+	TI 2025-09-30 11:28:01.0			
0104	DC	01-B4 DHU_OP_COPY			
0105	C.	[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0106	C.				
0107	+	TI 2025-09-30 11:28:01.0			
0108	DC	01-B5 DHU_OPOG_COPY			
0109	C.	[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0110	C.				
0111	+	TI 2025-09-30 11:32:59.5			
0112	DC	01-B2 DHU_OP_START			
0113	C.	[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0114	C.				
0115	C.				
0116	C.	[HK1_TI_CMD_ENA/DIS]	EQ		ENA
0117	C.	[HK1_TI_CMD_NUM]	EQ		4
0118	C.	[HK1_NEXT_EXEC_PIM]	EQ		DHU
0119	C.	[HK1_NEXT_EXEC_DC]	EQ		0xB3
0120	C.				
0121	C.	*****			
0122	C.	TI 2025-09-30 11:32:59.5			
0123	C.	*****			
0124	C.				
0125	C.	TI_TBL (0x03AB00-0x03AEFF; 1024byte)			
0126	+	DC 01-23 DHU_DMA_DMP_PRM_SET			
0127	BC	(03 ab 03 01 02)			
0128	C.	[HK1_DMP_TOP_ADRS_1]	EQ	07	
0129	C.	[HK1_DMP_TOP_ADRS_0]	EQ	2B	
0130	C.	[HK1_DMP_BLOCK_NUM]	EQ	3	
0131	C.	[HK1_DMP_REPEAT_NUM]	EQ	0	
0132	C.	[HK1_DMA_DMP_PIM]	EQ		DHU
0133	+	DC 01-22 DHU_MODE_CHNG			
0134	BC	(07 0b f8)			
0135	C.	[HK1_PKT_FORM_NO]	EQ	7	
0136	C.	[HK1_PKT_GEN_TIME]	EQ	0.25	s
0137	C.	[HK1_S_TLM_BIT_RATE]	EQ	32k	
0138	C.	[HK1_X_TLM_BIT_RATE]	EQ	4M	
0139	C.	[HK1_DMP_CHK_FLG]	EQ		EXEC
0140	C.				
0141	C.				
0142	C.	[HK1_DMP_CHK_FLG]	EQ		NON
0143	C.				
0144	C.	RAM ID=TI_TBL			
0145	C.				
0146	C.	DHU			
0147	+	DC 01-22 DHU_MODE_CHNG			
0148	BC	(02 0a f8)			
0149	C.	[HK1_PKT_FORM_NO]	EQ	2	
0150	C.	[HK1_PKT_GEN_TIME]	EQ	0.5S	
0151	C.	[HK1_S_TLM_BIT_RATE]	EQ	32K	
0152	C.	[HK1_X_TLM_BIT_RATE]	EQ	4M	
0153	C.				
0154	C.	Stop EIS observation and temporarily disable EIS mode changes			
0155	C.				
0156	C.				
0157	C.	***** Start EIS operation (TI set) *****			
0158	C.	Execute, after the success of OP upload.			
0159	C.	Set EIS TI-commands			
0160	+	TI 2025-09-30 11:32:30.0			
0161	DC	07-FC EIS_MODE_MANU			
0162	BC	(21 02)			
0163	+	TI 2025-09-30 11:32:40.0			
0164	DC	07-FC EIS_MODE_CHG_DIS			
0165	BC	(22)			
0166	C.	[HK1_TI_CMD_NUM]	EQ	2	COUNTUP
0167	C.	***** End EIS operation (TI set) *****			
0168	C.				
0169	C.				
0170	C.				
0171	C.	***** XRT START *****			
0172	C.	Execute, after the success of OP upload.			
0173	+	TI 2025-09-30 11:32:00.0			
0174	DC	07-F0 MDP_XRT_MODE_STBY			
0175	BC	(c3)			
0176	C.	[HK1_TI_CMD_NUM]	EQ	1	COUNTUP
0177	C.				
0178	C.	***** XRT END *****			
0179	C.				
0180	C.	***** MDP			
0181	C.	(			
0182	S.	DC-BC dcbc-402:DCBC			
0183		(MDP_known_event)			
0184	C.				
0185	C.				
0186	C.	*****			
0187	S.	DC-BC dcbc-153:DCBC			
0188		(SPECIAL			

[illegible]

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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 4307095.6 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 27s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-09-30 11:32:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0165 C. (¼å°î¥Ö¥Å¥Ê¥Ð¥Ê¥Å¥Ç¥ÊñÊ¼ñ¼Å»Û¹ñë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±¿ÍÑñÊ'Øñ¹ñëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥Á¥S¥Ã¥¹¼Å»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

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```
main-548 2025-09-30 14:20:04 186 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲçŲÃŲ×Ųí;¼ŲÉ;ËÈè²µ•íîÊ;ËÈÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Öòò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÒÄÖñ¬°ÄÄêñ•ñççé;ç°Ê²¼ñî°EÄ,¼ê½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. °° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ»°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲEÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñ¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. °° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ»°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 20 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 85 83 08 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 + DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 0c)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 0e)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2025-09-30 11:32:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîñÎ»ö¼ÝñÊÃÐñ¹ñÐDCBC•×²è *****
0173 C. (¾å°î¥Ó¥Ã¥Ê¥Ð¥Ê¥â¥ç¥ËñÊ½¾ñ¼Å»Ûñ¹ñË)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** ¥Ð¥¹•Ï Daily±¿îññÊ´Øñ¹ñÐDCBC•×²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ¿ãLOS¥Ã¥$¥Ã¥¬¼Å»Û¿ã
0184 C.
0185 . C. ***** LOS *****
0186 C.
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*** OP Sequence for XRT ***

2025/09/30	11:42:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	11:42:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	11:42:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	11:43:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 00 00 ac cd
2025/09/30	11:43:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	11:43:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	11:43:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	11:43:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	11:43:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	11:45:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/30	11:45:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/30	11:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	11:58:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	11:58:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	11:58:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	11:59:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 d6 67
2025/09/30	11:59:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	11:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	11:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	11:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	11:59:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	12:01:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/30	12:01:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/30	12:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	12:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	12:14:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	12:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	12:15:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/09/30	12:15:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/09/30	12:15:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/09/30	12:15:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	12:17:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/09/30	12:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	12:30:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	12:30:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	12:30:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	12:31:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 29 99
2025/09/30	12:31:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	12:31:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	12:31:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	12:31:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	12:31:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	12:33:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/30	12:33:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/30	12:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	12:46:54.0	XRT_CTRL_MANU_402_OG [0x192]			

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2025/09/30	12:46:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	12:46:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	12:47:00.0	AOCs_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/09/30	12:47:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 53 33	
2025/09/30	12:47:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	12:47:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	12:47:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	12:47:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	12:49:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	12:49:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2025/09/30	12:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/09/30	13:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	13:02:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	13:02:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	13:03:00.0	AOCs_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/09/30	13:03:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 d6 36 b7 8e	
2025/09/30	13:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	13:03:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	13:03:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	13:03:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	13:05:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	13:05:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2025/09/30	13:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/09/30	13:12:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	13:12:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	13:12:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	13:13:00.0	AOCs_Ore-point_Start_7_OG [0x09d]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/09/30	13:13:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 b4 b5 db 75	
2025/09/30	13:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	13:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	13:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	13:13:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	13:15:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	13:15:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2025/09/30	13:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/09/30	13:28:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	13:28:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	13:28:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	13:29:00.0	AOCs_Ore-point_Start_8_OG [0x09e]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/09/30	13:29:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 ac 5b 00 00	
2025/09/30	13:29:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	13:29:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	13:29:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	13:29:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	13:31:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	13:31:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	

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2025/09/30	13:32:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	13:44:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	13:44:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	13:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	13:45:00.0	AOCS_ORe-point_Start_9_OG [0x09f]			
		AOCU_NM	5	02-76	00 b4 b5 24 8b
2025/09/30	13:45:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	13:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	13:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	13:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	13:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	13:47:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/30	13:47:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/30	13:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	14:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	14:00:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	14:00:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	14:01:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 d6 36 48 72
2025/09/30	14:01:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	14:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	14:01:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	14:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	14:01:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	14:03:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/30	14:03:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/30	14:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	14:10:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	14:10:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	14:10:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	14:11:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]			
		AOCU_NM	5	02-76	00 29 ca b7 8e
2025/09/30	14:11:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	14:11:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	14:11:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	14:11:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	14:11:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	14:13:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2025/09/30	14:13:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/09/30	14:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/09/30	14:20:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	14:20:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/09/30	14:20:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/09/30	14:21:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]			
		AOCU_NM	5	02-76	00 4b 4b db 75
2025/09/30	14:21:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/09/30	14:21:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/09/30	14:21:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/09/30	14:21:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/09/30	14:21:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/09/30	14:23:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]			

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2025/09/30	14:23:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2025/09/30	14:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/30	14:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	14:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	14:36:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	14:37:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/09/30	14:37:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	53 a5 00 00
2025/09/30	14:37:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	14:37:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	14:37:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	14:37:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	14:39:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	14:39:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2025/09/30	14:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/30	14:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	14:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	14:52:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	14:53:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/09/30	14:53:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	4b 4b 24 8b
2025/09/30	14:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	14:53:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	14:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	14:53:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	14:55:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	14:55:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2025/09/30	14:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/30	15:08:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	15:08:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	15:08:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	15:09:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/09/30	15:09:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	29 db 48 72
2025/09/30	15:09:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	15:09:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	15:09:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	15:09:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	15:11:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	15:11:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2025/09/30	15:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/30	15:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	15:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	15:17:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	15:18:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2025/09/30	15:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	03 be 01 f3
2025/09/30	15:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	15:18:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	15:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
			MDP_XRT_ARS_DIS	1	07-F0	d5	

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2025/09/30	15:18:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	15:20:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2025/09/30	15:20:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/30	15:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	16:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	16:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	16:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	16:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/09/30	16:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/09/30	16:52:30.0	XRT_Custom_430_OG [0x1ae]					
2025/09/30	16:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	17:40:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	17:40:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/09/30	17:41:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00 00 00 00
2025/09/30	17:41:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/09/30	17:41:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/09/30	17:41:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	17:43:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
2025/09/30	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	17:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	17:50:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	17:50:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2025/09/30	17:51:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	04	03 be 01 f3
2025/09/30	17:51:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/09/30	17:51:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/09/30	17:51:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/09/30	17:51:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/09/30	17:51:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	17:53:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
2025/09/30	17:53:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/09/30	17:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	18:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	18:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	18:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	18:05:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/09/30	18:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/09/30	18:29:00.0	XRT_Custom_430_OG [0x1ae]					
2025/09/30	18:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	19:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	19:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	19:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/09/30	19:42:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/09/30	19:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/09/30	20:05:30.0	XRT_Custom_430_OG [0x1ae]					
2025/09/30	20:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/09/30	21:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/09/30	21:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2025/09/30	21:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/09/30	21:19:06.0	MDP_XRT_FLD_RESET XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/09/30	21:22:14.0	MDP_XRT_PREFLR_STRT XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/09/30	21:42:30.0	MDP_XRT_PREFLR_STOP XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/09/30	21:43:30.0	MDP_XRT_CTRL_AUTO XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/09/30	22:56:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/09/30	22:56:02.0	MDP_XRT_CTRL_MANU XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/09/30	22:56:04.0	MDP_XRT_CTRL_MANU XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/09/30	22:56:06.0	MDP_XRT_FLD_RESET XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/09/30	22:59:14.0	MDP_XRT_PREFLR_STRT XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/09/30	23:18:00.0	MDP_XRT_PREFLR_STOP XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/09/30	23:19:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/10/01	00:33:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/01	00:33:02.0	MDP_XRT_CTRL_MANU XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/01	00:33:04.0	MDP_XRT_CTRL_MANU XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/01	00:33:06.0	MDP_XRT_FLD_RESET XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/01	00:36:14.0	MDP_XRT_PREFLR_STRT XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/01	00:45:30.0	MDP_XRT_PREFLR_STOP XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/10/01	00:46:30.0	MDP_XRT_CTRL_AUTO XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/10/01	02:01:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/01	02:01:02.0	MDP_XRT_CTRL_MANU XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/01	02:01:04.0	MDP_XRT_CTRL_MANU XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/01	02:01:06.0	MDP_XRT_FLD_RESET XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/01	02:04:14.0	MDP_XRT_PREFLR_STRT XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/01	02:16:00.0	MDP_XRT_PREFLR_STOP XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/10/01	02:17:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/10/01	03:31:30.0	MDP_XRT_CTRL_AUTO XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/01	03:31:32.0	MDP_XRT_CTRL_MANU XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/01	03:31:34.0	MDP_XRT_CTRL_MANU XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/01	03:31:36.0	MDP_XRT_FLD_RESET XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/01	03:34:44.0	MDP_XRT_PREFLR_STRT XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/01	03:52:00.0	MDP_XRT_PREFLR_STOP XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2025/10/01	03:53:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1
2025/10/01	03:59:54.0	MDP_XRT_CTRL_AUTO XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/01	03:59:56.0	MDP_XRT_CTRL_MANU XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/01	03:59:58.0	MDP_XRT_CTRL_MANU XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22 ff aa 00
2025/10/01	04:00:00.0	XRT_FOCUS_POSITION AOCS_Ore-point_Start_3_OG [0x099]	5	02-76	00 00 00 00 00
2025/10/01	04:00:18.0	AOCU_NM XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8
2025/10/01	04:00:20.0	MDP_XRT_FLD_ENA XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8
2025/10/01	04:00:22.0	MDP_XRT_FLRCTRL_ENA XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0
2025/10/01	04:00:24.0	MDP_XRT_AEC_RESET XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
2025/10/01	04:00:26.0	MDP_XRT_ARS_DIS XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da
2025/10/01	04:02:56.0	MDP_XRT_FLD_RESET XRT_QT_PROG_SET_433_OG [0x1b1]	2	07-F0	c4 11
2025/10/01	04:02:58.0	MDP_XRT_QT_PROG_SET XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0	c5 0e
2025/10/01	04:03:00.0	MDP_XRT_FL_PROG_SET XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2025/10/01	05:00:00.0	MDP_XRT_CTRL_AUTO XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/01	05:00:02.0	MDP_XRT_CTRL_MANU XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/01	05:00:04.0	MDP_XRT_CTRL_MANU XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/01	05:00:06.0	MDP_XRT_FLD_RESET XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
		MDP_XRT_PREFLR_STRT			

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2025/10/01	05:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	05:29:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/01	05:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	05:56:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	05:56:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	05:56:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/01	05:57:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/01	05:57:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/01	05:57:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/01	05:59:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/10/01	06:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	06:06:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	06:06:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	06:06:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/01	06:07:00.0	AOCs_ORe-point_Start_16_OG [0x0a6]			
		AOCU_NM	5	02-76	04 03 be 01 f3
2025/10/01	06:07:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/01	06:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/01	06:07:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/01	06:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/01	06:07:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	06:09:56.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/10/01	06:09:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/01	06:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	06:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	06:40:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	06:40:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	06:40:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	06:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	07:06:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/01	07:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	08:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	08:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	08:20:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	08:20:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	08:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	08:43:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/01	08:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	09:59:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	09:59:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	09:59:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	09:59:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	10:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	10:19:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/01	10:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	11:41:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	11:41:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	11:41:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	11:41:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	11:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

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2025/10/01	11:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	11:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/10/01	15:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	15:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	15:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	15:27:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	15:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	15:36:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/01	15:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/10/01	17:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	17:00:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	17:00:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	17:00:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	17:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	17:24:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/01	17:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/10/01	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_FOCUS_POSITION	1	07-F0	c1
2025/10/01	18:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/01	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2025/10/01	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/01	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/01	18:02:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/01	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/10/01	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_FOCUS_POSITION	1	07-F0	c1
2025/10/01	18:10:00.0	AOCs_ORe-point_Start_16_OG [0x0a6]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/01	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04 03 be 01 f3
2025/10/01	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/01	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/01	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/01	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/01	18:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/10/01	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/01	18:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	18:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	18:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	18:37:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/01	18:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/01	19:00:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/01	19:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/10/01	20:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/01	20:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	20:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/01	20:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2025/10/02	05:32:32.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	05:32:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/02	05:32:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/02	05:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/02	06:01:00.5	XRT_Custom_430_OG [0x1ae]			
2025/10/02	06:02:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/02	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	06:10:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	06:10:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/02	06:10:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/02	06:10:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/02	06:10:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/02	06:13:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/10/02	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/02	06:20:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	06:20:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	06:20:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/02	06:20:30.0	AOCs_OrE-point_Start_16_OG [0x0a6]			
		AOCU_NM	5	02-76	04 03 be 01 f3
2025/10/02	06:20:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/02	06:20:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/02	06:20:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/02	06:20:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/02	06:20:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/02	06:23:26.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/10/02	06:23:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/02	06:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/02	07:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	07:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	07:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/02	07:12:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/02	07:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/02	07:38:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/02	07:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/02	08:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	08:52:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	08:52:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/02	08:52:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/02	08:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/02	09:15:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/02	09:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/02	10:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	10:32:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/02	10:32:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/02	10:32:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/02	10:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/02	11:09:00.0	AOCs_OrE-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00