

XRT Timeline to be uploaded on 2025/10/04

Period: 2025/10/04 10:26:00 - 2025/10/09 10:53:00

* * * * *

Normal mode

* * * * *

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with														
Term			Pointing (x, y)				Comment							
10/04 10:39:00 - 10/04 17:59:54			Track (463.6, 31.4) @ 10/04 10:36:00				# OP start + 10min + AR14236							
10/04 18:13:00 - 10/05 03:59:54			Track (521.7, 35.9) @ 10/04 18:10:00				#AR14236							
10/05 06:21:30 - 10/05 17:30:30			Track (608.6, 44.1) @ 10/05 06:18:30				#AR 14236							
10/05 18:11:30 - 10/05 19:44:54			Track (684.6, 53.0) @ 10/05 18:04:30				#AR 14236							
10/07 06:06:30 - 10/07 11:37:30			Track (856.4, 84.8) @ 10/07 06:03:30				#AR 14236							
PROG= 12 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
Seqn= 92		1-time(s)		2.0sec										
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2		4-time(s)		2.0sec										
Seqn= 47		1-time(s)		2.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 77		4-time(s)		300.0sec										
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)														
Term			Pointing (x, y)			Comment								
10/04 18:03:00 - 10/04 18:09:54			Fixed (0.0, 0.0)			synoptic								
10/05 06:11:30 - 10/05 06:18:24			Fixed (0.0, 0.0)			HOP 349 + synoptic, shifted 8.5 min								
10/05 17:57:30 - 10/05 18:04:24			Fixed (0.0, 0.0)			synoptic, shifted -5.5 min								
10/06 05:40:00 - 10/06 05:46:54			Fixed (0.0, 0.0)			HOP 349 + synoptic, shifted -23.0 min								
10/06 17:40:30 - 10/06 17:47:24			Fixed (0.0, 0.0)			synoptic, shifted -22.5 min								
10/07 05:56:30 - 10/07 06:03:24			Fixed (0.0, 0.0)			HOP 349 + synoptic, shifted -6.5 min								

PROG= 06															1-time(s)																							
Subr= 1															1-time(s)										2.0sec													
Seqn= 5															1-time(s)										2.0sec													
Open/Ti-poly															Open/thick-Al										close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
Open/Ti-poly															Open/thick-Al										close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
Open/Ti-poly															Open/thick-Al										close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
Open/Ti-poly															Open/thick-Al										close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)				DPCM	0	0	2.0sec
Open/Ti-poly															Open/thick-Al										close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)				DPCM	0	0	2.0sec
Seqn= 26															1-time(s)										2.0sec													
Open/Al-mesh															Open/Al-mesh										close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Open/Al-mesh															Open/Al-mesh										close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Open/Al-mesh															Open/Al-mesh										close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Seqn= 99															1-time(s)										2.0sec													
Al-poly/Open															Al-poly/Open										close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Al-poly/Open															Al-poly/Open										close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Al-poly/Open															Al-poly/thick-Al										close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Seqn= 83															1-time(s)										2.0sec													
thin-Be/Open															thin-Be/Open										close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
thin-Be/Open															thin-Be/Open										close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
thin-Be/Open															thin-Be/Open										close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Seqn= 23															1-time(s)										4.0sec													
Open/G-band															Open/G-band										open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)				Q=90	0	0	2.0sec
Open/G-band															Open/G-band										close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Subr= 2															1-time(s)										2.0sec													
Seqn= 41															1-time(s)										2.0sec													
Open/thick-Be															Open/thick-Be										close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
Seqn= 17															1-time(s)										2.0sec													
med-Al/Open															med-Al/thick-Al										close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
med-Al/Open															med-Al/thick-Al										close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
med-Al/Open															med-Al/Open										close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Seqn= 33															1-time(s)										2.0sec													
med-Be/Open															Open/thick-Al										close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
med-Be/Open															med-Be/Open										close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
med-Be/Open															med-Be/Open										close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)				Q=95	0	0	2.0sec
Seqn= 75															1-time(s)										2.0sec													
Al-poly/Ti-poly															Al-poly/thick-Al										close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec
Al-poly/Ti-poly															Al-poly/thick-Al										close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)				Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
----------------	----------------	-----	------	-------	------	-----	-----	--------------------	-------	------------	----------

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
10/05 04:23:00 - 10/05 05:31:00	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted 8.5 min
10/06 04:03:00 - 10/06 05:36:54	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -23.0 min
10/07 04:03:00 - 10/07 05:53:24	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -6.5 min

PROG= 17 Inf.-time(s)											
Subr= 1 1-time(s) 300.0sec											
Seqn=100 1-time(s) 2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 84 1-time(s) 2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 57 1-time(s) 2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0 2.0sec
Seqn= 81 1-time(s) 2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0 2.0sec
Subr= 2 6-time(s) 1200.0sec											
Seqn= 8 1-time(s) 2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0 2.0sec
Seqn= 6 1-time(s) 2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0 2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0 2.0sec
Seqn= 29 1-time(s) 2.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0 2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1B96: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 360s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
10/05 19:48:00 - 10/06 03:59:54	Fixed (701.5, -645.1)	HOP 513 SOOP
10/06 05:50:00 - 10/06 17:37:24	Fixed (701.5, -645.1)	#HOP 513 SOOP

PROG= 19 Inf.-time(s)											
Subr= 1 1-time(s) 2.0sec											
Seqn= 30 1-time(s) 2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0 2.0sec
Subr= 2 10-time(s) 360.0sec											
Seqn= 8 1-time(s) 2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0 2.0sec
Seqn= 6 1-time(s) 2.0sec											
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0 2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #1D0D: HOP476_SOOP_AR_Be_Thin - (Med-Be) - Med-Be (Al-Thick) - AEC3 - PFB - Be-Thick- Be-Med 384X384 - AEC3

Term	Pointing (x, y)	Comment
10/06 17:50:30 - 10/07 03:59:54	Track (808.9, 73.3) @ 10/06 17:47:30	#HOP 515 SOOP AR 14236

PROG= 04 Inf.-time(s)											
Subr= 1 1-time(s) 2.0sec											
Seqn= 92 1-time(s) 2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0 2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0 2.0sec
Subr= 2 180-time(s) 2.0sec											
Seqn= 3 1-time(s) 2.0sec											
Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2 2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	2 2.0sec
Seqn= 39 1-time(s) 2.0sec											
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1 2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	1 2.0sec
Seqn= 80 1-time(s) 2.0sec											
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0 2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0 2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

* * * * *

Flare mode

* * * * *

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
10/04 10:39:00 - 10/04 17:59:54	Track (463.6, 31.4) @ 10/04 10:36:00	# OP start + 10min + AR14236
10/04 18:13:00 - 10/05 03:59:54	Track (521.7, 35.9) @ 10/04 18:10:00	#AR14236
10/05 04:23:00 - 10/05 05:31:00	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted 8.5 min
10/05 06:21:30 - 10/05 17:30:30	Track (608.6, 44.1) @ 10/05 06:18:30	#AR 14236
10/05 18:11:30 - 10/05 19:44:54	Track (684.6, 53.0) @ 10/05 18:04:30	#AR 14236
10/05 19:48:00 - 10/06 03:59:54	Fixed (701.5, -645.1)	HOP 513 SOOP
10/06 04:03:00 - 10/06 05:36:54	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -23.0 min
10/06 05:50:00 - 10/06 17:37:24	Fixed (701.5, -645.1)	#HOP 513 SOOP
10/07 04:03:00 - 10/07 05:53:24	Fixed (0.0, 0.0)	HOP 349 + synoptic, shifted -6.5 min
10/07 06:06:30 - 10/07 11:37:30	Track (856.4, 84.8) @ 10/07 06:03:30	#AR 14236

PROG= 14 30-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 68 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 73 1-time(s) 10.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Subr= 3 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 87 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1D26: HOP500_SOOP_FLARE_MODE Be_Thick_6s_Fixed_Exposure

Term	Pointing (x, y)	Comment
10/06 17:50:30 - 10/07 03:59:54	Track (808.9, 73.3) @ 10/06 17:47:30	#HOP 515 SOOP AR 14236

PROG= 16 30-time(s)

Subr= 1 1-time(s) 2.0sec													
Seqn= 87 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 2 120-time(s) 2.0sec													
Seqn= 44 1-time(s) 2.0sec													
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec	
Seqn= 32 10-time(s) 2.0sec													
Open/thick-Be	Open/thick-Be	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

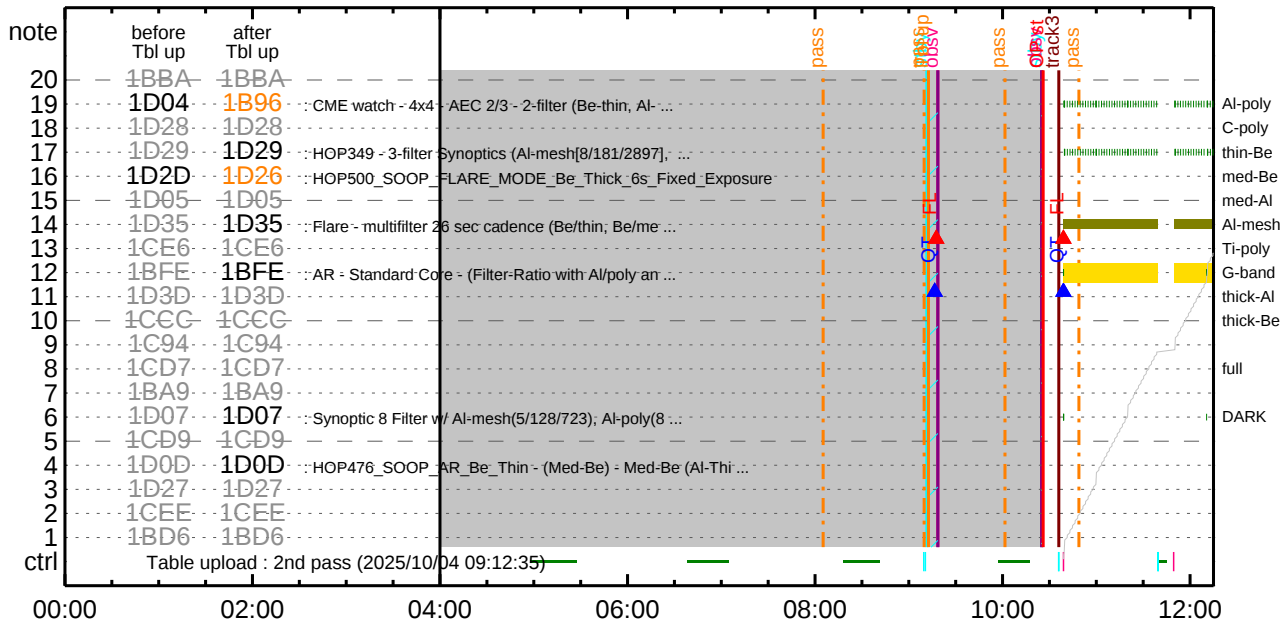
Flare Detection

* * * * *

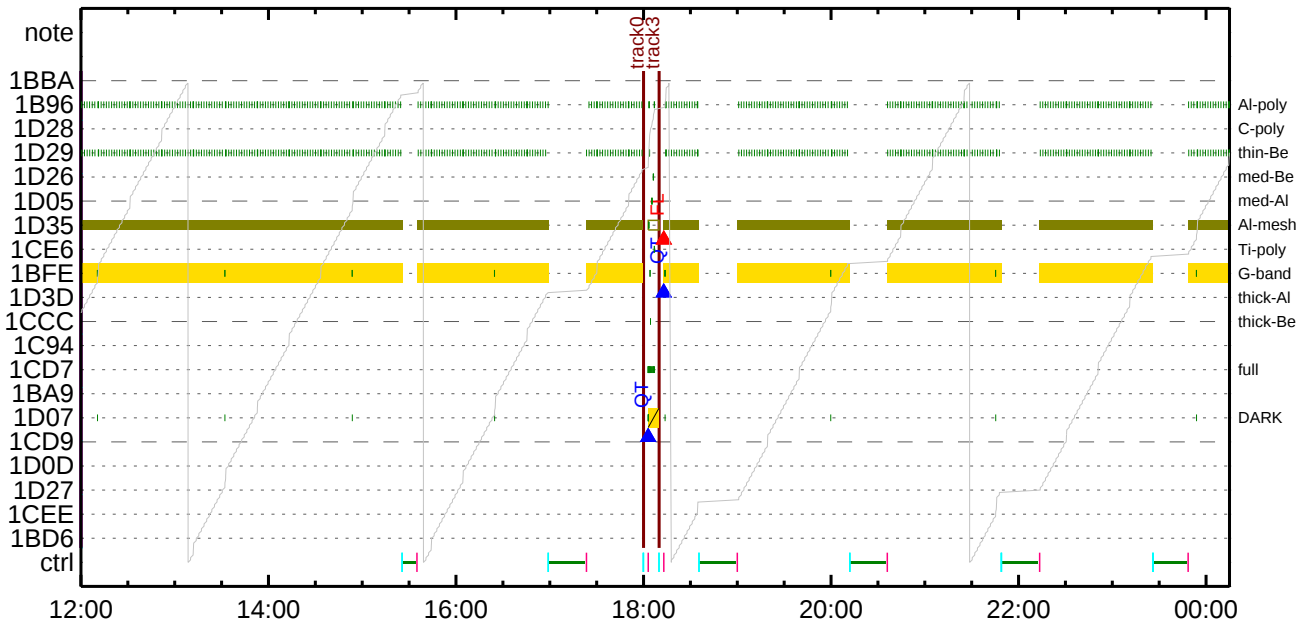
FLD Patrol

Term	Pointing (x, y)	Comment									
10/04 09:13:35 - 10/04 18:00:18	cannot be identified										
10/04 18:10:18 - 10/05 06:08:48	Track (521.7, 35.9) @ 10/04 18:10:00	#AR14236									
10/05 06:18:48 - 10/05 17:54:48	Track (608.6, 44.1) @ 10/05 06:18:30	#AR 14236									
10/05 18:08:48 - 10/06 05:37:18	Track (684.6, 53.0) @ 10/05 18:04:30	#AR 14236									
10/06 05:47:16 - 10/06 17:37:48	Fixed (701.5, -645.1)	#HOP 513 SOOP									
10/06 17:47:48 - 10/07 05:53:48	Track (808.9, 73.3) @ 10/06 17:47:30	#HOP 515 SOOP AR 14236									
10/07 06:03:48 - 10/09 10:53:00	Track (856.4, 84.8) @ 10/07 06:03:30	#AR 14236									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50	30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

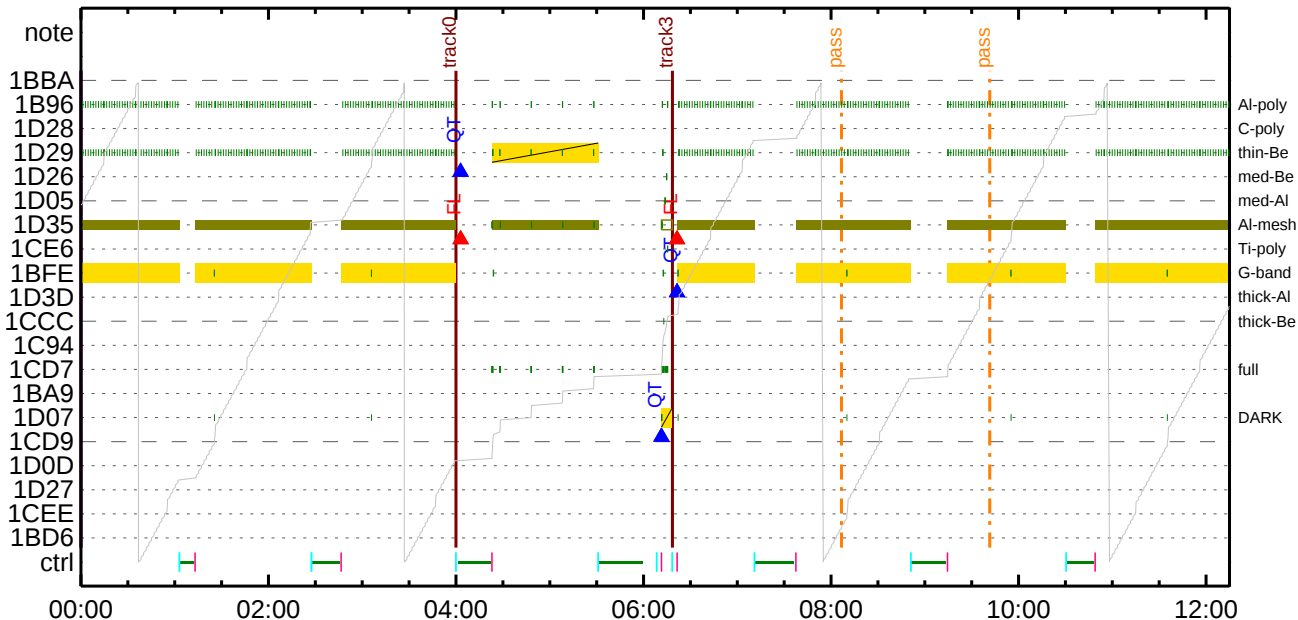
CMDI #0881 2025/10/04



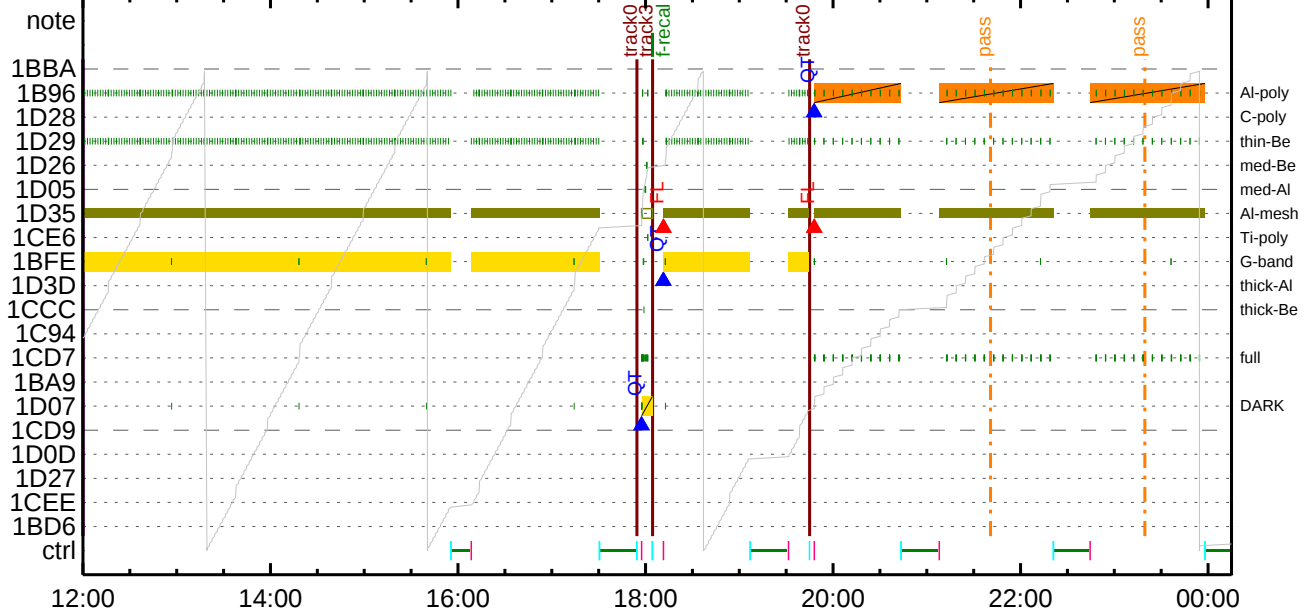
CMDI #0881 2025/10/04



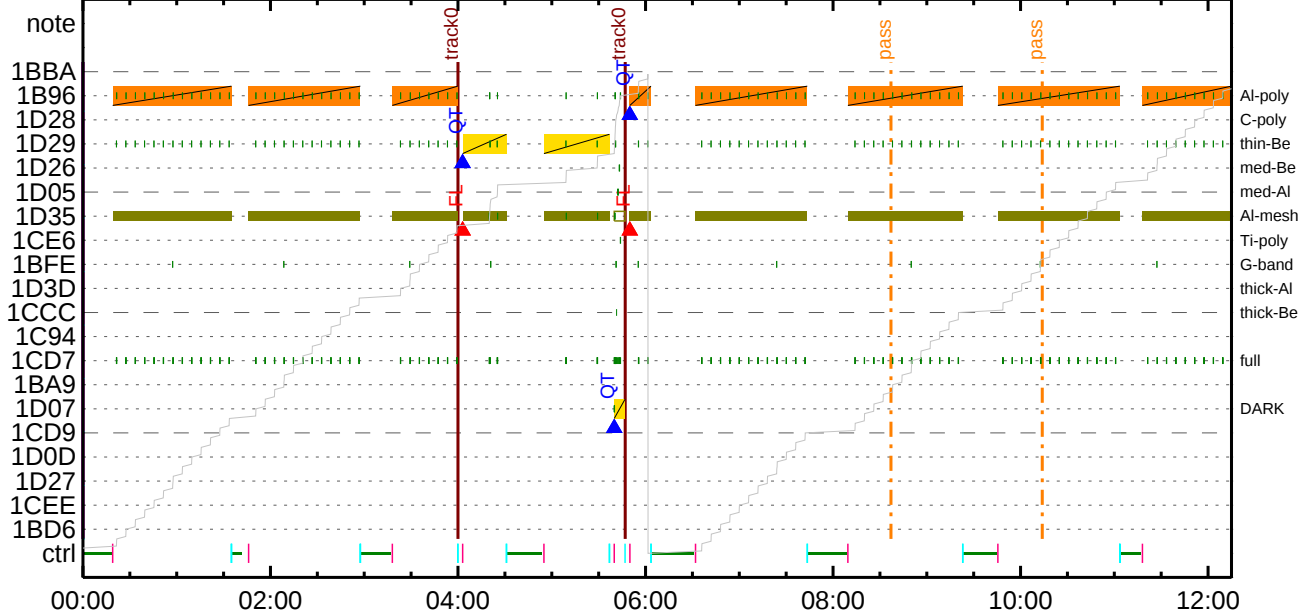
CMDI #0881 2025/10/05



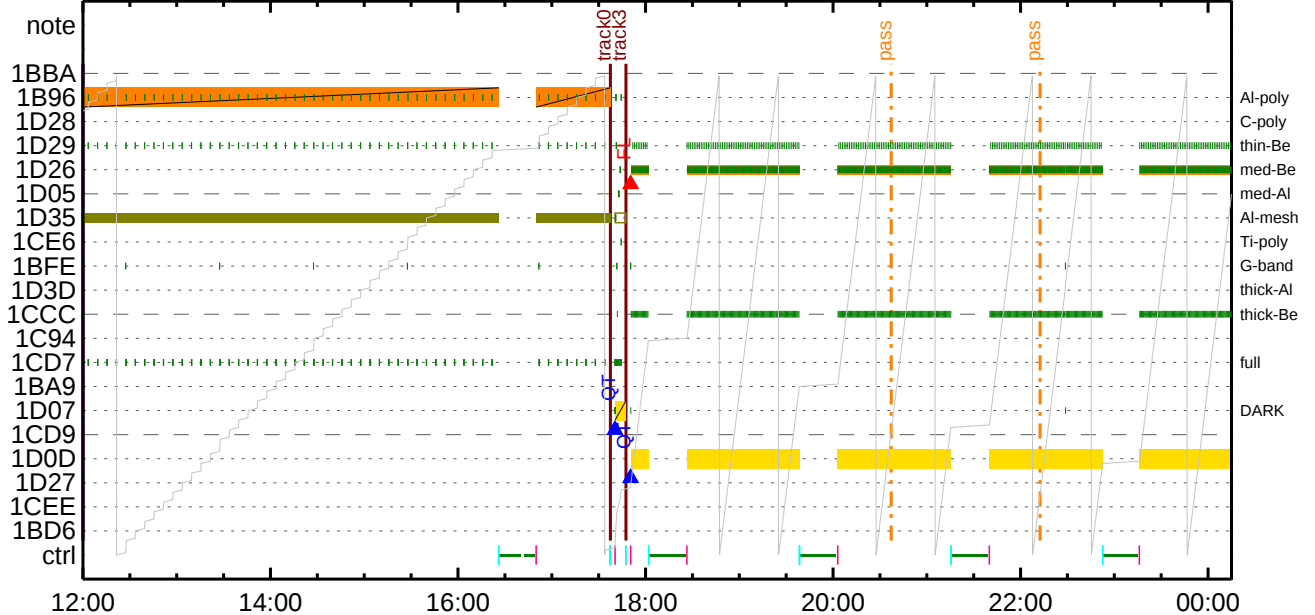
CMDI #0881 2025/10/05



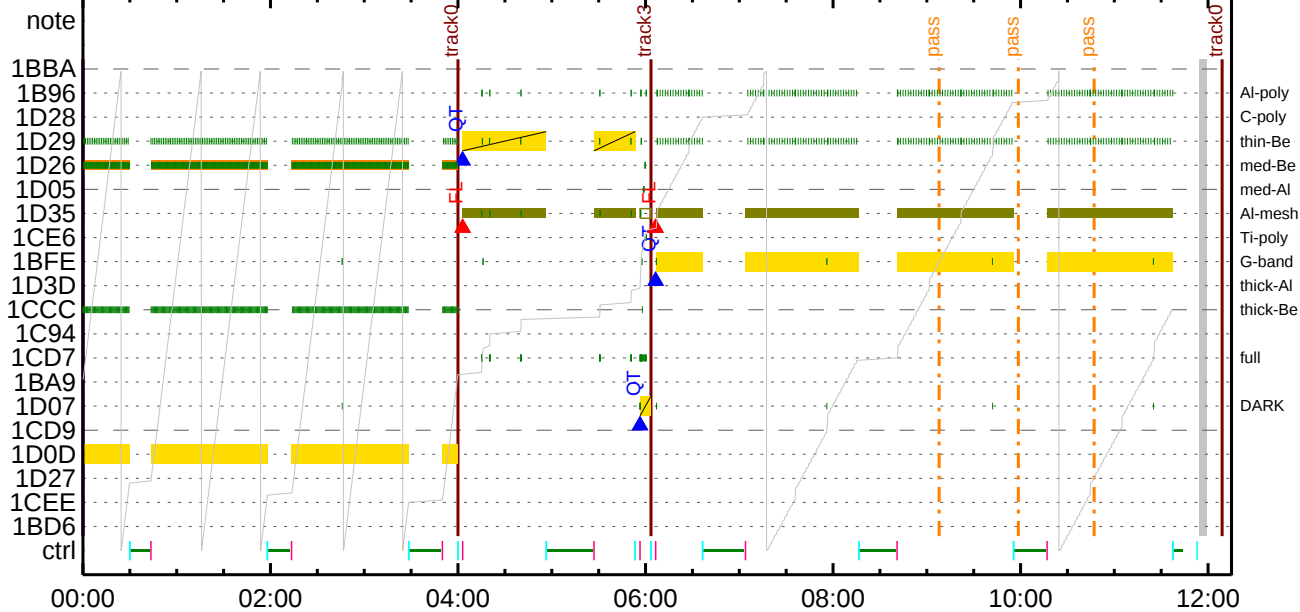
CMDI #0881 2025/10/06



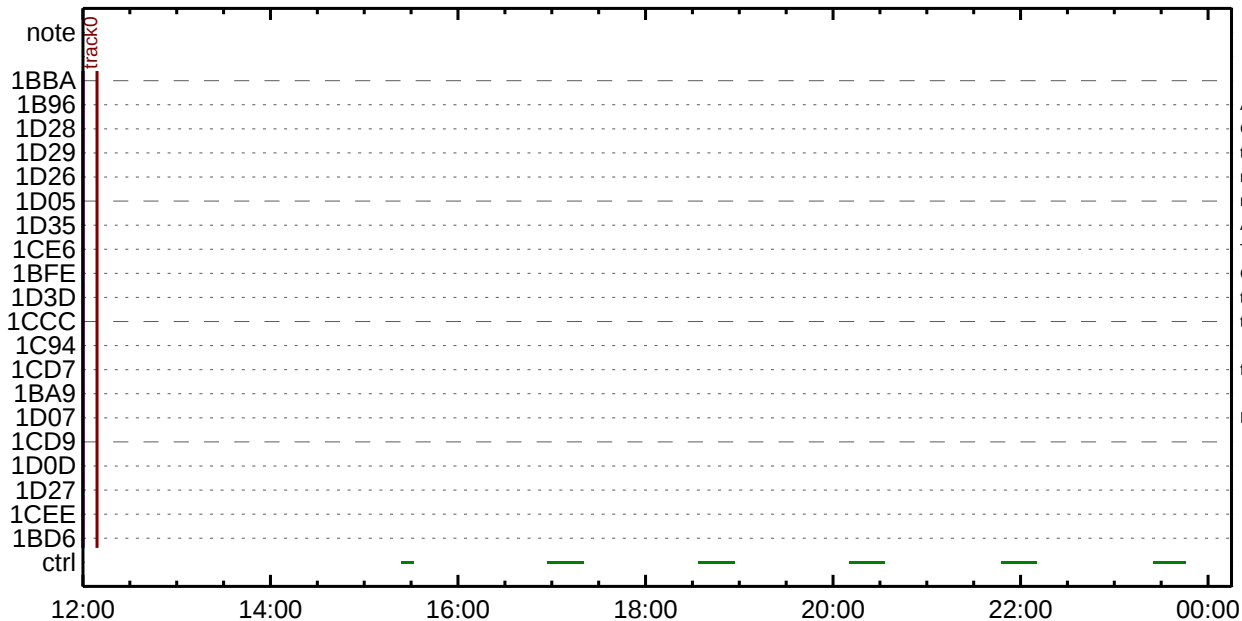
CMDI #0881 2025/10/06



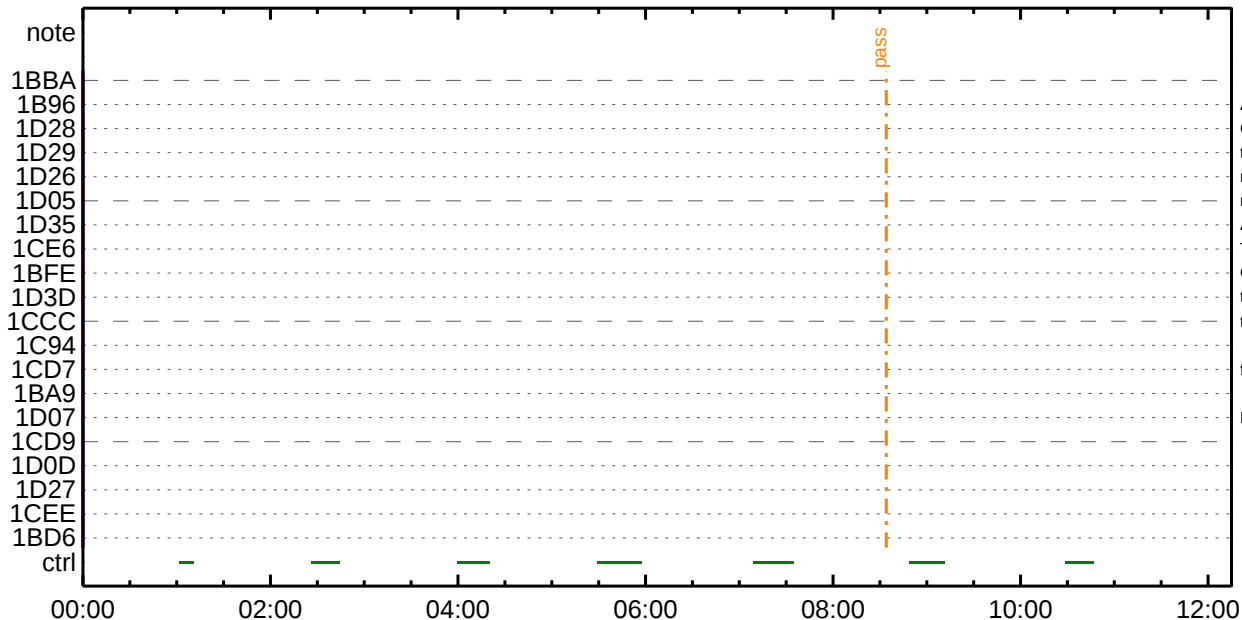
CMDI #0881 2025/10/07



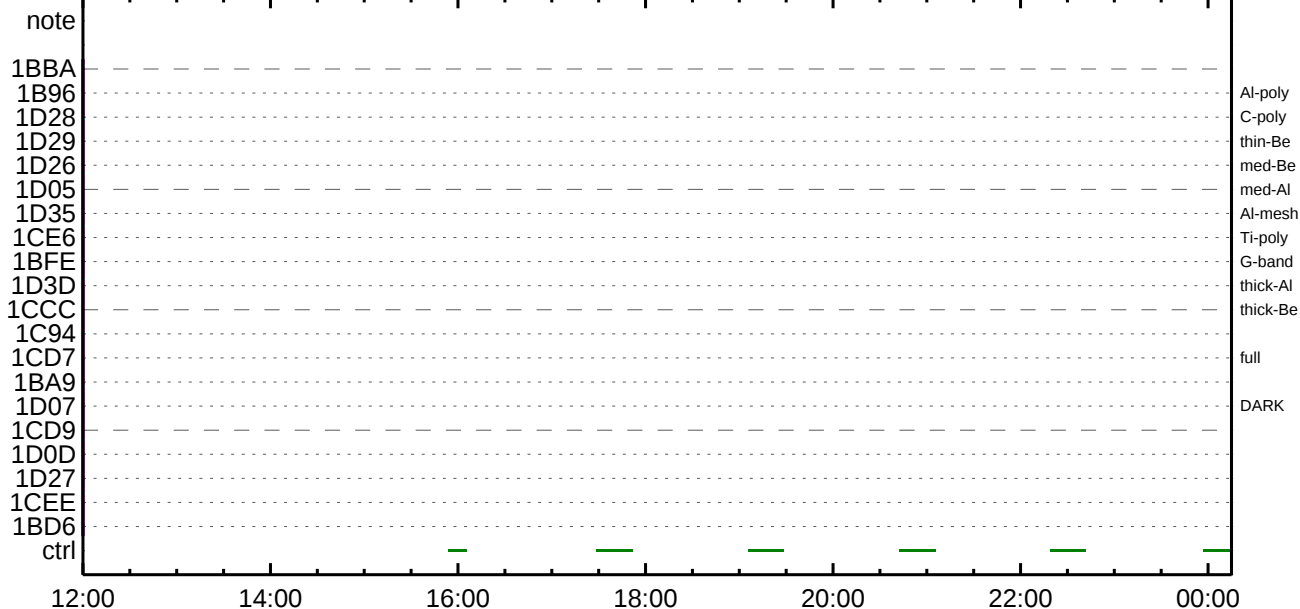
CMDI #0881 2025/10/07



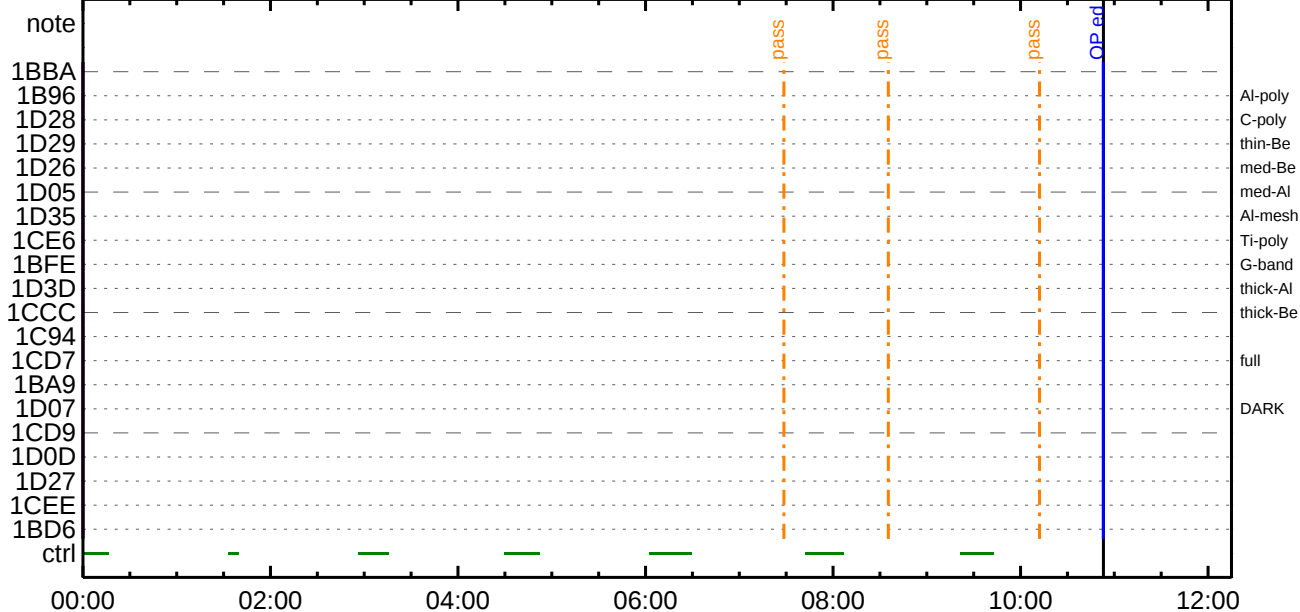
CMDI #0881 2025/10/08



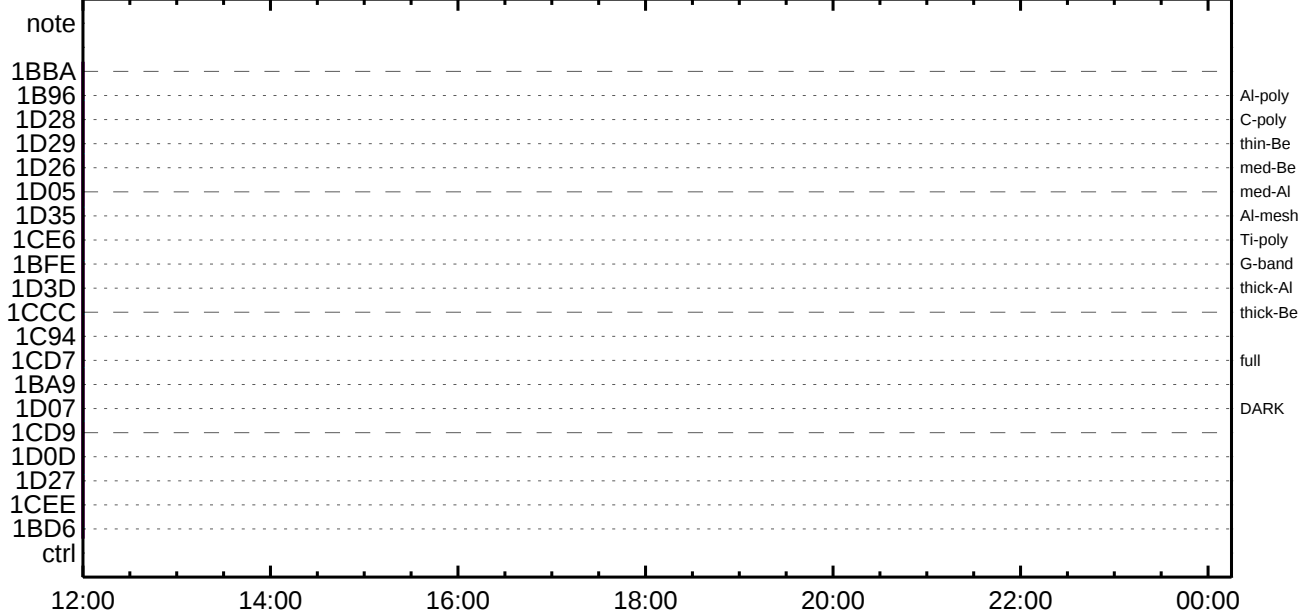
CMDI #0881 2025/10/08



CMDI #0881 2025/10/09



CMDI #0881 2025/10/09



[illegible]

```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 15s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-10-04 10:25:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÄîñî»ö¼ÝñÄðñ¹ñèDCBC•×²è *****
0156 C. (¼â°îÝÖÝÄÝÈÞÝÈÝÄÝÇÝèñÈ½¼ñ¼Ä»Üñ¹ñè)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•İ Daily±;İññÈ'Øñ¹ñèDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;äLOS¥Á¥$¥Ä¥~¼Ä»Ü;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-557 2025-10-04 12:38:27 102 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸSŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë»¿»Ä»•µ°»»Í×ÄÇ»ÍŸÇŸÄŸ×Ÿí;¼ŸÉ;ËËèµ•ííË;Ë»Ë¼°ÇÖ»•»¿¼¹Ç»Í;ÇÄ®,ù»¹»è»D»ÇÄ÷¿®»•»Ë»»»³»È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 85 83 08 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b 80 80 08 08)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 80 80 06 06)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0f 80 80 06 06)
0058 +. DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 10 80 80 08 08)
0060 +. DC 07-F0 MDP_XRT_FLD_ENA
0061 BC (d8)
0062 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0063 BC (c8)
0064 +. DC 07-F0 MDP_XRT_ARS_DIS
0065 BC (d5)
0066 +. DC 07-F0 MDP_XRT_AEC_RESET
0067 BC (d0)
0068 +. DC 07-F0 MDP_XRT_FLD_RESET
0069 BC (da)
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0071 BC (c4 0c)
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0073 BC (c5 0e)
0074 . C. ----- Success Verify ? OK / NG ____
0075 C.
0076 C.
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0078 C.
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV
0080 BC (c2)
0081 +. TI 2025-10-04 10:25:02.0
0082 DC 07-F0 MDP_XRT_MODE_OBSV
0083 BC (c2)
0084 . C. ----- Success Verify ? OK / NG ____
0085 C.
0086 C. ***** XRT END *****
0087 C.
0088 . C. ***** MDP 'ûÄî»Í»ö¼Ÿ»ËÄD»¹»èDCBC•×²è *****
0089 C. (¾ä°íŸÓŸÄŸËŸDŸËŸÄŸÇŸËèË¾»»¼Ä»Ü»¹»è)
0090 . S. DC-BC dcbc-402:DCBC
0091 (MDP_known_event)
0092 C.
0093 C.
0094 . C. ***** ŸDŸ¹•í Daily÷¿íÑ»Ë'Ø»¹»èDCBC•×²è *****
0095 . S. DC-BC dcbc-153:DCBC
```

0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÃŸ-¼Â»Ü;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.

```
main-558 2025-10-04 12:38:27 194 33 SOLAR-B MAIN //
```

```
main-558 2025-10-04 12:28:27 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸPŸóŸÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçñÄñ•µ°E»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèñµ•ííÉ;ËñÉ¼°ÇÖñ•ñç¼í¹çñí;çÄ®, ùñ¹ñèñPñÇÁ÷ç®•ñËñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÄŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-558:OP
0020 ()
0021 . S. OG og-558:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPîî°èŸÄŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñò³îÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñò³îÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñî¼Ë¹ç•ë²îOKñò³îÇ$
0081 C.
0082 . C. ***** °Ë²¼ñî¼Ä´¶Ä°ñËË¬ñ°Á÷ç® (¼¾µ-ŸÄŸóŸ×¼è½çñòÄÓÄæñÇ½ªñ¬ñè¼í¹çñÇñâ) *****
0083 C. DHUŸä;¼ŸÉ;Ë¼Ÿ½,Ÿí;¼ŸÉ;Ëñòíäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñî¼í¹ç;ç°Ë²¼ñîTI-CMDÁ÷ç®ñî¼Ä¹Ôñ•ñËñññ³ñÈ;£
```

```

0096 C.          SET EDUMP I±°iNŸ¹C¹Ô|³E;E
0097 C.
0098 C. TIŸ³ŸPŸóŸÉ±ðÄDİ¿ (UT)
0099 +. TI 2025-10-04 10:21:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2025-10-04 10:21:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2025-10-04 10:21:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2025-10-04 10:25:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C. °Ê¼IÄê¼iÍÑIŸÁŸSŸÄŸ¹àIÜ
0116 C.          ÇÇ [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 4
0118 C.          ÇÇ [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.          ÇÇ [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èŸÄŸóŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          ÇÇ [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.          ÇÇ [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.          ÇÇ [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.          ÇÇ [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.          ÇÇ [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          ÇÇ [HK1_PKT_FORM_NO] EQ 7
0136 C.          ÇÇ [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.          ÇÇ [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.          ÇÇ [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.          ÇÇ [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C. ŸÄŸóŸ×½ªİ»ð³İÇS
0142 C.          ÇÇ [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C. RAM ID=TI_TBLI¼È¹Ç•ê²İOK±ð³İÇS
0145 C.
0146 C. DHUŸâ;¼ŸÉ;Ê¼Ÿ½,Ÿi;¼ŸÈ;Ë±ðİá±¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          ÇÇ [HK1_PKT_FORM_NO] EQ 2
0150 C.          ÇÇ [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.          ÇÇ [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.          ÇÇ [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2025-10-04 10:25:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2025-10-04 10:25:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2025-10-04 10:25:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´ûÃİI»ö¼Ÿ±ÊÄ±¹±ÊDCBC•×²è *****
0181 C. (¼â°İŸÓŸÄŸÈŸPŸÈŸÄŸÇŸÈ±Ê¼±¼Ä»Ü±¹±è)
0182 C. S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** ŸĐŸ¹•İ Daily±¿İÑ±Ê´Ø±¹±ÊDCBC•×²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;äLOSŸÁŸSŸÄŸ¹¼Ä»Ü;ä
0192 C.
0193 C. ***** LOS *****

```


Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 1/10

*** OP Sequence for XRT ***

2025/10/04	10:35:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	10:35:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	10:35:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/04	10:36:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/04	10:36:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/04	10:36:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/04	10:36:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/04	10:36:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/04	10:36:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	10:38:56.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/10/04	10:38:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/04	10:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	11:39:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	11:39:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	11:39:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	11:39:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	11:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	11:48:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	11:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	15:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	15:25:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	15:25:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	15:25:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	15:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	15:34:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	15:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	16:59:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	16:59:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	16:59:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	16:59:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	17:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	17:22:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	17:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/04	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/10/04	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/04	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/04	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/04	18:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/04	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/04	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/04	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 2/10

2025/10/04	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/04	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/04	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/04	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	18:12:56.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/10/04	18:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/04	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	18:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	18:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	18:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	18:35:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	18:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	18:59:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	19:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	20:12:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	20:12:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	20:12:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	20:12:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	20:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	20:35:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	20:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	21:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	21:49:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	21:49:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	21:49:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	21:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	22:12:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	22:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/04	23:26:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	23:26:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/04	23:26:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/04	23:26:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/04	23:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/04	23:47:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/04	23:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	01:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	01:03:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	01:03:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/05	01:03:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/05	01:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/05	01:12:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/05	01:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	02:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	02:27:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	02:27:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/05	02:27:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/05	02:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/05	02:45:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/05	02:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 3/10

2025/10/05	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/05	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/10/05	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/05	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/05	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/05	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/05	04:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/05	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2025/10/05	04:22:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/05	04:23:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2025/10/05	05:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/10/05	05:31:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/05	05:31:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	05:31:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/05	05:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/05	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/05	06:08:26.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_MANU_402_OG [0x192]				
2025/10/05	06:08:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	06:08:48.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/05	06:08:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/10/05	06:08:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/10/05	06:11:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/05	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2025/10/05	06:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/05	06:18:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	06:18:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	06:18:30.0	AOCs_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/10/05	06:18:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 be 01 f3	
2025/10/05	06:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/05	06:18:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/05	06:18:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/05	06:18:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/05	06:21:26.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/05	06:21:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2025/10/05	06:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/05	07:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/05	07:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	07:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05	07:11:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/05	07:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/05	07:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/05	07:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2025/10/05	08:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/10/05	08:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/05	08:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/05			MDP_XRT_FLD_RESET	1	07-F0	da	

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 4/10

2025/10/05	08:51:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/05	08:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/05	09:13:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/05	09:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	10:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	10:30:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	10:30:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/05	10:30:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/05	10:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/05	10:48:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/05	10:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	15:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	15:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/05	15:55:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/05	15:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/05	16:07:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/05	16:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	17:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	17:30:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	17:30:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/05	17:30:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/05	17:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/05	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	17:54:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	17:54:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/05	17:54:30.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/10/05	17:54:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/05	17:54:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/05	17:54:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/05	17:57:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/05	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	18:04:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	18:04:28.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2025/10/05	18:04:30.0	AOCs_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/05	18:08:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/05	18:08:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/05	18:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/05	18:08:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/05	18:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/05	18:08:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/05	18:11:26.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/10/05	18:11:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/05	18:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/05	19:07:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	19:07:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/05	19:07:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 5/10

2025/10/05	19:07:06.0	XRT_PREFLR_STRT_404_OG [0x194]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/05	19:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/05	19:30:30.0	XRT_Custom_430_OG [0x1ae]					
2025/10/05	19:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/05	19:44:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	19:44:56.0	XRT_FOCUS_POSITION_406_OG [0x196]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2025/10/05	19:45:00.0	AOCs_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 39 58 c1 a2		
2025/10/05	19:45:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/10/05	19:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/10/05	19:45:20.0	XRT_AEC_RESET_448_OG [0x1c0]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2025/10/05	19:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/10/05	19:45:24.0	XRT_FLD_RESET_446_OG [0x1be]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/05	19:47:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2025/10/05	19:47:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2025/10/05	19:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/05	20:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	20:43:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	20:43:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/05	20:43:36.0	XRT_PREFLR_STRT_404_OG [0x194]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/05	20:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/05	21:07:00.0	XRT_Custom_430_OG [0x1ae]					
2025/10/05	21:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/05	22:21:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	22:21:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	22:21:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/05	22:21:06.0	XRT_PREFLR_STRT_404_OG [0x194]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/05	22:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/05	22:43:30.0	XRT_Custom_430_OG [0x1ae]					
2025/10/05	22:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/05	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	23:58:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/05	23:58:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/05	23:58:06.0	XRT_PREFLR_STRT_404_OG [0x194]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/06	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/06	00:18:00.0	XRT_Custom_430_OG [0x1ae]					
2025/10/06	00:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/06	01:35:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/06	01:35:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/06	01:35:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/06	01:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/06	01:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/06	01:45:00.0	XRT_Custom_430_OG [0x1ae]					
2025/10/06	01:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/06	02:57:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/06	02:57:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/06	02:57:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/06	02:57:36.0	XRT_PREFLR_STRT_404_OG [0x194]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/06	03:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/06	03:17:00.0	XRT_Custom_430_OG [0x1ae]					
2025/10/06	03:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					

Saturday October 04, 2025

5/10

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 6/10

2025/10/06	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	04:00:00.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/06	04:00:18.0	AOCs_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2025/10/06	04:00:20.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/06	04:00:22.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/06	04:00:24.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/06	04:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/06	04:02:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	04:02:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2025/10/06	04:03:00.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/06	04:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	04:31:02.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	04:31:04.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	04:31:06.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	04:31:14.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/06	04:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/06	04:54:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/06	04:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	05:36:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	05:36:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	05:37:18.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/06	05:37:20.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/06	05:37:22.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/06	05:39:58.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/06	05:40:00.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/06	05:46:54.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	05:46:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	05:47:00.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	05:47:16.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/06	05:47:18.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 39 58 c1 a2
2025/10/06	05:47:20.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/06	05:47:22.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/06	05:47:24.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/06	05:47:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/06	05:47:28.0	XRT_FLD_RESET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	05:49:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2025/10/06	05:49:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/06	05:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	06:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	06:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	06:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	06:03:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/06	06:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/06	06:31:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/06	06:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	07:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	07:43:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 7/10

2025/10/06	07:43:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/06	07:43:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/06	07:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/06	08:08:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/06	08:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2025/10/06	09:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/06	09:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	09:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/06	09:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/06	09:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/06	09:44:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/06	09:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2025/10/06	11:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/06	11:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	11:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/06	11:03:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/06	11:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/06	11:17:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/06	11:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2025/10/06	16:26:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/06	16:26:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	16:26:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da
2025/10/06	16:26:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	e8
2025/10/06	16:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9
2025/10/06	16:49:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/06	16:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2025/10/06	17:37:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	17:37:26.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	17:37:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22 ff aa 00
2025/10/06	17:37:30.0	AOCs_ORe-point_Start_2_OG [0x098]	5	02-76	00 00 00 00 00
2025/10/06	17:37:48.0	XRT_FLD_DIS_409_OG [0x199]	1	07-F0	d9
2025/10/06	17:37:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	1	07-F0	c9
2025/10/06	17:37:52.0	XRT_ARS_DIS_435_OG [0x1b3]	1	07-F0	d5
2025/10/06	17:40:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	2	07-F0	c4 06
2025/10/06	17:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2025/10/06	17:47:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	17:47:26.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1
2025/10/06	17:47:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	4	07-F8	22 fe 97 00
2025/10/06	17:47:30.0	AOCs_ORe-point_Start_1_OG [0x097]	5	02-76	03 03 be 01 f3
2025/10/06	17:47:48.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8
2025/10/06	17:47:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8
2025/10/06	17:47:52.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0
2025/10/06	17:47:54.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5
2025/10/06	17:47:56.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0	da
2025/10/06	17:50:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0	c4 04
2025/10/06	17:50:28.0	XRT_FL_PROG_SET_403_OG [0x193]	2	07-F0	c5 10
2025/10/06	17:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0
2025/10/06	18:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1
2025/10/06	18:02:02.0	XRT_CTRL_MANU_402_OG [0x192]			

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 8/10

2025/10/06	18:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	18:02:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	18:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/06	18:25:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/06	18:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	19:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	19:38:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	19:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	19:38:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/06	19:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/06	20:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	20:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	21:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	21:15:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	21:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	21:15:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/06	21:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/06	21:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	21:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	22:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	22:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/06	22:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/06	22:52:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/06	22:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/06	23:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/06	23:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	00:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	00:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	00:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/07	00:30:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/07	00:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/07	00:42:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/07	00:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	01:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	01:58:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	01:58:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/07	01:58:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/07	02:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/07	02:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/07	02:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	03:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	03:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	03:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/07	03:28:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/07	03:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/07	03:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/07	03:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/07	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]				

Oct 04, 25 12:38

XRT_OGLIST_0881.chk

Page 9/10

2025/10/07	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2025/10/07	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2025/10/07	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8			
2025/10/07	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET		1	07-F0	d0			
2025/10/07	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS		1	07-F0	d5			
2025/10/07	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET		1	07-F0	da			
2025/10/07	04:02:56.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11		
2025/10/07	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0e		
2025/10/07	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO		1	07-F0	c0			
2025/10/07	04:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	04:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	04:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET		1	07-F0	da			
2025/10/07	04:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT		1	07-F0	e8			
2025/10/07	04:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP		1	07-F0	e9			
2025/10/07	05:26:00.0	XRT_Custom_430_OG [0x1ae]							
2025/10/07	05:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO		1	07-F0	c0			
2025/10/07	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	05:53:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	05:53:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00
2025/10/07	05:53:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS		1	07-F0	d9			
2025/10/07	05:53:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9			
2025/10/07	05:53:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS		1	07-F0	d5			
2025/10/07	05:56:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	06		
2025/10/07	05:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO		1	07-F0	c0			
2025/10/07	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	06:03:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	06:03:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00
2025/10/07	06:03:30.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM		5	02-76	03	03	be	01
2025/10/07	06:03:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA		1	07-F0	d8			
2025/10/07	06:03:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8			
2025/10/07	06:03:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET		1	07-F0	d0			
2025/10/07	06:03:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS		1	07-F0	d5			
2025/10/07	06:03:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET		1	07-F0	da			
2025/10/07	06:06:26.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c		
2025/10/07	06:06:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0e		
2025/10/07	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO		1	07-F0	c0			
2025/10/07	06:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	06:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	06:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET		1	07-F0	da			
2025/10/07	06:36:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT		1	07-F0	e8			
2025/10/07	06:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP		1	07-F0	e9			
2025/10/07	07:03:00.0	XRT_Custom_430_OG [0x1ae]							
2025/10/07	07:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO		1	07-F0	c0			
2025/10/07	08:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	08:16:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU		1	07-F0	c1			
2025/10/07	08:16:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET		1	07-F0	da			
2025/10/07	08:16:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT		1	07-F0	e8			

[illegible]