

XRT Timeline to be uploaded on 2025/10/14

Period: 2025/10/14 11:07:00 - 2025/10/18 11:51:00

* * * * *

Normal mode

* * * * *

XOB #1D1F: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with														
Term		Pointing (x, y)				Comment								
10/14 11:20:00 - 10/14 18:03:24		Track (483.3, 246.4)		@ 10/14 11:17:00		# OP start + 10min								
10/14 18:16:30 - 10/14 19:59:54		Track (530.4, 250.2)		@ 10/14 18:13:30		# AR 14246								
10/15 00:14:00 - 10/15 01:59:54		Track (567.8, 253.6)		@ 10/15 00:00:00		AR 14246								
10/15 05:45:00 - 10/15 11:49:54		Track (603.0, 257.1)		@ 10/15 05:42:00		# AR 14246								
10/15 15:33:00 - 10/15 17:56:30		Track (659.1, 263.6)		@ 10/15 15:30:00		AR 14246								
10/15 18:33:30 - 10/15 19:59:54		Track (675.2, 265.6)		@ 10/15 18:30:30		# AR 14246								
10/16 00:03:00 - 10/16 01:54:00		Track (703.1, 269.5)		@ 10/16 00:00:00		AR 14246								
10/16 06:15:30 - 10/16 11:14:00		Track (732.2, 274.0)		@ 10/16 06:12:30		# AR 14246								
PROG= 08 Inf.-time(s)														
Subr= 1		1-time(s)		2.0sec										
└ Seqn= 92		1-time(s)		2.0sec										
└┐		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└┐		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└┐		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		4-time(s)		2.0sec										
└ Seqn= 47		1-time(s)		2.0sec										
└┐		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└┐		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└┐		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
└┐		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Seqn= 77		4-time(s)		600.0sec										
└┐		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
└┐		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
└┐		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
└┐		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
└┐		Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
└┐		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)													
Term		Pointing (x, y)			Comment								
10/14 18:06:30 - 10/14 18:13:24		Fixed (0.0, 0.0)			synoptic, shifted 3.5 min								
10/15 05:34:32 - 10/15 05:41:54		Fixed (0.0, 0.0)			synoptic, shifted -4 hours, combined with HOP 346, 448								
10/15 18:23:30 - 10/15 18:30:24		Fixed (0.0, 0.0)			synoptic, shifted 20.5 min								
10/16 06:05:12 - 10/16 06:12:24		Fixed (0.0, 0.0)			synoptic, shifted -4 hours, combined with HOP349, 448								
PROG= 06 1-time(s)													
Subr= 1		1-time(s)			2.0sec								
Seqn= 5		1-time(s)			2.0sec								
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26		1-time(s)			2.0sec								
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99		1-time(s)			2.0sec								
Al-poly/Open		Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83		1-time(s)			2.0sec								
thin-Be/Open		thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23		1-time(s)			4.0sec								
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)			2.0sec								
Seqn= 41		1-time(s)			2.0sec								
Open/thick-Be		Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17		1-time(s)			2.0sec								
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33		1-time(s)			2.0sec								
med-Be/Open		Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open		med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75		1-time(s)			2.0sec								
Al-poly/Ti-poly		Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✱ ✱ ✱ ✱ ✱

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-AI,thick-Be -384x384 + AI-p														
Term		Pointing (x, y)						Comment						
10/14 11:20:00 - 10/14 18:03:24		Track (483.3, 246.4) @ 10/14 11:17:00						# OP start + 10min						
10/14 18:16:30 - 10/14 19:59:54		Track (530.4, 250.2) @ 10/14 18:13:30						# AR 14246						
10/14 20:03:00 - 10/14 23:53:00		Fixed (-22.0, 858.0)						HOP 512						
10/15 00:14:00 - 10/15 01:59:54		Track (567.8, 253.6) @ 10/15 00:00:00						AR 14246						
10/15 02:03:00 - 10/15 05:34:00		Fixed (0.0, 0.0)						synoptic, shifted -4 hours, combined with HOP 346, 448						
10/15 05:45:00 - 10/15 11:49:54		Track (603.0, 257.1) @ 10/15 05:42:00						# AR 14246						
10/15 11:53:00 - 10/15 15:29:54		Fixed (-22.0, 858.0)						HOP 512						
10/15 15:33:00 - 10/15 17:56:30		Track (659.1, 263.6) @ 10/15 15:30:00						AR 14246						
10/15 18:33:30 - 10/15 19:59:54		Track (675.2, 265.6) @ 10/15 18:30:30						# AR 14246						
10/15 20:03:00 - 10/15 23:59:54		Fixed (-22.0, 858.0)						HOP512						
10/16 00:03:00 - 10/16 01:54:00		Track (703.1, 269.5) @ 10/16 00:00:00						AR 14246						
10/16 02:08:30 - 10/16 06:04:40		Fixed (0.0, 0.0)						synoptic, shifted -4 hours, combined with HOP349, 448						
10/16 06:15:30 - 10/16 11:14:00		Track (732.2, 274.0) @ 10/16 06:12:30						# AR 14246						
PROG= 14 30-time(s)														
Subr= 1 1-time(s) 2.0sec														
└ Seqn= 68 1-time(s) 2.0sec														
└ Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1024, 1024) Q=95 0 0 2.0sec														
Subr= 2 20-time(s) 2.0sec														
└ Seqn= 11 1-time(s) 2.0sec														

	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73	1-time(s)	10.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3	1-time(s)	2.0sec											
Seqn= 10	1-time(s)	2.0sec											
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

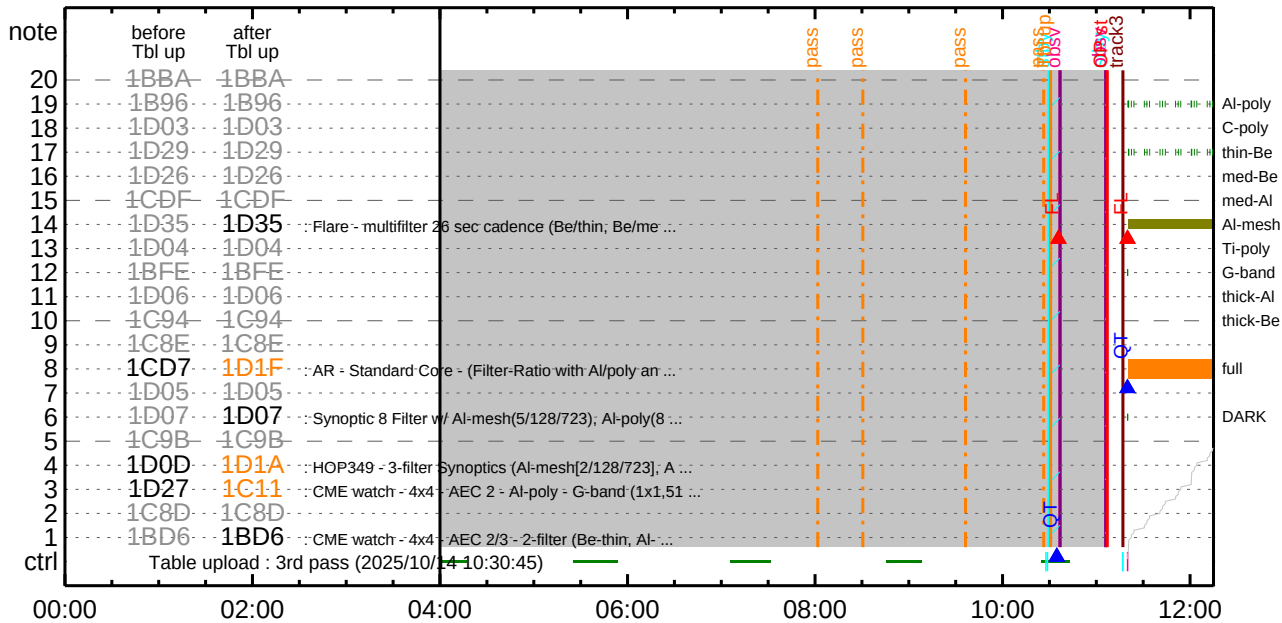
* * * * *

Flare Detection

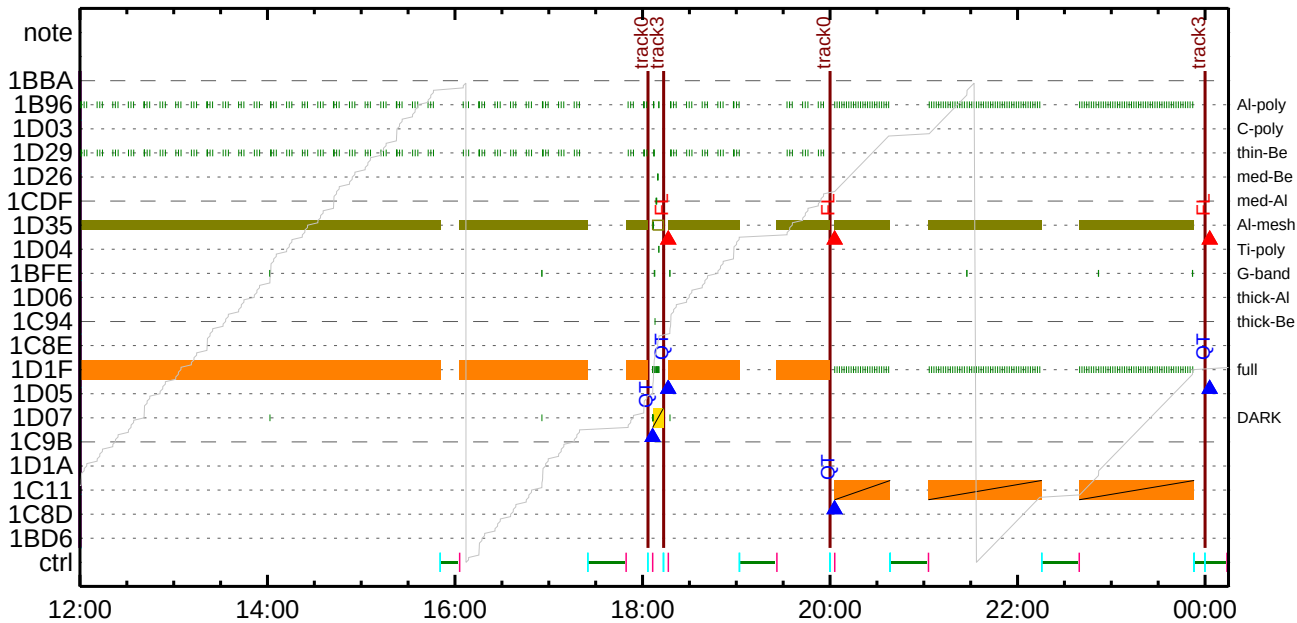
* * * * *

FLD Patrol													
Term			Pointing (x, y)						Comment				
10/14 10:31:45 - 10/14 18:03:48			cannot be identified										
10/14 18:13:48 - 10/15 05:34:24			Track (530.4, 250.2) @ 10/14 18:13:30						# AR 14246				
10/15 05:42:18 - 10/15 18:20:48			Track (603.0, 257.1) @ 10/15 05:42:00						# AR 14246				
10/15 18:30:48 - 10/16 06:05:04			Track (675.2, 265.6) @ 10/15 18:30:30						# AR 14246				
10/16 06:12:48 - 10/18 11:51:00			Track (732.2, 274.0) @ 10/16 06:12:30						# AR 14246				
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

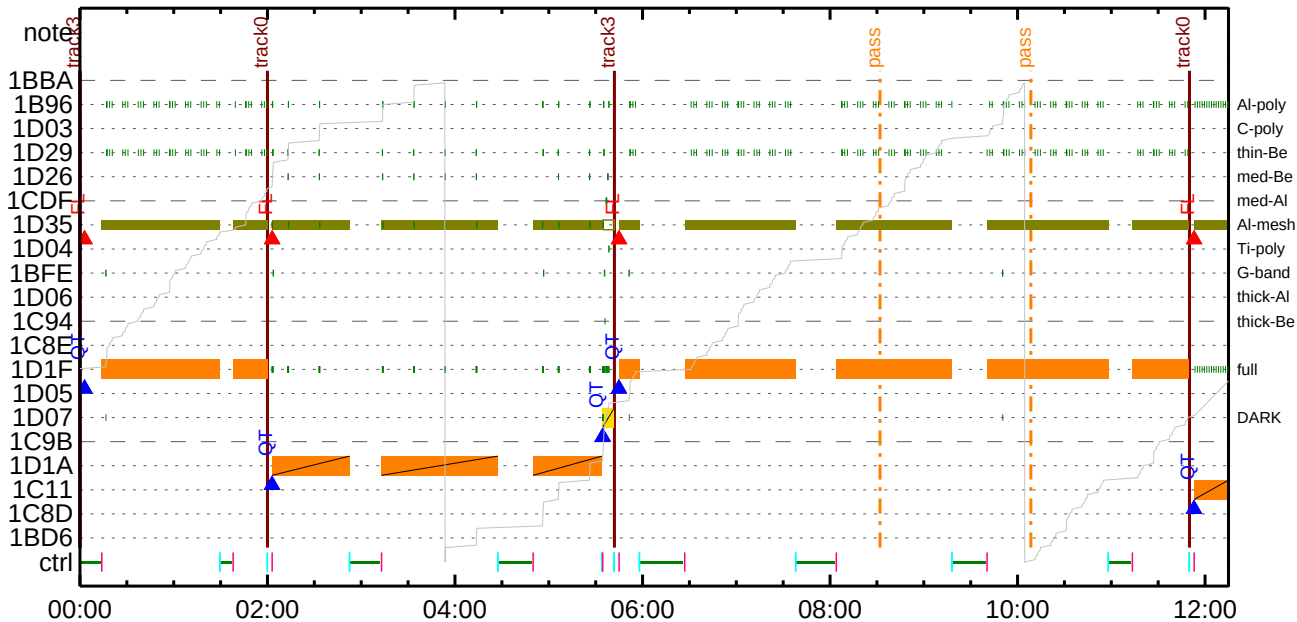
CMDI #0900 2025/10/14



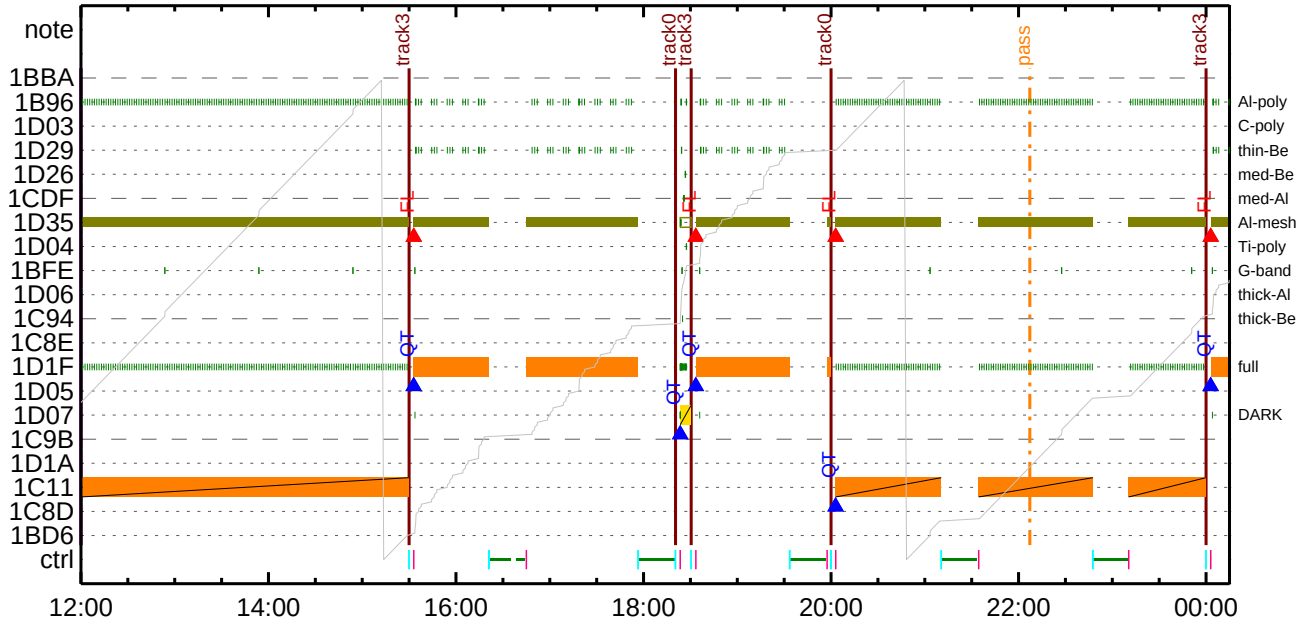
CMDI #0900 2025/10/14



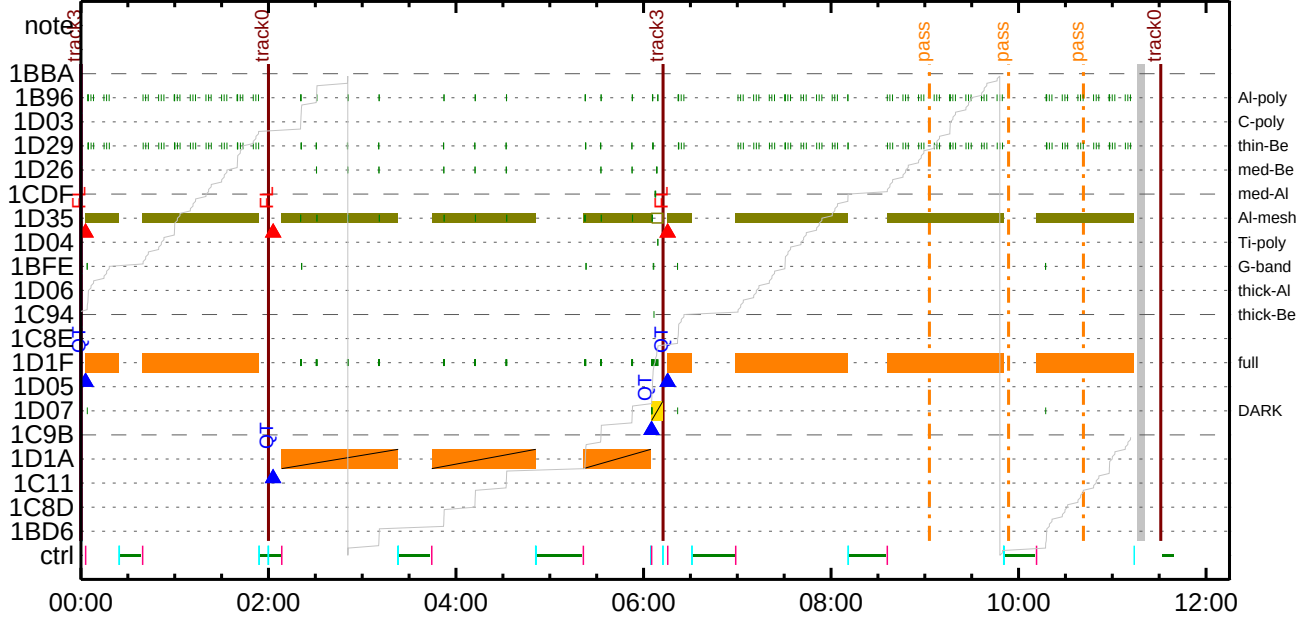
CMDI #0900 2025/10/15



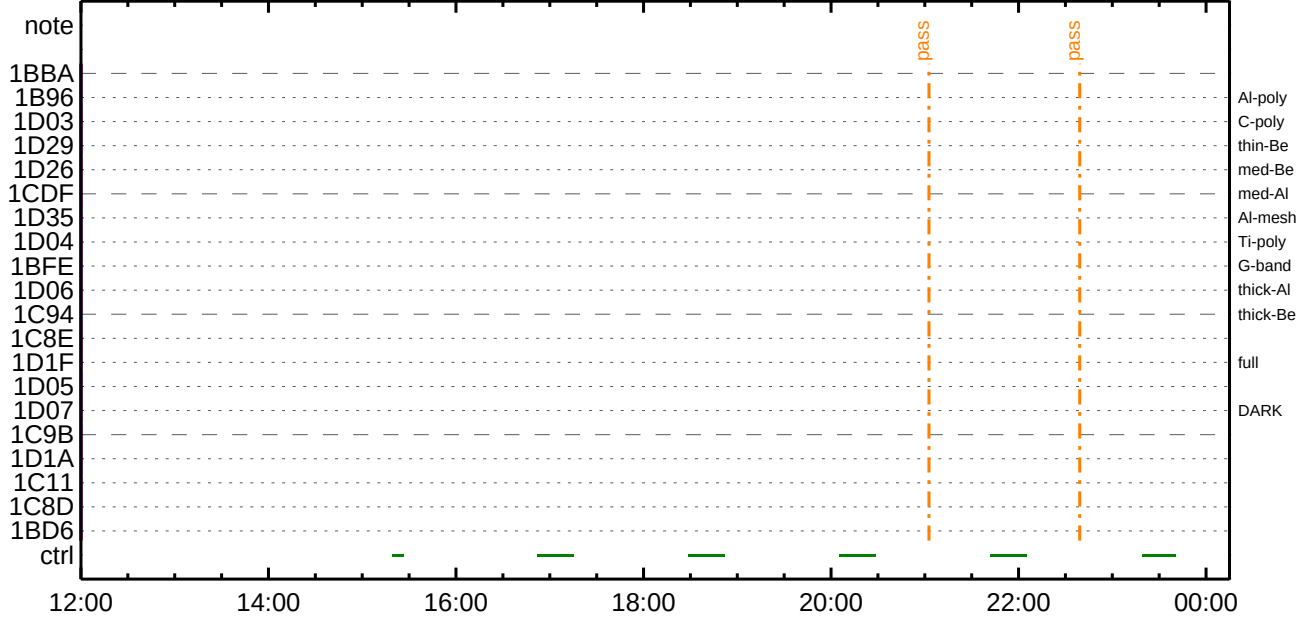
CMDI #0900 2025/10/15



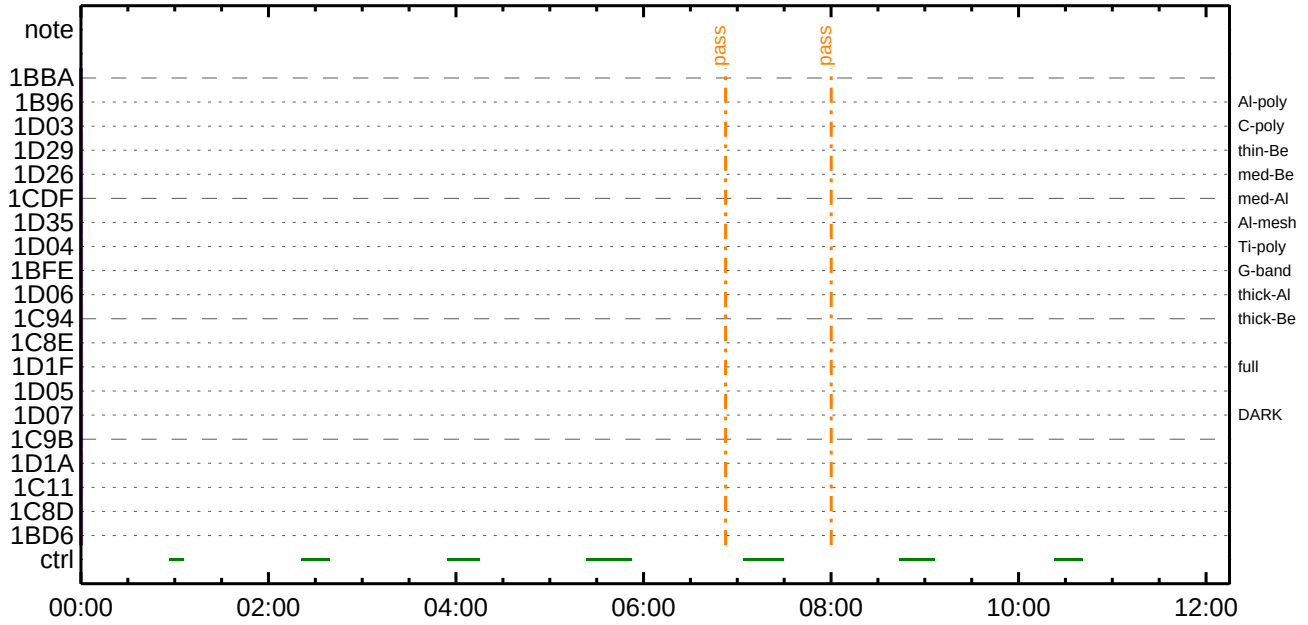
CMDI #0900 2025/10/16



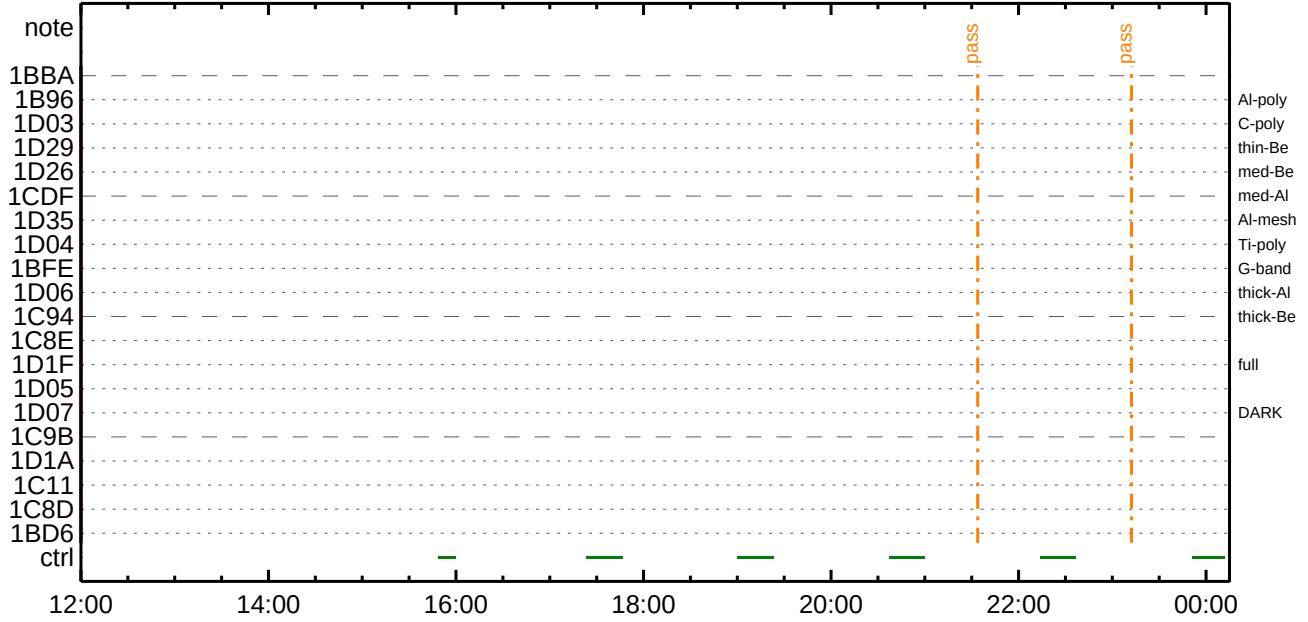
CMDI #0900 2025/10/16



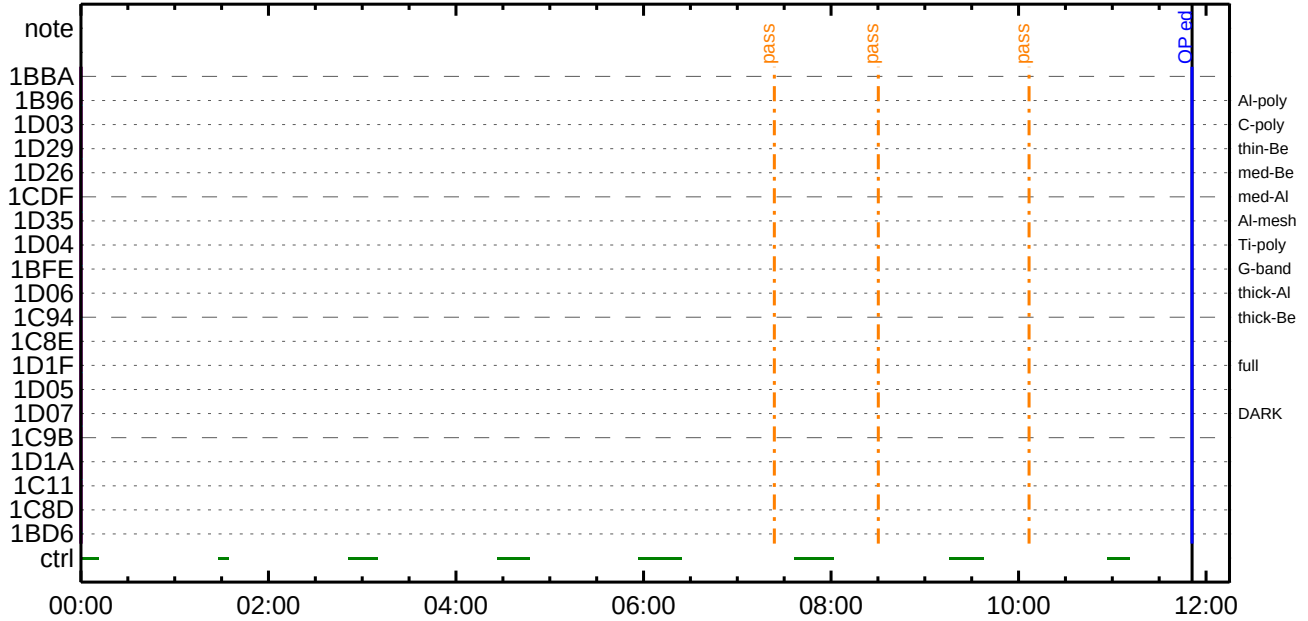
CMDI #0900 2025/10/17



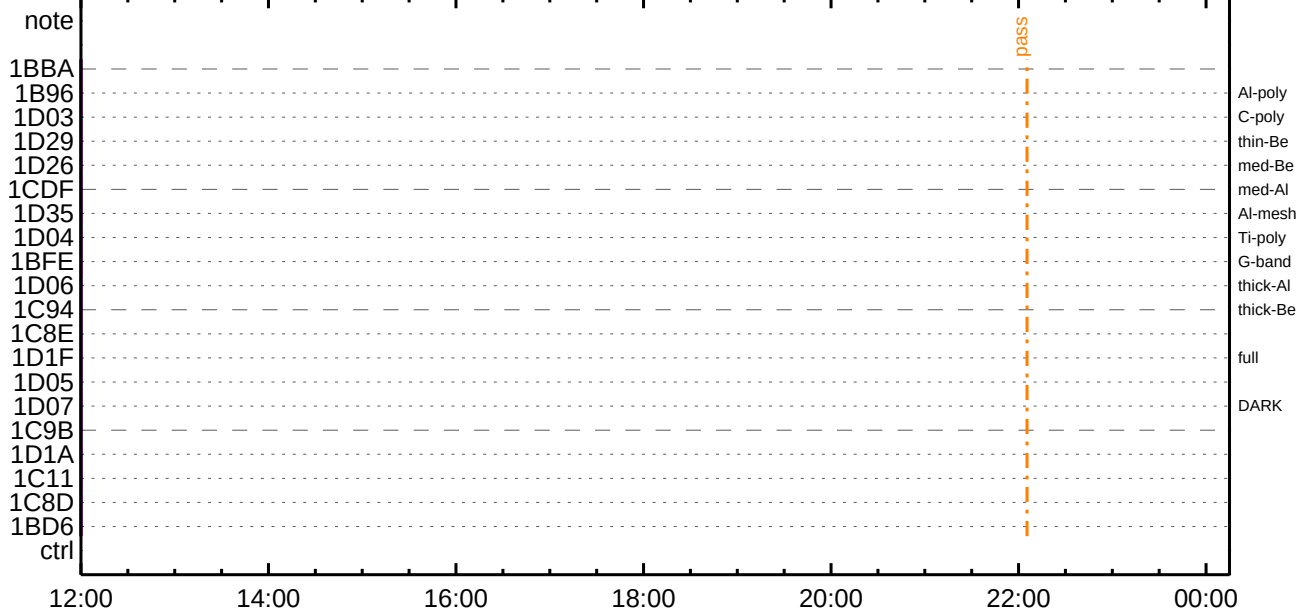
CMDI #0900 2025/10/17



CMDI #0900 2025/10/18



CMDI #0900 2025/10/18



```
main-574 2025-10-14 11:34:16 194 33 SOLAR-B MAIN //
```

```
main-574 2025-10-14 11:34:16 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸPŸóŸÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçñÄñ•µ°E»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèñµ•ííÉ;ËñÉ¼°ÇÖñ•ñç¼í¹ÇñÍ;çÄ®, ùñ¹ñèñPñÇÁ÷ç®•ñËñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÄŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-574:OP
0020 ()
0021 . S. OG og-574:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPîî°èŸÄŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñò³îÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¼Ë¹ç•ë²îOKñò³îÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÄŸóŸ×½ªî»ñò³îÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñî¼Ë¹ç•ë²îOKñò³îÇ$
0081 C.
0082 . C. ***** °Ë²¼ñî¼Ä´¶Ä°ñËË¬ñ°Á÷ç® (¼äµ-ŸÄŸóŸ×¼è½çñòÄÖÄæñÇ½ñ"ñè¼í¹çñÇñä) *****
0083 C. DHUŸä;¼ŸÉ;Ë¼Ÿ½,Ÿí;¼ŸÉ;Ëñòíäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñî¼í¹ç;ç°Ë²¼ñîTI-CMDÁ÷ç®ñî¼Ä¹Ôñ•ñËñññ³ñÈ;£
```


0096	C.	Pm;ç;çSETmEDUMPmÏE±°iYÑY¹mÇ¹Ôm m³mE;f		
0097	C.			
0098	C.	TIY³YD¥óYÉmòÄDİç (UT)		
0099	+. TI	2025-10-14 11:02:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+. TI	2025-10-14 11:02:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+. TI	2025-10-14 11:02:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+. TI	2025-10-14 11:06:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	çç [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼mİÄê¼iİñmİVÁ¥\$YÁY~¹àİÜ		
0116	C.	çç [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	çç [HK1_TI_CMD_NUM]	EQ	4
0118	C.	çç [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	çç [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°èYÁóY×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL (0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	çç [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	çç [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	çç [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	çç [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	çç [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+. DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	çç [HK1_PKT_FORM_NO]	EQ	7
0136	C.	çç [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	çç [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	çç [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	çç [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	YÁóY×½ªİ»mò³İÇ\$		
0142	C.	çç [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBLmİ¼È¹Ç•ë²İOKmò³İÇ\$		
0145	C.			
0146	C.	DHU¥â;¼YÉ;Ê¼ý½,¥i;¼YÉ;Ëmòİáª¹		
0147	+. DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	çç [HK1_PKT_FORM_NO]	EQ	2
0150	C.	çç [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	çç [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	çç [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+. TI	2025-10-14 11:06:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+. TI	2025-10-14 11:06:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+. TI	2025-10-14 11:06:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP 'üÄİmİ»ö¼YmÈÄDm¹mèDCBC•×²è *****		
0181	C.	(¼ä°İYÓYÁYÉYD¥YÉYÁYÇYÈmÈ¼¼m¼Ä»Üm¹mè)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** YD¥¹•İ Daily±çİñmÈ'Øm¹mèDCBC•×²è *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	C.	;äLOS¥ÁY\$YÁY~¼Ä»Ü;ä		
0192	C.			
0193	C.	***** LOS *****		


```
main-575 2025-10-14 11:34:16 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÐñÇñÎ»p´Öòò¹íî,ñ•;çÉÖÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÔÄÖñ¬°ÄÄèñ•ñç,ç°Ê²¼ñî°EÄ,¼è½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñç¼ñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öñññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲEŲÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñ¼¹¹Çñİ´°î»ñ¹ñèñÐñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñç¼ñ¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öñññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 5387104.0 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 2s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-821:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-10-14 11:06:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0165 C. (¾å°î¥Ö¥Å¥Ê¥Ð¥Ê¥Å¥Ç¥ÊñÊ¼¾ñ¼Å»Û¹ñë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±¿ÍÑñÊ'Øñ¹ñëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥Á¥S¥Ã¥¹¼Å»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-576 2025-10-14 11:34:16 98 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¼¿¼Ä¼•µ°Æ»Í×Ä¼¼Ÿ¿ŸÄŸ×Ÿí;¼ŸÉ;ËËè¼µ•ííË;Ë¼Ë¼°ÇÖ¼¼•¼¿¼¹¿¼Í;¿Ä®,ù¼¹¼è¼D¼ÇÄ÷¿®¼•¼Ë¼¼¼³¼Ë;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 08 08)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 85 83 06 06)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 +. DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 +. DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 +. DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 +. DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 01)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 0e)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2025-10-14 11:06:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 C.
0084 . C. ***** MDP 'ûÄî¼î»ö¼Ÿ¼ËÄ¼¼¼èDCBC•×²è *****
0085 C. (%ää°íŸÖŸÄŸËŸDŸËŸÄŸ¿ŸË¼Ë¼¼Ä»Ü¼¹¼Ë)
0086 . S. DC-BC dcbc-402:DCBC
0087 (MDP_known_event)
0088 C.
0089 C.
0090 . C. ***** ŸDŸ¹•İ Daily÷¿İŸ¼Ë'Ø¼¹¼èDCBC•×²è *****
0091 . S. DC-BC dcbc-153:DCBC
0092 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0093 C.
0094 C.
0095 . C. ;ãLOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
```

0096 C.
0097 . C. ***** LOS *****
0098 C.

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 1/8

*** OP Sequence for XRT ***

2025/10/14	11:16:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	11:16:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	11:16:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/14	11:17:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/14	11:17:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/14	11:17:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/14	11:17:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/14	11:17:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/14	11:17:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/14	11:19:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/10/14	11:19:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/14	11:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/14	15:50:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	15:50:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	15:50:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/14	15:50:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/14	15:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/14	16:02:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/14	16:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/14	17:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	17:25:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	17:25:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/14	17:25:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/14	17:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/14	17:48:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/14	17:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/14	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	18:03:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	18:03:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/14	18:03:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/10/14	18:03:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/14	18:03:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/14	18:03:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/14	18:06:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/14	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/14	18:13:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	18:13:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/14	18:13:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/14	18:13:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/14	18:13:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/14	18:13:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/14	18:13:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/14	18:13:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/14	18:13:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/14	18:16:26.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/10/14	18:16:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/14	18:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]			

Tuesday October 14, 2025

1/8

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 2/8

2025/10/14	19:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/14	19:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	19:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	19:02:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/14	19:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/14	19:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/14	19:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2025/10/14	19:59:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/14	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	19:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	20:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2025/10/14	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCS_Ore-point_Start_3_OG [0x099]	5	02-76	00 b3 bf 01 f3		
2025/10/14	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	1	07-F0	d8		
2025/10/14	20:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/10/14	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/10/14	20:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2025/10/14	20:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2025/10/14	20:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/14	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2025/10/14	20:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e		
2025/10/14	20:38:32.0	XRT_CTRL_MANU_402_OG [0x192]						
2025/10/14	20:38:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/14	20:38:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	20:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	21:02:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/14	21:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/14	22:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/14	22:15:32.0	XRT_CTRL_MANU_402_OG [0x192]						
2025/10/14	22:15:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/14	22:15:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	22:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	22:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/14	22:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/14	23:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/14	23:53:02.0	XRT_CTRL_MANU_402_OG [0x192]						
2025/10/14	23:53:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2025/10/14	23:53:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	23:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2025/10/14	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_RESET	1	07-F0	da		
2025/10/14	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2025/10/14	23:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2025/10/15	00:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2025/10/15	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCS_Ore-point_Start_1_OG [0x097]	5	02-76	03 03 be 01 f3		
2025/10/15	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	1	07-F0	d8		
2025/10/15	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2025/10/15	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2025/10/15	00:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2025/10/15	00:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 3/8

2025/10/15	00:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2025/10/15	00:13:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/10/15	00:14:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/15	01:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	01:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	01:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	01:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	01:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/15	01:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/15	01:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/15	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	02:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/15	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/10/15	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/15	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/15	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/15	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/15	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2025/10/15	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/15	02:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	02:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	02:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	02:52:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	02:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/15	03:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/15	03:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/15	04:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	04:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	04:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	04:27:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	04:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/15	04:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/15	04:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/15	05:34:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	05:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	05:34:04.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	05:34:24.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/15	05:34:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/10/15	05:34:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/10/15	05:34:30.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/15	05:34:32.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2025/10/15	05:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	05:41:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	05:41:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	05:42:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
		AOCU_NM		5	02-76	03 03 be 01 f3	

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 4/8

2025/10/15	05:42:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/15	05:42:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/15	05:42:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/15	05:42:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/15	05:42:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	05:44:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/10/15	05:44:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/15	05:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	05:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	05:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	05:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	05:58:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/15	06:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/15	06:26:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/15	06:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	07:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	07:38:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	07:38:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	07:38:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/15	07:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/15	08:03:00.0	XRT_Custom_430_OG [0x1ae]			
2025/10/15	08:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	09:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	09:18:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	09:18:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	09:18:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/15	09:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/15	09:39:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/15	09:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	10:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	10:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	10:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	10:58:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/15	11:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/15	11:12:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/15	11:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	11:49:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	11:49:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	11:49:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/15	11:50:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 b3 bf 01 f3
2025/10/15	11:50:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/15	11:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/15	11:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/15	11:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/15	11:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	11:52:56.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/10/15	11:52:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/15	11:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 5/8

2025/10/15	15:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	15:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	15:30:00.0	AOCs_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/15	15:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/15	15:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/15	15:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/15	15:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/15	15:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/15	15:32:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	15:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/10/15	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/15	16:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	16:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	16:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	16:21:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	16:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/15	16:44:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/15	16:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	e9
2025/10/15	17:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	17:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	17:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	17:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	17:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/15	18:20:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/15	18:20:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	18:20:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	18:20:30.0	AOCs_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/15	18:20:48.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00
2025/10/15	18:20:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/15	18:20:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/15	18:23:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/15	18:23:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/15	18:30:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	18:30:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	18:30:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	18:30:30.0	AOCs_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/15	18:30:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/15	18:30:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/15	18:30:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/15	18:30:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/15	18:30:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/15	18:33:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/15	18:33:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/10/15	18:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/15	19:33:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/15	19:33:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/15	19:33:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 6/8

2025/10/15	19:33:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	19:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/15	19:56:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/15	19:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	19:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	19:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/15	20:00:00.0	AOCs_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 b3 bf 01 f3	
2025/10/15	20:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/15	20:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/15	20:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/15	20:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/15	20:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	20:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2025/10/15	20:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/15	20:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	21:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	21:10:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	21:10:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	21:10:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/15	21:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/15	21:33:30.0	XRT_Custom_430_OG [0x1ae]					
2025/10/15	21:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	22:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	22:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	22:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/15	22:47:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/15	22:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/15	23:09:30.0	XRT_Custom_430_OG [0x1ae]					
2025/10/15	23:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/15	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	23:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/15	23:59:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/10/16	00:00:00.0	AOCs_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 03 be 01 f3	
2025/10/16	00:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/16	00:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/16	00:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/16	00:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/16	00:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/16	00:02:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2025/10/16	00:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/16	00:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/16	00:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/16	00:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/16	00:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/16	00:24:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/16	00:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/16	00:38:30.0	XRT_Custom_430_OG [0x1ae]					

Oct 14, 25 11:34

XRT_OGLIST_0900.chk

Page 7/8

2025/10/16	00:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/16	01:54:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	01:54:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	01:54:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/16	01:54:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/16	01:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/16	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/16	02:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/10/16	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/16	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/16	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/16	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/16	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/16	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2025/10/16	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/16	02:07:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/16	02:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/16	03:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	03:23:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	03:23:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/16	03:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/16	03:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/16	03:43:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/16	03:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/16	04:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	04:51:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	04:51:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/16	04:51:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/16	04:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/16	05:20:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/16	05:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/16	06:04:40.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	06:04:42.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	06:04:44.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/16	06:05:04.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/16	06:05:06.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/16	06:05:08.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/16	06:05:10.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/16	06:05:12.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/16	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	06:12:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/16	06:12:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/16	06:12:30.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 be 01 f3
2025/10/16	06:12:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/16	06:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/16	06:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0

