

XRT Timeline to be uploaded on 2025/10/23

Period: 2025/10/23 10:45:00 - 2025/10/28 11:14:00

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Normal mode

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XOB #1C11: CME watch - 4x4 - AEC 2 - Al-poly - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 90s cad													
Term		Pointing (x, y)				Comment							
10/23 10:58:00 - 10/23 14:29:54		Fixed (-22.0, 858.0)				# OP start + 10min + HOP512							
10/24 12:03:00 - 10/24 15:29:54		Fixed (-22.0, 858.0)				HOP512							
PROG= 03 Inf.-time(s)													
Subr= 1		40-time(s)		90.0sec									
Seqn= 62		1-time(s)		4.0sec									
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 30		1-time(s)		2.0sec									
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0 0 2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0 0 2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1D23: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 1800s cad (G-band/Leak last)													
Term		Pointing (x, y)				Comment							
10/23 14:33:00 - 10/23 17:59:24		Track (-37.2, -307.3) @ 10/23 14:30:00				AR 14262 obs							
10/23 18:12:30 - 10/24 03:59:54		Track (-3.7, -307.0) @ 10/23 18:09:30				AR obs							
10/24 06:43:00 - 10/24 11:59:54		Track (110.7, -305.5) @ 10/24 06:40:00				AR obs							
10/24 15:33:00 - 10/24 17:51:30		Track (190.4, -303.7) @ 10/24 15:30:00				AR obs							
10/24 18:28:00 - 10/25 03:59:54		Track (216.4, -302.9) @ 10/24 18:25:00				AR obs							
10/25 06:11:30 - 10/25 11:26:00		Track (318.9, -299.4) @ 10/25 06:08:30				AR obs							
PROG= 19 Inf.-time(s)													
Subr= 1		8-time(s)		1800.0sec									
Seqn= 8		1-time(s)		2.0sec									
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
└ thin-Be/Open		med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 30		1-time(s)		2.0sec									
└ Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5792)													
Term		Pointing (x, y)				Comment							
10/23 18:02:30 - 10/23 18:09:24		Fixed (0.0, 0.0)				synoptic, shifted -0.5 min							
10/24 06:33:00 - 10/24 06:39:54		Fixed (0.0, 0.0)				HOP349 + synoptic, shifted 30.0 min							
10/24 18:18:00 - 10/24 18:24:54		Fixed (0.0, 0.0)				synoptic, shifted 15.0 min							
10/25 06:01:30 - 10/25 06:08:24		Fixed (0.0, 0.0)				HOP349 + synoptic, shifted -1.5 min							
PROG= 06 1-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 5		1-time(s)		2.0sec									
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└ Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 26		1-time(s)		2.0sec									
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 83		1-time(s)		2.0sec									
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23		1-time(s)		4.0sec									
└ Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└ Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)		2.0sec									
Seqn= 41		1-time(s)		2.0sec									
└ Open/thick-Be		Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17		1-time(s)		2.0sec									
└ med-Al/Open		med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ med-Al/Open		med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 33 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D1A: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 120min cad) + CME wa

Term	Pointing (x, y)	Comment
10/24 04:03:00 - 10/24 06:29:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 30.0 min
10/25 04:03:00 - 10/25 05:58:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -1.5 min

PROG= 04 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL

Term	Pointing (x, y)	Comment
10/23 10:58:00 - 10/23 14:29:54	Fixed (-22.0, 858.0)	# OP start + 10min + HOP512
10/23 14:33:00 - 10/23 17:59:24	Track (-37.2, -307.3) @ 10/23 14:30:00	AR 14262 obs
10/23 18:12:30 - 10/24 03:59:54	Track (-3.7, -307.0) @ 10/23 18:09:30	AR obs
10/24 04:03:00 - 10/24 06:29:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 30.0 min
10/24 06:43:00 - 10/24 11:59:54	Track (110.7, -305.5) @ 10/24 06:40:00	AR obs
10/24 12:03:00 - 10/24 15:29:54	Fixed (-22.0, 858.0)	HOP512
10/24 15:33:00 - 10/24 17:51:30	Track (190.4, -303.7) @ 10/24 15:30:00	AR obs
10/24 18:28:00 - 10/25 03:59:54	Track (216.4, -302.9) @ 10/24 18:25:00	AR obs
10/25 04:03:00 - 10/25 05:58:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -1.5 min
10/25 06:11:30 - 10/25 11:26:00	Track (318.9, -299.4) @ 10/25 06:08:30	AR obs

PROG= 14 30-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 68 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

┌	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

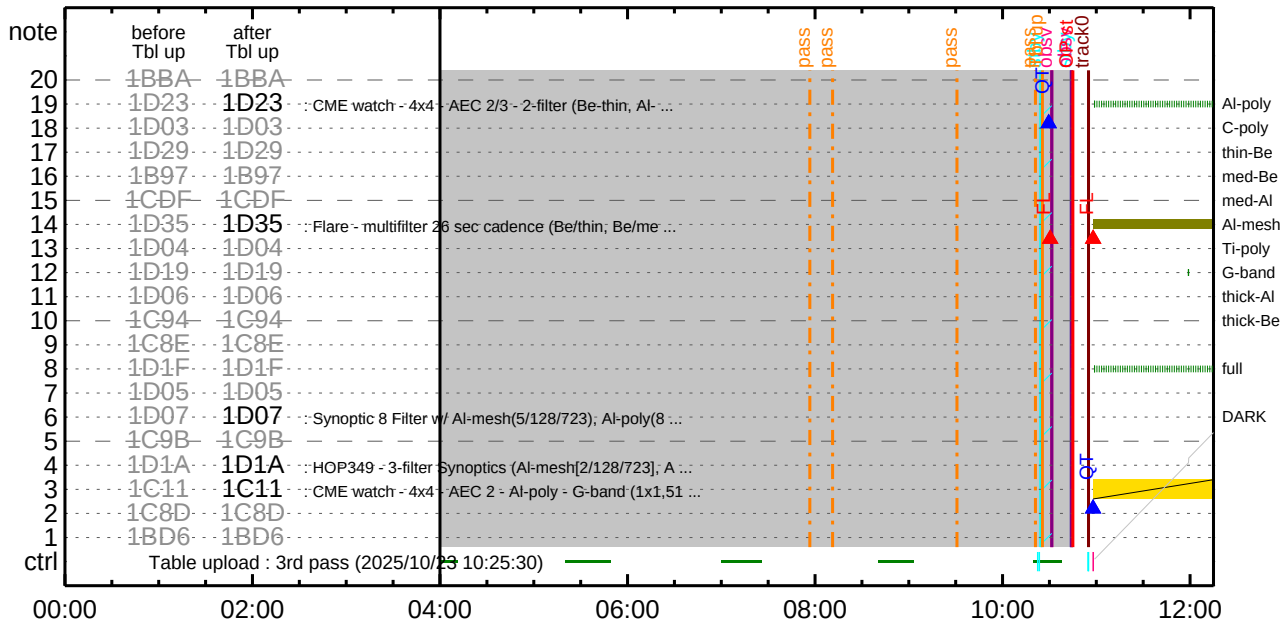
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Flare Detection

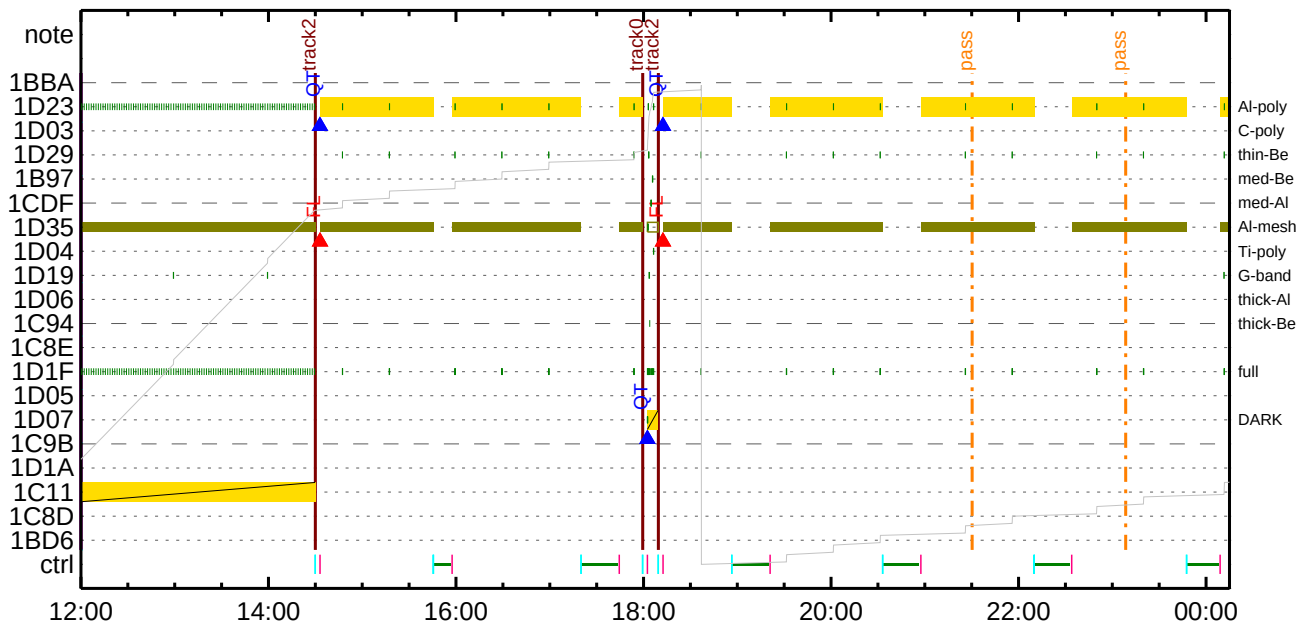
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FLD Patrol													
Term						Pointing (x, y)				Comment			
10/23 10:26:30 - 10/23 17:59:48						cannot be identified							
10/23 18:09:48 - 10/24 06:30:18						Track (-3.7, -307.0) @ 10/23 18:09:30				AR obs			
10/24 06:40:18 - 10/24 18:15:18						Track (110.7, -305.5) @ 10/24 06:40:00				AR obs			
10/24 18:25:18 - 10/25 05:58:48						Track (216.4, -302.9) @ 10/24 18:25:00				AR obs			
10/25 06:08:48 - 10/28 11:14:00						Track (318.9, -299.4) @ 10/25 06:08:30				AR obs			
AI-poly/Open		AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

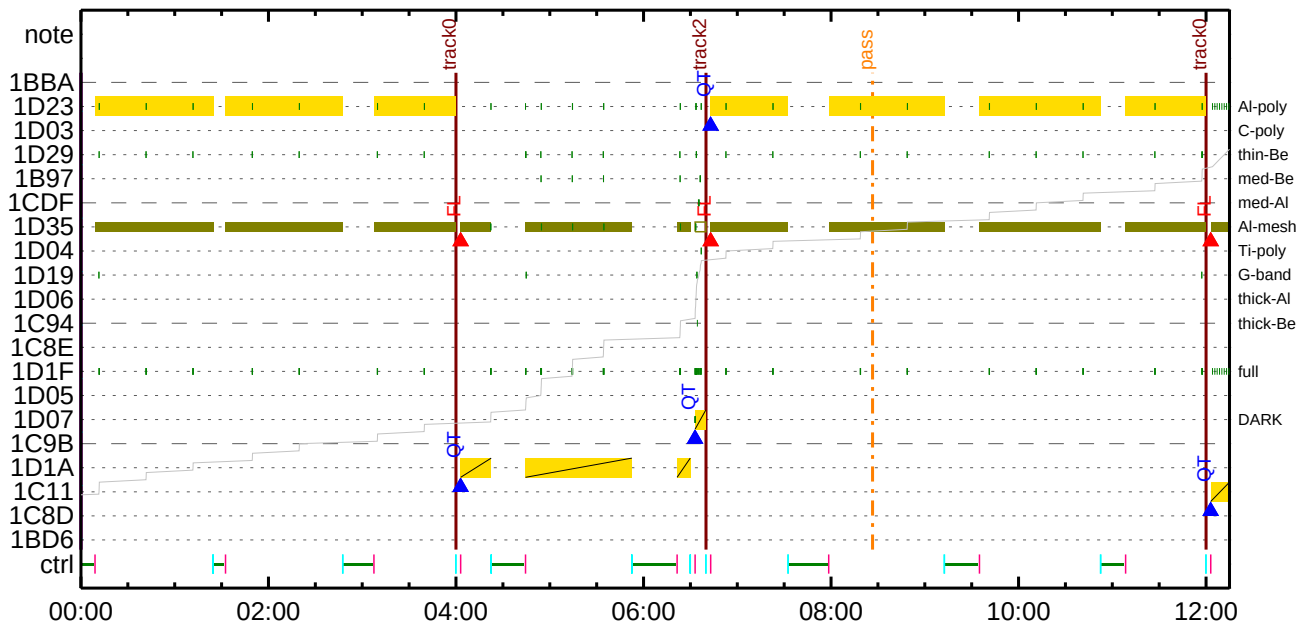
CMDI #0918 2025/10/23



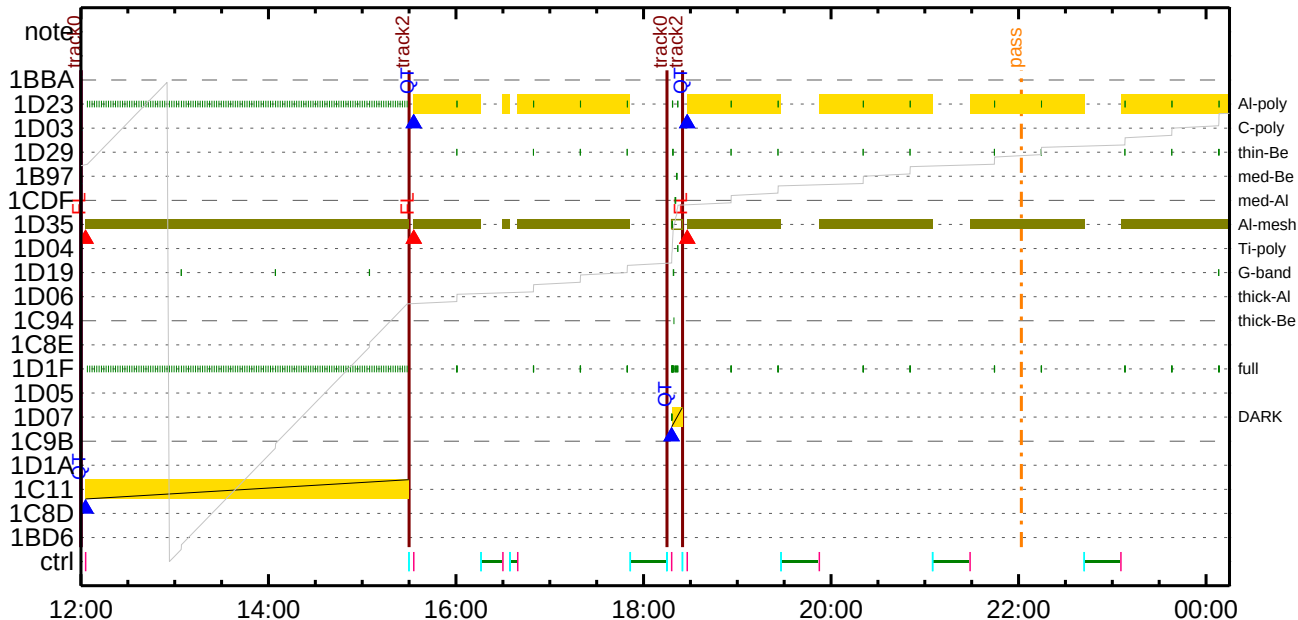
CMDI #0918 2025/10/23



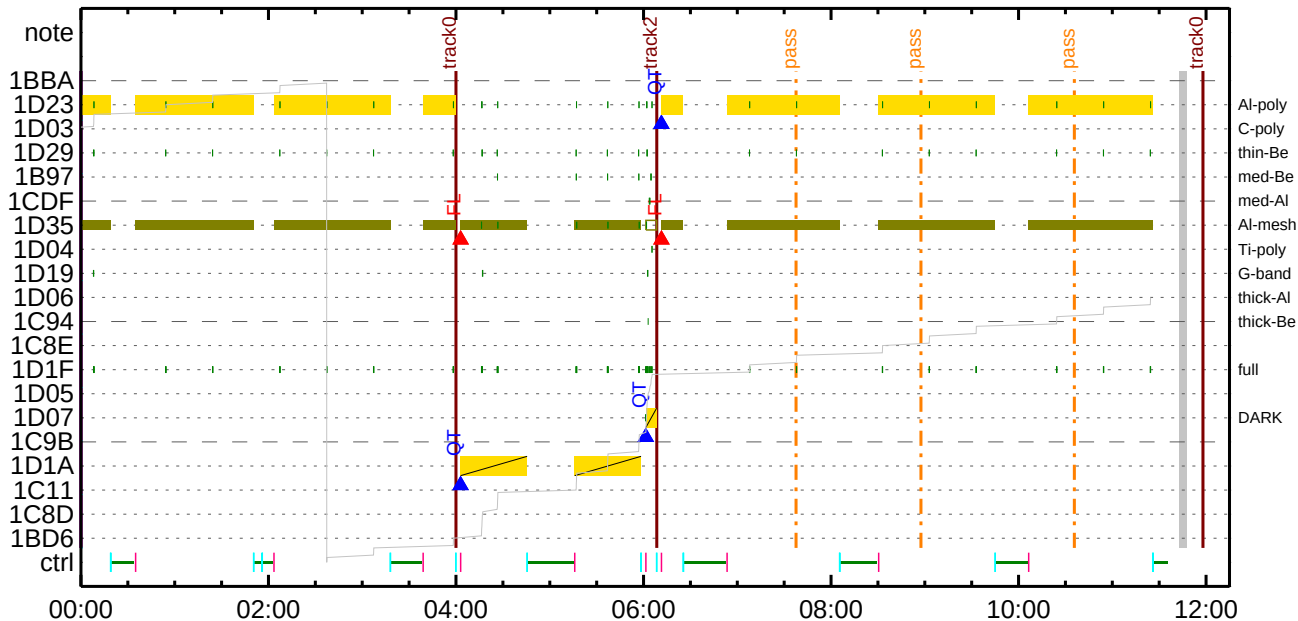
CMDI #0918 2025/10/24



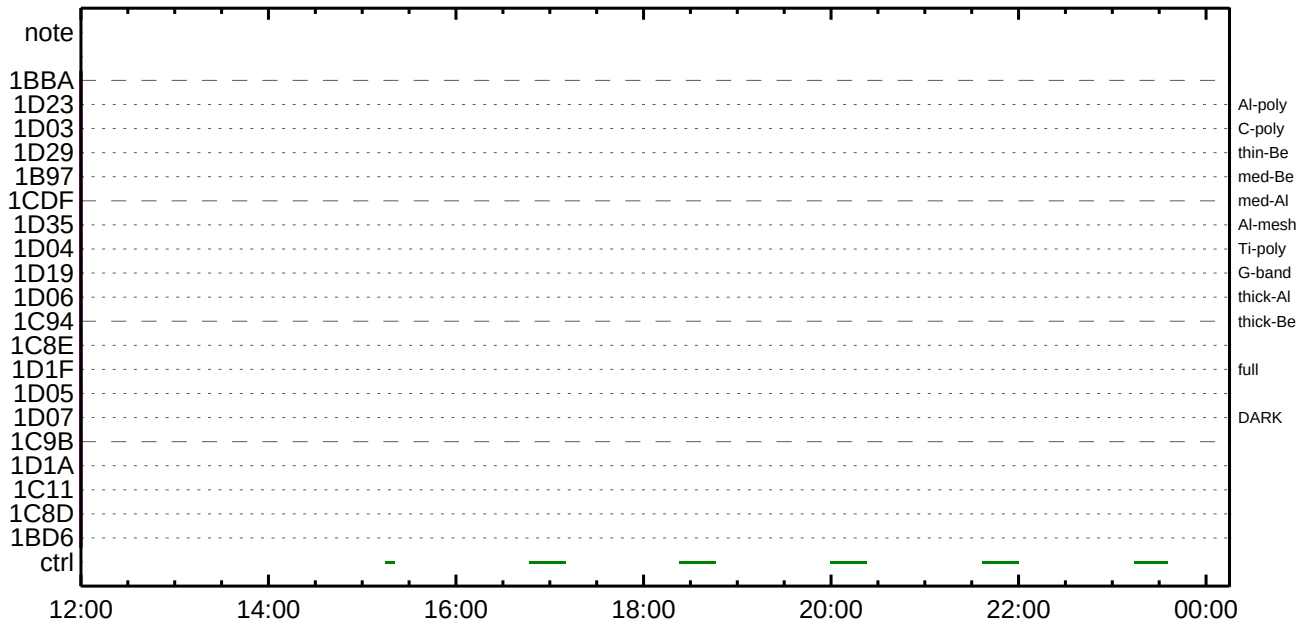
CMDI #0918 2025/10/24



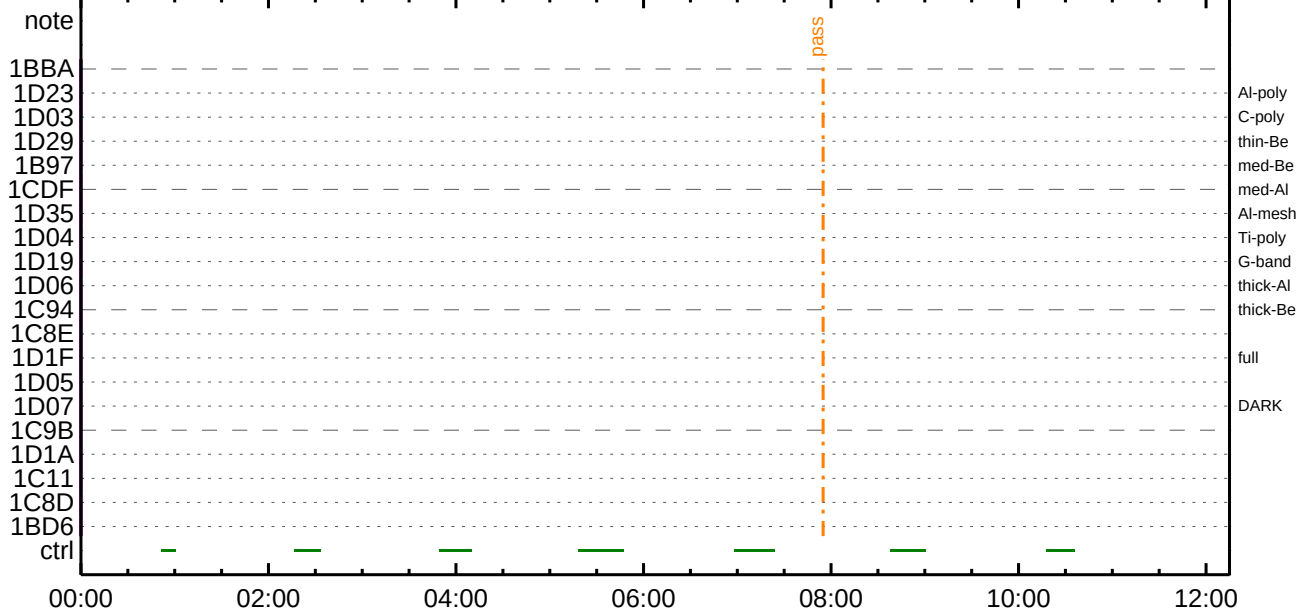
CMDI #0918 2025/10/25



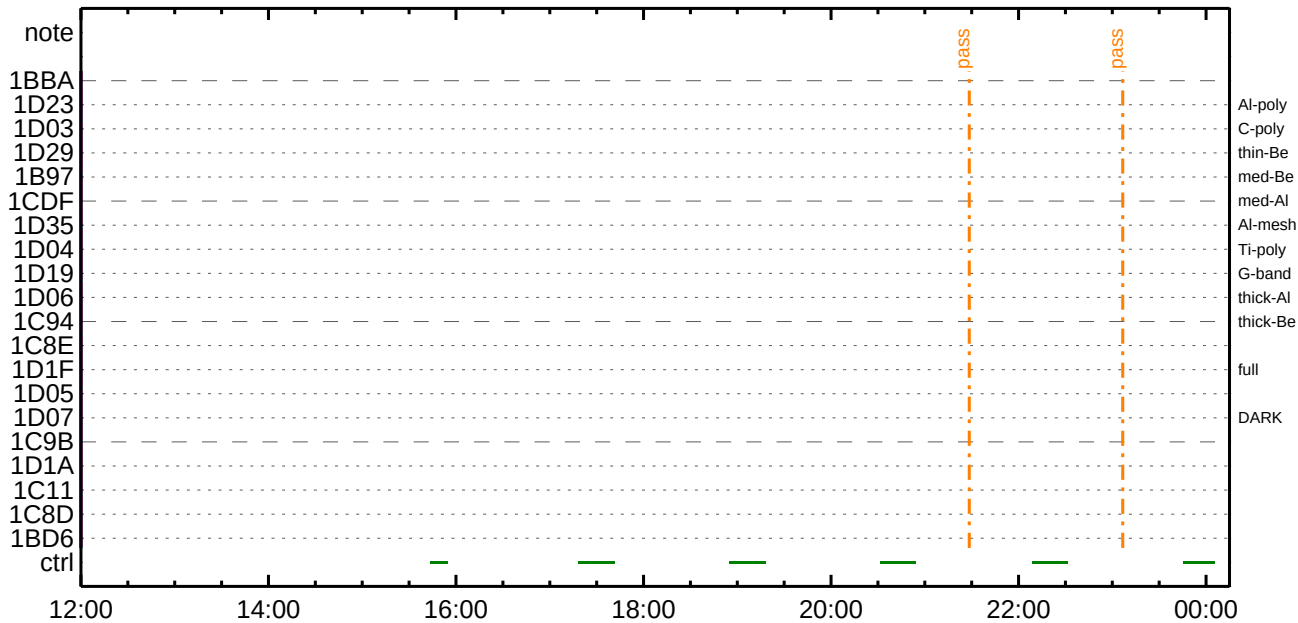
CMDI #0918 2025/10/25



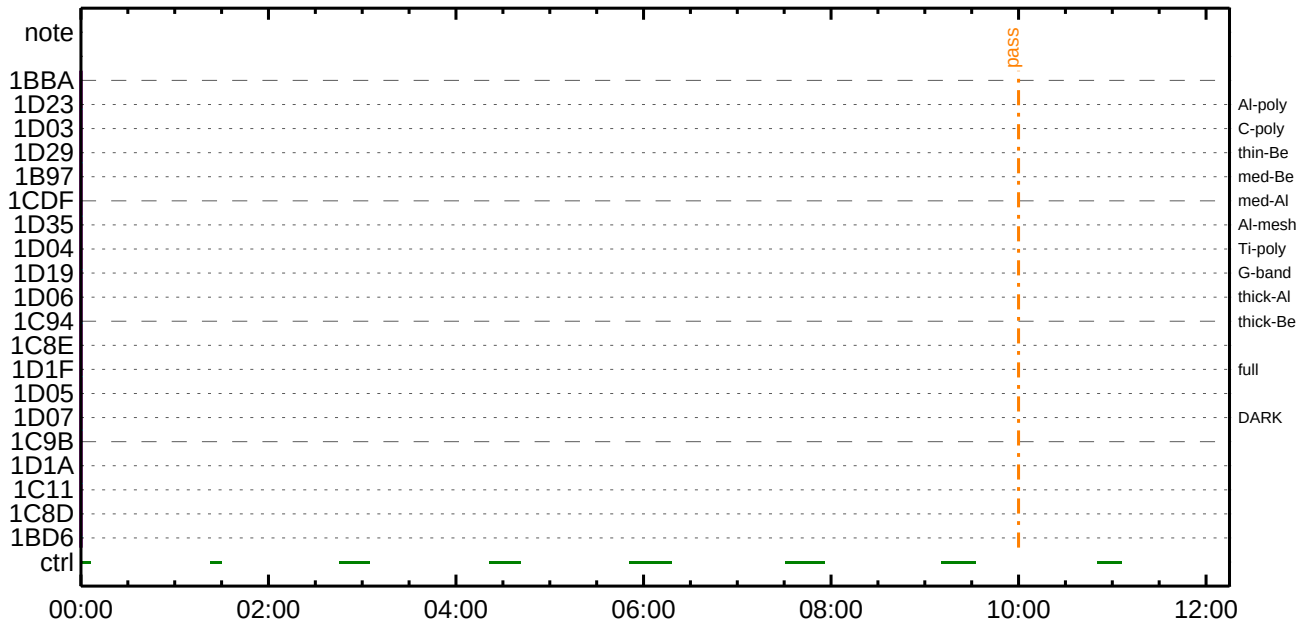
CMDI #0918 2025/10/26



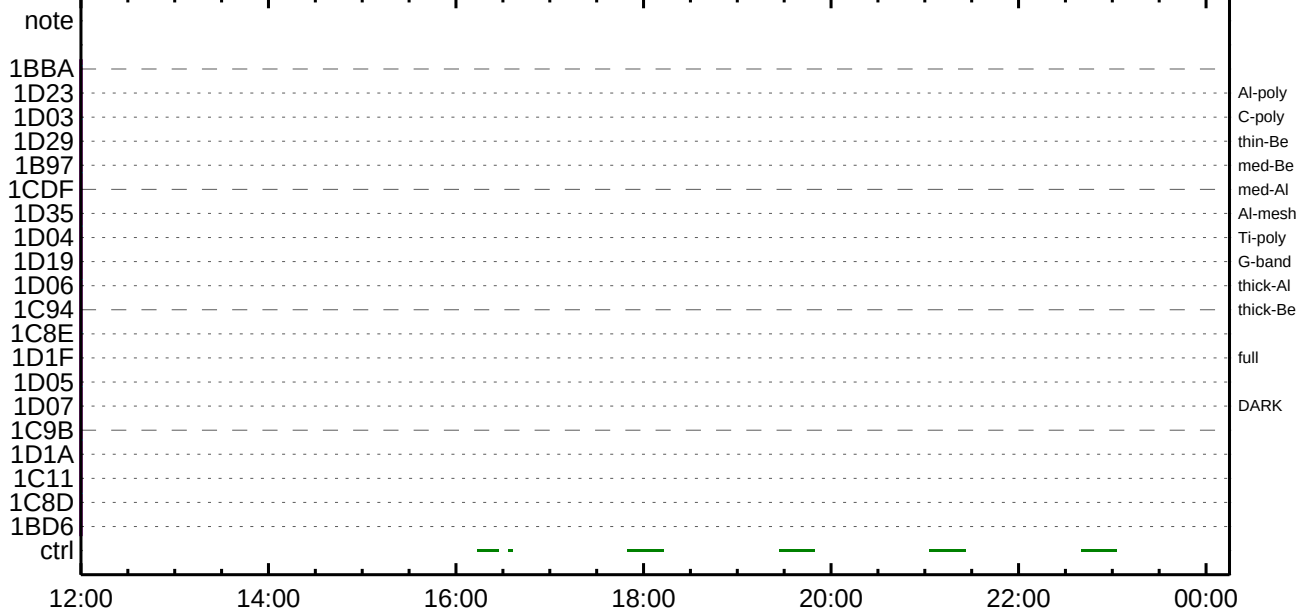
CMDI #0918 2025/10/26



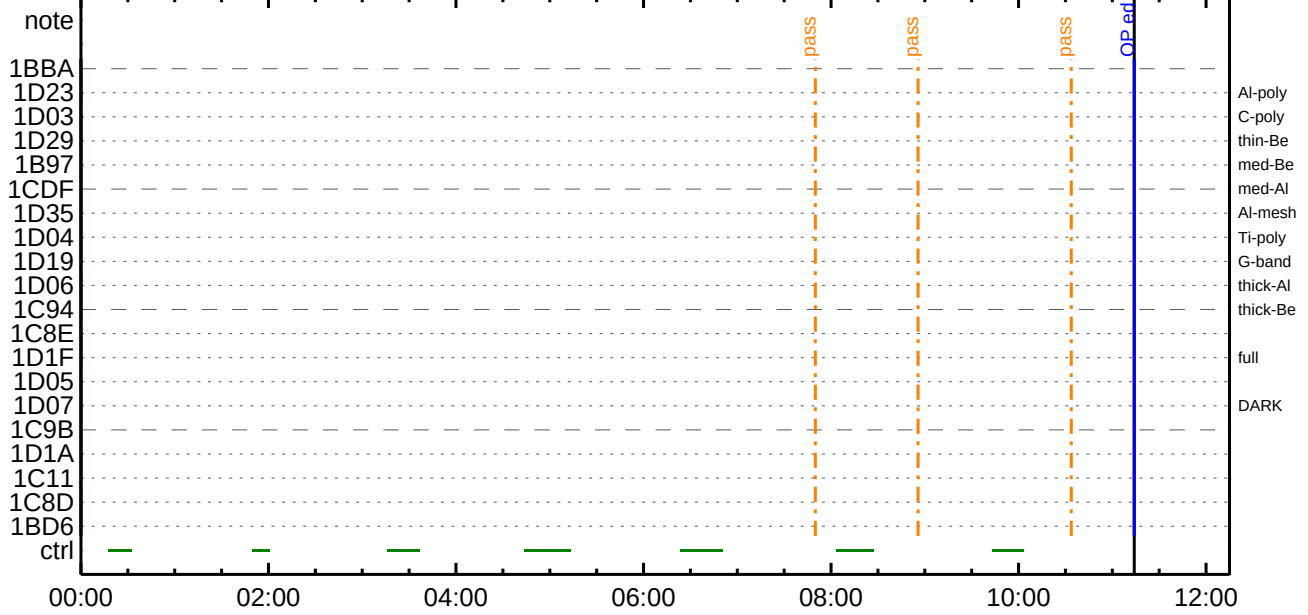
CMDI #0918 2025/10/27



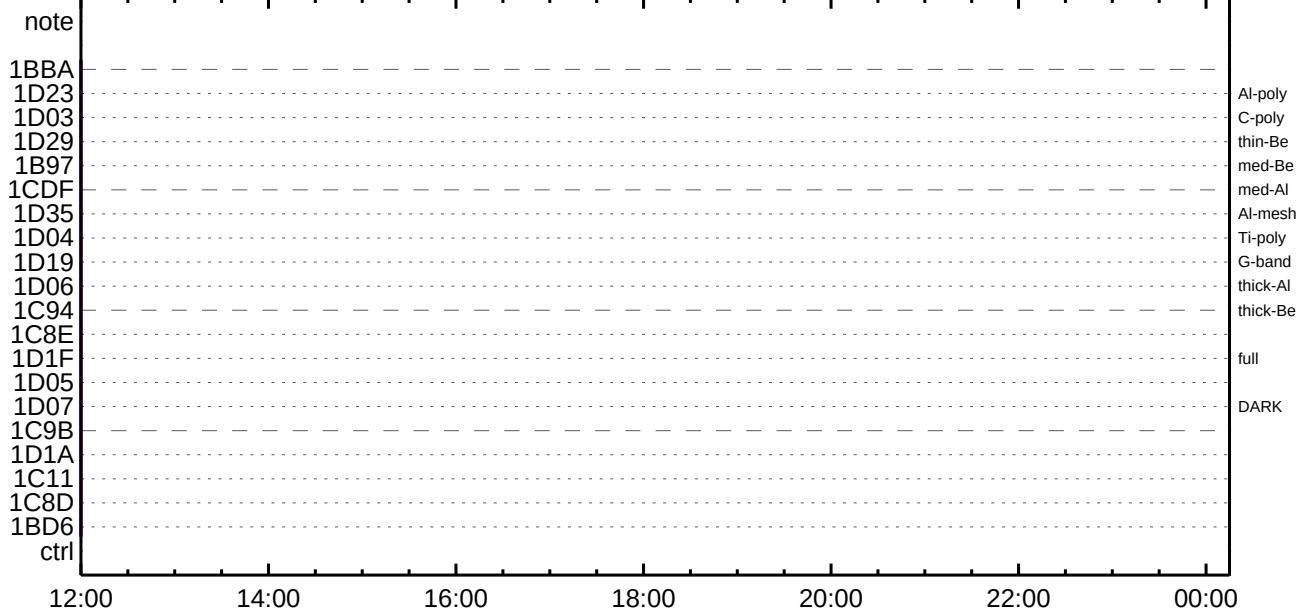
CMDI #0918 2025/10/27



CMDI #0918 2025/10/28



CMDI #0918 2025/10/28



(a) Spacecraft Operation Procedure (real-commands)

```
main-591 2025-10-23 13:09:30 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOS¥Á¥$¥Á¥~¼Ä»Ü;ä
0005 C.
0006 C. ¥À¥ß;¼¥³¥Ð¥ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ê¼¿¼Ä¼•µ°Æ»Í×ÁÇ¼Í¥Ç¥Á¥×¥í;¼¥É;ÊÊè¼µ•ííÊ;Ê¼Ê¼°Ç®¼•¼¿¼¹Ç¼Í;ÇÀ®,ù¼¹¼è¼Ð¼ÇÁ÷¿®¼•¼Ê¼¼¼³¼È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OG¥í;¼¥É;|¥À¥ó¥×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OG¥í;¼¥É;ä
0019 . S. OP op-591:OP
0020 ()
0021 . S. OG og-591:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÍî°è¥Á¥ó¥×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0036 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ¥À¥ó¥×¼°Áî»¼³ÍÇ$
0041 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¼Í¼Ê¹Ç•è²ÍOK¼³ÍÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0055 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ¥À¥ó¥×¼°Áî»¼³ÍÇ$
0060 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¼Í¼Ê¹Ç•è²ÍOK¼³ÍÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0074 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ¥À¥ó¥×¼°Áî»¼³ÍÇ$
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OP¼Í¼Ê¹Ç•è²ÍOK¼³ÍÇ$
0081 C.
0082 . C. ***** °Ê²¼¼¼Í¼Ä´¼¼°¼ÊÊ¬¼°Á÷¿® (¼¼¼µ-¥À¥ó¥×¼°Ê²Ç¼¼°Á¼Ä¼¼Ç¼°¼¼¼¼¼¹Ç¼Ç¼¼) *****
0083 C. DHU¥á;¼¥É;Ê¼ý½, ¥í;¼¥É;Ê¼òÍá¼¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0087 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOAD¼¬Á÷¿®NG¼Í¼¹¹Ç;Ç°Ê²¼¼¼ÍTI-CMDÁ÷¿®¼Í¼Ä¹Ô¼•¼Ê¼¼¼³¼È;£
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0096	C.				
0097	C.				
0098	.	C.	TI¥³¥¤¥ó¥É¤ðÃDİ; (UT)		
0099	+	TI	2025-10-23 10:40:00.0		
0100		DC	01-B3 DHU_OP_STOP		
0101	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.				
0103	+	TI	2025-10-23 10:40:01.0		
0104		DC	01-B4 DHU_OP_COPY		
0105	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.				
0107	+	TI	2025-10-23 10:40:01.0		
0108		DC	01-B5 DHU_OPOG_COPY		
0109	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.				
0111	+	TI	2025-10-23 10:44:59.5		
0112		DC	01-B2 DHU_OP_START		
0113	C.		¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.				
0115	C.		°Ê²¼¤İÄê¼İÍÑ¤İ¥Á¥\$¥Ä¥¯¹àìÜ		
0116	C.		¢¢[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.		¢¢[HK1_TI_CMD_NUM]	EQ	4
0118	C.		¢¢[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.		¢¢[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.				
0121	.	C.	*****		
0122	C.	TIÎî°è¥À¥ó¥×			
0123	C.	*****			
0124	C.				
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)			
0126	+	DC	01-23 DHU_DMA_DMP_PRM_SET		
0127		BC	(03 ab 03 01 02)		
0128	C.		¢¢[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.		¢¢[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.		¢¢[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.		¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.		¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC	01-22 DHU_MODE_CHNG		
0134		BC	(07 0b f8)		
0135	C.		¢¢[HK1_PKT_FORM_NO]	EQ	7
0136	C.		¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.		¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.		¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.		¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.				
0141	.	C.	¥À¥ó¥×½ªİ»¤ò³İÇ\$		
0142	C.		¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.				
0144	.	C.	RAM ID=TI_TBL¤İ¾È¹Ç•ë²ÌOK¤ò³İÇ\$		
0145	C.				
0146	.	C.	DHUYâ;¼¥É;Ê¼ý½,¥ì;¼¥È;Ë¤òİá¤¹		
0147	+	DC	01-22 DHU_MODE_CHNG		
0148		BC	(02 0a f8)		
0149	C.		¢¢[HK1_PKT_FORM_NO]	EQ	2
0150	C.		¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.		¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.		¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.				
0154	C.				
0155	C.		***** XRT START *****		
0156	C.		Execute, after the success of OP upload.		
0157	+	TI	2025-10-23 10:44:00.0		
0158		DC	07-F0 MDP_XRT_MODE_STBY		
0159		BC	(c3)		
0160	.	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0161	C.				
0162	C.		***** XRT END *****		
0163	.	C.	Stop EIS observation and temporarily disable EIS mode changes		
0164	C.				
0165	C.				
0166	C.		***** Start EIS operation (TI set) *****		
0167	C.		Execute, after the success of OP upload.		
0168	C.		Set EIS TI-commands		
0169	+	TI	2025-10-23 10:44:30.0		
0170		DC	07-FC EIS_MODE_MANU		
0171		BC	(21 02)		
0172	+	TI	2025-10-23 10:44:40.0		
0173		DC	07-FC EIS_MODE_CHG_DIS		
0174		BC	(22)		
0175	.	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0176	C.		***** End EIS operation (TI set) *****		
0177	C.				
0178	C.				
0179	C.				
0180	.	C.	***** MDP `üÃİ¤İ»ö¼Ý¤ĖÂ¤¤¹¤èDCBC•×²è *****		
0181	C.		(¼å°İıŸŒÅİ¥P¥È¥ā¥ç¥è¤Ė¼¤¤¼Å»Ű¤¹¤ē)		
0182	.	S.	DC-BC dcbc-402:DCBC		
0183			(MDP_known_event)		
0184	C.				
0185	C.				
01					


```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 7s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-10-23 10:44:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîîî»ö¼ÝñÊÃÐñ¹ñëDCBC•×²è *****
0156 C. (¾å°îÿÖ¥Ã¥Ê¥Ð¥Ê¥â¥ç¥ëñÊ½¼ññ¼Ã»Ûñ¹ñë)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•ï Daily±¿íññÊ'Øñ¹ñëDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Ã¥$¥Ã¥~¼Ã»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-593 2025-10-23 13:09:30 96 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸDŸóŸÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¼¿¼Ä¼•µ°Æ»Í×ÁÇ¼ÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËè¼µ•ííË;Ë¼Ë¼°ÇÖ¼•¼¿¼¹Ç¼Í;çÄ®,ù¼¹¼è¼D¼ÇÄ÷¿®¼•¼Ë¼¼¼³¼Ë;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 08 08)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 +. DC 07-F0 MDP_XRT_FLD_ENA
0055 BC (d8)
0056 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0057 BC (c8)
0058 +. DC 07-F0 MDP_XRT_ARS_DIS
0059 BC (d5)
0060 +. DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 +. DC 07-F0 MDP_XRT_FLD_RESET
0063 BC (da)
0064 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0065 BC (c4 13)
0066 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0067 BC (c5 0e)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C.
0071 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0072 C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074 BC (c2)
0075 +. TI 2025-10-23 10:44:02.0
0076 DC 07-F0 MDP_XRT_MODE_OBSV
0077 BC (c2)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C. ***** XRT END *****
0081 C.
0082 . C. ***** MDP ´ûÄí¼Í»ö¾Ÿ¼ËÄD¼¹¼èDCBC•x²è *****
0083 C. (¾ä°íŸÖŸÄŸËŸDŸËŸÄŸçŸè¼Ë¾¼¼Ä»Ü¼¹¼è)
0084 . S. DC-BC dcbc-402:DCBC
0085 (MDP_known_event)
0086 C.
0087 C.
0088 . C. ***** ŸDŸ¹•í Daily÷¿íÑ¼Ë´Ø¼¹¼èDCBC•x²è *****
0089 . S. DC-BC dcbc-153:DCBC
0090 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0091 C.
0092 C.
0093 . C. ;ãLOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0094 C.
0095 . C. ***** LOS *****
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XRT_OGLIST_0918.chk

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*** OP Sequence for XRT ***

2025/10/23	10:54:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	10:54:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	10:54:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/23	10:55:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 b3 bf 01 f3
2025/10/23	10:55:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/23	10:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/23	10:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/23	10:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/23	10:55:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	10:57:56.0	XRT_QT_PROG_SET_403_OG [0x193]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/10/23	10:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/23	10:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	14:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	14:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	14:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/23	14:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 03 be 01 f3
2025/10/23	14:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/23	14:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/23	14:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/23	14:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/23	14:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	14:32:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2025/10/23	14:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/23	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	15:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	15:45:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	15:45:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	15:45:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/23	15:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/23	15:56:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/23	15:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	17:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	17:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	17:20:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	17:20:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/23	17:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/23	17:43:30.0	XRT_Custom_430_OG [0x1ae]			
2025/10/23	17:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	17:59:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	17:59:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	17:59:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/23	17:59:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/10/23	17:59:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/10/23	17:59:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/10/23	17:59:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/23	18:02:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/10/23	18:02:30.0	XRT_CTRL_AUTO_408_OG [0x198]			

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2025/10/23	18:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	18:09:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	18:09:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	18:09:30.0	XRT_FOCUS_POSITION	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/23	18:09:48.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 03 be 01 f3
2025/10/23	18:09:50.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/23	18:09:52.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/23	18:09:54.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/23	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/23	18:12:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	18:12:28.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2025/10/23	18:12:30.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/23	18:56:30.0	XRT_CTRL_AUTO_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	18:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	18:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	18:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/23	18:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/23	19:20:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/23	19:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	20:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	20:33:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	20:33:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	20:33:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/23	20:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/23	20:56:30.0	XRT_Custom_430_OG [0x1ae]				
2025/10/23	20:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	22:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	22:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	22:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	22:10:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/23	22:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/23	22:33:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/23	22:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/23	23:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	23:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/23	23:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/23	23:47:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/23	23:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/24	00:08:00.0	XRT_Custom_430_OG [0x1ae]				
2025/10/24	00:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	01:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	01:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	01:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	01:24:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/24	01:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/24	01:31:30.0	XRT_Custom_430_OG [0x1ae]				
2025/10/24	01:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	02:47:32.0	XRT_CTRL_MANU_402_OG [0x192]				

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2025/10/24	02:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	02:47:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	02:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	03:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	03:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/24	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	04:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/24	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/10/24	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/24	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/24	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/24	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/24	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2025/10/24	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/24	04:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	04:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	04:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	04:22:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	04:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	04:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	04:44:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/24	05:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	05:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	05:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	05:52:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	05:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	06:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	06:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/24	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	06:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	06:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	06:30:18.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/24	06:30:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/10/24	06:30:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/10/24	06:32:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/24	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2025/10/24	06:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	06:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	06:39:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	06:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/10/24	06:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 03 be 01 f3	
2025/10/24	06:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/24	06:40:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/24	06:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/24	06:40:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	

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2025/10/24	06:42:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	06:42:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2025/10/24	06:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/24	07:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	07:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	07:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	07:32:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	07:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/24	07:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/24	07:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/10/24	09:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	09:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	09:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	09:12:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	09:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/24	09:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/24	09:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/10/24	10:52:30.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	10:52:32.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	10:52:34.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	10:52:36.5	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	10:55:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/24	11:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/24	11:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/10/24	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	11:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	12:00:00.0	AOCs_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/10/24	12:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCs_ORe-point_Start_1_OG	5	02-76	00 b3 bf 01 f3
2025/10/24	12:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	1	07-F0	d8
2025/10/24	12:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/24	12:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/24	12:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/24	12:02:56.0	XRT_QT_PROG_SET_403_OG [0x193]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/24	12:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/10/24	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/10/24	15:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/24	15:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/24	15:30:00.0	AOCs_ORe-point_Start_2_OG [0x098]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/10/24	15:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCs_ORe-point_Start_2_OG	5	02-76	02 03 be 01 f3
2025/10/24	15:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	1	07-F0	d8
2025/10/24	15:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/10/24	15:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/10/24	15:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/10/24	15:32:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/10/24	15:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/24	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e

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2025/10/24	16:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	16:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	16:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	16:16:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	16:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	16:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	16:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/24	16:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	16:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	16:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	16:34:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	16:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	16:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	16:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/24	17:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	17:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	17:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	17:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	17:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	17:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]					
2025/10/24	18:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	18:14:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	18:15:00.0	AOCs_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/24	18:15:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/10/24	18:15:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/10/24	18:15:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/10/24	18:17:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/24	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2025/10/24	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	18:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	18:24:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	18:25:00.0	AOCs_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/10/24	18:25:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 03 be 01 f3	
2025/10/24	18:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/24	18:25:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/24	18:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/24	18:25:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/24	18:27:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	18:27:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2025/10/24	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/10/24	19:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	19:28:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	19:28:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	19:28:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	19:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	19:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	19:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/10/24	21:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2025/10/24	21:05:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	21:05:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	21:05:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	21:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	21:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	21:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/24	22:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	22:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	22:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/24	22:42:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/24	22:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/24	23:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/24	23:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	00:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	00:19:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	00:19:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	00:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	00:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	00:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	00:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	01:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	01:50:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	01:50:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	01:50:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	01:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	01:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	01:56:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	01:56:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	01:56:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	01:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	02:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	02:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	03:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	03:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	03:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	03:18:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	03:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	03:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	03:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	04:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/10/25	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/10/25	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/25	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/25	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/25	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/25	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2025/10/25	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2025/10/25	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/10/25	04:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	04:45:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	04:45:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	04:45:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	04:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	05:15:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	05:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	05:58:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	05:58:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	05:58:48.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/10/25	05:58:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/10/25	05:58:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/10/25	06:01:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/25	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2025/10/25	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	06:08:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	06:08:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	06:08:30.0	AOCs_Or-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2025/10/25	06:08:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	03 be 01 f3
2025/10/25	06:08:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/10/25	06:08:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/10/25	06:08:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/10/25	06:08:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/10/25	06:11:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	06:11:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2025/10/25	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/10/25	06:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	06:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	06:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	06:25:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	06:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	06:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	06:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	08:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	08:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	08:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	08:05:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	08:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/10/25	08:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/10/25	08:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/10/25	09:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	09:45:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	09:45:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/10/25	09:45:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/10/25	09:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/25	10:05:30.0	XRT_Custom_430_OG	[0x1ae]			
2025/10/25	10:06:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/10/25	11:26:00.0	XRT_CTRL_MANU_400_OG	[0x190]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/25	11:26:02.0	XRT_CTRL_MANU_402_OG	[0x192]			
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/10/25	11:26:04.0	XRT_FLD_RESET_415_OG	[0x19f]			
			MDP_XRT_FLD_RESET	1	07-F0	da
2025/10/25	11:26:06.0	XRT_PREFLR_STRT_404_OG	[0x194]			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/10/25	11:29:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/10/25	11:58:00.0	AOCS_ORe-point_Start_3_OG	[0x099]			
			AOCU_NM	5	02-76	00 00 00 00 00