

Period: 2025/11/01 10:39:00 - 2025/11/06 11:08:00

XOB #1D07: Synoptic 8 Filter w/ Al-mesh(5/128/723), Al-poly(8/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(128/5795														
Term		Pointing (x, y)				Comment								
11/01 17:57:00 - 11/01 18:03:54		Fixed (0.0, 0.0)				synoptic, shifted -6.0 min								
11/02 06:27:00 - 11/04 11:03:54		Fixed (0.0, 0.0)				synoptic, shifted 24.0 min + HOP349								
PROG= 06 1-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 5 1-time(s) 2.0sec														
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 26 1-time(s) 2.0sec														
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 99 1-time(s) 2.0sec														
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 83 1-time(s) 2.0sec														
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23 1-time(s) 4.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 1-time(s) 2.0sec														
Seqn= 41 1-time(s) 2.0sec														
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 17 1-time(s) 2.0sec														
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 33 1-time(s) 2.0sec														

	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 75 1-time(s) 2.0sec													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D1A: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak -													
Term			Pointing (x, y)				Comment						
11/02 02:07:00 - 11/02 06:23:54			Fixed (0.0, 0.0)				synoptic, shifted 24.0 min + HOP349						
PROG= 04 Inf.-time(s)													
Subr= 1		1-time(s)		600.0sec									
Seqn= 55		1-time(s)		2.0sec									
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
Seqn= 98		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
Seqn= 79		1-time(s)		2.0sec									
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
Seqn= 30		1-time(s)		2.0sec									
└ Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0 0 2.0sec
└ Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0 0 2.0sec
Subr= 2		6-time(s)		1200.0sec									
Seqn= 8		1-time(s)		2.0sec									
└ thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2 0 2.0sec
Seqn= 74		1-time(s)		2.0sec									
└ med-Be/Open		med-Be/Open		close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ med-Be/Open		med-Be/Open		close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
Seqn= 6		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2 0 2.0sec
Seqn= 29		1-time(s)		2.0sec									
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

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Flare mode

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XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384																
Term			Pointing (x, y)					Comment								
11/01 10:52:00 - 11/01 15:29:54			Track (650.7, -34.3) @ 11/01 10:49:00					# OP start + 10min + AR14267								
PROG= 13			1-time(s)													
Subr= 1			1-time(s)		2.0sec											
Seqn= 67			1-time(s)		2.0sec											
			thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2			1-time(s)		2.0sec											
Seqn= 59			255-time(s)		5.0sec											
			thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
			med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)		Q=95	3	0	2.0sec
			Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL													
Term		Pointing (x, y)		Comment									
11/01 15:33:00 - 11/01 17:53:54		Track (650.7, -34.3) @ 11/01 10:49:00		# OP start + 10min + AR14267									
11/01 18:07:00 - 11/02 01:59:54		Track (697.8, -30.2) @ 11/01 18:04:00		# AR14267									
11/02 02:07:00 - 11/02 06:23:54		Fixed (0.0, 0.0)		synoptic, shifted 24.0 min + HOP349									
PROG= 14 30-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 68 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 20-time(s) 2.0sec													
Seqn= 11 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec											
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87	1-time(s)	2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

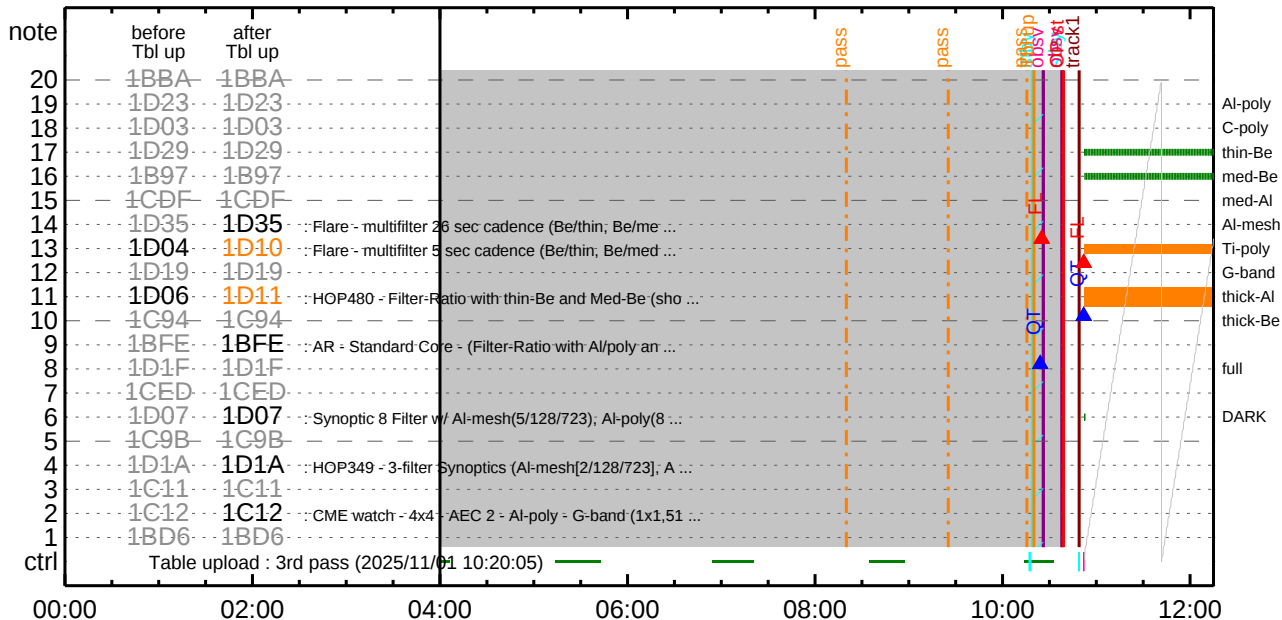
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Flare Detection

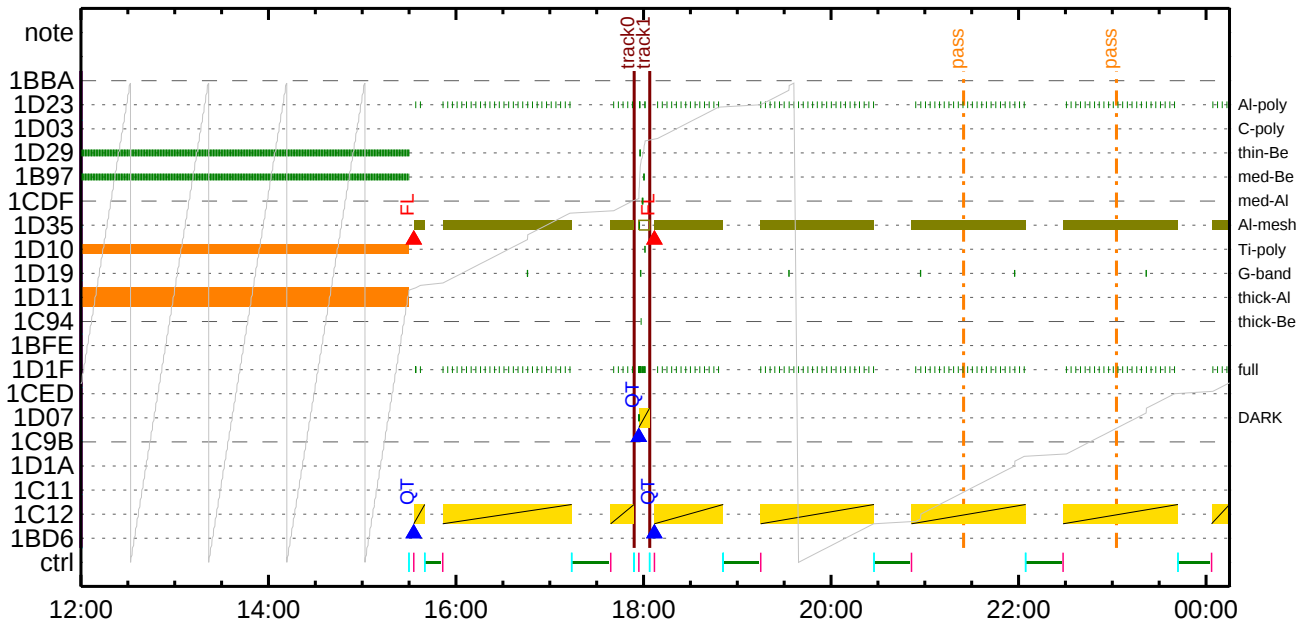
* * * * *

FLD Patrol													
Term				Pointing (x, y)						Comment			
11/01 10:21:05 - 11/01 17:54:18				cannot be identified									
11/01 18:04:18 - 11/02 06:24:18				Track (697.8, -30.2) @ 11/01 18:04:00 # AR14267									
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8			Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

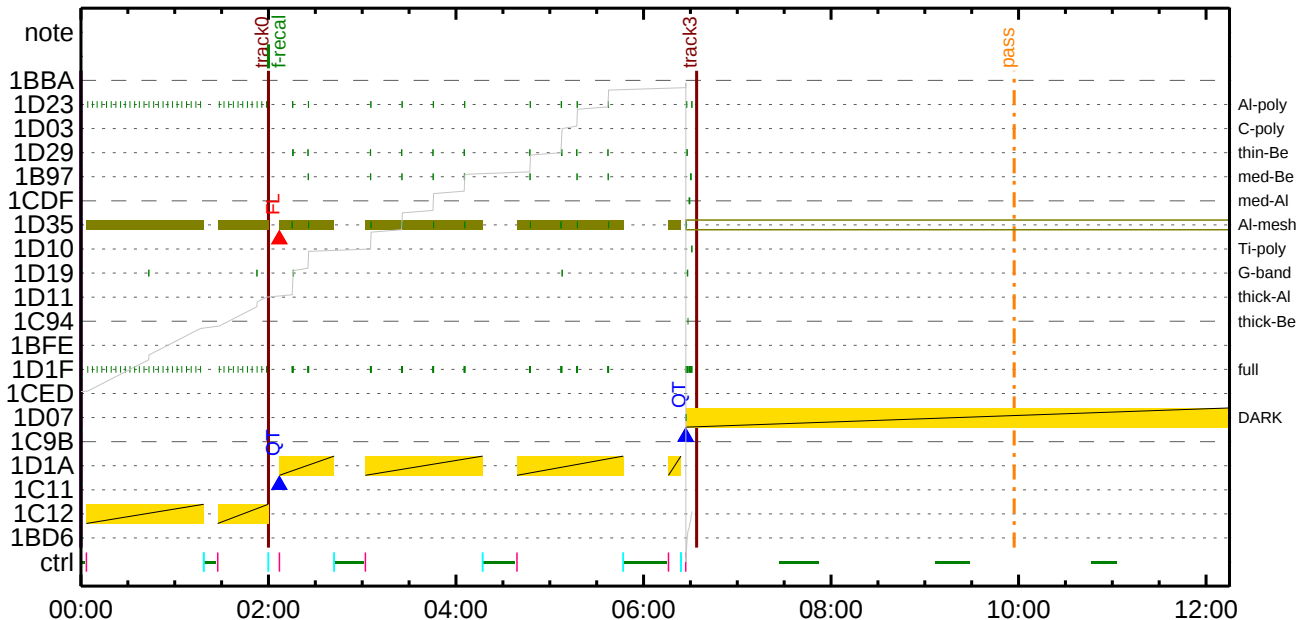
CMDI #0936 2025/11/01



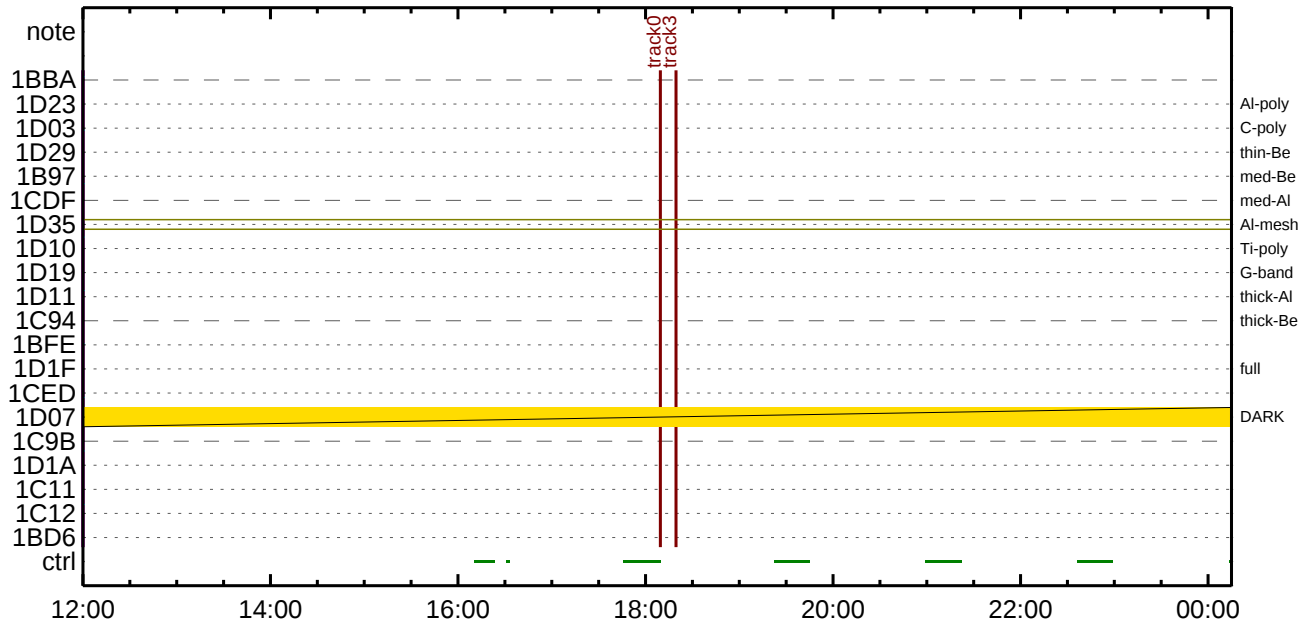
CMDI #0936 2025/11/01



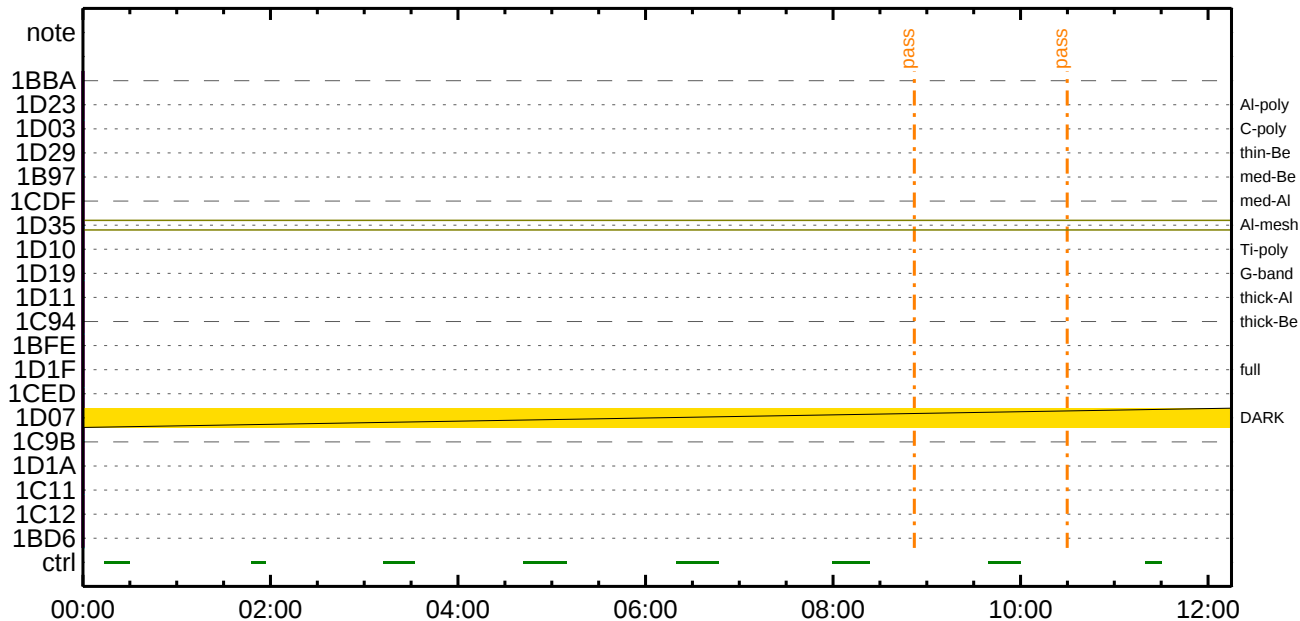
CMDI #0936 2025/11/02



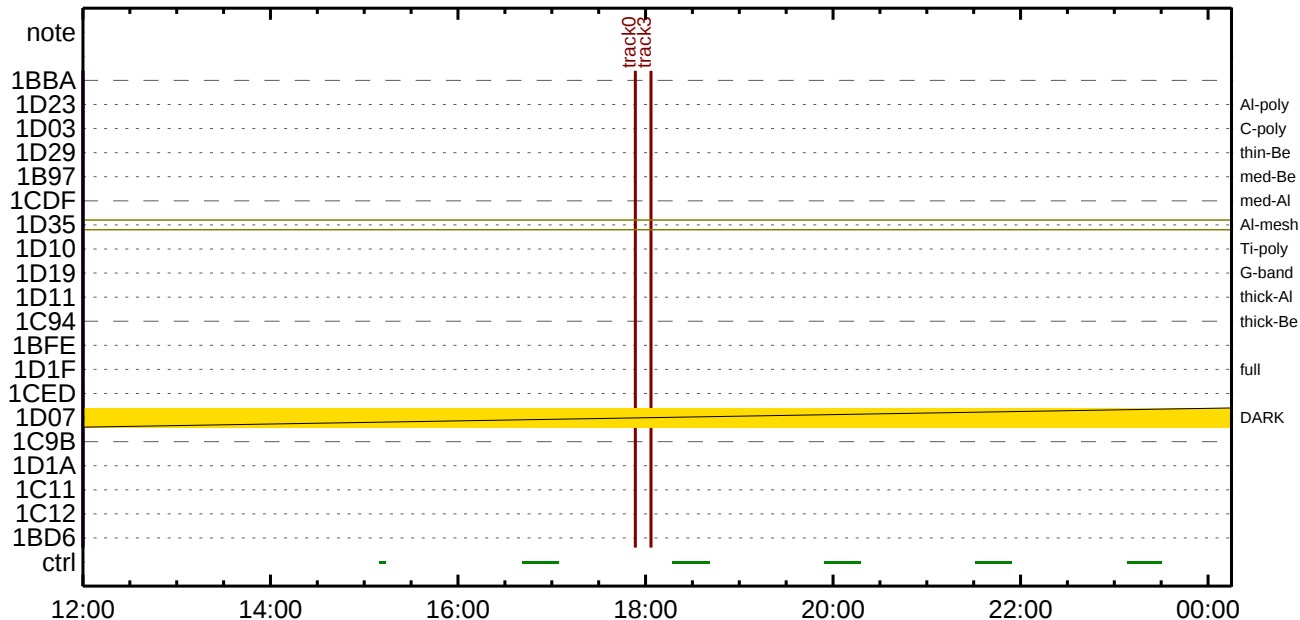
CMDI #0936 2025/11/02



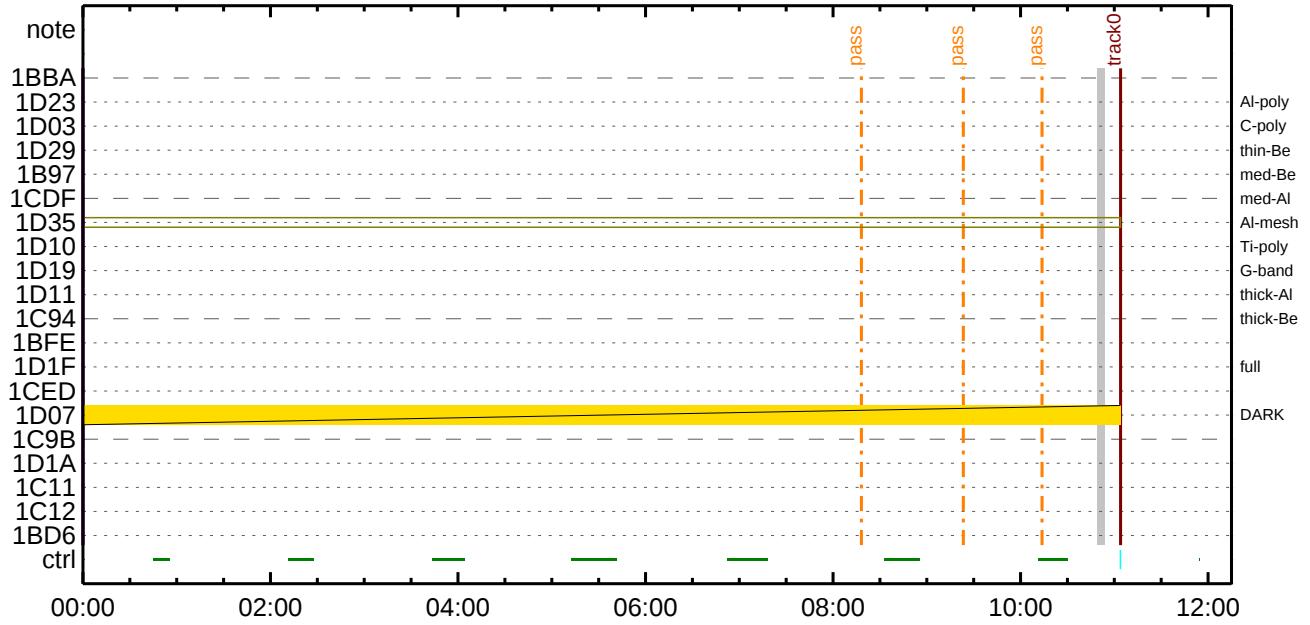
CMDI #0936 2025/11/03



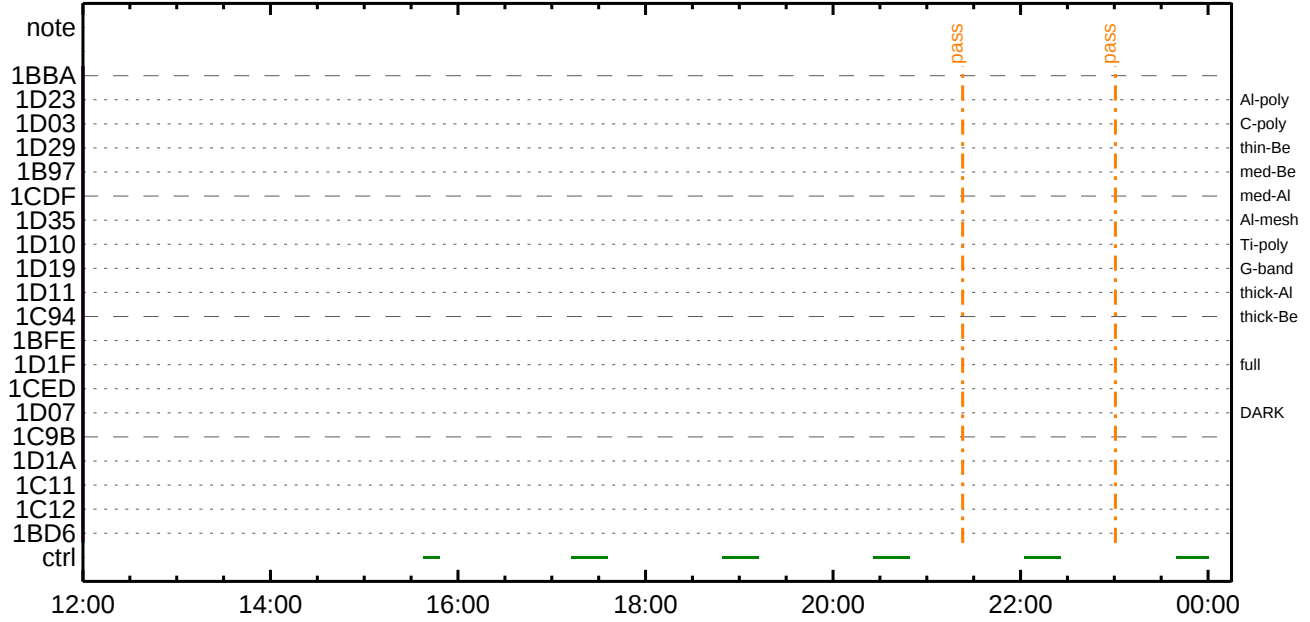
CMDI #0936 2025/11/03



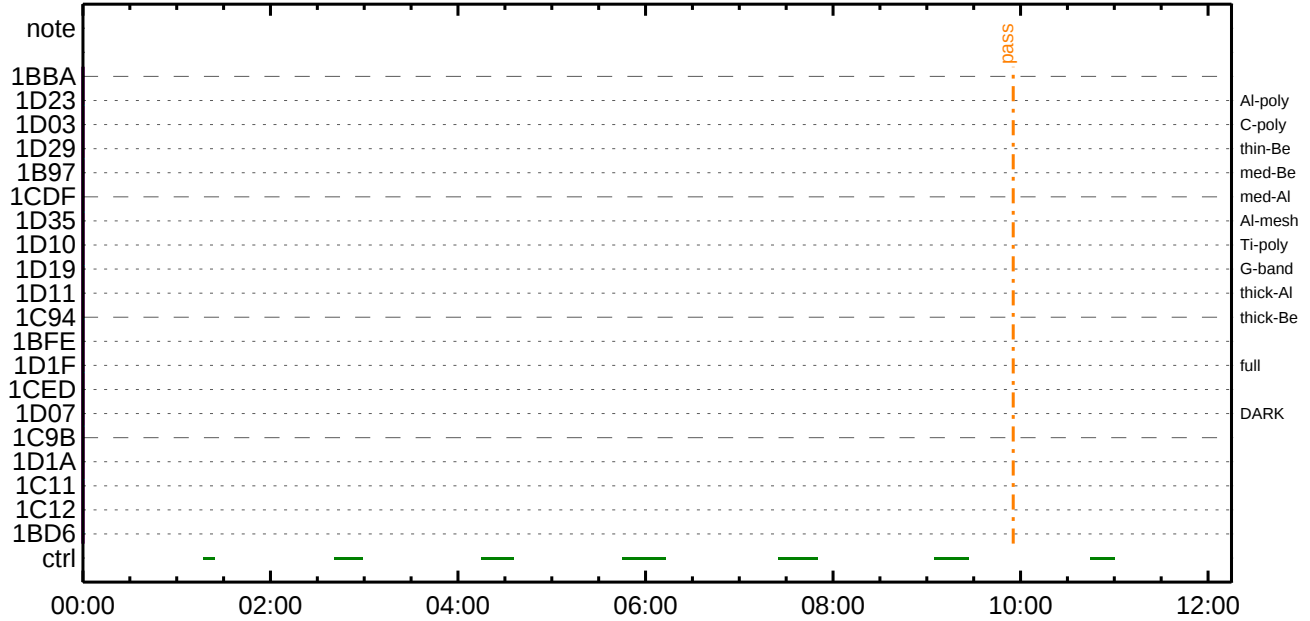
CMDI #0936 2025/11/04



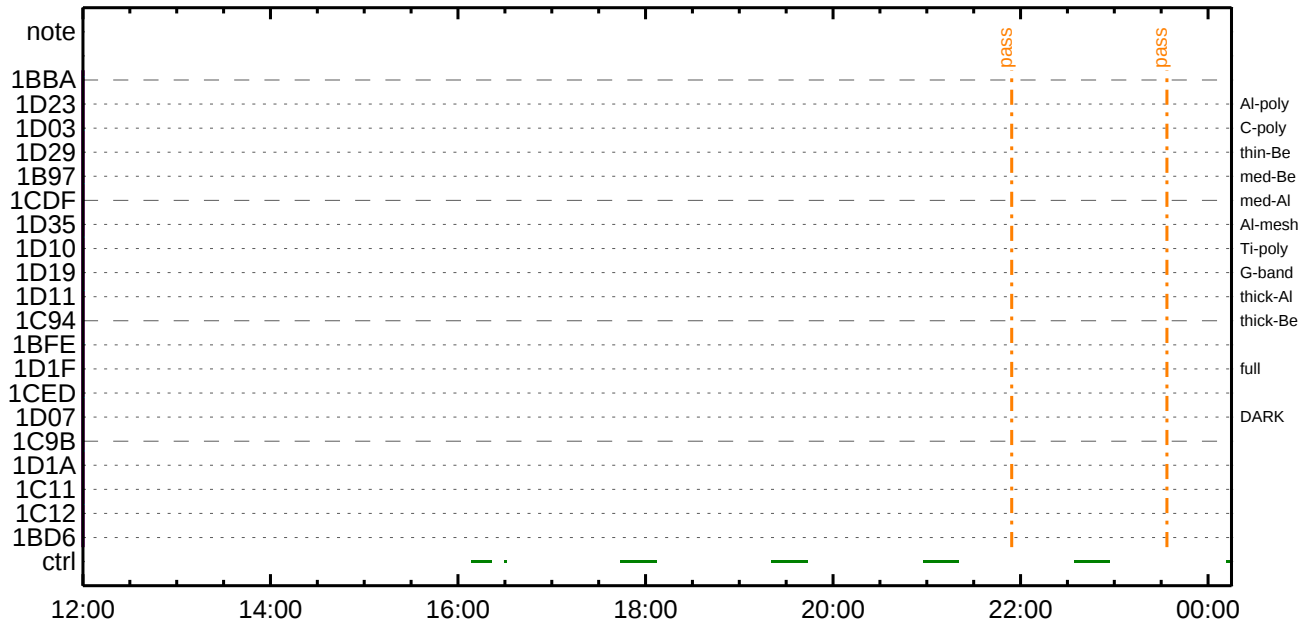
CMDI #0936 2025/11/04



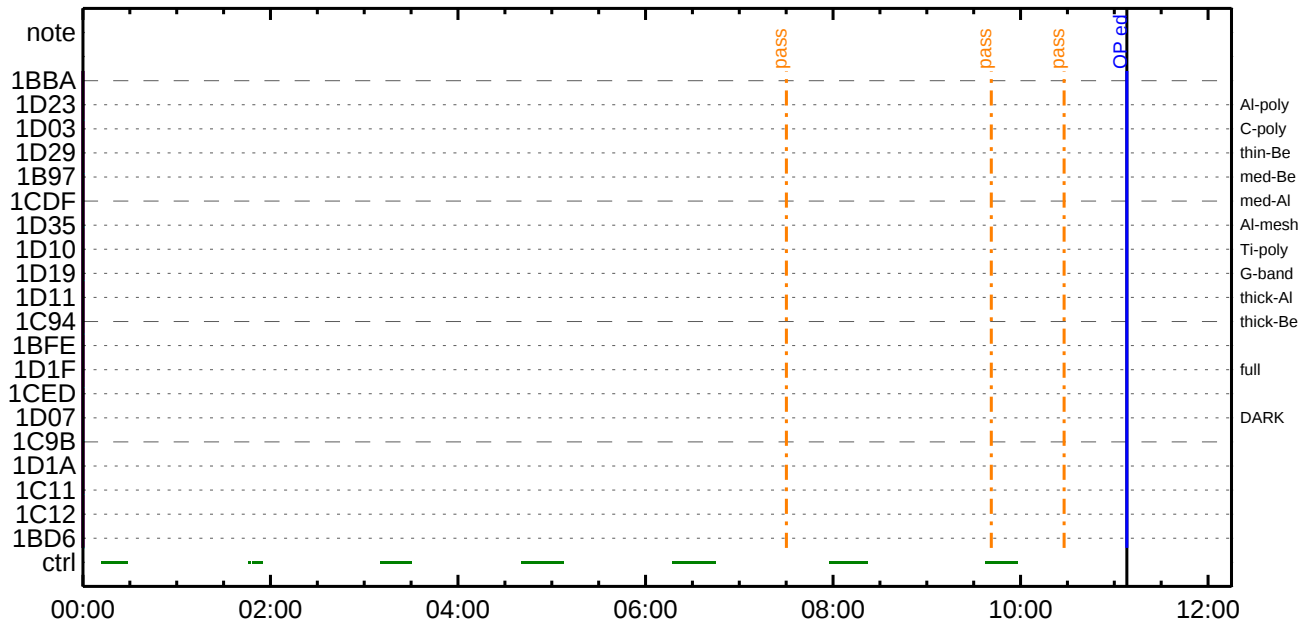
CMDI #0936 2025/11/05



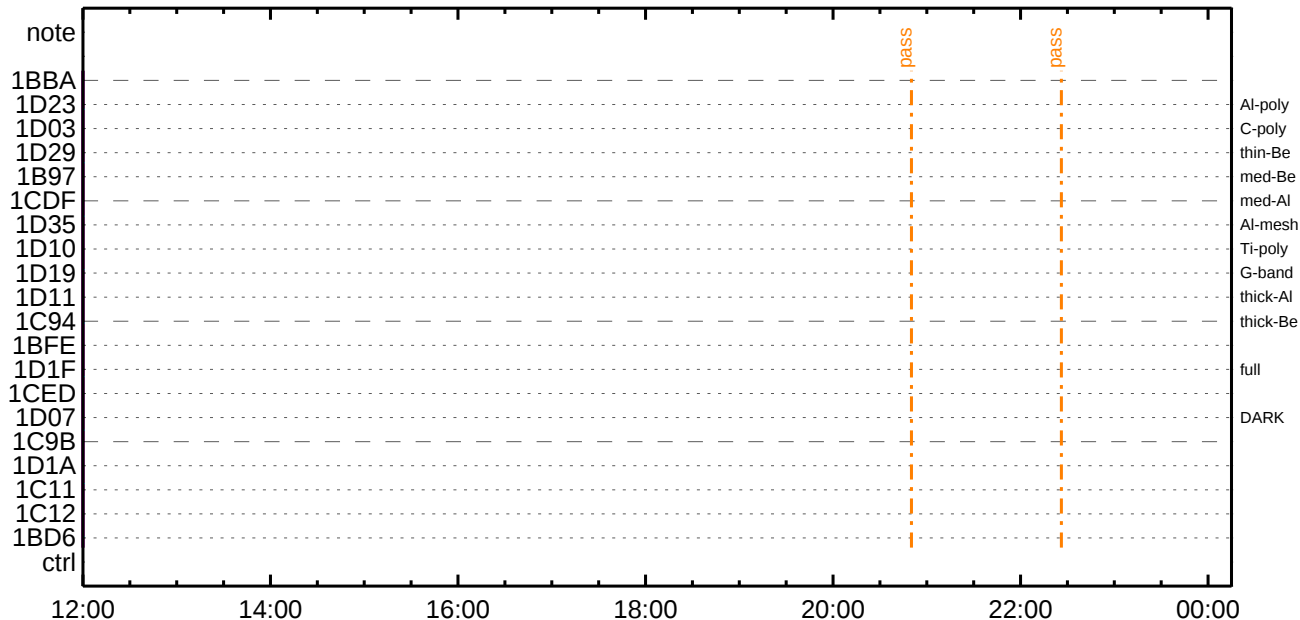
CMDI #0936 2025/11/05



CMDI #0936 2025/11/06



CMDI #0936 2025/11/06



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main-11-01 11:32:37 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŸÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÀŸß;¼Ÿ³ŸÐŸóŸÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ëñ¿²Äñ•µ°E»Í×ÅÇñÎŸÇŸÃŸ×Ÿí;¼ŸÉ;ËËè²µ•íîÊ;ËñÊ¼°ÇÖñ•ñ¿½¹¹Çñİ;ÇÀ®,ùñ¹ñÈñÐñÇÁ÷¿®ñ•ñÊñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÀŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-608:OP
0020 ()
0021 . S. OG og-608:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPîî°èŸÀŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0036 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÀŸóŸ×½ªªî»ñò³îÇŞ
0041 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¾Ê¹Ç•ë²ïOKñò³îÇŞ
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0055 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÀŸóŸ×½ªªî»ñò³îÇŞ
0060 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¾Ê¹Ç•ë²ïOKñò³îÇŞ
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ÇÇ[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ÇÇ[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ÇÇ[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ÇÇ[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ÇÇ[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ÇÇ[HK1_PKT_FORM_NO] EQ 7
0074 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ÇÇ[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÀŸóŸ×½ªªî»ñò³îÇŞ
0079 C. ÇÇ[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñî¾Ê¹Ç•ë²ïOKñò³îÇŞ
0081 C.
0082 . C. ***** °Ê²¼ñî¾Ä`¶¹°ñÊÉ¬ñ°Á÷¿® (¾µ-ŸÀŸóŸ×¼ê½ÇñòÁÓÃæñÇ½ªñ``ñë¾ì¹ÇñÇñâ) *****
0083 C. DHUŸâ;¼ŸÉ;Ë¼Ÿ½,Ÿí;¼ŸÉ;Ëñòîáñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ÇÇ[HK1_PKT_FORM_NO] EQ 2
0087 C. ÇÇ[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ÇÇ[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ÇÇ[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷¿®NGñî¾ì¹Ç;Ç°Ê²¼ñîTI-CMDÁ÷¿®ñî¾Ä¹Ôñ•ñÊñññ³ñÈ;£
```

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0096 C.          SET EDUMP I±°iNŸ¹C¹Ô|³E;E
0097 C.
0098 C. TIŸ³ŸPŸóŸÉ±ðÄDİ¿ (UT)
0099 +. TI 2025-11-01 10:34:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2025-11-01 10:34:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2025-11-01 10:34:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2025-11-01 10:38:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C. °Ê¼IÄê¼iÍÑIŸÁŸSŸÄŸ¹àIÜ
0116 C.          ÇÇ [HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.          ÇÇ [HK1_TI_CMD_NUM] EQ 4
0118 C.          ÇÇ [HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.          ÇÇ [HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C. *****
0122 C. TIÎÎ°èŸÄŸóŸ×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.          ÇÇ [HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.          ÇÇ [HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.          ÇÇ [HK1_DMP_BLOCK_NUM] EQ 3
0131 C.          ÇÇ [HK1_DMP_REPEAT_NUM] EQ 0
0132 C.          ÇÇ [HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.          ÇÇ [HK1_PKT_FORM_NO] EQ 7
0136 C.          ÇÇ [HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.          ÇÇ [HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.          ÇÇ [HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.          ÇÇ [HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C. ŸÄŸóŸ×¼ªªÎ»±ð³ÎÇS
0142 C.          ÇÇ [HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C. RAM ID=TI_TBLI¼È¹Ç•ê²ÌOK±ð³ÎÇS
0145 C.
0146 C. DHUŸâ;¼ŸÉ;Ê¼Ÿ½,Ÿi;¼ŸÈ;Ë±ðÌá±¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.          ÇÇ [HK1_PKT_FORM_NO] EQ 2
0150 C.          ÇÇ [HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.          ÇÇ [HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.          ÇÇ [HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. Stop EIS observation and temporarily disable EIS mode changes
0155 C.
0156 C.
0157 C. ***** Start EIS operation (TI set) *****
0158 C. Execute, after the success of OP upload.
0159 C. Set EIS TI-commands
0160 +. TI 2025-11-01 10:38:30.0
0161 DC 07-FC EIS_MODE_MANU
0162 BC (21 02)
0163 +. TI 2025-11-01 10:38:40.0
0164 DC 07-FC EIS_MODE_CHG_DIS
0165 BC (22)
0166 C.          [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0167 C. ***** End EIS operation (TI set) *****
0168 C.
0169 C.
0170 C.
0171 C. ***** XRT START *****
0172 C. Execute, after the success of OP upload.
0173 +. TI 2025-11-01 10:38:00.0
0174 DC 07-F0 MDP_XRT_MODE_STBY
0175 BC (c3)
0176 C.          [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0177 C.
0178 C. ***** XRT END *****
0179 C.
0180 C. ***** MDP ´ûÃÎI»ö¼Ÿ±ÊÄ±¹±ÊDCBC•×²è *****
0181 C. (¼å°iŸÓŸÄŸÈŸPŸÈŸÄŸÇŸÈ±Ê¼¼Ä»Ü±¹±è)
0182 C. S. DC-BC dcbc-402:DCBC
0183 (MDP_known_event)
0184 C.
0185 C.
0186 C. ***** ŸÐŸ¹•İ Daily±¿IÑ±Ê´Ø±¹±ÊDCBC•×²è *****
0187 C. S. DC-BC dcbc-153:DCBC
0188 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0189 C.
0190 C.
0191 C. ;äLOSŸÁŸSŸÄŸ¹¼Ä»Ü;ä
0192 C.
0193 C. ***** LOS *****

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main-609 2025-11-01 11:23:37 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÔñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EA, Í×ÈÝñäLOSñÞñÇñİ»Þ'Òðò¹íî,ñ•;ÇÉÔÍ×ñÈXÄÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ [HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ [HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ [HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ [HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲ~¾ÔÄÖñ¬°ÄÄêñ•ñççé;Ç°Ê²¼ñî°EA,¼ê½Çñðò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0043 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0044 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲÈÄÚÂØ;ÊÄ•Ä°²óÈò;Ë,ãñî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ [HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0050 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0051 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EA,ñ¬¼«E°Ää»ßñ•ñçç,ä;Ç°Ê²¼ñðò¼Ä¹Ôñ¹ñè;£
0055 C. ŲÇŲóŲÉŲÈÄÚÂØñäÄ•Ä°²óÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0069 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0070 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0071 C.
0072 . C. ;ãŲÇŲóŲÉŲÈÄÚÂØ;ÊÄ•Ä°²óÈò;Ë,ãñî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ [HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0076 C. ÇÇ [HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0077 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ [HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ [HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ [HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ [HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ [HK1_XPA_ON/OFF] EQ OFF
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0096 C.  
0097 C.  
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0099 C. Upload the Orbit Element and the Target Attitude  
0100 C. RAM-ID:TARGET_ATT  
0101 . S. RAM ram-150:TARGET_ATT  
0102 (  
0103 C.  
0104 C.  
0105 C. Set the dump memory area of TARGET_ATT  
0106 +. DC 02-48 AOcu_DUMP_SET  
0107 BC (07 00 00 00 18 00)  
0108 C.  
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]  
0110 C.  
0111 C.  
0112 C. Change the TLMFormatNo for the AOCs Dump Format  
0113 +. DC 01-22 DHU_MODE_CHNG  
0114 BC (04 0b f8)  
0115 C.  
0116 C. Wait for AOCSDUMP to end  
0117 C.  
0118 . C. Check the dump memory  
0119 C.  
0120 C. Result = OK [ ]  
0121 C.  
0122 +. DC 01-22 DHU_MODE_CHNG  
0123 BC (02 0a f8)  
0124 C.  
0125 C. <A_***>[TLM STS] FMT = 2 [ ]  
0126 C.  
0127 +. DC 02-8E AOcu_ORB_UPD  
0128 . C.  
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0130 +. DC 07-FC EIS_MODE_CHG_ENA  
0131 BC (20)  
0132 . C. Verify EIS_MODE_CHG_FLG is ENA  
0133 +. DC 07-FC EIS_MODE_MANU  
0134 BC (21 02)  
0135 . C. Verify EIS in MANUAL mode  
0136 . C. Estimated OBSTBL upload time is 18s  
0137 C. *****  
0138 C. EIS START OBSTBL LOAD  
0139 C. *****  
0140 . S. RAM ram-820:EIS_OBSTBL  
0141 (  
0142 +. DC 07-FC EIS_DUMP_OBSTBL  
0143 BC (07 07 07 00 00 70 00)  
0144 C.  
0145 C. Execute, after the success of OBSTBL upload.  
0146 C. Set EIS TI-commands  
0147 +. TI 2025-11-01 10:38:50.0  
0148 DC 07-FC EIS_MODE_CHG_ENA  
0149 BC (20)  
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0151 C. *****  
0152 C. EIS END OBSTBL LOAD  
0153 C. *****  
0154 C.  
0155 . C. ***** MDP 'ũÃî=Î»ö¼Ý¤ËÂÐ¤¹¤èDCBC•×²è *****  
0156 C. (%ã°î¥Ö¥Ä¥Ê¥P¥Æ¥ä¥ç¥é¤È½¾¤¤¼Å»Ü¤¹¤é)  
0157 . S. DC-BC dcbc-402:DCBC  
0158 (MDP_known_event)  
0159 C.  
0160 C.  
0161 . C. ***** ¥Ð¥¹•İ Daily±¿ÍÑ¤È'Ø¤¹¤èDCBC•×²è *****  
0162 . S. DC-BC dcbc-153:DCBC  
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0164 C.  
0165 C.  
0166 . C. ;ãLOS¥Á¥$¥Ä¥£~¼Å»Ü;ä  
0167 C.  
0168 . C. ***** LOS *****  
0169 C.
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main-610 2025-11-01 11:32:37 98 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë²ç²Å²•µ°E»Í×ÀÇ²ÎŲçŲÄŲ×Ųí;¼ŲÈ;ËÈè½µ•îİÊ;ËÈÈ¼°CÔ²•²ç¾¹¹ Ç²İ;çÀ®,ù²¹²ë²²²ÇÄ÷ç®•²È²²²³²È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG_____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR _____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 08 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 +. DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 +. DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 +. DC 07-F0 MDP_XRT_AEC_RESET
0063 BC (d0)
0064 +. DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 09)
0068 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 0e)
0070 . C. ----- Success Verify ? OK / NG _____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2025-11-01 10:38:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG _____
0081 C.
0082 C. ***** XRT END *****
0083 C.
0084 . C. ***** MDP ´ûÄÎ²İ»ö¾Ý²ÈÄ²²¹²ëDCBC•x²è *****
0085 C. (¾â°ìŲÓŲÄŲÈŲÐŲÈŲÄŲçŲé²È¾²¾¼Ä»Ü²¹²ë)
0086 . S. DC-BC dcbc-402:DCBC
0087 (MDP_known_event)
0088 C.
0089 C.
0090 . C. ***** ŲÐŲ!•İ Daily±çİÑ²È´Ø²¹²ëDCBC•x²è *****
0091 . S. DC-BC dcbc-153:DCBC
0092 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0093 C.
0094 C.
0095 . C. ;ãLOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
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0096 C.
0097 . C. ***** LOS *****
0098 C.

Nov 01, 25 11:32

XRT_OGLIST_0936.chk

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*** OP Sequence for XRT ***

2025/11/01	10:48:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	10:48:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	10:48:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/01	10:49:00.0	AOCs_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 03 be 01 f3
2025/11/01	10:49:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/01	10:49:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/01	10:49:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/01	10:49:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/01	10:49:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	10:51:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2025/11/01	10:51:58.0	XRT_FL_PROG_SET_418_OG [0x1a2]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2025/11/01	10:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	15:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	15:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/01	15:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/01	15:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/01	15:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/01	15:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/01	15:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	15:32:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/11/01	15:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/11/01	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	15:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	15:40:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	15:40:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	15:40:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/01	15:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/01	15:50:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/01	15:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	17:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	17:14:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	17:14:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	17:14:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/01	17:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/01	17:38:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/01	17:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	17:53:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	17:53:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/01	17:54:00.0	AOCs_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/11/01	17:54:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/01	17:54:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/01	17:54:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/01	17:56:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2025/11/01	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]			

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2025/11/01	18:03:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	18:03:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	18:04:00.0	AOCs_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/01	18:04:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 03 be 01 f3
2025/11/01	18:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/01	18:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/01	18:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/01	18:04:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/01	18:06:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	18:06:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/11/01	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/11/01	18:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	18:51:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	18:51:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	18:51:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	18:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/01	19:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/01	19:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/01	20:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	20:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	20:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	20:27:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	20:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/01	20:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/01	20:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/01	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	22:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	22:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	22:04:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	22:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/01	22:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/01	22:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/01	23:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/01	23:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	23:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/01	23:42:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/01	23:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/02	00:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/02	00:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/02	01:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/02	01:18:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/02	01:18:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/02	01:18:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/02	01:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/02	01:26:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/02	01:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/02	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/02	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/02	01:59:58.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/11/02	02:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_RECAL	2	07-F8	78	00
		AOCU_NM		5	02-76	00	00 00 00 00
2025/11/02	02:03:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/11/02	02:04:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/02	02:04:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/02	02:04:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/02	02:04:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/02	02:04:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/02	02:06:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2025/11/02	02:06:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e
2025/11/02	02:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/02	02:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	02:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	02:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/02	02:42:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/02	02:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/02	03:01:00.0	XRT_Custom_430_OG [0x1ae]					
2025/11/02	03:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/02	04:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	04:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	04:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/02	04:17:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/02	04:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/02	04:38:00.0	XRT_Custom_430_OG [0x1ae]					
2025/11/02	04:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/02	05:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	05:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	05:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/02	05:47:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/02	05:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/02	06:15:00.0	XRT_Custom_430_OG [0x1ae]					
2025/11/02	06:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/02	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	06:23:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/02	06:23:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2025/11/02	06:24:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/11/02	06:24:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/11/02	06:24:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/02	06:26:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2025/11/02	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/02	06:34:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	03 be 01 f3
2025/11/02	06:35:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_405_OG [0x195]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2025/11/02	18:09:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2025/11/02	18:19:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	03 be 01 f3
2025/11/03	17:53:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2025/11/03	18:03:30.5	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	03 be 01 f3
2025/11/04	11:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/04	11:04:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00