

Period: 2025/11/04 10:54:00 - 2025/11/08 11:24:00

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XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -All/mesh(2048ms) - 1x1, All/Poly(1443ms) - 2x2 - w leak image-1msCCD														
Term		Pointing (x, y)					Comment							
11/05 12:33:00 - 11/05 12:39:54		Fixed (-528.4, 528.4)					Post bakeout Q4							
PROG= 15 1-time(s) Subr= 1 1-time(s) 2.0sec Seqn= 14 1-time(s) 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec Open/thick-Be Open/thick-Be close Safe Dark 1ms Obs 1x1 1024x1024 (1536, 512) Q=98 0 0 2.0sec Subr= 2 1-time(s) 120.0sec														

Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 3 2-time(s) 2.0sec													
Seqn= 34 1-time(s) 60.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
11/05 12:43:00 - 11/05 12:49:54	Fixed (0.0, 0.0)	Post bakeout synoptics
11/05 18:10:36 - 11/05 18:17:24	Fixed (0.0, 0.0)	synoptic, shifted 7.5 min
11/06 05:54:00 - 11/06 06:00:54	Fixed (0.0, 0.0)	synoptic, shifted -9.0 min + HOP349

PROG= 12 1-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 5 1-time(s) 2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 55 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 98 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 76 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23 1-time(s) 4.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 1-time(s) 2.0sec													
Seqn= 41 1-time(s) 2.0sec													
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 18 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 86 1-time(s) 2.0sec													
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 54 1-time(s) 2.0sec													
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/05 12:53:00 - 11/05 17:44:00	Track (-663.0, 313.9) @ 11/05 12:50:00	AR at NE limb
11/05 22:58:00 - 11/06 01:59:54	Track (-605.2, 310.8) @ 11/05 22:30:00	AR at EN limb
11/06 06:04:00 - 11/06 09:37:00	Track (-556.9, 308.6) @ 11/06 06:01:00	# AR at NE limb

PROG= 09 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 92 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2 4-time(s) 2.0sec													
Seqn= 47 1-time(s) 2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 77 4-time(s) 300.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec	
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1C8E: Alignment with East limb Al/poly 1443ms Q95 2x2 (G-band and VLS=CLS) - 8 min cad																
Term		Pointing (x, y)						Comment								
11/05 20:45:00 - 11/05 22:29:54		Fixed (-970.0, 0.0)						Co-alignment (E-limb)								
PROG= 20		1-time(s)														
┌	Subr= 1	1-time(s)				2.0sec										
	┌	Seqn= 23	1-time(s)				2.0sec									
	┌	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=90	0	0	2.0sec	
	┌	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)		Q=95	0	0	2.0sec	
┌	Subr= 2	15-time(s)				480.0sec										
	┌	Seqn= 70	1-time(s)				2.0sec									
┌		Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	1024x1024 (1536, 1536)		Q=95	0	0	2.0sec	
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval		

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-AI,thick-Be -384x384 + AI-poly 512x512 2x2) + GL													
Term		Pointing (x, y)						Comment					
11/05 12:53:00 - 11/05 17:44:00		Track (-663.0, 313.9) @ 11/05 12:50:00						AR at NE limb					
11/05 22:58:00 - 11/06 01:59:54		Track (-605.2, 310.8) @ 11/05 22:30:00						AR at EN limb					
11/06 02:03:00 - 11/06 05:50:54		Fixed (0.0, 0.0)						synoptic, shifted -9.0 min + HOP349					
11/06 06:04:00 - 11/06 09:37:00		Track (-556.9, 308.6) @ 11/06 06:01:00						# AR at NE limb					
PROG= 14 30-time(s) Subr= 1 1-time(s) 2.0sec Seqn= 68 1-time(s) 2.0sec Open/G-band Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1024,1024) Q=95 0 0 2.0sec Subr= 2 20-time(s) 2.0sec Seqn= 11 1-time(s) 2.0sec													

└─┐	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─┐	Seqn= 73 1-time(s) 10.0sec												
└─┐	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Subr= 3 1-time(s) 2.0sec												
└─┐	Seqn= 10 1-time(s) 2.0sec												
└─┐	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Seqn= 11 1-time(s) 2.0sec												
└─┐	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─┐	Seqn= 87 1-time(s) 2.0sec												
└─┐	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─┐	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─┐	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─┐	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

* * * * *

NOT USED

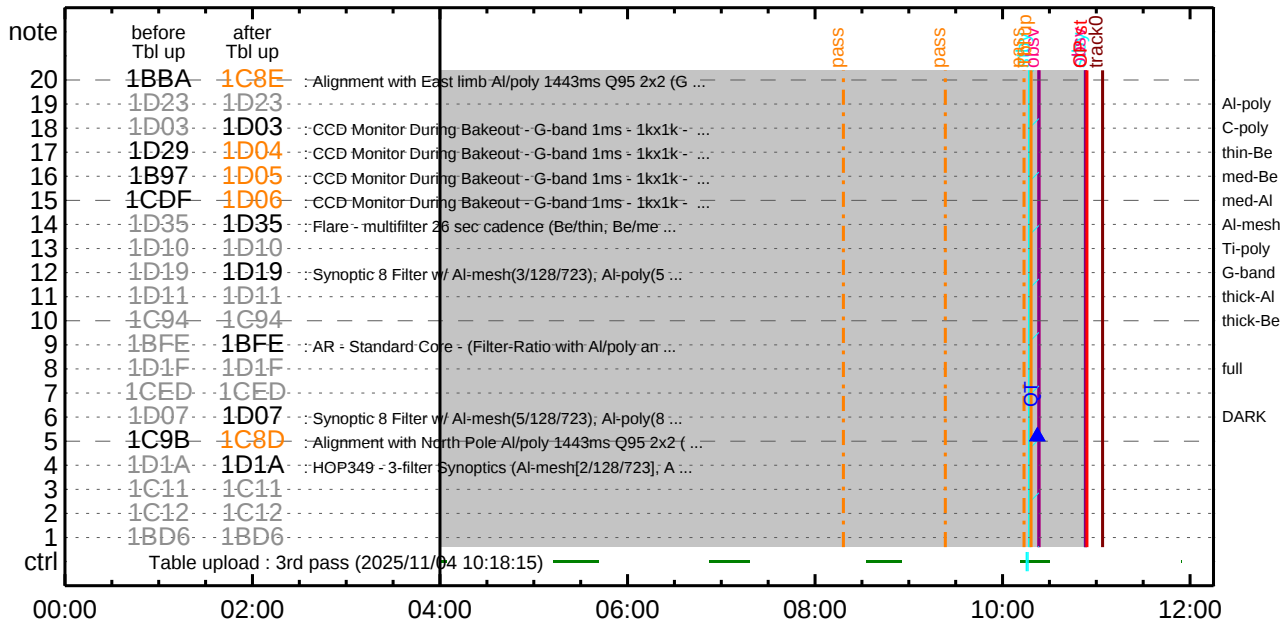
* * * * *

Flare Detection

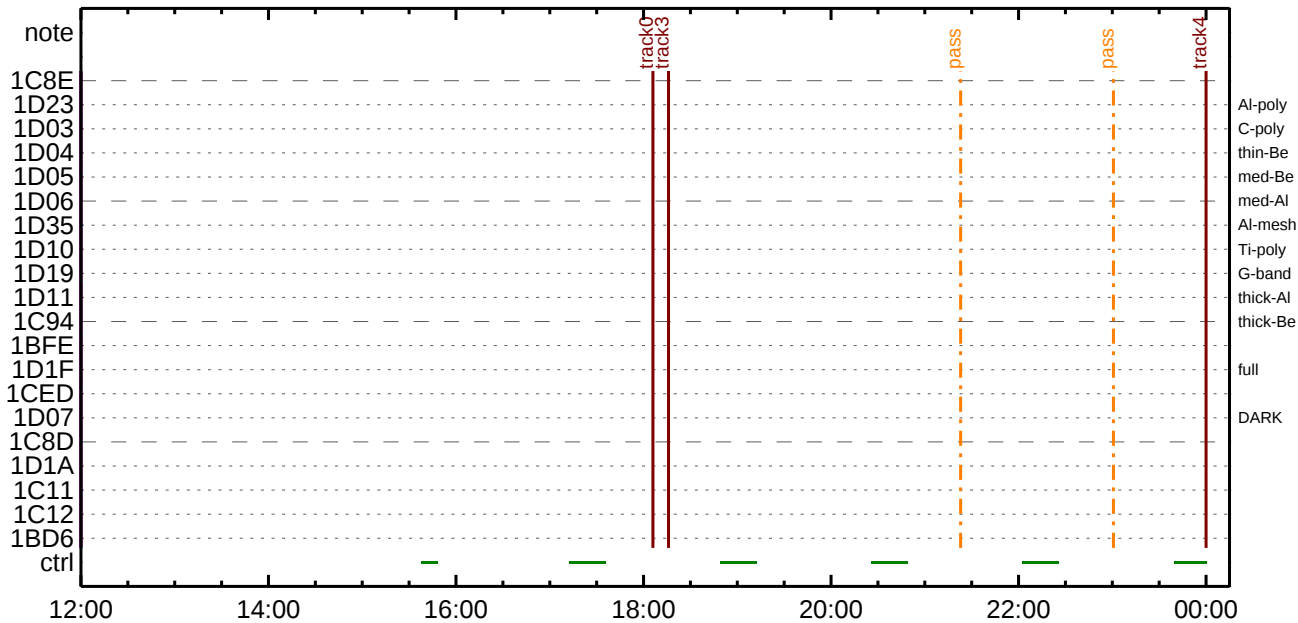
* * * * *

FLD Patrol													
Term			Pointing (x, y)					Comment					
11/05 12:50:18 - 11/05 18:07:53			Track (-663.0, 313.9) @ 11/05 12:50:00					AR at NE limb					
11/05 22:30:18 - 11/06 05:51:18			Track (-605.2, 310.8) @ 11/05 22:30:00					AR at EN limb					
11/06 06:01:18 - 11/08 11:24:00			Track (-556.9, 308.6) @ 11/06 06:01:00					# AR at NE limb					
Al-poly/Open			Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50			
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

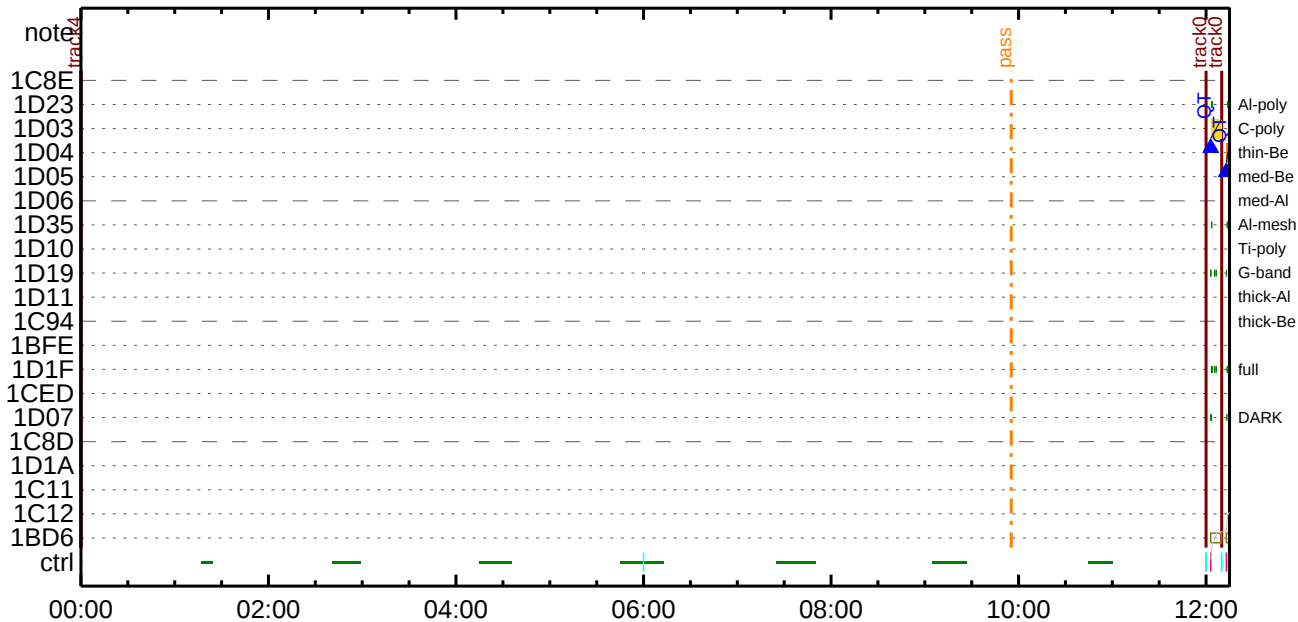
CMDI #0940 2025/11/04



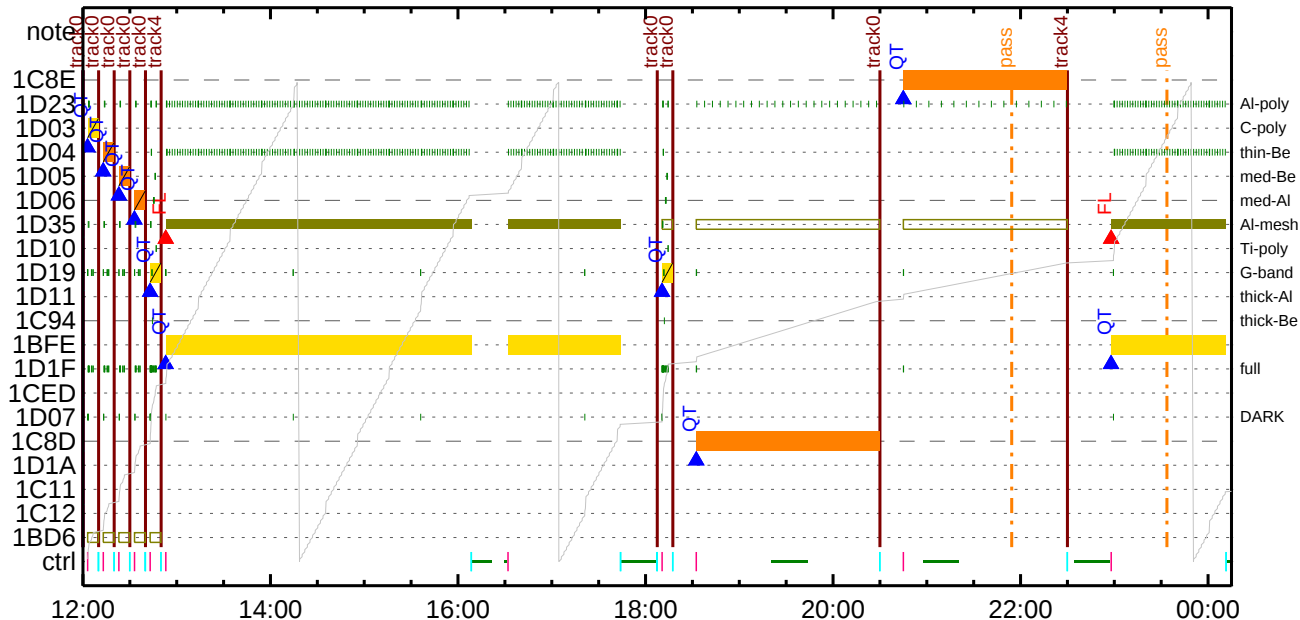
CMDI #0940 2025/11/04



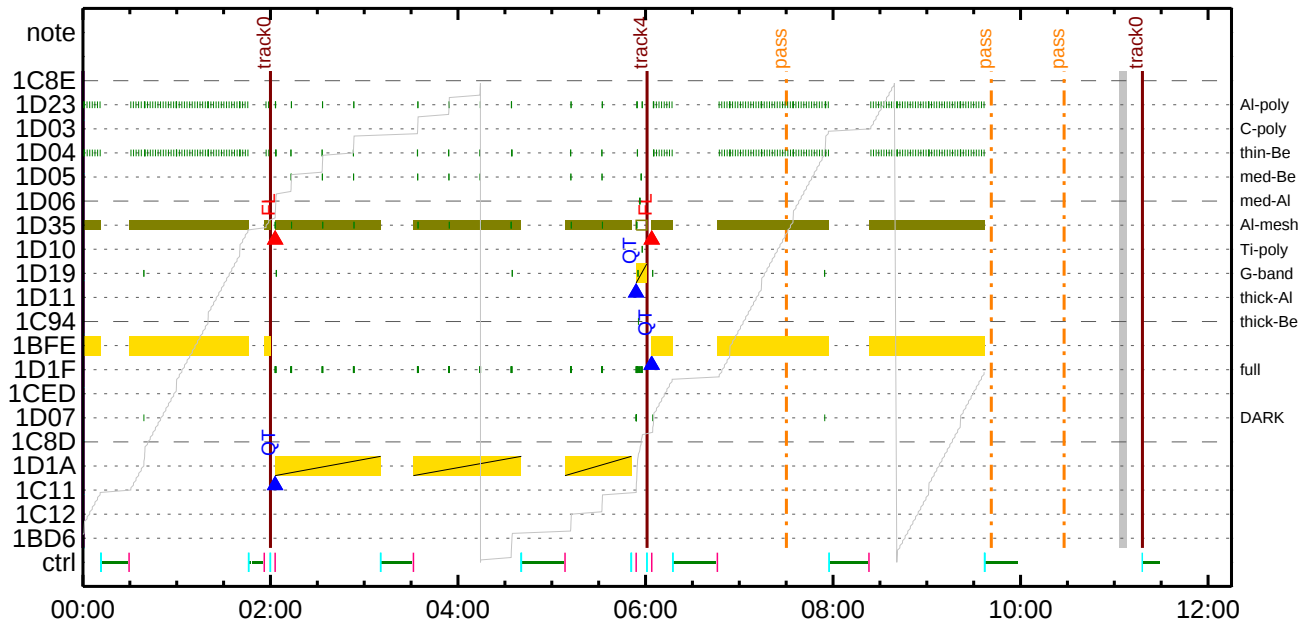
CMDI #0940 2025/11/05



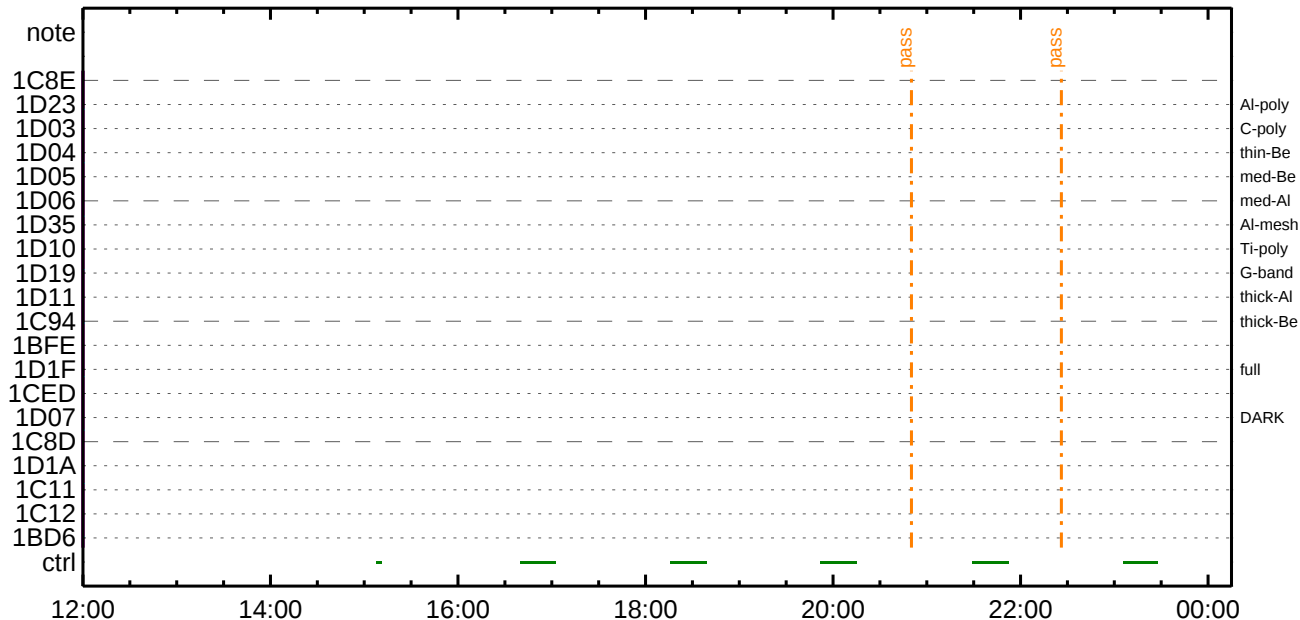
CMDI #0940 2025/11/05



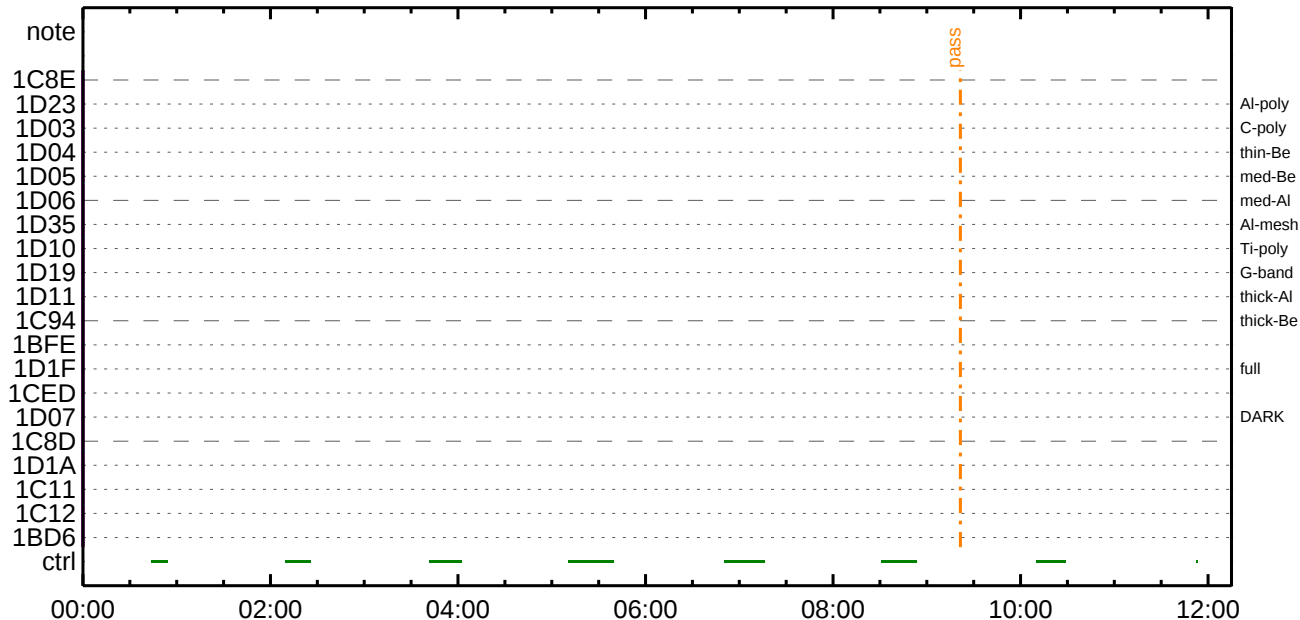
CMDI #0940 2025/11/06



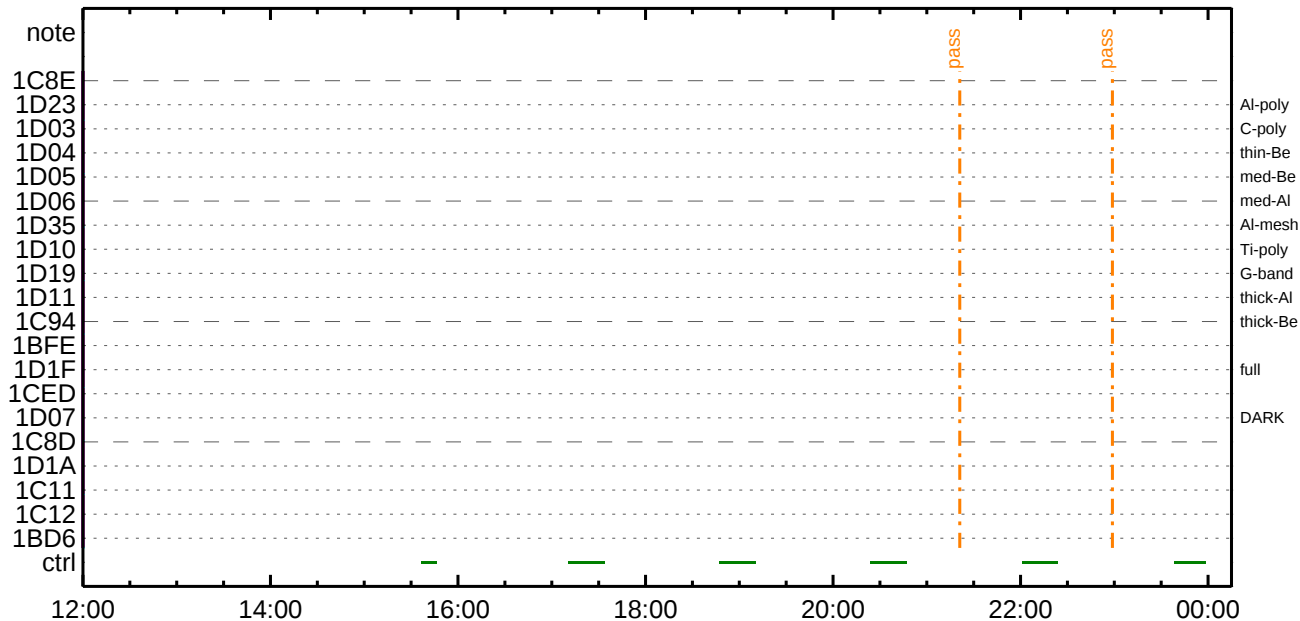
CMDI #0940 2025/11/06



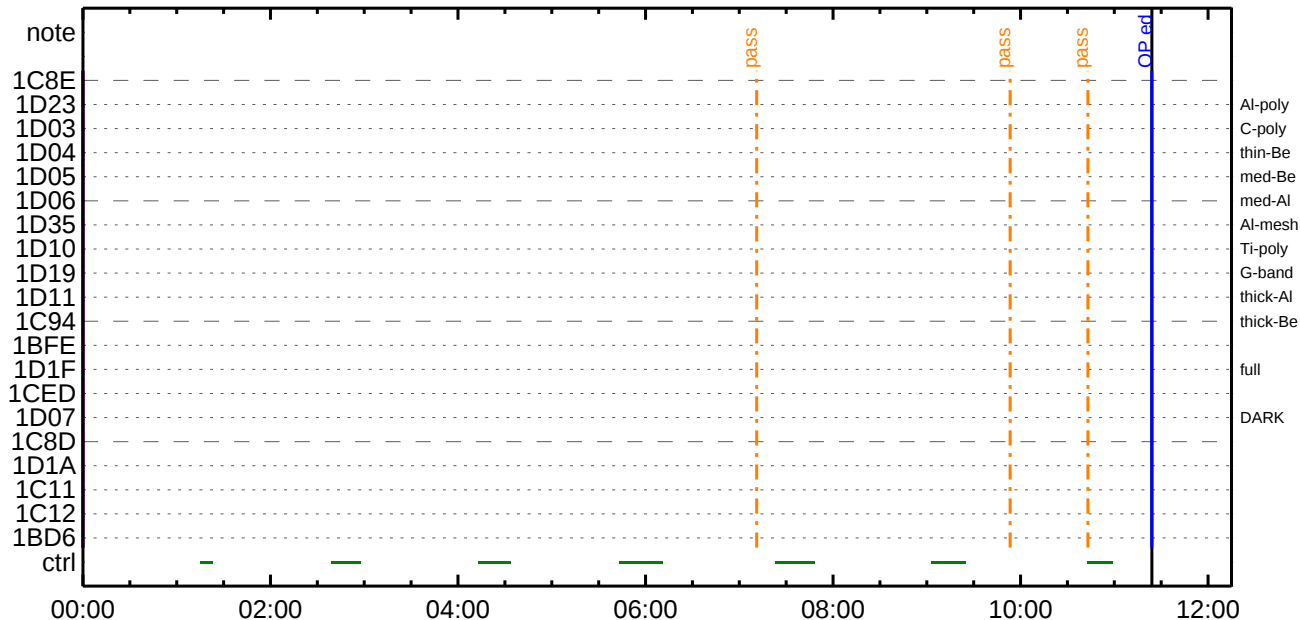
CMDI #0940 2025/11/07



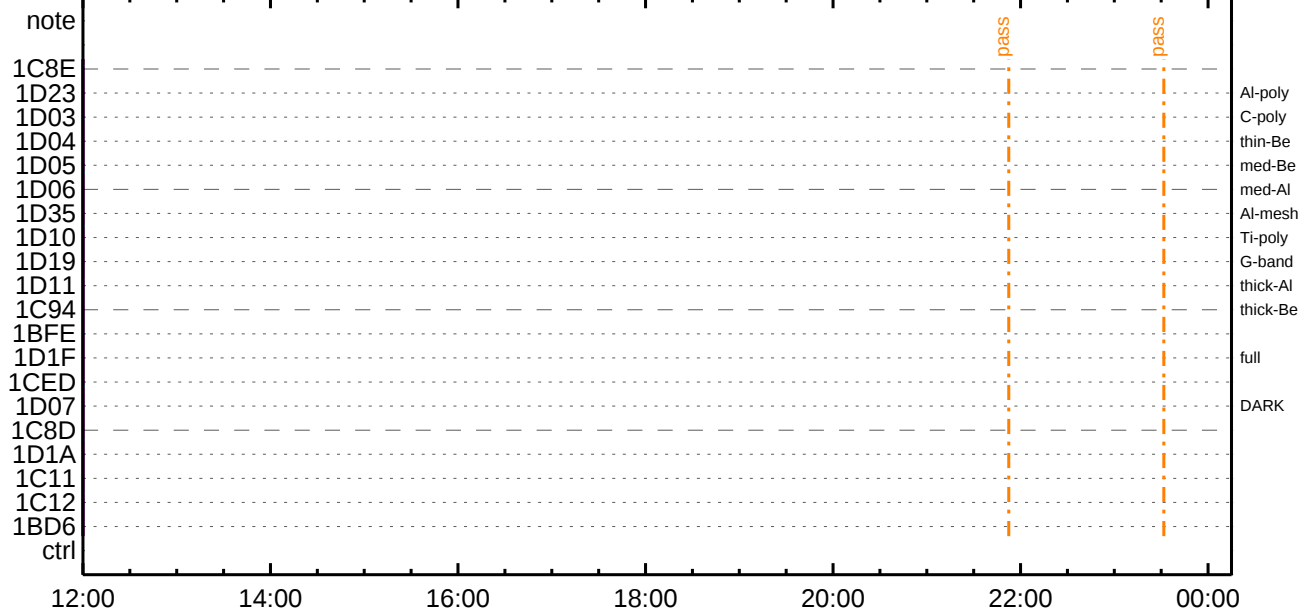
CMDI #0940 2025/11/07



CMDI #0940 2025/11/08



CMDI #0940 2025/11/08



```
main-613 2025-11-04 11:30:27 194 33 SOLAR-B MAIN //
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main-613 2025-11-04 11:30:27 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲĂŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲĂŲß;¼Ų³ŲPŲóŲĖÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āĩ;ĖñçġÄñ•µ°Ė»Ī×ĀÇñĪŲçŲĂŲ×Ųĩ;¼ŲĖ;ĖĖĖñµ•ĩĪĖ;ĖñĖ¼°ÇŌñ•ñç¼ĩ¹çñĪ;çÄ®, ùñ¹ñĖñPñÇÄ÷ç®ñ•ñĖñññ³ñĖ;Ė
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŲĩ;¼ŲĖ;|ŲĂŲóŲ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŲĩ;¼ŲĖ;ä
0019 . S. OP op-613:OP
0020 ()
0021 . S. OG og-613:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPĪĪ°ĖŲĂŲóŲ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŲĂŲóŲ×½ªĪ»ñò³ĪÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñĪ¼Ė¹ç•Ė²ĪOKñò³ĪÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŲĂŲóŲ×½ªĪ»ñò³ĪÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñĪ¼Ė¹ç•Ė²ĪOKñò³ĪÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŲĂŲóŲ×½ªĪ»ñò³ĪÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñĪ¼Ė¹ç•Ė²ĪOKñò³ĪÇ$
0081 C.
0082 . C. ***** °Ė²¼ñĪ¼Ā´ĪĀ°ñĖĖĖñ°Ā÷ç® (¼Āµ-ŲĂŲóŲ×¼Ė½çñòĀŌĀæñÇ½ªñ•ñĖ¼ĩ¹çñÇñâ) *****
0083 C. DHUŲä;¼ŲĖ;Ėñ¼Ų½,Ųĩ;¼ŲĖ;ĖñòĪäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñŲĀ÷ç®NGñĪ¼ĩ¹ç;ç°Ė²¼ñĪĪTI-CMDÄ÷ç®ñĪ¼Ā¹Ōñ•ñĖñññ³ñĖ;Ė
```

0096	C.	SET EDUMP I± iYŶY¹ QÇ¹ Ô± ; ±³ ±Ē ; Ē		
0097	C.			
0098	C.	TIY³YDYOYÉ±ðĀDİ; (UT)		
0099	+. TI	2025-11-04 10:49:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+. TI	2025-11-04 10:49:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+. TI	2025-11-04 10:49:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+. TI	2025-11-04 10:53:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ē²¼± ĨĀē%ĩĩÑ± ĨYĀY\$YĀY¹ ãiŪ		
0116	C.	ÇÇ [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	4
0118	C.	ÇÇ [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	ÇÇ [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°ēYĀYOY×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	ÇÇ [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	ÇÇ [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	ÇÇ [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	ÇÇ [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	ÇÇ [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+. DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	7
0136	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	YĀYOY×½ª Ĩ»±ð³ ĨÇ\$		
0142	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL± Ĩ¼Ē¹ Ç•ē² İOK±ð³ ĨÇ\$		
0145	C.			
0146	C.	DHUYā;¼YĒ;Ē¼Y½,Yĩ;¼YĒ;Ē±ðİā±¹		
0147	+. DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	2
0150	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+. TI	2025-11-04 10:53:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+. TI	2025-11-04 10:53:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+. TI	2025-11-04 10:53:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP 'ĠĀİ± Ĩ»ō¼Y±ĒĀD±¹ ±ēDCBC•×²ē *****		
0181	C.	(¼Ā°İYŌYĀYĒYDYEYĀYÇYē±Ē½¼±¼Ā»Ū±¹ ē)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** YDY¹•İ Daily± ĨİÑ±Ē 'Ø±¹ ±ēDCBC•×²ē *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	C.			


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main-614 2025-11-04 11:30:27 178 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŷÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸÐŸóŸÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŸÇŸ×Ÿí;¼ŸÉ;ËÈè²µ•íîÊ;ËÈÈ¼°ÇÔñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Òðò¹íî,ñ•;ÇÉÔÍ×ñÈXÂÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŸÐŸóŸÉŸíŸÄŸŸ~¾ÔÖñ¬°ÄÄêñ•ñççé;Ç°Ê²¼ñî°EÄ,¼ê½Çñðò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0045 C.
0046 . C. ;ãŸÇŸóŸEŸEÄÚÂØ;ÊÄ•Ä°²óÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ã;Ç°Ê²¼ñðò¼Ä¹Ôñ¹ñè;£
0055 C. ŸÇŸóŸEŸEÄÚÂØñäÄ•Ä°²óÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Ôññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0071 C.
0072 . C. ;ãŸÇŸóŸEŸEÄÚÂØ;ÊÄ•Ä°²óÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;ÇXÁ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1 XPA ON/OFF] EO OFF
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 6729914.1 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 38s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ()
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2025-11-04 10:53:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0165 C. (¾å°î¥Ö¥Å¥Ê¥Ð¥Ê¥Å¥Ç¥ÊñÊ¼¾ñ¼Å»Û¹ñë)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ¥Ð¥¹•İ Daily±¿ÍÑñÊ'Øñ¹ñëDCBC•×²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOS¥Á¥S¥Ã¥¹¼Å»Û;ä
0176 C.
0177 . C. ***** LOS *****
0178 C.

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[illegible]

0096 C.
0097 . C. ;ãLOS¥Á¥\$¥Ã¥~¼Ã»Ü;ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

Nov 04, 25 11:30

XRT_OGLIST_0940.chk

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*** OP Sequence for XRT ***

2025/11/04	11:04:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76 00 b3 bf 01 f3	
2025/11/04	18:06:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00 00	
2025/11/04	18:16:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76 03 00 00 00 00	
2025/11/05	00:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76 04 03 be 01 f3	
2025/11/05	06:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	06:00:02.0	XRT_TCIB_XRT_S_HTR_A_DIS_445_OG [0x1bd]			
		TCIB_XRT_S_HTR_A_DIS	0	04-C0	
2025/11/05	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	11:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	11:59:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/11/05	12:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76 00 2e f9 2e f9	
2025/11/05	12:02:52.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2025/11/05	12:02:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2025/11/05	12:02:56.0	XRT_FLD_DIS_432_OG [0x1b0]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2025/11/05	12:02:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 12	
2025/11/05	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/11/05	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:09:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/11/05	12:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]			
		AOCU_NM	5	02-76 00 2e f9 d1 07	
2025/11/05	12:12:52.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2025/11/05	12:12:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2025/11/05	12:12:56.0	XRT_FLD_DIS_432_OG [0x1b0]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2025/11/05	12:12:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 11	
2025/11/05	12:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/11/05	12:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:19:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:19:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/11/05	12:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]			
		AOCU_NM	5	02-76 00 d1 07 d1 07	
2025/11/05	12:22:52.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2025/11/05	12:22:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2025/11/05	12:22:56.0	XRT_FLD_DIS_432_OG [0x1b0]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2025/11/05	12:22:58.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 10	
2025/11/05	12:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/11/05	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:29:58.0	XRT_FOCUS_POSITION_442_OG [0x1ba]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/11/05	12:30:00.0	AOCS_ORe-point_Start_8_OG [0x09e]			
		AOCU_NM	5	02-76 00 d1 07 2e f9	
2025/11/05	12:32:52.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0 d5	
2025/11/05	12:32:54.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9	
2025/11/05	12:32:56.0	XRT_FLD_DIS_432_OG [0x1b0]			
		MDP_XRT_FLD_DIS	1	07-F0 d9	
2025/11/05	12:32:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0f	
2025/11/05	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2025/11/05	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:39:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2025/11/05	12:39:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00	
2025/11/05	12:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]			

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2025/11/05	12:40:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2025/11/05	12:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2025/11/05	12:40:22.0	XRT_ARS_DIS_435_OG [0x1b3]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2025/11/05	12:42:58.0	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2025/11/05	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2025/11/05	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	12:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]								
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2025/11/05	12:50:00.0	AOCs_ORe-point_Start_4_OG [0x09a]								
		AOCU_NM		5	02-76	04	03	be	01	f3
2025/11/05	12:50:18.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2025/11/05	12:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2025/11/05	12:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]								
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2025/11/05	12:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2025/11/05	12:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2025/11/05	12:52:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	09			
2025/11/05	12:52:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]								
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0e			
2025/11/05	12:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2025/11/05	16:08:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	16:08:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	16:08:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2025/11/05	16:08:36.0	XRT_PREFLR_STRT_404_OG [0x194]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2025/11/05	16:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2025/11/05	16:31:00.0	XRT_Custom_430_OG [0x1ae]								
2025/11/05	16:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2025/11/05	17:44:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	17:44:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	17:44:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET		1	07-F0	da				
2025/11/05	17:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]								
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2025/11/05	17:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2025/11/05	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	18:07:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	18:07:28.0	XRT_ROI_A_417_OG [0x1a1]								
		MDP_XRT_ROI_SET		6	07-F0	cd	06	80	80	20
		MDP_XRT_ROI_SET		6	07-F0	cd	07	80	80	20
		MDP_XRT_ROI_SET		6	07-F0	cd	08	80	80	20
		MDP_XRT_ROI_SET		6	07-F0	cd	09	a0	80	18
		MDP_XRT_ROI_SET		6	07-F0	cd	0a	80	80	08
		MDP_XRT_ROI_SET		6	07-F0	cd	0d	85	83	06
		MDP_XRT_ROI_SET		6	07-F0	cd	0e	80	60	20
		MDP_XRT_ROI_SET		6	07-F0	cd	0f	80	80	06
2025/11/05	18:07:28.5	XRT_ROI_B_441_OG [0x1b9]								
		MDP_XRT_ROI_SET		6	07-F0	cd	0f	80	80	06
		MDP_XRT_ROI_SET		6	07-F0	cd	10	80	80	08
2025/11/05	18:07:30.0	AOCs_ORe-point_Start_2_OG [0x098]								
		AOCU_NM		5	02-76	00	00	00	00	00
2025/11/05	18:07:33.5	XRT_FOCUS_POSITION_406_OG [0x196]								
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2025/11/05	18:07:53.5	XRT_FLD_DIS_409_OG [0x199]								
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2025/11/05	18:07:55.5	XRT_FLRCTRL_DIS_413_OG [0x19d]								
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2025/11/05	18:07:57.5	XRT_ARS_DIS_435_OG [0x1b3]								
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2025/11/05	18:10:33.5	XRT_QT_PROG_SET_410_OG [0x19a]								
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	0c			
2025/11/05	18:10:36.5	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2025/11/05	18:17:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	18:17:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2025/11/05	18:17:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]								

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2025/11/05	18:17:30.0	AOCs_ORe-point_Start_9_OG [0x09f]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	00	ad	59	00 00
2025/11/05	18:17:48.0	XRT_FLD_DIS_421_OG [0x1a5]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2025/11/05	18:32:24.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2025/11/05	18:32:26.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/05	18:32:28.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2025/11/05	18:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/05	20:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/05	20:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/05	20:29:58.0	XRT_FOCUS_POSITION_447_OG [0x1bf]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/11/05	20:30:00.0	AOCs_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	00	00	56 35
2025/11/05	20:30:18.0	XRT_FLD_DIS_421_OG [0x1a5]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2025/11/05	20:44:54.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/05	20:44:56.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2025/11/05	20:44:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2025/11/05	20:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/05	22:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/05	22:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/05	22:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/11/05	22:30:00.0	AOCs_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04	03	be	01 f3
2025/11/05	22:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/11/05	22:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/11/05	22:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/11/05	22:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/05	22:30:26.0	XRT_FLD_RESET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/05	22:57:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2025/11/05	22:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/11/05	22:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/06	00:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/06	00:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/06	00:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/06	00:11:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/06	00:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/11/06	00:28:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/06	00:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/06	01:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/06	01:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/06	01:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/06	01:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/06	01:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/11/06	01:55:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/06	01:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/06	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/06	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/06	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2025/11/06	02:00:00.0	AOCs_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00 00
2025/11/06	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/11/06	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			

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2025/11/06	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/06	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/06	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/06	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2025/11/06	02:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/11/06	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/06	03:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	03:10:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	03:10:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/06	03:10:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/06	03:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/06	03:30:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/06	03:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/06	04:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	04:40:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	04:40:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/06	04:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/06	04:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/06	05:07:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/06	05:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/06	05:50:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	05:50:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	05:50:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/06	05:51:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/06	05:51:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/06	05:51:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/06	05:53:58.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/11/06	05:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/06	06:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	06:00:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	06:00:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/06	06:01:00.0	AOCs_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	04 03 be 01 f3
2025/11/06	06:01:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/06	06:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/06	06:01:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/06	06:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/06	06:01:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/06	06:03:56.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2025/11/06	06:03:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/11/06	06:04:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/06	06:17:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	06:17:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/06	06:17:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/06	06:17:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/06	06:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/06	06:45:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/06	06:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/06	07:57:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

XRT_OGLIST_0940.chk

[illegible]