

XRT Timeline to be uploaded on 2025/11/13

Period: 2025/11/13 10:49:00 - 2025/11/18 11:00:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with														
Term		Pointing (x, y)				Comment								
11/13 11:02:00 - 11/13 11:54:54		Track (713.5, 333.2) @ 11/13 10:59:00				# OP start + 10min, AR Obs.								
11/13 13:58:00 - 11/13 18:00:24		Track (726.8, 334.4) @ 11/13 13:55:00				AR Obs.								
11/13 18:13:30 - 11/14 03:59:54		Track (745.1, 336.1) @ 11/13 18:10:30				AR Obs.								
11/14 06:29:30 - 11/14 10:38:30		Track (790.9, 341.2) @ 11/14 06:26:30				AR Obs.								
11/14 16:53:00 - 11/14 17:38:00		Track (821.2, 345.5) @ 11/14 16:50:00				AR Obs.								
11/14 18:14:30 - 11/15 03:59:54		Track (824.6, 346.0) @ 11/14 18:11:30				AR Obs.								
11/15 05:58:00 - 11/15 09:31:00		Track (847.9, 350.8) @ 11/15 05:55:00				AR Obs.								
PROG= 10 Inf.-time(s)														
Subr= 1		1-time(s) 2.0sec												
Seqn= 92		1-time(s) 2.0sec												
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
Subr= 2		5-time(s) 2.0sec												
Seqn= 47		1-time(s) 2.0sec												
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
Seqn= 96		4-time(s) 120.0sec												
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec	
Al-poly/Open		thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
thin-Be/Open		med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1BD6: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak first)															
Term			Pointing (x, y)			Comment									
11/13 11:58:00 - 11/13 13:54:54			Track (-58.6, 408.6) @ 11/13 11:55:00			CH obs.									
PROG= 03 Inf.-time(s)															
Subr= 1		1-time(s)		2.0sec											
Seqn= 30		1-time(s)		2.0sec											
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0	0	2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0	0	2.0sec
Subr= 2		8-time(s)		900.0sec											
Seqn= 8		1-time(s)		2.0sec											
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Seqn= 6		1-time(s)		2.0sec											
Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3	0	2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2	0	2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/32768)														
Term			Pointing (x, y)			Comment								
11/13 18:03:30 - 11/13 18:10:24			Fixed (0.0, 0.0)			synoptic, shifted 0.5 min								
11/14 06:19:30 - 11/14 06:26:24			Fixed (0.0, 0.0)			HOP349, synoptic								
11/14 18:04:30 - 11/14 18:11:24			Fixed (0.0, 0.0)			synoptic, shifted 1.5 min								
11/15 05:48:00 - 11/15 05:54:54			Fixed (0.0, 0.0)			HOP349, synoptic								
PROG= 12 1-time(s)														
└ Subr= 1 1-time(s) 2.0sec														
└ Seqn= 5 1-time(s) 2.0sec														
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec														
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec														
└ Seqn= 55 1-time(s) 2.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Seqn= 98 1-time(s) 2.0sec														
└┐ Al-poly/Open Al-poly/Open close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└┐ Al-poly/Open Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└┐ Al-poly/Open Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Seqn= 76 1-time(s) 2.0sec														
└┐ thin-Be/Open thin-Be/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└┐ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														

thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 41 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 18 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 86 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D1A: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 120min cad) + CME wa

Term	Pointing (x, y)	Comment
11/14 04:03:00 - 11/14 06:16:24	Fixed (0.0, 0.0)	HOP349, synoptic
11/15 04:03:00 - 11/15 05:44:54	Fixed (0.0, 0.0)	HOP349, synoptic

PROG= 04 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
11/14 10:56:01 - 11/14 16:49:54	Fixed (-21.0, -959.0)	HOP81 S-Pole

PROG= 07 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 16 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 90 1-time(s) 30.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 60-time(s) 60.0sec												
Seqn= 24 1-time(s) 30.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
11/13 11:02:00 - 11/13 11:54:54	Track (713.5, 333.2) @ 11/13 10:59:00	# OP start + 10min, AR Obs.
11/13 11:58:00 - 11/13 13:54:54	Track (-58.6, 408.6) @ 11/13 11:55:00	CH obs.

11/13 13:58:00 - 11/13 18:00:24	Track (726.8, 334.4) @ 11/13 13:55:00	AR Obs.
11/13 18:13:30 - 11/14 03:59:54	Track (745.1, 336.1) @ 11/13 18:10:30	AR Obs.
11/14 04:03:00 - 11/14 06:16:24	Fixed (0.0, 0.0)	HOP349, synoptic
11/14 06:29:30 - 11/14 10:38:30	Track (790.9, 341.2) @ 11/14 06:26:30	AR Obs.
11/14 10:56:01 - 11/14 16:49:54	Fixed (-21.0, -959.0)	HOP81 S-Pole
11/14 16:53:00 - 11/14 17:38:00	Track (821.2, 345.5) @ 11/14 16:50:00	AR Obs.
11/14 18:14:30 - 11/15 03:59:54	Track (824.6, 346.0) @ 11/14 18:11:30	AR Obs.
11/15 04:03:00 - 11/15 05:44:54	Fixed (0.0, 0.0)	HOP349, synoptic
11/15 05:58:00 - 11/15 09:31:00	Track (847.9, 350.8) @ 11/15 05:55:00	AR Obs.

PROG= 14 30-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 68 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 20-time(s) 2.0sec												
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 73 1-time(s) 10.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 3 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 87 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

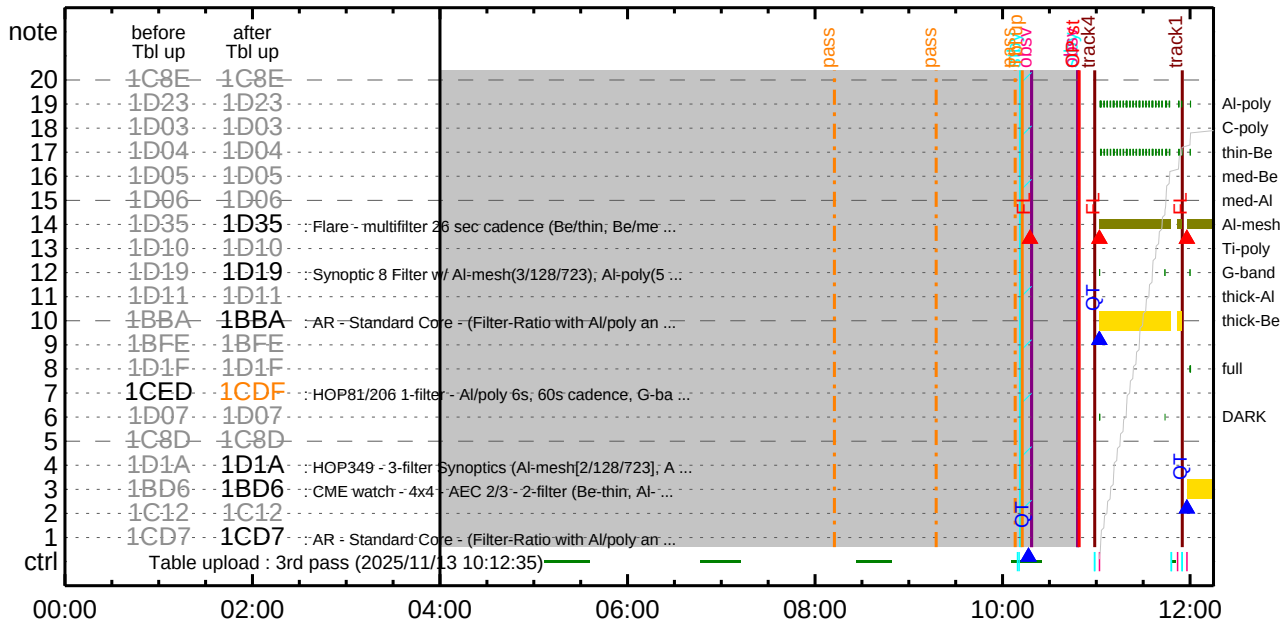
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Flare Detection

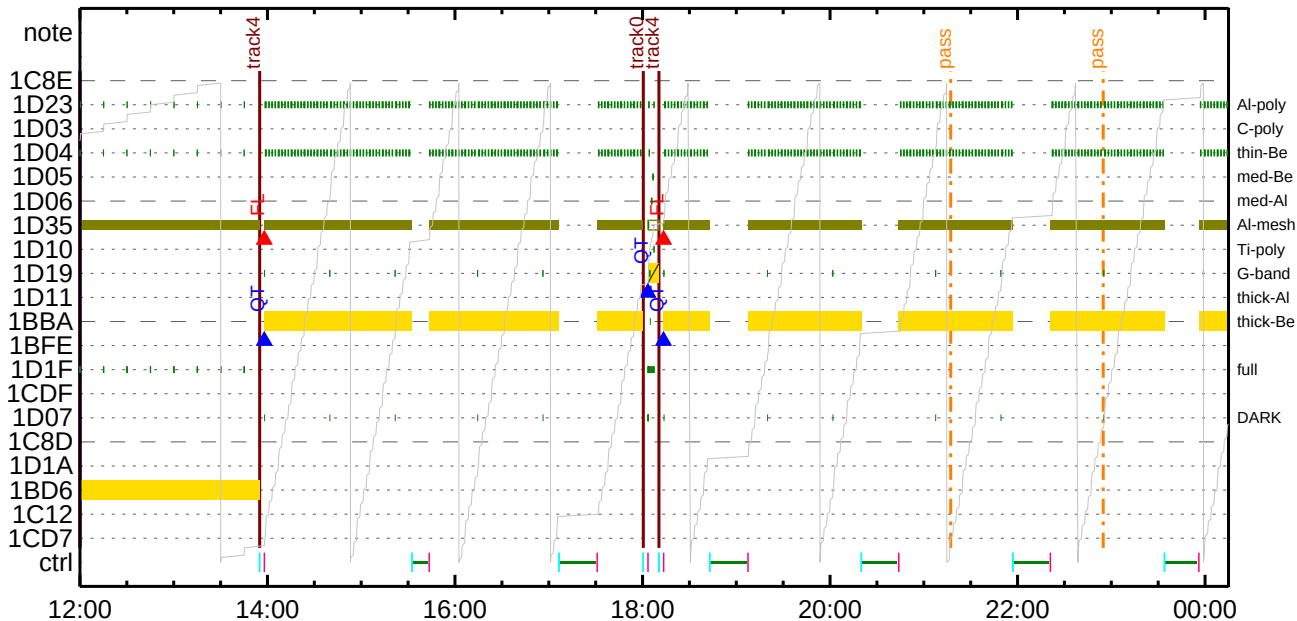
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FLD Patrol												
Term		Pointing (x, y)						Comment				
11/13 10:13:35 - 11/13 18:00:48		cannot be identified										
11/13 18:10:48 - 11/14 06:16:48		Track (745.1, 336.1) @ 11/13 18:10:30					AR Obs.					
11/14 06:26:48 - 11/14 18:01:48		Track (790.9, 341.2) @ 11/14 06:26:30					AR Obs.					
11/14 18:11:48 - 11/15 05:45:18		Track (824.6, 346.0) @ 11/14 18:11:30					AR Obs.					
11/15 05:55:18 - 11/18 11:00:00		Track (847.9, 350.8) @ 11/15 05:55:00					AR Obs.					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

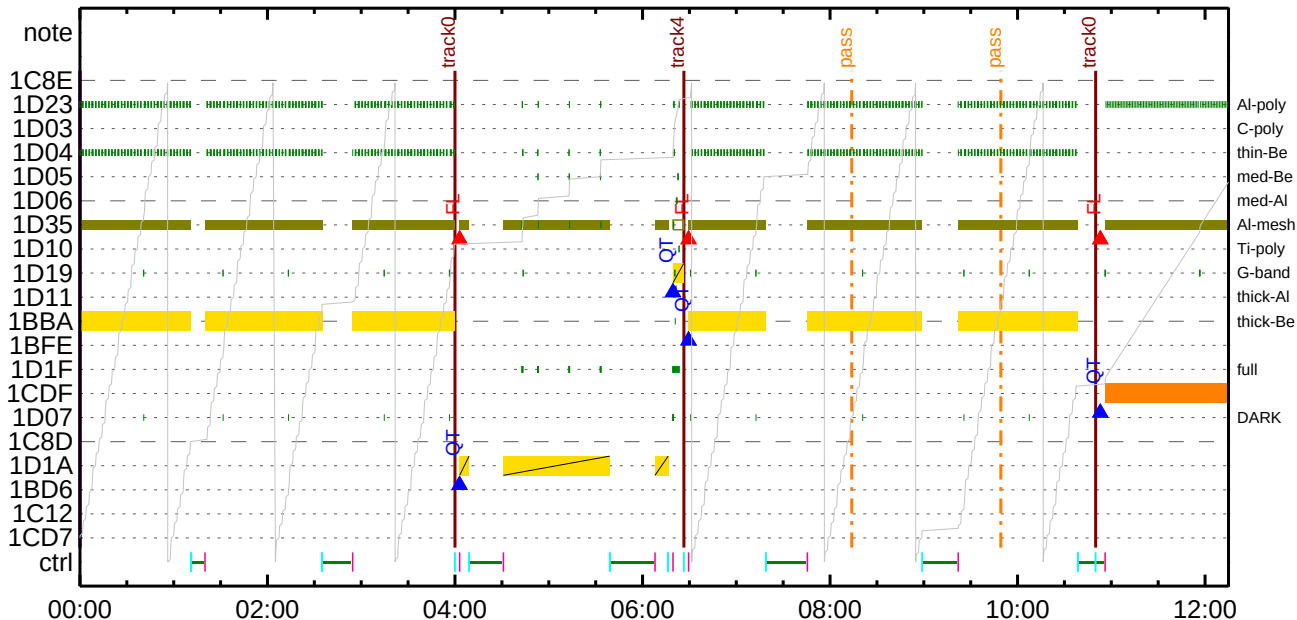
CMDI #0961 2025/11/13



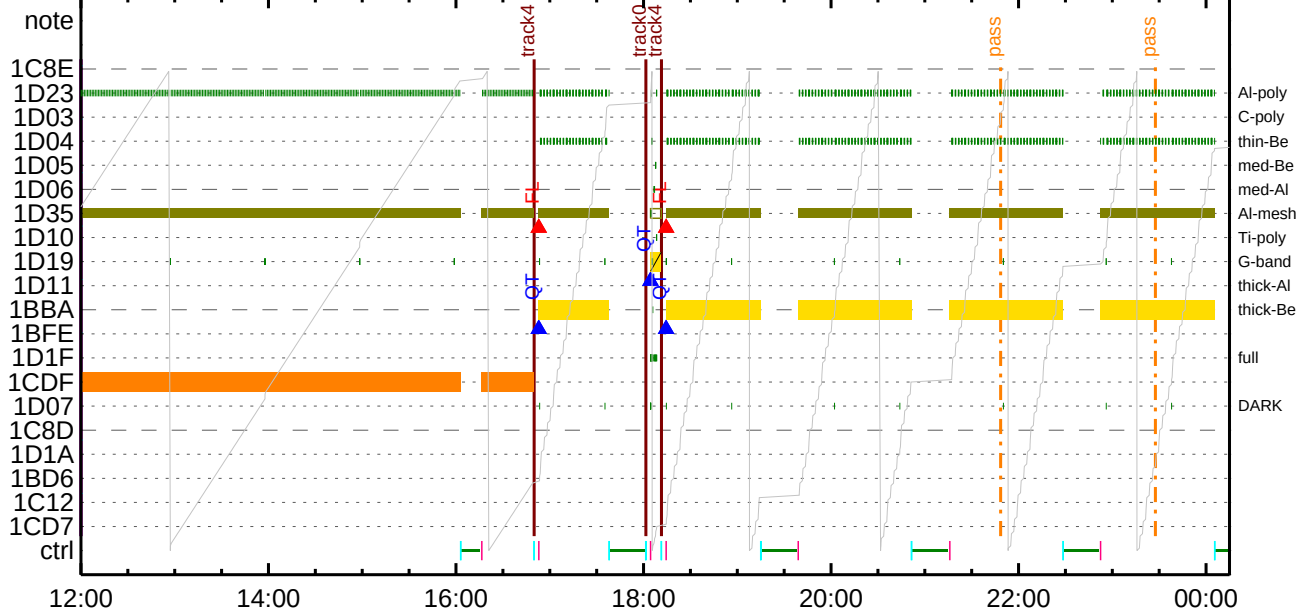
CMDI #0961 2025/11/13



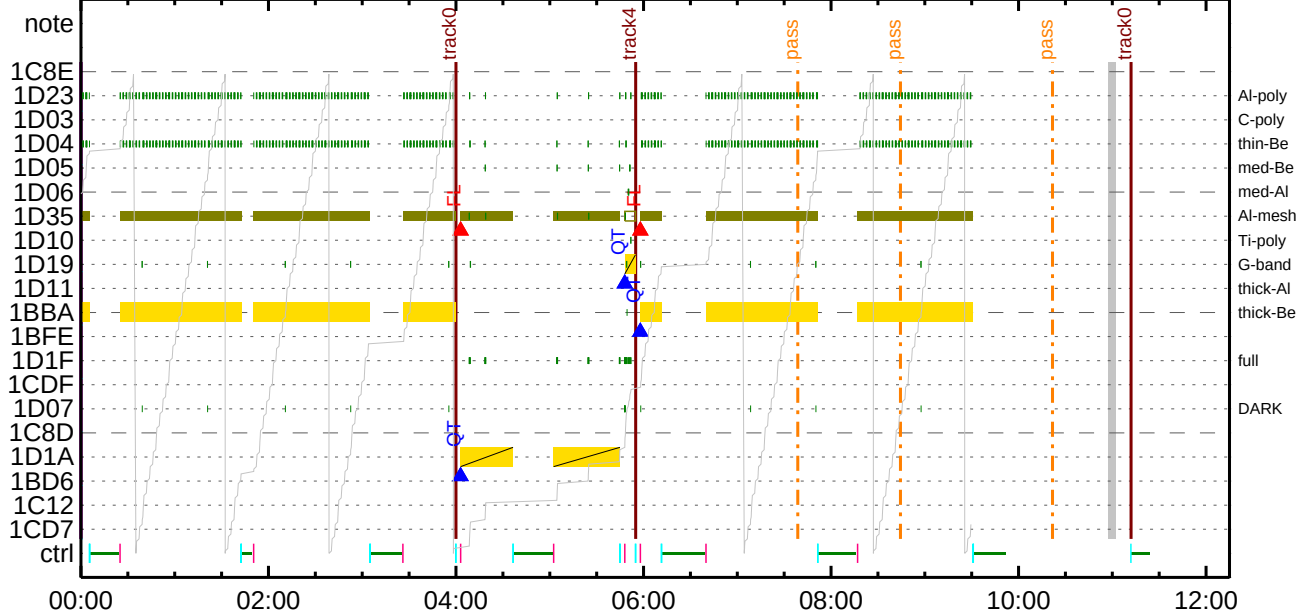
CMDI #0961 2025/11/14



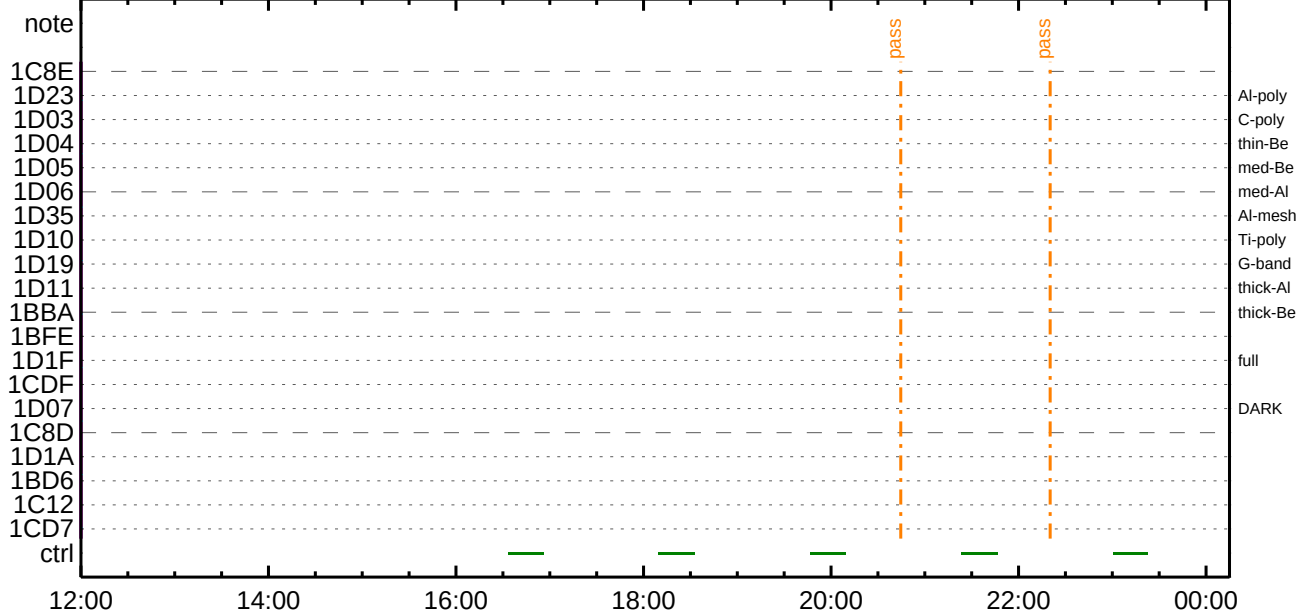
CMDI #0961 2025/11/14



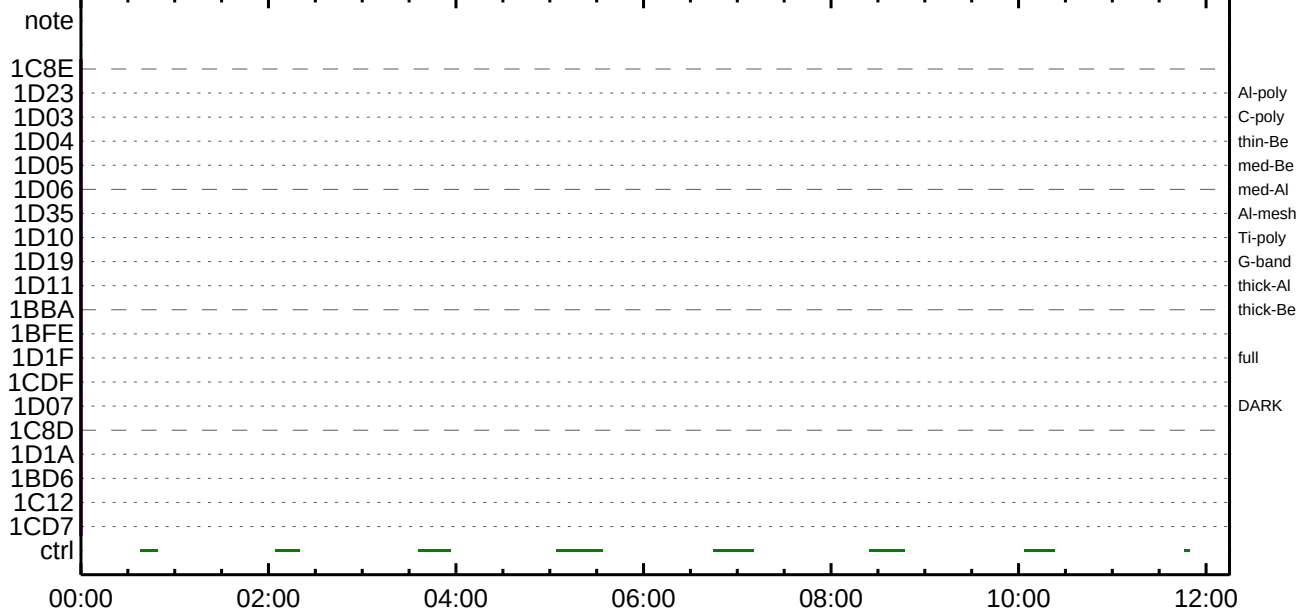
CMDI #0961 2025/11/15



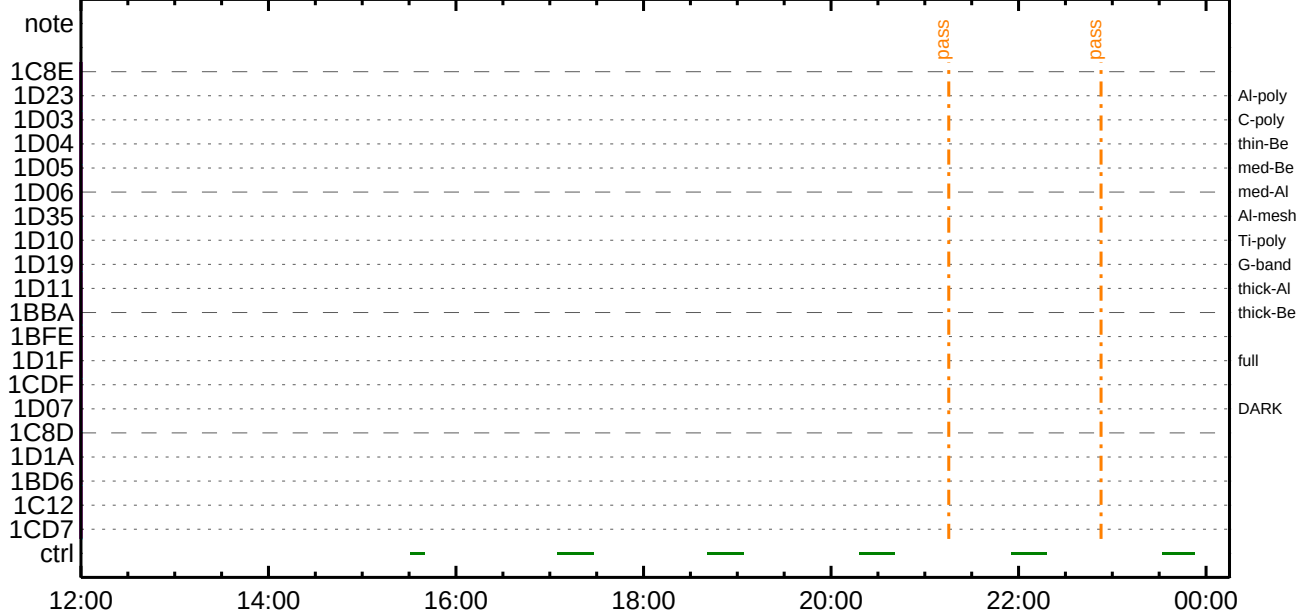
CMDI #0961 2025/11/15



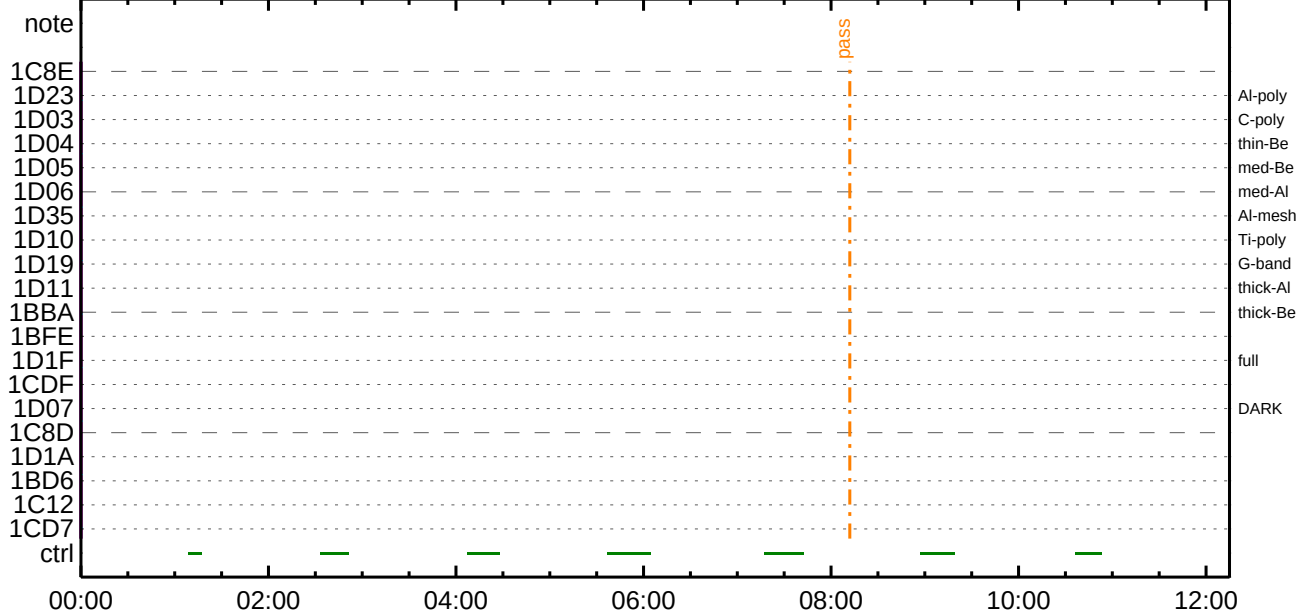
CMDI #0961 2025/11/16



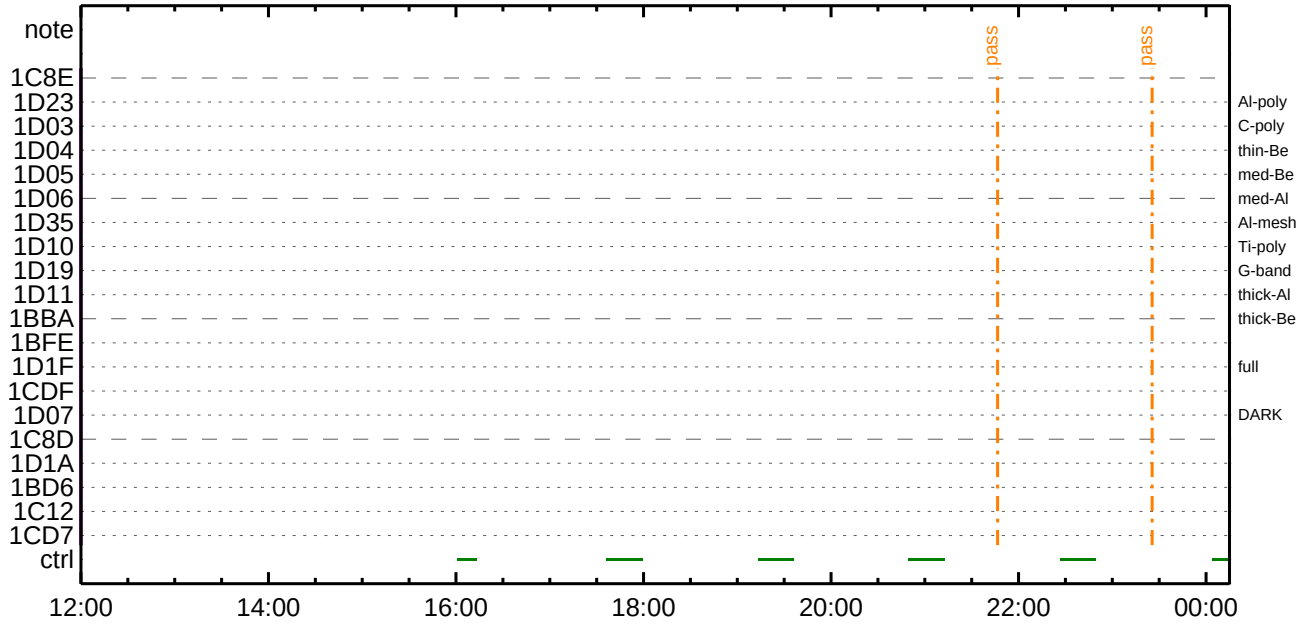
CMDI #0961 2025/11/16



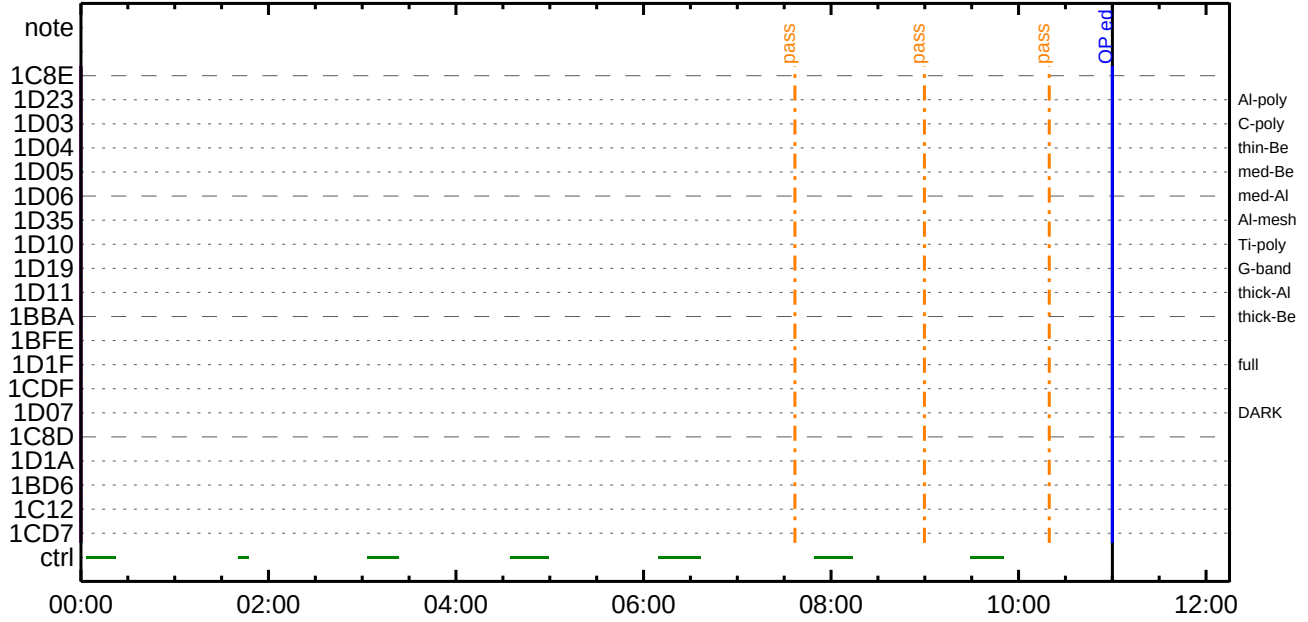
CMDI #0961 2025/11/17



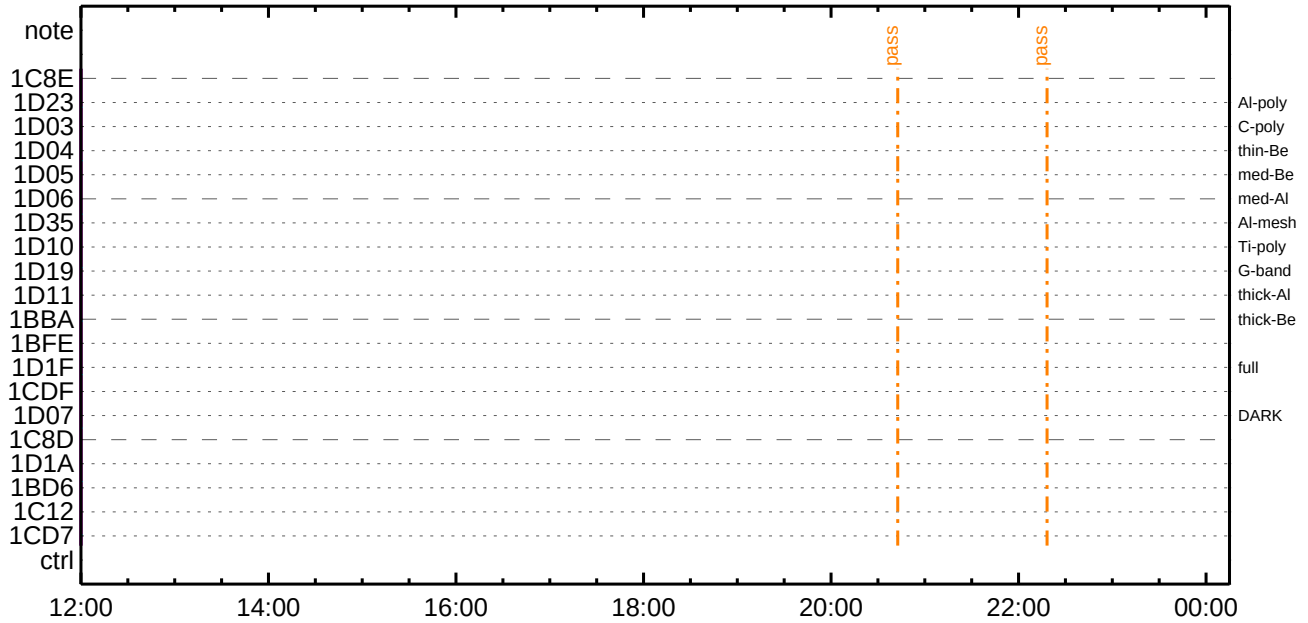
CMDI #0961 2025/11/17



CMDI #0961 2025/11/18



CMDI #0961 2025/11/18



```
main-632 2025-11-13 12:36:25 194 33 SOLAR-B MAIN //
```

```
main-632 2025-11-13 12:36:25 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŸÁŸ$ŸÄŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŸÄŸß;¼Ÿ³ŸPŸóŸÉÁ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçñÄñ•µ°E»Í×ÄÇñÍŸçŸÄŸ×Ÿí;¼ŸÉ;ËËèµ•íÍË;ËñË¼°ÇÓñ•ñç¼í¹ÇñÍ;çÄ®,ùñ¹ñËñPñÇÁ÷ç®ñ•ñËñññ³ñË;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŸí;¼ŸÉ;|ŸÄŸóŸ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŸí;¼ŸÉ;ä
0019 . S. OP op-632:OP
0020 ()
0021 . S. OG og-632:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPÎî°èŸÄŸóŸ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŸÄŸóŸ×½ªî»ñò³ÎÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñÎ¼Ë¹Ç•ë²ÎOKñò³ÎÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŸÄŸóŸ×½ªî»ñò³ÎÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñÎ¼Ë¹Ç•ë²ÎOKñò³ÎÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŸÄŸóŸ×½ªî»ñò³ÎÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPñÎ¼Ë¹Ç•ë²ÎOKñò³ÎÇ$
0081 C.
0082 . C. ***** °Ë²¼ñÎ¼Ä´¶Á°ñËË¬ñ°Ä÷ç® (¾âµ-ŸÄŸóŸ×¼è½ÇñòÄÓæñÇ½ªñ''ñë¼í¹ÇñÇñâ) *****
0083 C. DHUŸä;¼ŸÉ;Ë¼ý½,Ÿí;¼ŸÉ;ËñòÍäñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Á÷ç®NGñÎ¼í¹Ç;ç°Ë²¼ñÎTI-CMDÁ÷ç®ñÎ¼Ä¹Ôñ•ñËñññ³ñË;£
```


0096	C.	SET EDUMP I±°iYNY¹C¹Ô±;³E;f		
0097	C.			
0098	C.	TIY³YDYOYÉ±òÄDİ; (UT)		
0099	+. TI	2025-11-13 10:44:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+. TI	2025-11-13 10:44:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+. TI	2025-11-13 10:44:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+. TI	2025-11-13 10:48:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼±İÄê¼İİÑ±İYÄY\$YÄY¹¹äiÜ		
0116	C.	ÇÇ [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	4
0118	C.	ÇÇ [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	ÇÇ [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°èYÄYOY×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	ÇÇ [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	ÇÇ [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	ÇÇ [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	ÇÇ [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	ÇÇ [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+. DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	7
0136	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	YÄYOY×½¹İ»±ò³İÇ\$		
0142	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL±İ¼È¹Ç•è²İOK±ò³İÇ\$		
0145	C.			
0146	C.	DHUYâ;¼YÈ;È¼Y½,Yİ;¼YÈ;È±òİá±¹		
0147	+. DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	2
0150	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+. TI	2025-11-13 10:48:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+. TI	2025-11-13 10:48:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+. TI	2025-11-13 10:48:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP 'GÄİ±İ»ö¼Y±ÈÄD±¹±èDCBC•×²è *****		
0181	C.	(¼Ä°İYÖYÄYÈYDYEYÄYÇYè±È½¼±¼Ä»Ü±¹±è)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** YDY¹•İ Daily±İİÑ±È'Ø±¹±èDCBC•×²è *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	C.	İäLOSYÄY\$YÄY¹¹Ä»Ü;ä		
01				

[illegible]

```
0096 C.  
0097 C.  
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****  
0099 C. Upload the Orbit Element and the Target Attitude  
0100 C. RAM-ID:TARGET_ATT  
0101 . S. RAM ram-150:TARGET_ATT  
0102 (  
0103 C.  
0104 C.  
0105 C. Set the dump memory area of TARGET_ATT  
0106 +. DC 02-48 AOCU_DUMP_SET  
0107 BC (07 00 00 00 18 00)  
0108 C.  
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]  
0110 C.  
0111 C.  
0112 C. Change the TLMFormatNo for the AOCs Dump Format  
0113 +. DC 01-22 DHU_MODE_CHNG  
0114 BC (04 0b f8)  
0115 C.  
0116 C. Wait for AOCS DUMP to end  
0117 C.  
0118 . C. Check the dump memory  
0119 C.  
0120 C. Result = OK [ ]  
0121 C.  
0122 +. DC 01-22 DHU_MODE_CHNG  
0123 BC (02 0a f8)  
0124 C.  
0125 C. <A_***>[TLM STS] FMT = 2 [ ]  
0126 C.  
0127 +. DC 02-8E AOCU_ORB_UPD  
0128 . C.  
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0130 +. DC 07-FC EIS_MODE_CHG_ENA  
0131 BC (20)  
0132 . C. Verify EIS_MODE_CHG_FLG is ENA  
0133 +. DC 07-FC EIS_MODE_MANU  
0134 BC (21 02)  
0135 . C. Verify EIS in MANUAL mode  
0136 . C. Estimated OBSTBL upload time is 18s  
0137 C. *****  
0138 C. EIS START OBSTBL LOAD  
0139 C. *****  
0140 . S. RAM ram-820:EIS_OBSTBL  
0141 (  
0142 +. DC 07-FC EIS_DUMP_OBSTBL  
0143 BC (07 07 07 00 00 70 00)  
0144 C.  
0145 C. Execute, after the success of OBSTBL upload.  
0146 C. Set EIS TI-commands  
0147 +. TI 2025-11-13 10:48:50.0  
0148 DC 07-FC EIS_MODE_CHG_ENA  
0149 BC (20)  
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0151 C. *****  
0152 C. EIS END OBSTBL LOAD  
0153 C. *****  
0154 C.  
0155 . C. ***** MDP `ûÄîñÎ»ö¼ÝñĖÄðñ¹ñĖDCBC•×²è *****  
0156 C. (%â°îŸÖŸÄŸĖPŸĖŸÄŸÇŸĖñĖ½¾¼ñ¼Ä»Üñ¹ñĖ)  
0157 . S. DC-BC dcbc-402:DCBC  
0158 (MDP_known_event)  
0159 C.  
0160 C.  
0161 . C. ***** ŸÐŸ¹•İ Daily±;İññĖ'Øñ¹ñĖDCBC•×²è *****  
0162 . S. DC-BC dcbc-153:DCBC  
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0164 C.  
0165 C.  
0166 . C. ;äLOSŸÄŸSŸÄŸ~¼Ä»Ü;ä  
0167 C.  
0168 . C. ***** LOS *****  
0169 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-634 2025-11-13 12:36:25 100 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;āAOSŸÁŸ$ŸĀŸ~¼Ā»Ü;ä
0005 C.
0006 C. ŸĀŸß;¼Ÿ³ŸÐŸóŸĒĀ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āí;Ēñ¿ñĀñ•µ°Ēñİ×ĀÇñİŸçŸĀŸ×Ÿí;¼ŸĒ;ĒĒē½µ•īīĒ;ĒñĒ¼°ÇŌñ•ñ¿¼ī¹Çñİ;çĀ®,ùñ¹ñēñÐñÇĀ÷¿®ñ•ñĒñññ³ñĒ;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU
0020 BC (c1)
0021 +. DC 07-F0 MDP_XRT_MODE_STBY
0022 BC (c3)
0023 . C. ----- Success Verify ? OK / NG____
0024 C.
0025 C. XRT Obs. Table Upload
0026 . S. RAM ram-291:MDP_OBS_X
0027 ()
0028 C.
0029 +. DC 07-F0 MDP_DUMP_XRTTBL
0030 BC (84 07 00 00 00 3a d4)
0031 . C. ----- Comparison Check ? OK / ERR ____
0032 C.
0033 C.
0034 +. DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 01 b1 b1 04 04)
0036 +. DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 02 b1 b1 08 08)
0038 +. DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 03 b1 b1 08 08)
0040 +. DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 04 b1 b1 06 06)
0042 +. DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 05 85 83 06 06)
0044 +. DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 06 85 83 06 06)
0046 +. DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 07 80 80 08 08)
0048 +. DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 08 80 80 20 20)
0050 +. DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 09 80 80 20 08)
0052 +. DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0a 80 80 08 20)
0054 +. DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0f 80 80 06 06)
0056 +. DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 10 80 80 08 08)
0058 +. DC 07-F0 MDP_XRT_FLD_ENA
0059 BC (d8)
0060 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0061 BC (c8)
0062 +. DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 +. DC 07-F0 MDP_XRT_AEC_RESET
0065 BC (d0)
0066 +. DC 07-F0 MDP_XRT_FLD_RESET
0067 BC (da)
0068 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0069 BC (c4 01)
0070 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0071 BC (c5 0e)
0072 . C. ----- Success Verify ? OK / NG ____
0073 C.
0074 C.
0075 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0076 C.
0077 +. DC 07-F0 MDP_XRT_MODE_OBSV
0078 BC (c2)
0079 +. TI 2025-11-13 10:48:02.0
0080 DC 07-F0 MDP_XRT_MODE_OBSV
0081 BC (c2)
0082 . C. ----- Success Verify ? OK / NG ____
0083 C.
0084 C. ***** XRT END *****
0085 C.
0086 . C. ***** MDP 'ġĀİñİ»ō¼ŸñĒĀÐñ¹ñēDCBC•×²ē *****
0087 C. (¼ā°īŸóŸĀŸĒŸÐŸĒŸĒŸāŸçŸĒñĒĒ¼ññ¼Ā»Üñ¹ñē)
0088 . S. DC-BC dcbc-402:DCBC
0089 (MDP_known_event)
0090 C.
0091 C.
0092 . C. ***** ŸÐŸ¹•İ Daily÷¿İññĒ'Øñ¹ñēDCBC•×²ē *****
0093 . S. DC-BC dcbc-153:DCBC
0094 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0095 C.
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0096 C.
0097 . C. ;ãLOS¥Á¥\$¥Ã¥~¼Ã»Ü;ä
0098 C.
0099 . C. ***** LOS *****
0100 C.

Nov 13, 25 12:36

XRT_OGLIST_0961.chk

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*** OP Sequence for XRT ***

2025/11/13	10:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	10:58:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	10:58:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/11/13	10:59:00.0	AOCs_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 74 01 db				
2025/11/13	10:59:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/13	10:59:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/13	10:59:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/13	10:59:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/13	10:59:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	11:01:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2025/11/13	11:01:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/11/13	11:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	11:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	11:48:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	11:48:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	11:48:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/13	11:51:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/13	11:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/13	11:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	11:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	11:54:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	11:54:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/11/13	11:55:00.0	AOCs_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2025/11/13	11:55:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/13	11:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/13	11:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/13	11:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/13	11:55:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	11:57:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2025/11/13	11:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/11/13	11:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	13:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	13:54:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	13:54:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/11/13	13:55:00.0	AOCs_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 74 01 db				
2025/11/13	13:55:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/13	13:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/13	13:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/13	13:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/13	13:55:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	13:57:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2025/11/13	13:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/11/13	13:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	15:32:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	15:32:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2025/11/13	15:32:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/13	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/13	15:42:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/13	15:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	17:06:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	17:06:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	17:06:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	17:06:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/13	17:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/13	17:30:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/13	17:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	18:00:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	18:00:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/11/13	18:00:30.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/11/13	18:00:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/11/13	18:00:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/11/13	18:00:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/13	18:03:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2025/11/13	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	18:10:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	18:10:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/11/13	18:10:30.0	AOCs_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 03 74 01 db				
2025/11/13	18:10:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/13	18:10:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/13	18:10:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/13	18:10:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/13	18:10:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	18:13:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2025/11/13	18:13:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e				
2025/11/13	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	18:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	18:43:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	18:43:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	18:43:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/13	18:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/13	19:06:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/13	19:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	20:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	20:20:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	20:20:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/13	20:20:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/13	20:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/13	20:43:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/13	20:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/13	21:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	21:57:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/13	21:57:04.0	XRT_FLD_RESET_415_OG [0x19f]							

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2025/11/13	21:57:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/13	22:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/13	22:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/13	22:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/13	23:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/13	23:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/13	23:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/13	23:34:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/13	23:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/13	23:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/13	23:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	01:11:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	01:11:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/14	01:11:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/14	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/14	01:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/14	01:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	02:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	02:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	02:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/14	02:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/14	02:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/14	02:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/14	02:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/11/14	04:00:00.0	AOCs_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/11/14	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/14	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/14	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/14	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/14	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/14	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2025/11/14	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/11/14	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/14	04:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	04:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	04:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/14	04:09:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/14	04:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/14	04:30:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/14	04:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	05:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	05:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/14	05:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/14	05:39:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/14	05:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					

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2025/11/14	06:07:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	06:08:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	06:16:26.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	06:16:28.0	XRT_FOCUS_POSITION_406_OG [0x196]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/14	06:16:48.0	XRT_FLD_DIS_409_OG [0x199]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/14	06:16:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/14	06:16:52.0	XRT_ARS_DIS_435_OG [0x1b3]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/14	06:19:28.0	XRT_QT_PROG_SET_410_OG [0x19a]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/11/14	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	06:26:26.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	06:26:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/14	06:26:30.0	AOCs_Ore-point_Start_1_OG [0x097]				
			AOCU_NM	5	02-76	04 03 74 01 db
2025/11/14	06:26:48.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/14	06:26:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/14	06:26:52.0	XRT_AEC_RESET_448_OG [0x1c0]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/14	06:26:54.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/14	06:26:56.0	XRT_FLD_RESET_434_OG [0x1b2]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	06:29:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/11/14	06:29:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/11/14	06:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	07:19:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	07:19:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	07:19:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	07:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/14	07:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	07:44:30.0	XRT_Custom_430_OG [0x1ae]				
2025/11/14	07:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	08:59:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	08:59:02.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	08:59:04.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	08:59:06.0	XRT_PREFLR_STRT_404_OG [0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/14	09:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	09:21:00.0	XRT_Custom_430_OG [0x1ae]				
2025/11/14	09:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	10:38:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	10:38:32.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	10:38:34.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	10:38:36.0	XRT_PREFLR_STRT_404_OG [0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/14	10:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	10:49:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	10:49:56.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	10:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/14	10:50:00.0	AOCs_Ore-point_Start_4_OG [0x09a]				
			AOCU_NM	5	02-76	00 55 3f 01 db
2025/11/14	10:50:18.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/14	10:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/14	10:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]				

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2025/11/14	10:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/14	10:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/14	10:52:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07		
2025/11/14	10:52:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/11/14	10:55:01.0	XRT_Custom_430_OG [0x1ae]							
2025/11/14	10:56:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/14	16:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	16:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	16:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/14	16:03:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/14	16:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/11/14	16:15:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/14	16:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/14	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	16:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	16:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/11/14	16:50:00.0	AOCs_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	03	74	01 db
2025/11/14	16:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/11/14	16:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/11/14	16:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/11/14	16:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/14	16:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/14	16:52:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2025/11/14	16:52:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e		
2025/11/14	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/14	17:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	17:38:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	17:38:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/14	17:38:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/14	17:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/11/14	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	18:01:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	18:01:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2025/11/14	18:01:30.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00 00
2025/11/14	18:01:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2025/11/14	18:01:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2025/11/14	18:01:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/14	18:04:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2025/11/14	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/14	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	18:11:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/14	18:11:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/11/14	18:11:30.5	AOCs_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	03	74	01 db
2025/11/14	18:11:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/11/14	18:11:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/11/14	18:11:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/11/14	18:11:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2025/11/14	18:11:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	18:14:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/11/14	18:14:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/11/14	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	19:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	19:15:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	19:15:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	19:15:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/14	19:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	19:38:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/14	19:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	20:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	20:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	20:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	20:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/14	20:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	21:15:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/14	21:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/14	22:28:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	22:28:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/14	22:28:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/14	22:28:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/14	22:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/14	22:51:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/14	22:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	00:05:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	00:05:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	00:05:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	00:05:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/15	00:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/15	00:24:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/15	00:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	01:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	01:42:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	01:42:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	01:42:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/15	01:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/15	01:49:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/15	01:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	03:05:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	03:05:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	03:05:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	03:05:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/15	03:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/15	03:25:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/15	03:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/15	04:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			

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2025/11/15	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	5	02-76 00 00 00 00 00
		MDP_XRT_FLD_ENA	1	07-F0 d8
2025/11/15	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2025/11/15	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2025/11/15	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/11/15	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/15	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	2	07-F0 c4 04
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 04
2025/11/15	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0 c5 0e
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0e
2025/11/15	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/15	04:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	04:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	04:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/15	04:36:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/11/15	04:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/11/15	05:01:30.0	XRT_Custom_430_OG [0x1ae]		
2025/11/15	05:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/15	05:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	05:44:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	05:44:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8 22 ff aa 00
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2025/11/15	05:45:18.0	XRT_FLD_DIS_409_OG [0x199]	1	07-F0 d9
		MDP_XRT_FLD_DIS	1	07-F0 d9
2025/11/15	05:45:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	1	07-F0 c9
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2025/11/15	05:45:22.0	XRT_ARS_DIS_435_OG [0x1b3]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/11/15	05:47:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	2	07-F0 c4 0c
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0c
2025/11/15	05:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/15	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	05:54:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	05:54:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	4	07-F8 22 fe 97 00
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2025/11/15	05:55:00.0	AOCs_Ore-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 04 03 74 01 db
2025/11/15	05:55:18.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0 d8
		MDP_XRT_FLD_ENA	1	07-F0 d8
2025/11/15	05:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0 c8
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2025/11/15	05:55:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0 d0
		MDP_XRT_AEC_RESET	1	07-F0 d0
2025/11/15	05:55:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0 d5
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/11/15	05:55:26.0	XRT_FLD_RESET_434_OG [0x1b2]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/15	05:57:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	2	07-F0 c4 0a
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2025/11/15	05:57:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0 c5 0e
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0e
2025/11/15	05:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/15	06:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	06:11:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	06:11:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/15	06:11:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/11/15	06:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/11/15	06:39:00.0	XRT_Custom_430_OG [0x1ae]		
2025/11/15	06:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0 c0
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/15	07:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	07:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0 c1
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/15	07:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0 da
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/15	07:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0 e8
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/11/15	07:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0 e9
		MDP_XRT_PREFLR_STOP	1	07-F0 e9

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2025/11/15	08:16:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/15	08:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	09:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	09:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	09:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/15	09:31:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/15	09:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/15	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	11:12:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				