

Period: 2025/11/15 11:02:00 - 2025/11/20 10:59:00

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/32768)														
Term		Pointing (x, y)			Comment									
11/15 17:48:30 - 11/15 17:55:24		Fixed (0.0, 0.0)			synoptic, shifted -14.5 min									
11/16 06:03:00 - 11/16 06:09:54		Fixed (0.0, 0.0)			synoptic, HOP 349									
11/16 18:03:00 - 11/16 18:09:54		Fixed (0.0, 0.0)			synoptic									
11/17 06:08:00 - 11/17 06:24:24		Fixed (0.0, 0.0)			synoptic, HOP 349									
11/17 18:04:26 - 11/17 18:09:54		Fixed (0.0, 0.0)			synoptic									
11/18 05:43:00 - 11/18 05:52:54		Fixed (0.0, 0.0)			synoptic, HOP 349									
PROG= 12 1-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 5 1-time(s) 2.0sec														
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 55 1-time(s) 2.0sec														
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 98 1-time(s) 2.0sec														
Al-poly/Open		Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 76 1-time(s) 2.0sec														
thin-Be/Open		thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23 1-time(s) 4.0sec														
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 1-time(s) 2.0sec														
Seqn= 41 1-time(s) 2.0sec														

Open/thick-Be	Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 18 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 86 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	86ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 54 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D1A: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 120min cad) + CME wa

Term	Pointing (x, y)	Comment
11/16 04:03:00 - 11/16 05:59:54	Fixed (0.0, 0.0)	synoptic, HOP 349
11/17 04:29:00 - 11/17 05:37:00	Fixed (0.0, 0.0)	synoptic, HOP 349
11/18 03:46:00 - 11/18 05:39:54	Fixed (0.0, 0.0)	synoptic, HOP 349

PROG= 04 Inf.-time(s)

Subr= 1 1-time(s) 600.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74 1-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

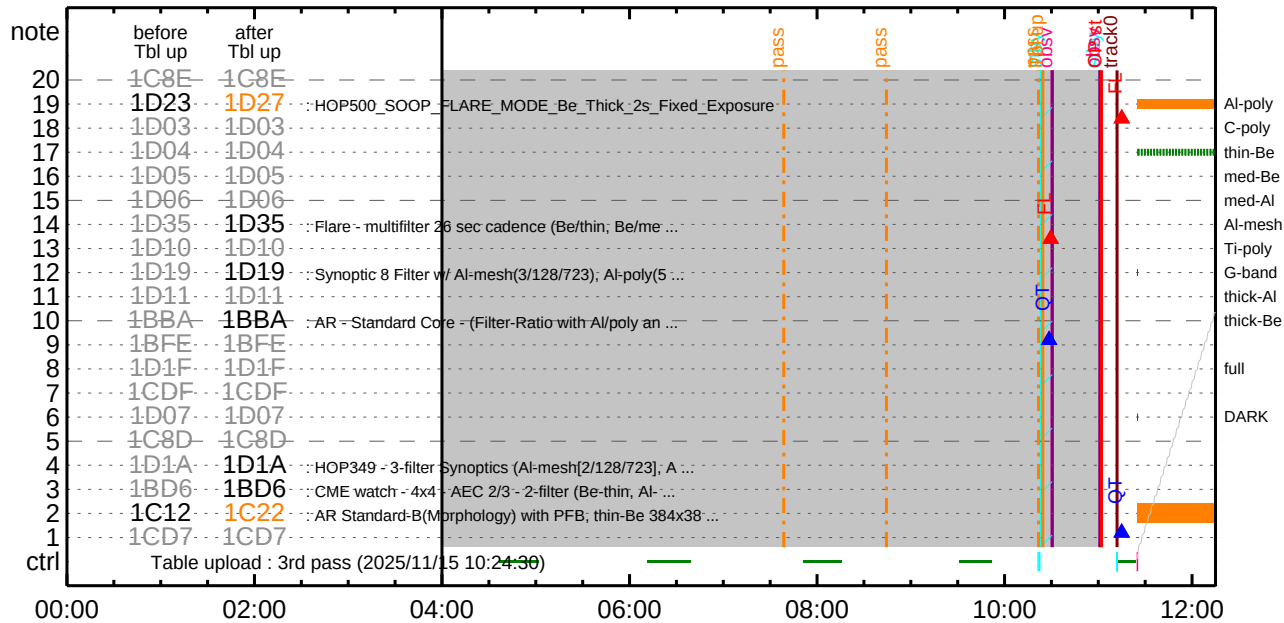
Term	Pointing (x, y)	Comment
11/16 13:53:00 - 11/16 17:59:54	Track (166.0, -218.0) @ 11/16 13:50:00	AR 14280
11/16 18:13:00 - 11/17 04:07:00	Track (205.5, -217.3) @ 11/16 18:10:00	AR obs
11/17 06:27:30 - 11/17 10:36:30	Track (314.7, -214.8) @ 11/17 06:24:30	AR obs
11/17 12:53:00 - 11/17 17:36:00	Track (370.2, -213.2) @ 11/17 12:50:00	AR obs
11/17 18:13:00 - 11/18 03:42:54	Track (415.1, -211.9) @ 11/17 18:10:00	AR obs
11/18 05:56:00 - 11/18 10:47:30	Track (509.5, -208.6) @ 11/18 05:53:00	AR obs

PROG= 10 Inf.-time(s)

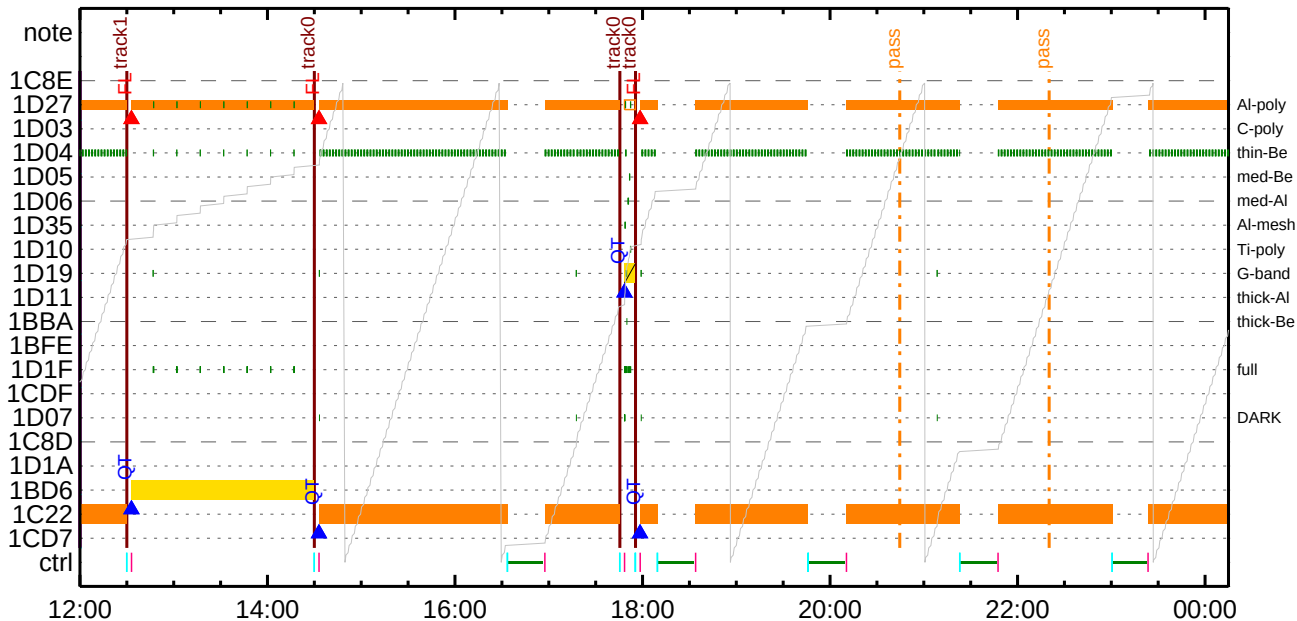
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 47 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 120.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

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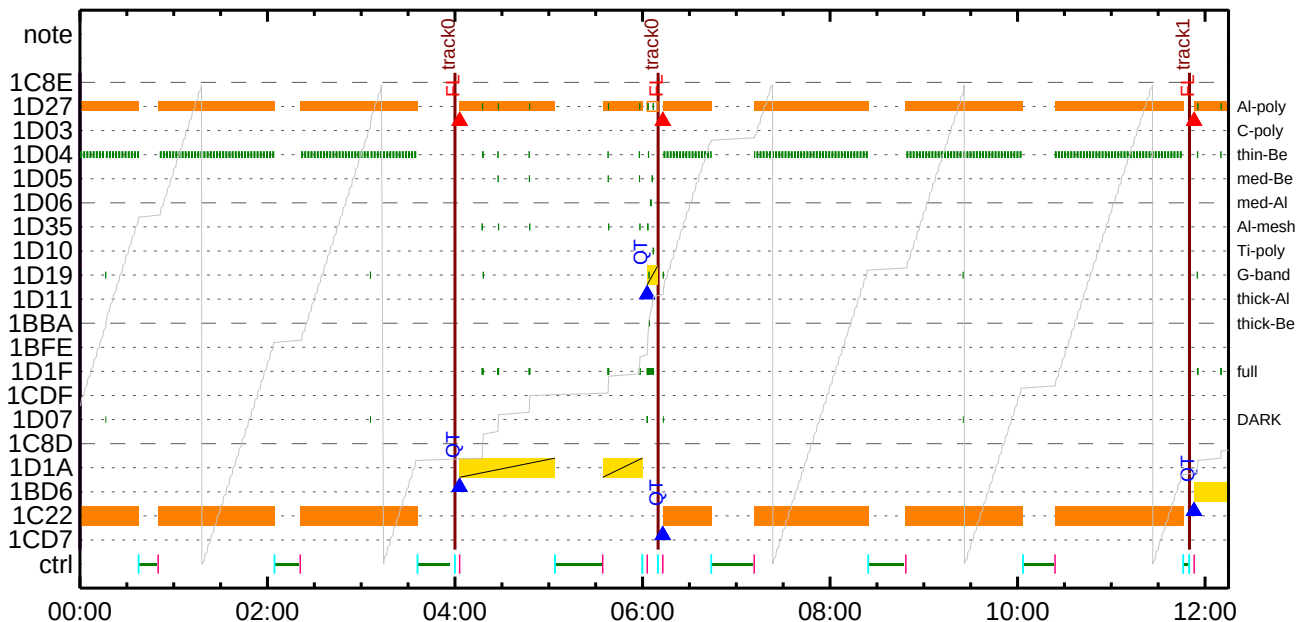
CMDI #0966 2025/11/15



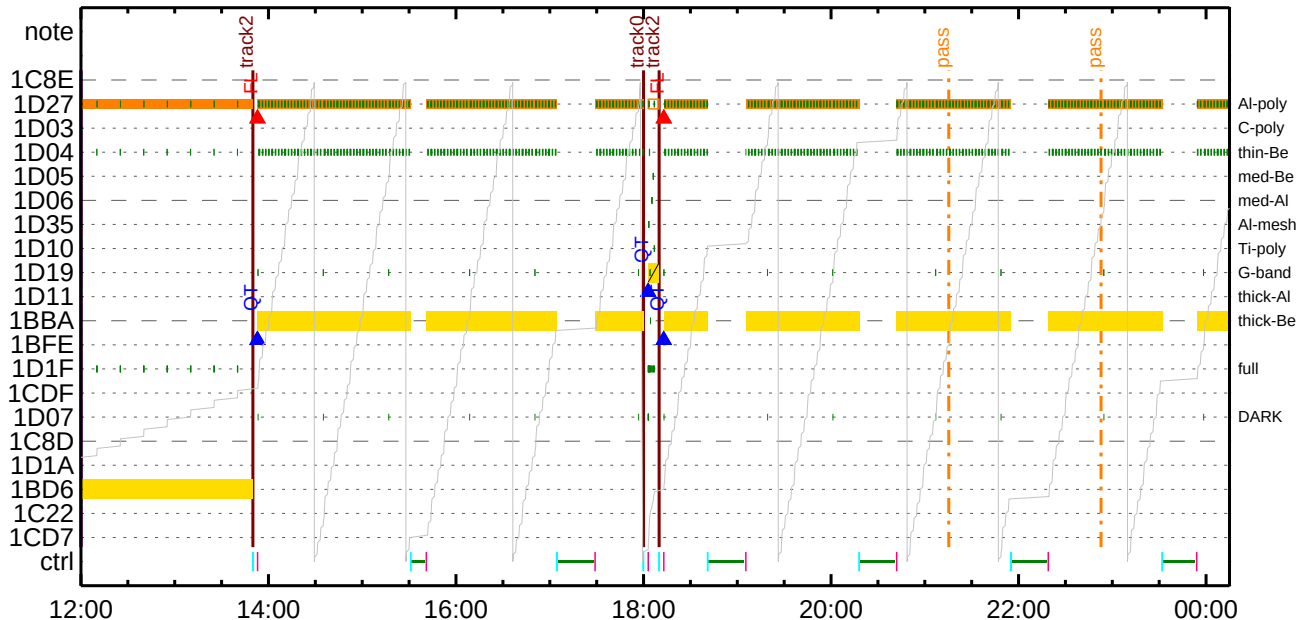
CMDI #0966 2025/11/15



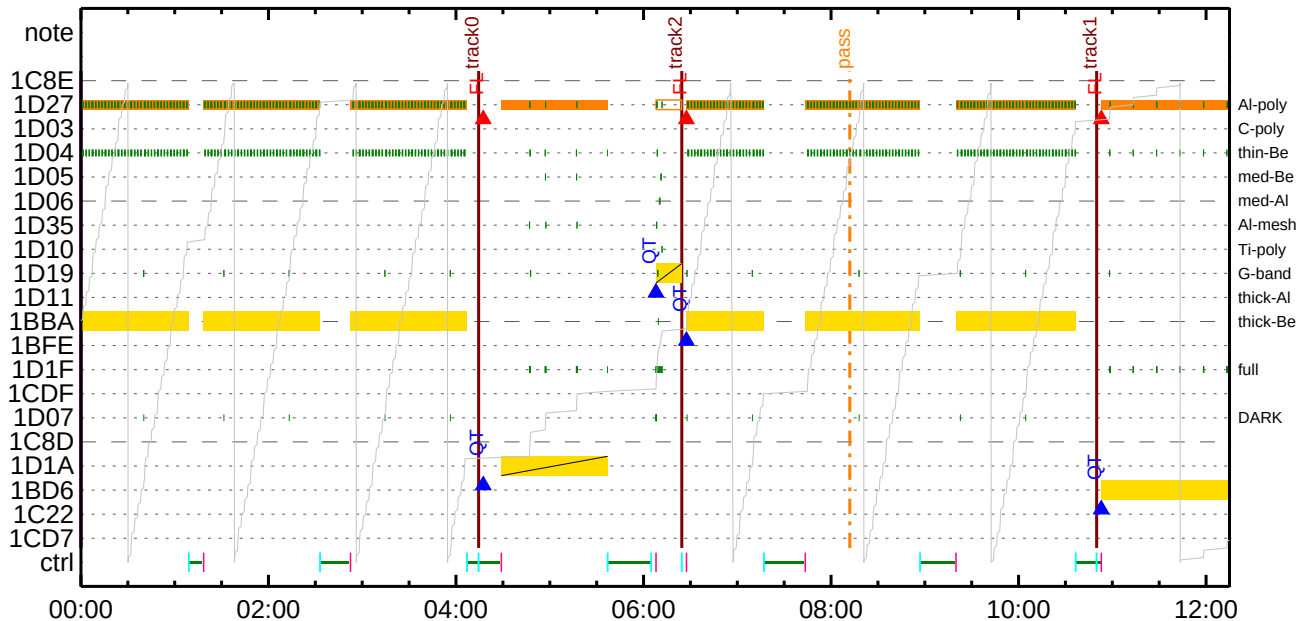
CMDI #0966 2025/11/16



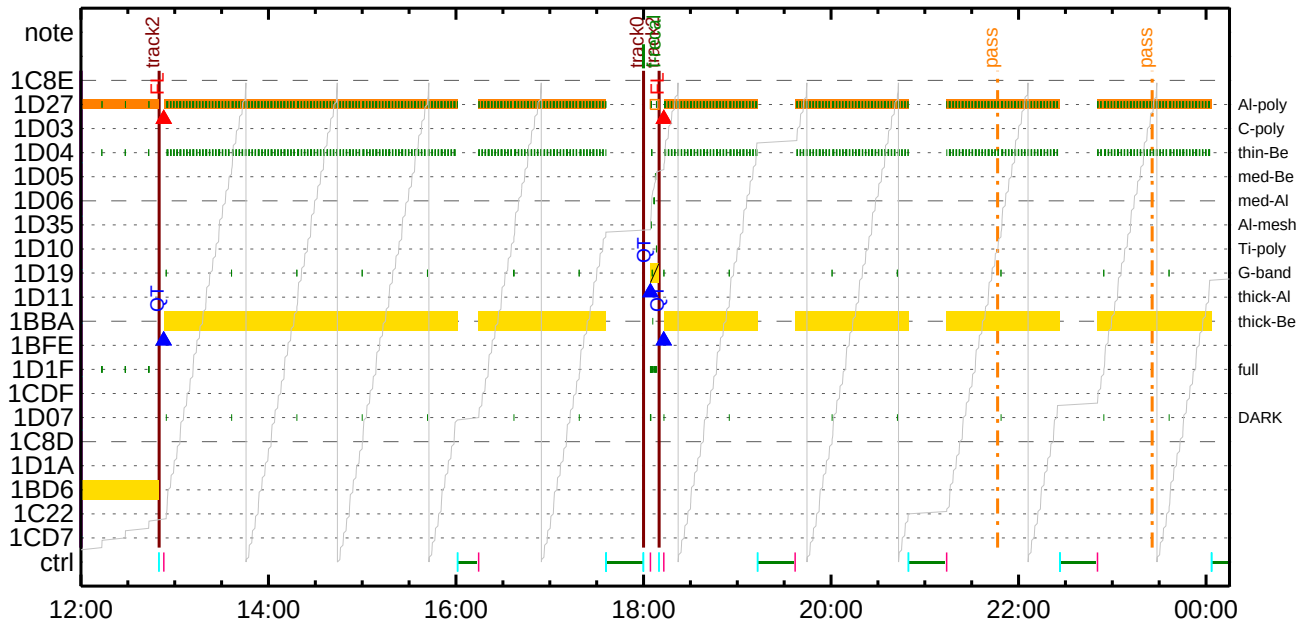
CMDI #0966 2025/11/16



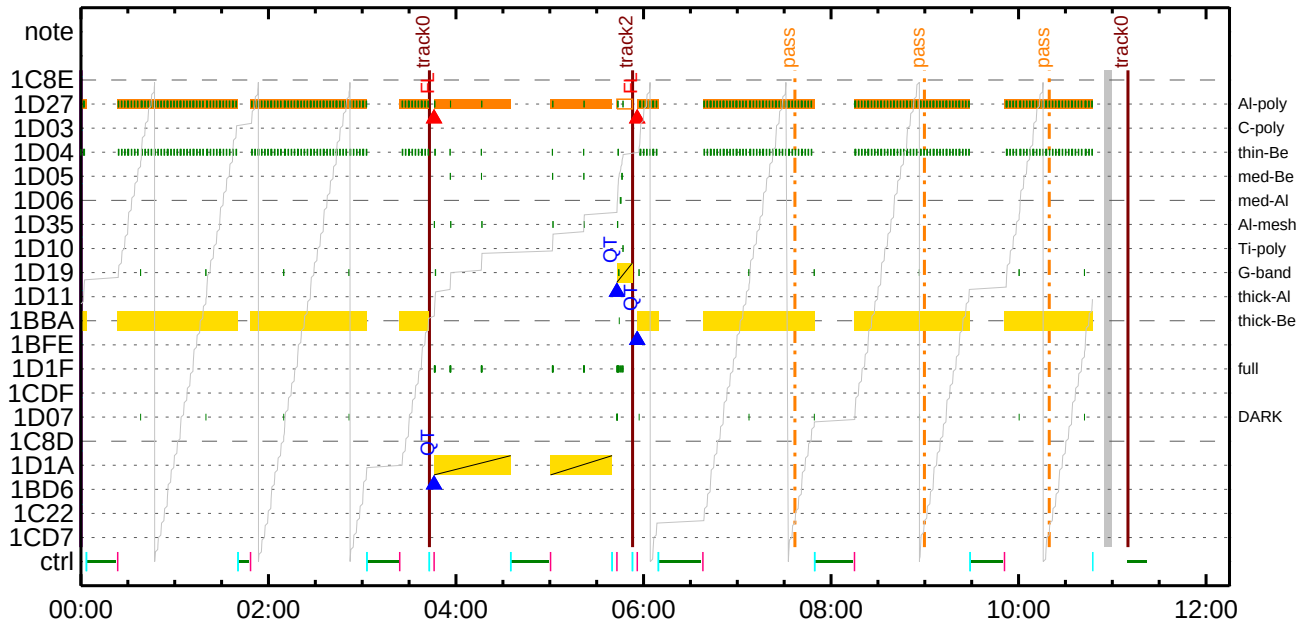
CMDI #0966 2025/11/17



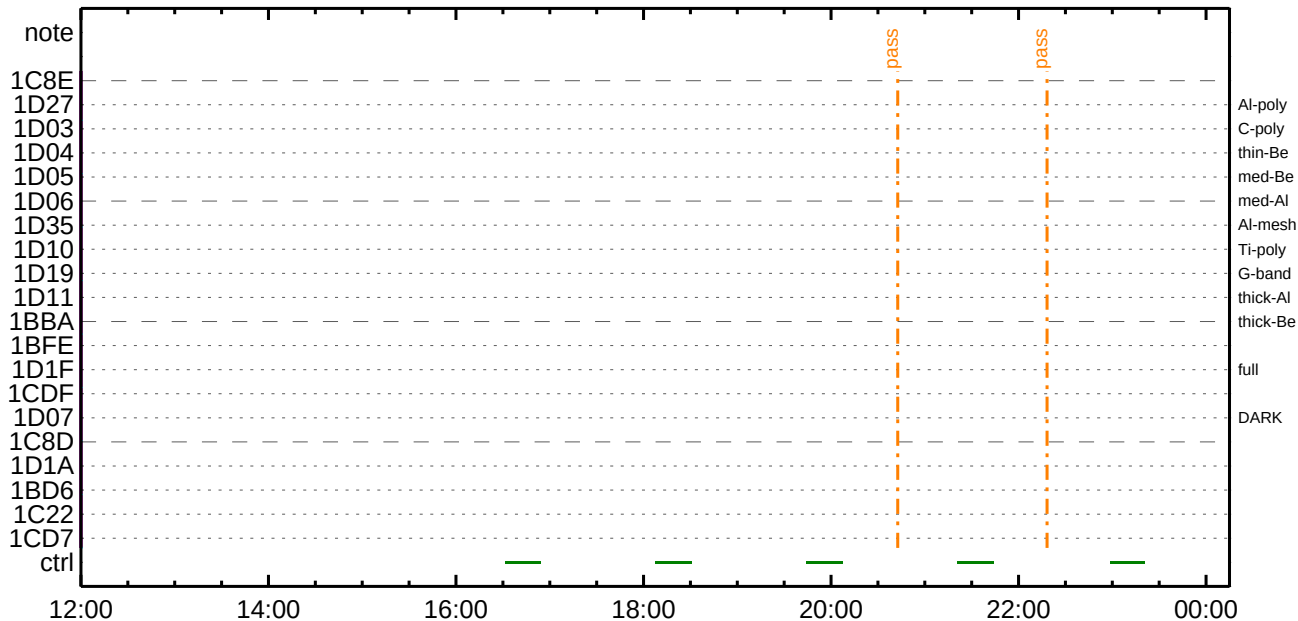
CMDI #0966 2025/11/17



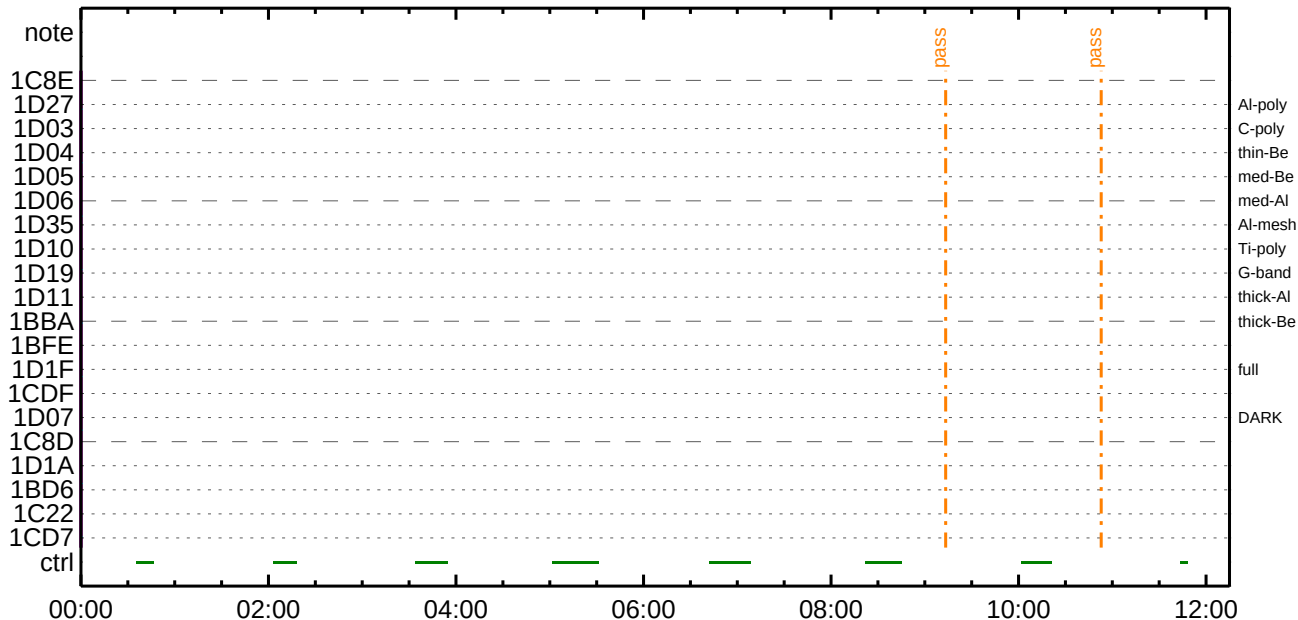
CMDI #0966 2025/11/18



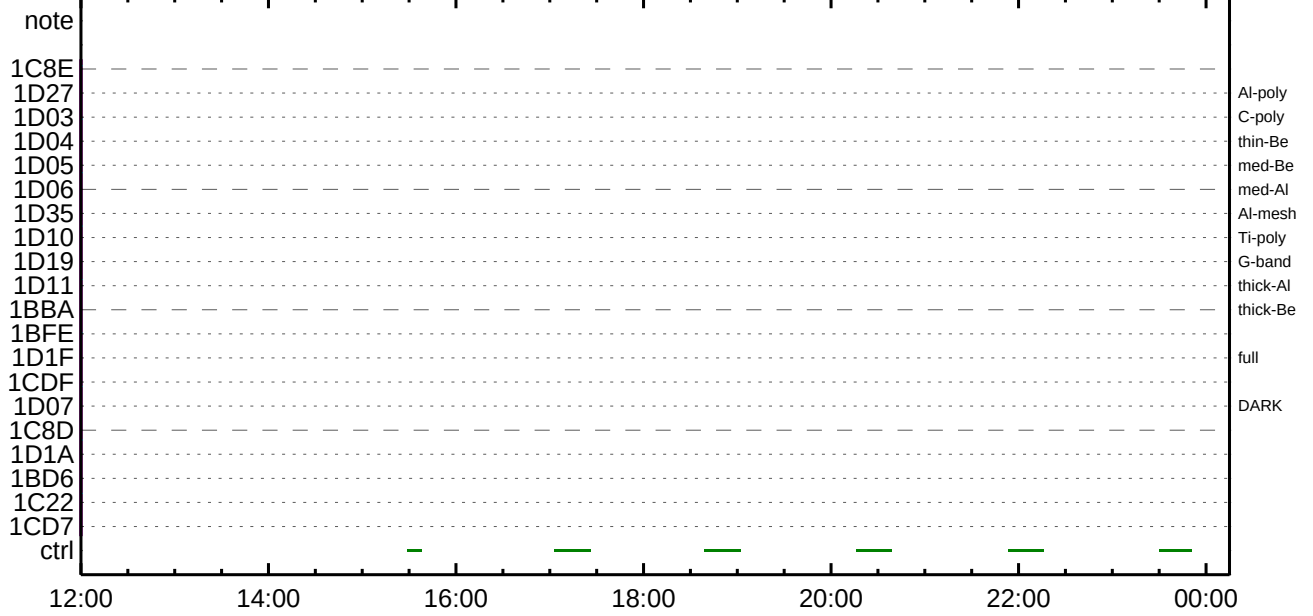
CMDI #0966 2025/11/18



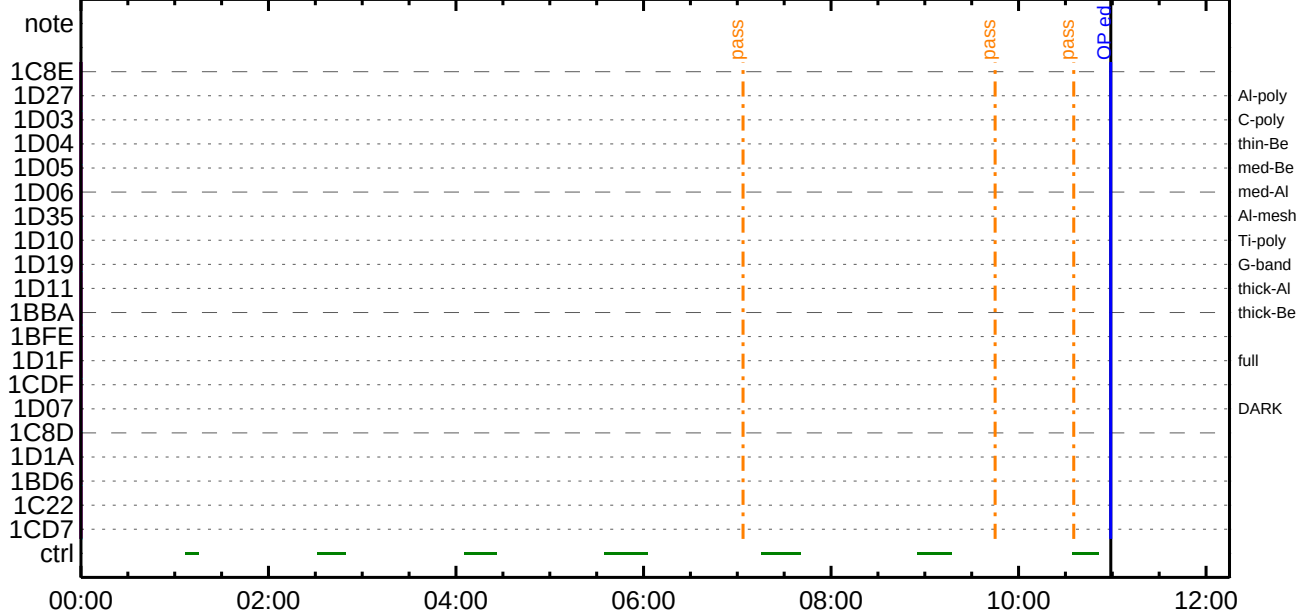
CMDI #0966 2025/11/19



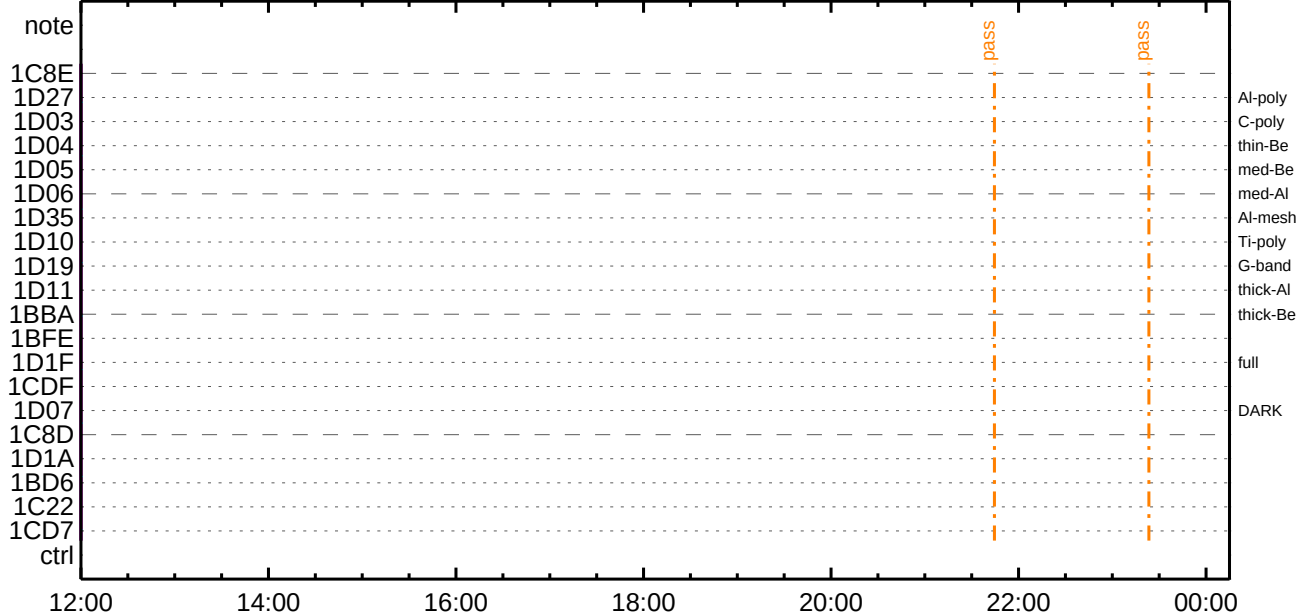
CMDI #0966 2025/11/19



CMDI #0966 2025/11/20



CMDI #0966 2025/11/20



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main-11-15 12:33:10 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŴÁŸ$ŴÃŸ~¼Ä»Ü;ä
0005 C.
0006 C. ŴÄŴß;¼Ŵ³ŴÐŴóŴÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;Ë¤¿¤Ä¤•µ°E»Í×ÀÇ¤ÎŴÇŴÄŴ×Ŵí;¼ŴÉ;ËÈè²µ•îîÊ;Ë¤Ê¼°ÇÖ¤¤¿½¹¹Ç¤İ;ÇÄ®,ù¤¹¤È¤¤¤ÇÄ÷¿®¤•¤È¤¤¤³¤È;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŴí;¼ŴÉ;|ŴÄŴóŴ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŴí;¼ŴÉ;ä
0019 . S. OP op-637:OP
0020 ()
0021 . S. OG og-637:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPîî°èŴÄŴóŴ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0036 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŴÄŴóŴ×½ªªî»¤ò³îÇ$
0041 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG¤î¾Ê¹Ç•ë²ïOK¤ò³îÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0055 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŴÄŴóŴ×½ªªî»¤ò³îÇ$
0060 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG¤î¾Ê¹Ç•ë²ïOK¤ò³îÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0074 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŴÄŴóŴ×½ªªî»¤ò³îÇ$
0079 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OP¤î¾Ê¹Ç•ë²ïOK¤ò³îÇ$
0081 C.
0082 . C. ***** °Ê²¼¤î¾Ä´¶¹°¤ÈÉ¬¤°Ä÷¿® (¾µ-ŴÄŴóŴ×¼ê½Ç¤òÄÓÄæ¤Ç½¤¤¤¤ë¾ì¹Ç¤Ç¤â) *****
0083 C. DHUŴâ;¼ŴÉ;Ë¼ý½,Ŵí;¼ŴÉ;Ë¤òîá¤¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0087 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOAD¤¬Ä÷¿®¤NG¤î¾ì¹Ç;Ç°Ê²¼¤î¾TI-CMDÄ÷¿®¤î¾Ä¹Ô¤¤¤¤È¤¤¤¤³¤È;£
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0096	C.	SET EDUMP I± iYŶY¹ Ç¹ Ō ±³ E; f		
0097	C.			
0098	C.	TIY³YDYOŶE± òÄDİ; (UT)		
0099	+	TI 2025-11-15 10:57:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+	TI 2025-11-15 10:57:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+	TI 2025-11-15 10:57:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+	TI 2025-11-15 11:01:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼ ±İÄê¼İİŶ ±İYÄY\$YÄY¹ àiÜ		
0116	C.	ÇÇ [HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	ÇÇ [HK1_TI_CMD_NUM]	EQ	4
0118	C.	ÇÇ [HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	ÇÇ [HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°èYÄYOŶ×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF; \$ 1024byte)		
0126	+	DC 01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	ÇÇ [HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	ÇÇ [HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	ÇÇ [HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	ÇÇ [HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	ÇÇ [HK1_DMA_DMP_PIM]	EQ	DHU
0133	+	DC 01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	7
0136	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	YÄYOŶ×½ªİ»±ò³İÇ\$		
0142	C.	ÇÇ [HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL±İ¼Ê¹Ç•è²İOK±ò³İÇ\$		
0145	C.			
0146	C.	DHUYâ;¼YÊ;Ê¼Y½,Yİ;¼YÊ;Ê±òİá±¹		
0147	+	DC 01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	ÇÇ [HK1_PKT_FORM_NO]	EQ	2
0150	C.	ÇÇ [HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	ÇÇ [HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	ÇÇ [HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+	TI 2025-11-15 11:01:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+	TI 2025-11-15 11:01:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+	TI 2025-11-15 11:01:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP 'ĞÄİ±İ»ö¼Y±EÄD±¹±èDCBC•×²è *****		
0181	C.	(¼Ä°İYÖYÄYÊYDYÊYÄYÇYèE½¼±¼Ä»Ü±¹±è)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** YDŶ¹•İ Daily±İŶ±E'Ø±¹±èDCBC•×²è *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	C.	;äLOSYÄY\$YÄY¹¼Ä»Ü;ä		


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0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EA, Í×ÈÝñäLOSñÞñÇñÎ»Þ'Öòò¹íî,ñ•;ÇÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÔÄÖñ¬°ÄÄèñ•ñççé;Ç°Ê²¼ñî°EA,¼ê½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»İ;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲËËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EA,ñ¬¼«E°Ää»ßñ•ñçç,ã;Ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲÇŲóŲÉŲËËÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»İ;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲÇŲóŲÉŲËËÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 13s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2025-11-15 11:01:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•×²è *****
0156 C. (%ã°îÿÖÿÄÿËÿÐÿËÿâÿçÿëñÊ¼ññ¼Ä»Û¹ñë)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•Ï Daily±¿íññÊ'Øñ¹ñëDCBC•×²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Á¥$¥Ä¥~¼Ä»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;ÇÀ®,ùñ¹ñèñÐñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EA, Í×ÈÝñäLOSñÐñÇñÎ»p´Öòò¹íî,ñ•;ÇÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲŲ~¾ÔÄÖñ¬°ÄÄèñ•ñççé;Ç°Ê²¼ñî°EA,¼è½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲÇŲóŲÉŲÈÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EA,ñ¬¼«E°Ää»ßñ•ñçç,ã;Ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲÇŲóŲÉŲÈÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ´°î»ñ¹ñèñÐñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;Ç°Ê²¼ñî¼Ä¹Öñññ°;ÇDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲÇŲóŲÉŲÈÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;ÇXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1 XPA ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 08 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 20 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 80 80 08 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 +. DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 +. DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 0a)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 0e)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2025-11-15 11:01:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîñÎ»ö¼ÝñÊÃÐñ¹ñÐDCBC•×²è *****
0173 C. (¾å°î¥Ó¥Ã¥Ê¥Ð¥Ê¥â¥ç¥Ëñ½¾¾ñ¼Ã»Ûñ¹ñË)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** ¥Ð¥¹•Ï Daily±¿îññË´Øñ¹ñÐDCBC•×²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ¿ãLOS¥Ã¥$¥Ã¥¹¼Ã»Û¿ã
0184 C.
0185 . C. ***** LOS *****
0186 C.
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*** OP Sequence for XRT ***

2025/11/15	11:11:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	11:11:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	11:11:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/15	11:12:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 dc 73 b7 1b
2025/11/15	11:12:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	11:12:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	11:12:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	11:12:06.5	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/15	11:12:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/15	11:12:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/15	11:12:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/15	11:12:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/15	11:12:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	11:14:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/11/15	11:14:58.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/15	11:15:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/15	11:24:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/15	11:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	12:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	12:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	12:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/15	12:30:00.5	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2025/11/15	12:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/15	12:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/15	12:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/15	12:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/15	12:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	12:32:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/11/15	12:32:58.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/15	12:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	14:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	14:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	14:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/15	14:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 dc 73 b7 1b
2025/11/15	14:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/15	14:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/15	14:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/15	14:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/15	14:30:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	14:32:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/11/15	14:32:58.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/15	14:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/15	16:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	16:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/15	16:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/15	16:33:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2025/11/15	16:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/15	16:56:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/15	16:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	17:45:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	17:45:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	17:45:28.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/11/15	17:45:30.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/11/15	17:45:48.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/11/15	17:45:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/11/15	17:45:52.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/15	17:48:28.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2025/11/15	17:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	17:55:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	17:55:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2025/11/15	17:55:30.0	AOCs_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 dc 73 b7 1b				
2025/11/15	17:55:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/15	17:55:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/15	17:55:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/15	17:55:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/15	17:55:56.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/15	17:58:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02				
2025/11/15	17:58:28.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2025/11/15	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	18:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	18:09:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	18:09:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/15	18:09:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/15	18:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/15	18:33:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/15	18:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	19:46:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	19:46:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/15	19:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/15	19:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/15	20:09:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/15	20:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	21:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	21:23:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	21:23:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/15	21:23:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/15	21:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/15	21:46:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/15	21:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/15	23:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	23:00:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/15	23:00:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/15	23:00:36.0	XRT_PREFLR_STRT_404_OG [0x194]							

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2025/11/15	23:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2025/11/15	23:22:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/15	23:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	00:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	00:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	00:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	00:37:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	00:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	00:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	00:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	02:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	02:04:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	02:04:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	02:04:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	02:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	02:20:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	02:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	03:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	03:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	03:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	03:36:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	03:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/11/16	04:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/11/16	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/16	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/16	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/16	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/16	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	04:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2025/11/16	04:02:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2025/11/16	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	05:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	05:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	05:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	05:04:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	05:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	05:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	05:34:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/11/16	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/11/16	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/11/16	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/16	06:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	

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2025/11/16	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/16	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	06:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/16	06:10:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 dc 73 b7 1b
2025/11/16	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/16	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/16	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/16	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/16	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/16	06:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2025/11/16	06:12:58.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/16	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/16	06:44:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	06:44:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	06:44:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/16	06:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/16	06:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/16	07:10:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/16	07:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/16	08:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	08:24:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	08:24:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/16	08:24:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/16	08:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/16	08:47:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/16	08:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/16	10:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	10:03:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	10:03:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/16	10:03:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/16	10:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/16	10:23:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/16	10:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/16	11:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	11:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	11:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/16	11:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/16	11:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/16	11:49:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	11:49:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/16	11:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/16	11:50:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2025/11/16	11:50:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/16	11:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/16	11:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/16	11:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/16	11:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2025/11/16	11:52:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2025/11/16	11:52:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2025/11/16	11:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/16	13:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	13:49:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	13:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/11/16	13:50:00.5	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02	03	74	01	db
2025/11/16	13:50:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/16	13:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/16	13:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/16	13:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/16	13:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/16	13:52:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2025/11/16	13:52:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2025/11/16	13:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/16	15:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	15:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	15:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/16	15:31:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/16	15:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/16	15:40:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/16	15:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/16	17:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	17:04:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	17:04:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/16	17:04:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/16	17:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/16	17:28:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/16	17:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/16	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/11/16	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2025/11/16	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/11/16	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/11/16	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/16	18:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2025/11/16	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/16	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/16	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/11/16	18:10:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02	03	74	01	db
2025/11/16	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/16	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/16	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/16	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/16	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/16	18:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							

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2025/11/16	18:12:58.0	XRT_FL_PROG_SET	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a
			405_OG [0x195]				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	13
2025/11/16	18:13:00.0	XRT_CTRL_AUTO_408_OG	[0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	18:41:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	18:41:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	18:41:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	18:41:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	18:44:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	19:04:30.0	XRT_Custom_430_OG	[0x1ae]				
2025/11/16	19:05:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	20:18:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	20:18:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	20:18:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	20:18:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	20:21:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	20:41:00.0	XRT_Custom_430_OG	[0x1ae]				
2025/11/16	20:42:00.0	XRT_CTRL_AUTO_424_OG	[0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	21:55:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	21:55:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	21:55:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	21:55:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	21:58:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	22:18:00.0	XRT_Custom_430_OG	[0x1ae]				
2025/11/16	22:19:00.0	XRT_CTRL_AUTO_424_OG	[0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/16	23:32:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	23:32:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/16	23:32:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/16	23:32:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/16	23:35:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/16	23:53:00.0	XRT_Custom_430_OG	[0x1ae]				
2025/11/16	23:54:00.0	XRT_CTRL_AUTO_424_OG	[0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	01:09:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	01:09:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	01:09:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	01:09:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	01:12:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/17	01:17:30.0	XRT_Custom_430_OG	[0x1ae]				
2025/11/17	01:18:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	02:33:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	02:33:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	02:33:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	02:33:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	02:36:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/17	02:51:30.0	XRT_Custom_430_OG	[0x1ae]				
2025/11/17	02:52:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	04:07:00.0	XRT_CTRL_MANU_400_OG	[0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	04:07:02.0	XRT_CTRL_MANU_402_OG	[0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	04:07:04.0	XRT_FLD_RESET_415_OG	[0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	04:07:06.0	XRT_PREFLR_STRT_404_OG	[0x194]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	04:10:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2025/11/17	04:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	04:14:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	04:14:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/17	04:14:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00
2025/11/17	04:14:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/17	04:14:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/17	04:14:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/17	04:14:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/17	04:14:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/17	04:17:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2025/11/17	04:17:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/17	04:28:00.0	XRT_Custom_430_OG [0x1ae]				
2025/11/17	04:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/17	05:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	05:37:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	05:37:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/17	05:37:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/17	05:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/17	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	06:04:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	06:04:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/17	06:05:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/17	06:05:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/17	06:05:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/17	06:07:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/11/17	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/17	06:24:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	06:24:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	06:24:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/17	06:24:30.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 03 74 01 db
2025/11/17	06:24:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/17	06:24:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/17	06:24:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/17	06:24:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/17	06:24:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/17	06:27:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/11/17	06:27:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/17	06:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/17	07:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	07:17:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	07:17:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/17	07:17:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/17	07:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/17	07:42:30.0	XRT_Custom_430_OG [0x1ae]				
2025/11/17	07:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/17	08:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	08:57:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/17	08:57:04.0	XRT_FLD_RESET_415_OG [0x19f]				

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2025/11/17	08:57:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	09:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	09:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/17	09:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	10:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	10:36:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	10:36:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	10:36:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	10:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/17	10:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	10:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	10:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/11/17	10:50:00.0	AOCs_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00	
2025/11/17	10:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/17	10:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/17	10:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/17	10:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/17	10:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	10:52:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2025/11/17	10:52:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2025/11/17	10:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	12:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	12:49:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	12:49:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/11/17	12:50:00.0	AOCs_Or-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 03 74 01 db	
2025/11/17	12:50:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/17	12:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/17	12:50:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/17	12:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/17	12:50:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	12:52:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2025/11/17	12:52:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2025/11/17	12:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	16:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	16:01:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	16:01:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	16:01:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	16:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/17	16:13:30.0	XRT_Custom_430_OG [0x1ae]					
2025/11/17	16:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/17	17:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	17:36:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	17:36:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/17	17:36:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/17	17:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/17	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/17	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2025/11/17	17:59:58.0	XRT_FOCUS_RECALIBRATE_407_OG [0x197]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2025/11/17	18:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2025/11/17	18:03:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2025/11/17	18:04:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2025/11/17	18:04:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2025/11/17	18:04:22.0	XRT_ARS_DIS_429_OG [0x1ad]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/17	18:04:24.0	XRT_QT_PROG_SET_410_OG [0x19a]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2025/11/17	18:04:26.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/17	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2025/11/17	18:10:00.0	AOCs_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02	03	74	01	db
2025/11/17	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/17	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/17	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/17	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/17	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/17	18:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2025/11/17	18:12:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2025/11/17	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/17	19:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	19:13:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	19:13:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/17	19:13:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/17	19:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/17	19:36:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/17	19:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/17	20:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	20:49:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	20:49:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/17	20:49:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/17	20:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/17	21:13:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/17	21:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/17	22:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	22:26:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/17	22:26:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/17	22:26:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/17	22:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/17	22:49:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/17	22:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/18	00:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/18	00:03:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/18	00:03:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/18	00:03:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/18	00:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/18	00:22:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/18	00:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/18	01:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							

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2025/11/18	01:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	01:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	01:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/18	01:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/18	01:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/18	01:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/18	03:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	03:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	03:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/18	03:03:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/18	03:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/18	03:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/18	03:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	03:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	03:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	03:42:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/11/18	03:43:00.0	AOCs_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/11/18	03:43:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/18	03:43:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/18	03:43:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/18	03:43:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/18	03:43:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/18	03:45:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2025/11/18	03:45:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2025/11/18	03:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/18	04:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	04:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	04:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/11/18	04:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/11/18	04:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/11/18	04:59:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/18	05:00:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	05:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	05:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	05:39:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/11/18	05:40:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2025/11/18	05:40:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2025/11/18	05:40:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/11/18	05:42:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2025/11/18	05:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/11/18	05:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	05:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/11/18	05:52:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2025/11/18	05:53:00.0	AOCs_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	02 03 74 01 db	
2025/11/18	05:53:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/11/18	05:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/11/18	05:53:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/11/18	05:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]					

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2025/11/18	05:53:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/18	05:55:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	05:55:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/11/18	05:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/18	06:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	06:09:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	06:09:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	06:09:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	06:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	06:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	06:38:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/11/18	07:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	07:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	07:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	07:49:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	07:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	08:14:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	08:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/11/18	09:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	09:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	09:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	09:29:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	09:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	09:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	09:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/11/18	10:47:30.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	11:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00