

XRT Timeline to be uploaded on 2025/11/18

Period: 2025/11/18 11:00:00 - 2025/11/22 10:25:00

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Normal mode

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XOB #1BD6: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 900s cad (G-band/Leak first)

Term	Pointing (x, y)	Comment
11/18 11:23:00 - 11/18 13:19:54	Track (741.8, 435.2) @ 11/18 11:10:00	# OP start + 10min, EIS CH obs
11/19 11:49:00 - 11/19 13:44:54	Fixed (800.0, 400.0)	EIS CH obs
PROG= 03 Inf.-time(s)		
Subr= 1	1-time(s) 2.0sec	
Seqn= 30	1-time(s) 2.0sec	
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec	
Subr= 2	8-time(s) 900.0sec	
Seqn= 8	1-time(s) 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec	
Seqn= 6	1-time(s) 2.0sec	
Al-poly/Open	Al-poly/Open close Safe Norm 125ms Obs 4x4 2048x2048 (1024, 1024) Q=98 3 0 2.0sec	
Al-poly/Open	Al-poly/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) DPCM 2 0 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1D1F: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
11/18 13:23:00 - 11/18 17:43:24	Track (396.9, -192.3) @ 11/18 13:20:00	AR 14284
11/18 17:56:30 - 11/19 01:59:54	Track (434.9, -191.1) @ 11/18 17:53:30	AR obs
11/19 06:12:30 - 11/19 11:43:30	Track (532.7, -187.7) @ 11/19 06:09:30	AR obs
11/19 13:48:00 - 11/19 17:59:54	Track (589.3, -185.5) @ 11/19 13:45:00	AR obs
11/19 18:13:00 - 11/20 01:59:54	Track (620.7, -184.1) @ 11/19 18:10:00	AR obs
11/20 06:25:30 - 11/20 10:34:30	Track (701.3, -180.2) @ 11/20 06:22:30	AR obs
PROG= 08 Inf.-time(s)		
Subr= 1	1-time(s) 2.0sec	
Seqn= 92	1-time(s) 2.0sec	
Open/G-band	Open/G-band open Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/G-band	Open/G-band close Safe Norm 1ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
Subr= 2	4-time(s) 2.0sec	
Seqn= 47	1-time(s) 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 2 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Seqn= 77	4-time(s) 600.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 0 95.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 1 95.0sec	
Al-poly/Open	thin-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec	
thin-Be/Open	med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 1 2 2.0sec	
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval	

XOB #1D19: Synoptic 8 Filter w/ Al-mesh(3/128/723), Al-poly(5/181/1443), Thin-Be(24/512/4096), Thick-Be(32768), Med-Al(181/5795/32768), Med-Be(88/4096/

Term	Pointing (x, y)	Comment
11/18 17:46:30 - 11/18 17:53:24	Fixed (0.0, 0.0)	synoptic, shifted -16.5 min
11/19 05:34:30 - 11/19 06:09:24	Fixed (0.0, 0.0)	synoptic, HOP 349 (4 hours)
11/19 18:03:00 - 11/19 18:09:54	Fixed (0.0, 0.0)	synoptic
11/20 06:06:00 - 11/20 06:22:24	Fixed (0.0, 0.0)	synoptic, HOP 349 (4 hours)

PROG= 12 1-time(s)

Subr= 1	1-time(s) 2.0sec	
Seqn= 5	1-time(s) 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
Open/Ti-poly	Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
Seqn= 55	1-time(s) 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh	Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 98	1-time(s) 2.0sec	
Al-poly/Open	Al-poly/Open close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	Al-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Al-poly/Open	Al-poly/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Seqn= 76	1-time(s) 2.0sec	
thin-Be/Open	thin-Be/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
thin-Be/Open	thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	

XOB #1D1A: HOP349 - 3-filter Synoptics (Al-mesh[2/128/723], Al-poly[5/181/1443], thin-Be[16/512/3897] with 512x512 G-band+Leak - 120min cad) + CME wa

Term	Pointing (x, y)	Comment
11/19 02:19:00 - 11/19 05:02:00	Fixed (0.0, 0.0)	synoptic, HOP 349 (4 hours)
11/20 02:03:00 - 11/20 05:35:00	Fixed (0.0, 0.0)	synoptic, HOP 349 (4 hours)

11/20 02:00:00	11/20 0
PROG= 04	Inf.-time(s)

Subr=	1-time(s)	600.0sec										
Seqn= 55	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 98	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 79	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 30	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2	6-time(s)	1200.0sec										
Seqn= 8	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 74	1-time(s)	2.0sec										
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	2.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

Flare mode

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XOB #1D27: HOP500 SOOP FLARE MODE Be Thick 2s Fixed Exposure

Term	Pointing (x, y)	Comment
11/18 11:23:00 - 11/18 13:19:54	Track (741.8, 435.2) @ 11/18 11:10:00	# OP start + 10min, EIS CH obs
11/18 13:23:00 - 11/18 17:43:24	Track (396.9, -192.3) @ 11/18 13:20:00	AR 14284
11/18 17:56:30 - 11/19 01:59:54	Track (434.9, -191.1) @ 11/18 17:53:30	AR obs
11/19 02:19:00 - 11/19 05:02:00	Fixed (0.0, 0.0)	synoptic, HOP 349 (4 hours)
11/19 06:12:30 - 11/19 11:43:30	Track (532.7, -187.7) @ 11/19 06:09:30	AR obs
11/19 11:49:00 - 11/19 13:44:54	Fixed (800.0, 400.0)	EIS CH obs
11/19 13:48:00 - 11/19 17:59:54	Track (589.3, -185.5) @ 11/19 13:45:00	AR obs
11/19 18:13:00 - 11/20 01:59:54	Track (620.7, -184.1) @ 11/19 18:10:00	AR obs
11/20 02:03:00 - 11/20 05:35:00	Fixed (0.0, 0.0)	synoptic, HOP 349 (4 hours)
11/20 06:25:30 - 11/20 10:34:30	Track (701.3, -180.2) @ 11/20 06:22:30	AR obs

PROG= 19 30-time(s)

Subr= 1		1-time(s)		2.0sec											
Seqn= 87		1-time(s)		2.0sec											
		Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
		Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
Subr= 2		120-time(s)		2.0sec											

└	Seqn= 31	1-time(s)	2.0sec											
	└	med-Be/Open	Open/thick-Al	close	Safe	Norm	16ms	Obs	1x1	384x384 (1024, 1024)	DPCM	3	0	2.0sec
└	Seqn= 20	10-time(s)	2.0sec											
	└	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

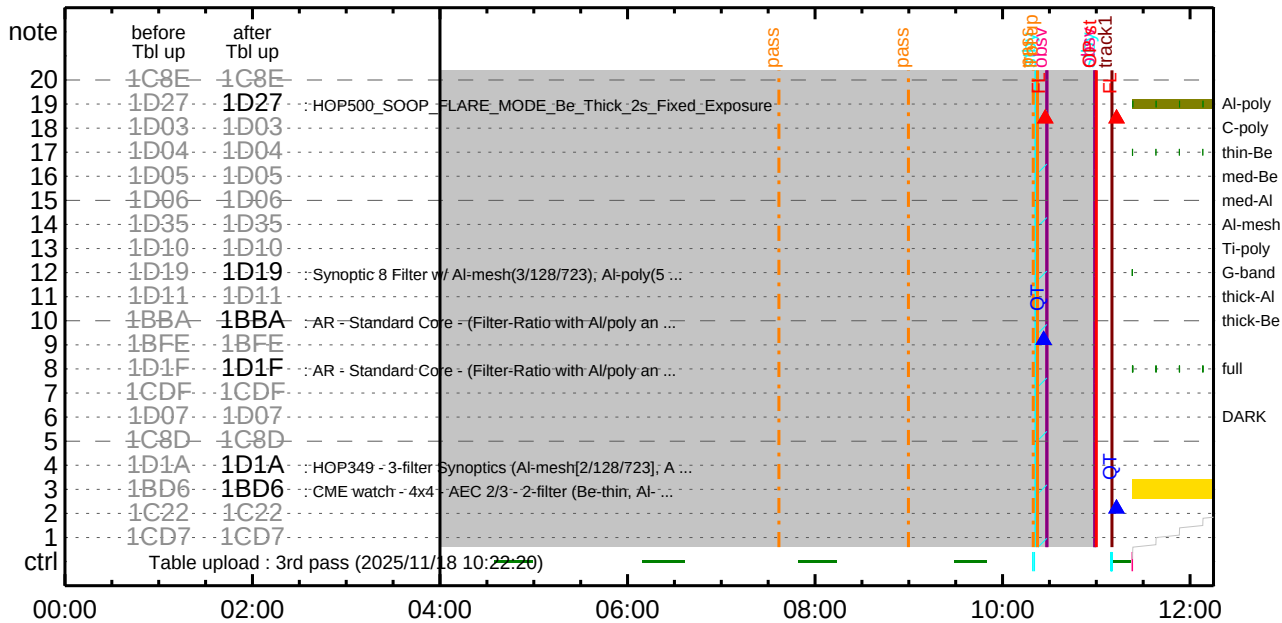
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Flare Detection

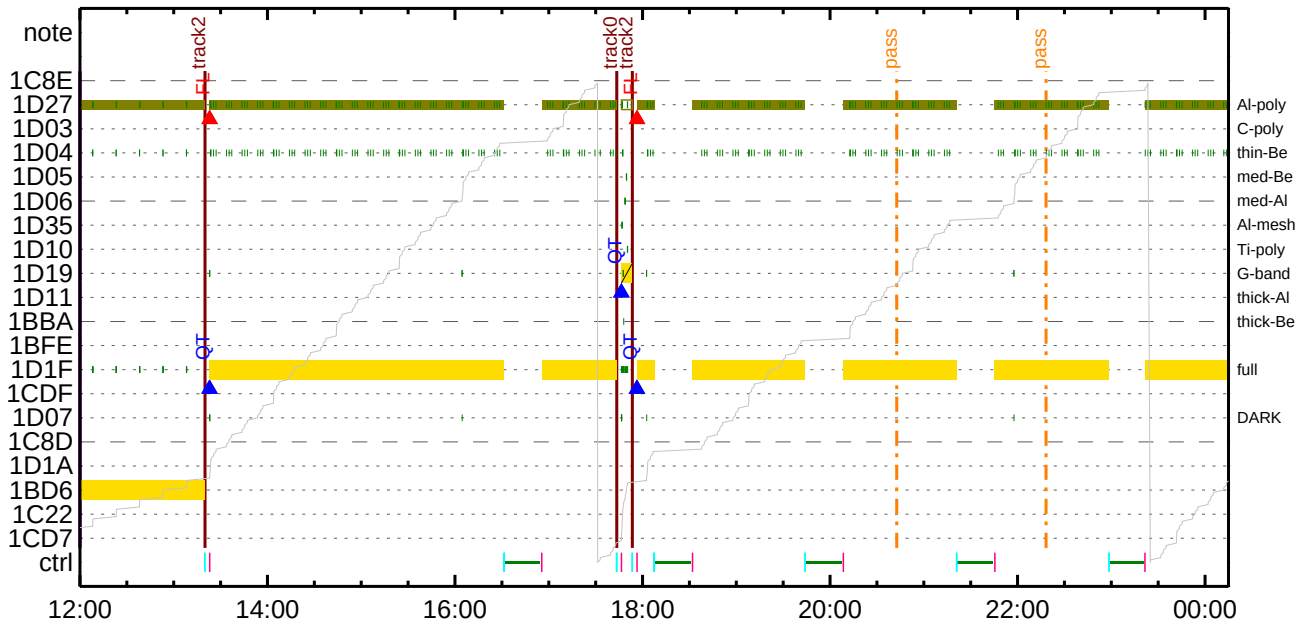
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FLD Patrol														
Term					Pointing (x, y)					Comment				
11/18 10:23:20 - 11/18 17:43:48					cannot be identified									
11/18 17:53:48 - 11/19 05:31:48					Track (	434.9,	-191.1)	@ 11/18 17:53:30	AR obs					
11/19 06:09:48 - 11/19 18:00:18					Track (	532.7,	-187.7)	@ 11/19 06:09:30	AR obs					
11/19 18:10:18 - 11/20 06:03:18					Track (	620.7,	-184.1)	@ 11/19 18:10:00	AR obs					
11/20 06:22:48 - 11/22 10:25:00					Track (	701.3,	-180.2)	@ 11/20 06:22:30	AR obs					
Al-poly/Open		Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8			Q=50	30sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

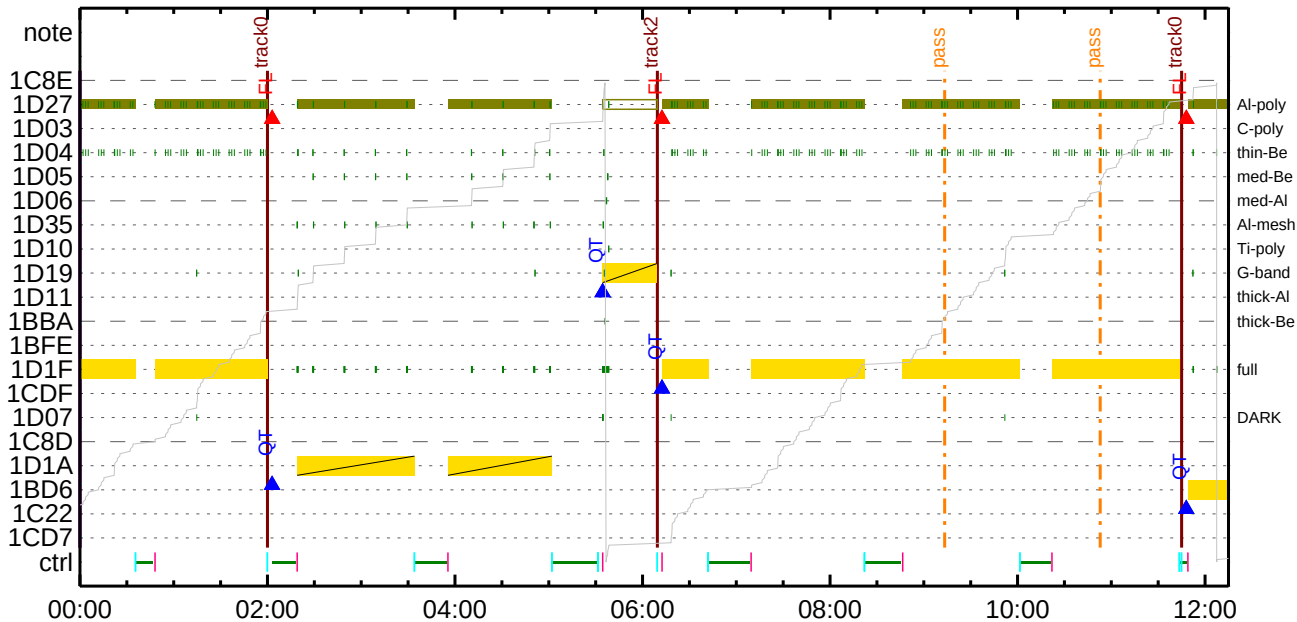
CMDI #0971 2025/11/18



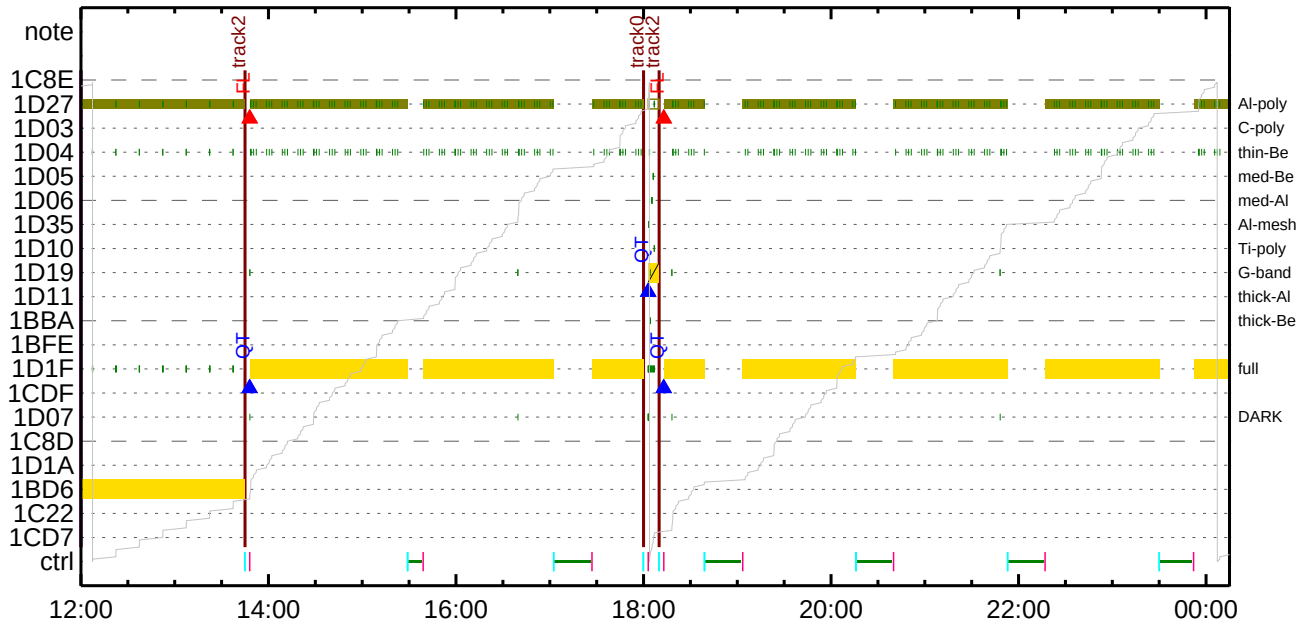
CMDI #0971 2025/11/18



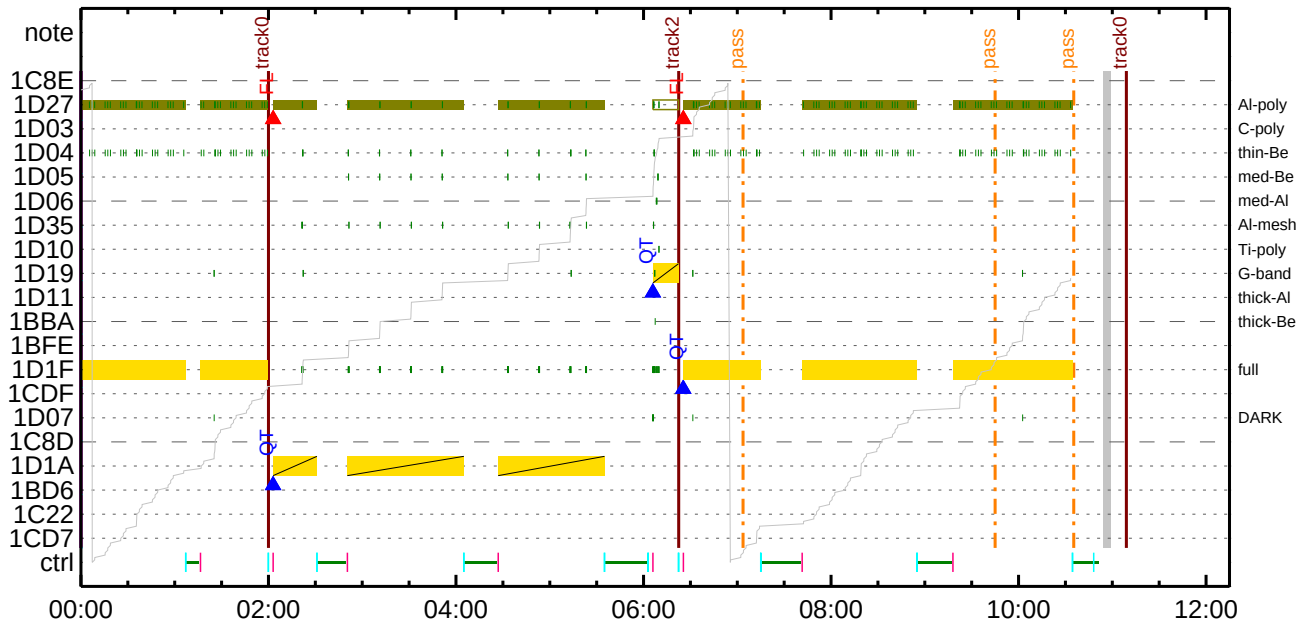
CMDI #0971 2025/11/19



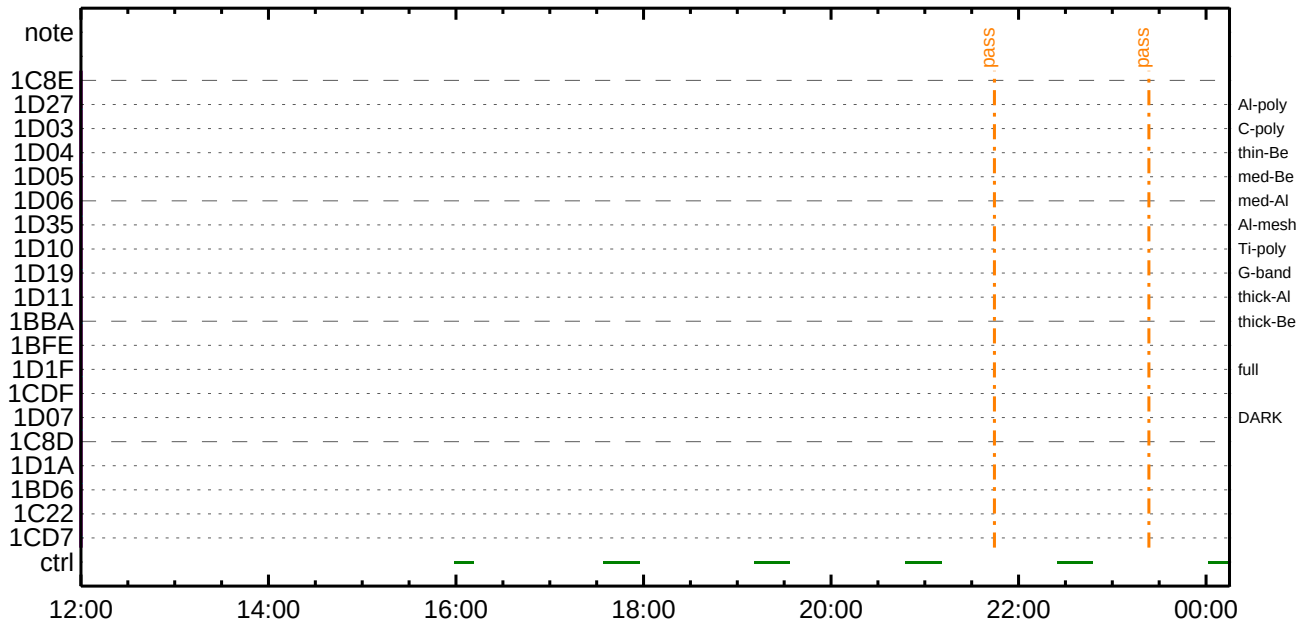
CMDI #0971 2025/11/19



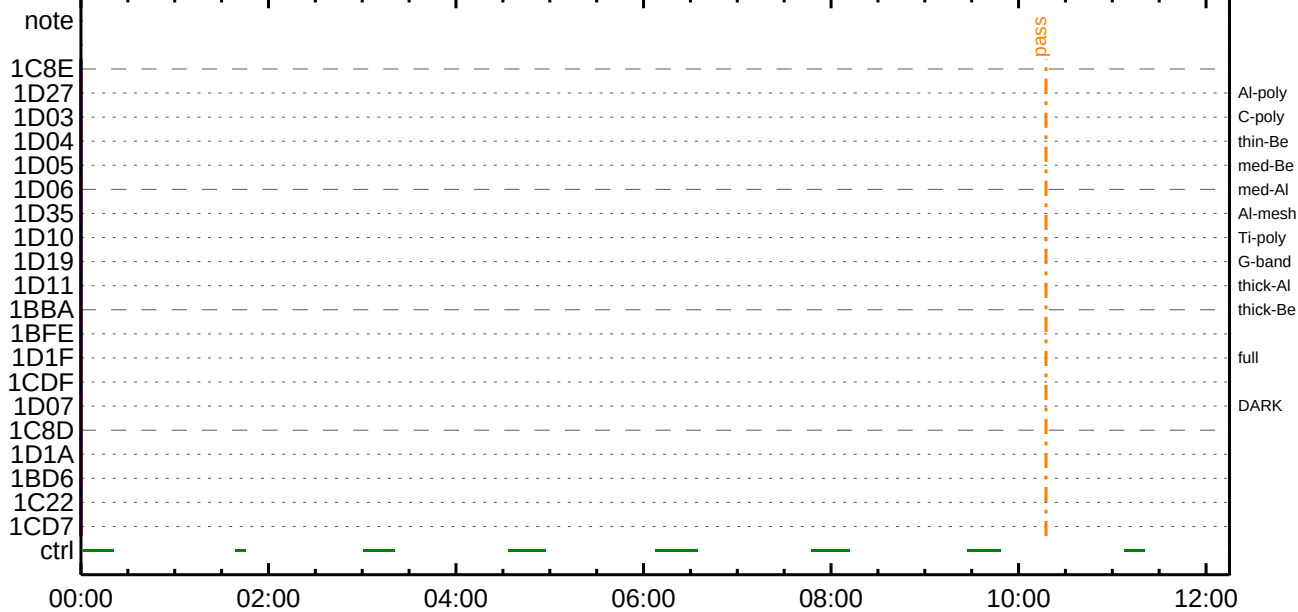
CMDI #0971 2025/11/20



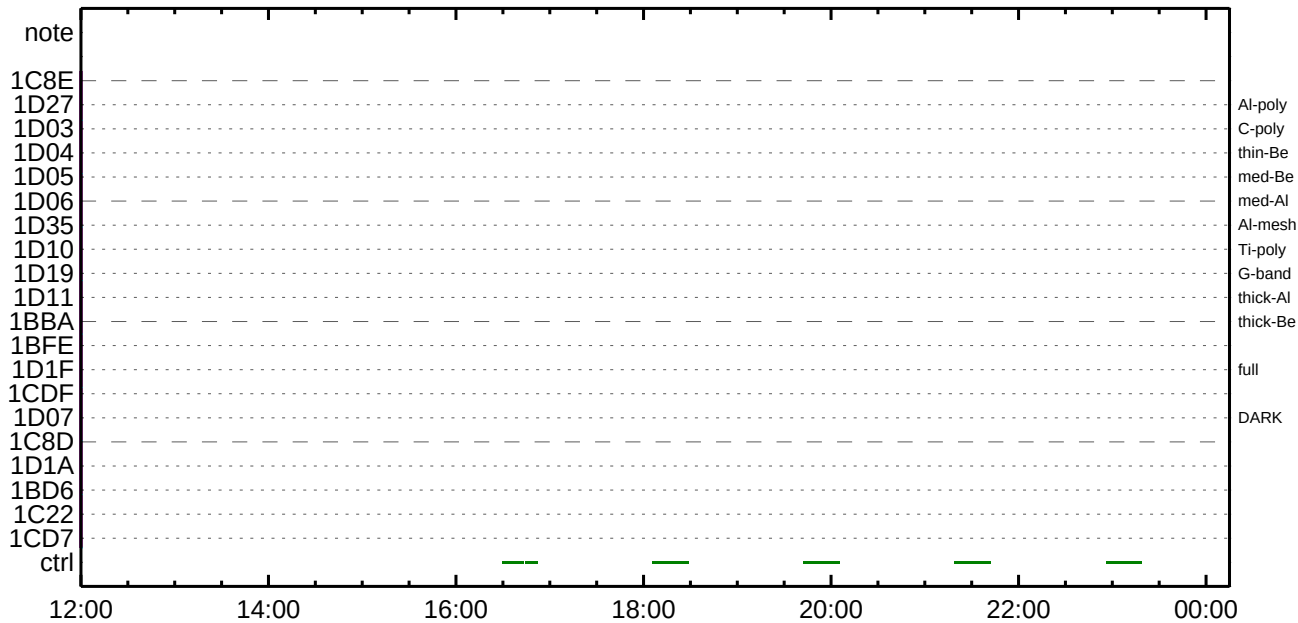
CMDI #0971 2025/11/20



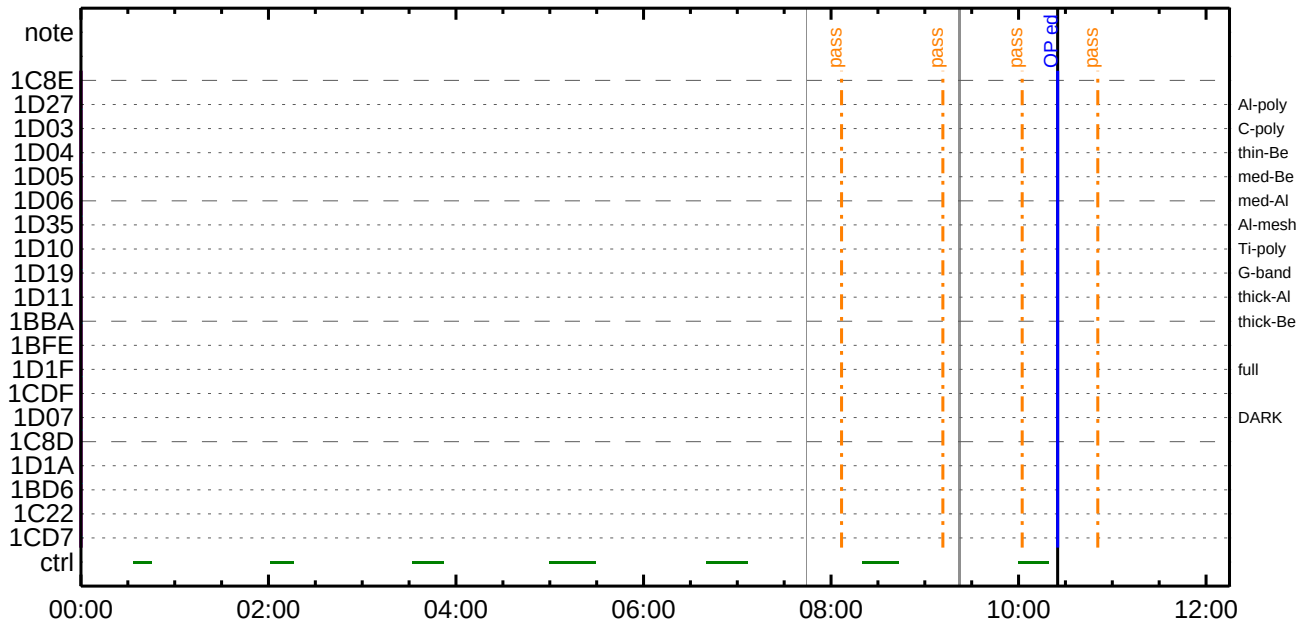
CMDI #0971 2025/11/21

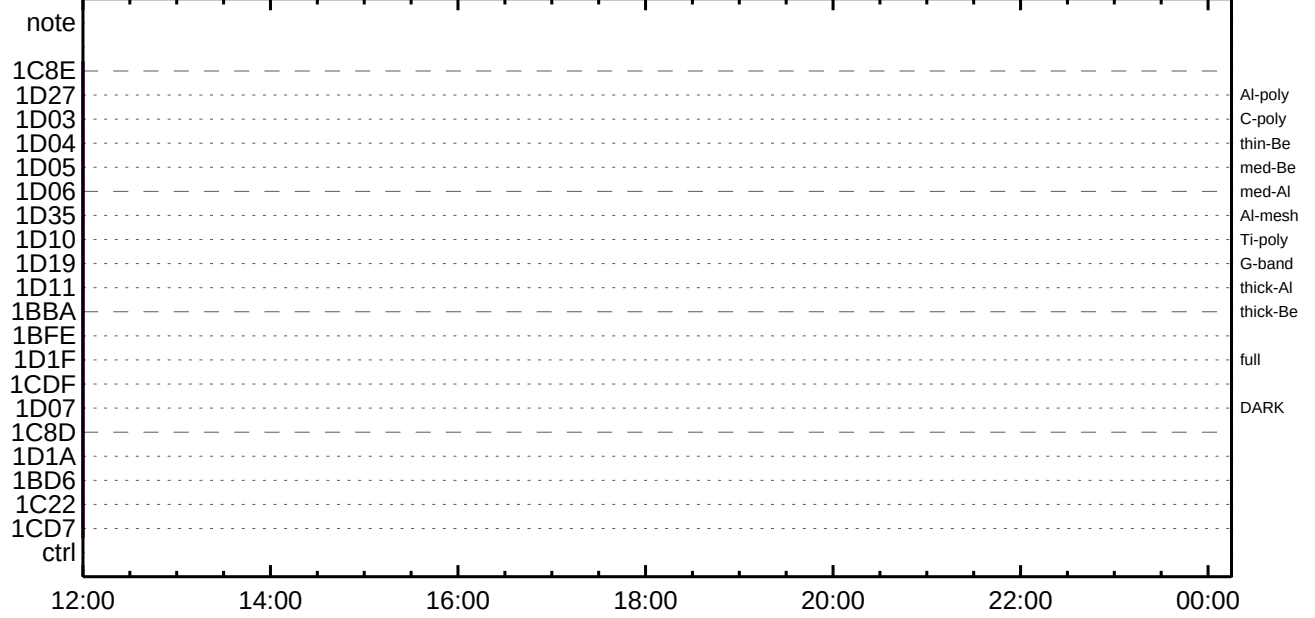


CMDI #0971 2025/11/21



CMDI #0971 2025/11/22





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main-641 2025-11-18 11:54:27 194 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ã»Ü;ä
0005 C.
0006 C. ŲÃŲß;¼Ų³ŲÐŲóŲÉÃ÷:ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āī;ĒñçĀñ•µ°E»İ×AÇñİŲçŲÃŲ×Ųī;¼ŲĒ;ĒĒèµ•īīĒ;ĒñĒ¼°ÇÖñ•ñç¼ī¹çñİ;çÀ®,ùñ¹ñĒñÐñÇÃ÷:ç®ñ•ñĒñññ³ñĒ;Ē
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGŲī;¼ŲĒ;|ŲÃŲóŲ×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGŲī;¼ŲĒ;ä
0019 . S. OP op-641:OP
0020 ()
0021 . S. OG og-641:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPîî°èŲÃŲóŲ×;ä
0025 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. ŲÃŲóŲ×½ªī»ñò³İÇ$
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGñî¼Ē¹ç•Ē²İOKñò³İÇ$
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. ŲÃŲóŲ×½ªī»ñò³İÇ$
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGñî¼Ē¹ç•Ē²İOKñò³İÇ$
0062 C.
0063 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. ŲÃŲóŲ×½ªī»ñò³İÇ$
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG, RAM ID=OPñî¼Ē¹ç•Ē²İOKñò³İÇ$
0081 C.
0082 . C. ***** °Ē²¼ñİ¼Ā`ŲĀ°ñĒĒ¬ñ°Ã÷:ç® (¼Āµ-ŲÃŲóŲ×¼Ē½çñòĀŌĀñÇ½ªñ•ñĒ¼ī¹çñÇñâ) *****
0083 C. DHUŲā;¼ŲĒ;Ē¼Ų½,Ųī;¼ŲĒ;Ēñòİāñ¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;$ OPOG UPLOADñ¬Ã÷:ç®NGñİ¼ī¹ç;ç°Ē²¼ñİTI-CMDÃ÷:ç®ñİ¼Ā¹Ōñ•ñĒñññ³ñĒ;Ē

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0096	C.	SET EDUMP I±°iYNY¹Q¹Ô±;³E;f		
0097	C.			
0098	C.	TIY³YDYOÉDÄDİ; (UT)		
0099	+. TI	2025-11-18 10:55:00.0		
0100	DC	01-B3 DHU_OP_STOP		
0101	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0102	C.			
0103	+. TI	2025-11-18 10:55:01.0		
0104	DC	01-B4 DHU_OP_COPY		
0105	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0106	C.			
0107	+. TI	2025-11-18 10:55:01.0		
0108	DC	01-B5 DHU_OPOG_COPY		
0109	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0110	C.			
0111	+. TI	2025-11-18 10:59:59.5		
0112	DC	01-B2 DHU_OP_START		
0113	C.	¢¢[HK1_TI_CMD_NUM]	EQ	1COUNTUP
0114	C.			
0115	C.	°Ê²¼±İÄë¼İİÑ±İYÄY\$YÄY¹¹äiÜ		
0116	C.	¢¢[HK1_TI_CMD_ENA/DIS]	EQ	ENA
0117	C.	¢¢[HK1_TI_CMD_NUM]	EQ	4
0118	C.	¢¢[HK1_NEXT_EXEC_PIM]	EQ	DHU
0119	C.	¢¢[HK1_NEXT_EXEC_DC]	EQ	0xB3
0120	C.			
0121	C.	*****		
0122	C.	TIİİ°èYÄYOY×		
0123	C.	*****		
0124	C.			
0125	C.	TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)		
0126	+. DC	01-23 DHU_DMA_DMP_PRM_SET		
0127	BC	(03 ab 03 01 02)		
0128	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	07
0129	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	2B
0130	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	3
0131	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0
0132	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU
0133	+. DC	01-22 DHU_MODE_CHNG		
0134	BC	(07 0b f8)		
0135	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7
0136	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s
0137	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k
0138	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0139	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC
0140	C.			
0141	C.	YÄYOY×½¹İ»±ò³İÇ\$		
0142	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON
0143	C.			
0144	C.	RAM ID=TI_TBL±İ¼È¹Ç•ë²İOK±ò³İÇ\$		
0145	C.			
0146	C.	DHUYâ;¼YÈ;È¼Y½,Yİ;¼YÈ;È±òİá±¹		
0147	+. DC	01-22 DHU_MODE_CHNG		
0148	BC	(02 0a f8)		
0149	C.	¢¢[HK1_PKT_FORM_NO]	EQ	2
0150	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S
0151	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K
0152	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M
0153	C.			
0154	C.	Stop EIS observation and temporarily disable EIS mode changes		
0155	C.			
0156	C.			
0157	C.	***** Start EIS operation (TI set) *****		
0158	C.	Execute, after the success of OP upload.		
0159	C.	Set EIS TI-commands		
0160	+. TI	2025-11-18 10:59:30.0		
0161	DC	07-FC EIS_MODE_MANU		
0162	BC	(21 02)		
0163	+. TI	2025-11-18 10:59:40.0		
0164	DC	07-FC EIS_MODE_CHG_DIS		
0165	BC	(22)		
0166	C.	[] [HK1_TI_CMD_NUM]	EQ	2 COUNTUP
0167	C.	***** End EIS operation (TI set) *****		
0168	C.			
0169	C.			
0170	C.			
0171	C.	***** XRT START *****		
0172	C.	Execute, after the success of OP upload.		
0173	+. TI	2025-11-18 10:59:00.0		
0174	DC	07-F0 MDP_XRT_MODE_STBY		
0175	BC	(c3)		
0176	C.	[] [HK1_TI_CMD_NUM]	EQ	1COUNTUP
0177	C.			
0178	C.	***** XRT END *****		
0179	C.			
0180	C.	***** MDP ´GÄİ±İ»ö¼Y±EÄD±¹±èDCBC•×²è *****		
0181	C.	(¼Ä°İYÖYÄYÈYDYEYÄYÇYè±E½¼±¼Ä»Ü±¹±è)		
0182	S.	DC-BC dcbc-402:DCBC		
0183		(MDP_known_event)		
0184	C.			
0185	C.			
0186	C.	***** YDY¹•İ Daily±İİÑ±E´Ø±¹±èDCBC•×²è *****		
0187	S.	DC-BC dcbc-153:DCBC		
0188		(SPECIAL-CMD_DAILY_OPERATIN_DCB)		
0189	C.			
0190	C.			
0191	C.	;äLOSYÄY\$YÄY¹¹Ä»Ü;ä		
0192				


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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 85 83 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 80 08 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 20 08)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 80 80 08 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 +. DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 +. DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 0a)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 13)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2025-11-18 10:59:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîñÎ»ö¼ÝñÊÃÐñ¹ñÐDCBC•×²è *****
0173 C. (¾å°î¥Ó¥Ã¥Ê¥Ð¥Ê¥â¥ç¥ËñÊ½¾ñ¼Ã»Ûñ¹ñË)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** ¥Ð¥¹•Ï Daily±¿îññÊ´Øñ¹ñÐDCBC•×²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ;ãLOS¥Á¥$¥Ã¥¬¼Ã»Û;ã
0184 C.
0185 . C. ***** LOS *****
0186 C.
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*** OP Sequence for XRT ***

2025/11/18	11:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	11:09:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	11:09:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	11:09:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	11:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	11:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	11:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/18	11:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2025/11/18	11:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/18	11:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/18	11:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/18	11:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/18	11:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	11:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	11:12:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2025/11/18	11:12:58.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/18	11:22:00.5	XRT_Custom_430_OG [0x1ae]			
2025/11/18	11:23:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	13:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	13:19:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	13:19:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/18	13:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 03 74 01 db
2025/11/18	13:20:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/18	13:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/18	13:20:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/18	13:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/18	13:20:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	13:22:56.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/11/18	13:22:58.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/18	13:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	16:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	16:31:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	16:31:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	16:31:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	16:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	16:54:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/18	16:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	17:43:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	17:43:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	17:43:28.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/18	17:43:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/11/18	17:43:48.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/18	17:43:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/18	17:43:52.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/18	17:46:28.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/11/18	17:46:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]			

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2025/11/18	17:53:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	17:53:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	17:53:30.0	AOCs_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/18	17:53:48.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 03 74 01 db
2025/11/18	17:53:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/18	17:53:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/18	17:53:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/18	17:53:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/18	17:56:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	17:56:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/11/18	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/18	18:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	18:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	18:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	18:07:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	18:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	18:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	18:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/18	19:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	19:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	19:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	19:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	19:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	20:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	20:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/18	21:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	21:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	21:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	21:21:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	21:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	21:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	21:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/18	22:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/18	22:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	22:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/18	22:58:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/18	23:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/18	23:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/18	23:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/19	00:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	00:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	00:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	00:35:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/19	00:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/19	00:47:01.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/19	00:48:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0
2025/11/19	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/11/19	02:00:00.0	AOCs_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2025/11/19	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/11/19	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/11/19	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/11/19	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/19	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/19	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2025/11/19	02:02:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2025/11/19	02:18:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/19	02:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/19	03:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	03:34:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	03:34:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/19	03:34:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/19	03:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/11/19	03:54:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/19	03:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/19	05:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	05:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	05:02:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/19	05:02:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/19	05:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2025/11/19	05:31:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	05:31:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	05:31:28.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2025/11/19	05:31:48.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2025/11/19	05:31:50.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2025/11/19	05:31:52.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/19	05:34:28.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2025/11/19	05:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/19	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	06:09:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	06:09:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2025/11/19	06:09:30.0	AOCs_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02	03	74	01
2025/11/19	06:09:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2025/11/19	06:09:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2025/11/19	06:09:52.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2025/11/19	06:09:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2025/11/19	06:09:56.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/19	06:12:26.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2025/11/19	06:12:28.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2025/11/19	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2025/11/19	06:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	06:42:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2025/11/19	06:42:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2025/11/19	06:42:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2025/11/19	06:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2025/11/19	07:08:30.0	XRT_Custom_430_OG [0x1ae]		
2025/11/19	07:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/19	08:22:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	08:22:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	08:22:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/19	08:22:06.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/11/19	08:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/11/19	08:45:30.0	XRT_Custom_430_OG [0x1ae]		
2025/11/19	08:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/19	10:01:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	10:01:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	10:01:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/19	10:01:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/11/19	10:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/11/19	10:21:00.0	XRT_Custom_430_OG [0x1ae]		
2025/11/19	10:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/19	11:43:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	11:43:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	11:43:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/19	11:43:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/11/19	11:44:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	11:44:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	11:44:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2025/11/19	11:45:00.0	AOCS_ORe-point_Start_4_OG [0x09a]		
		AOCU_NM	5	02-76 00 dc 73 b8 e6
2025/11/19	11:45:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2025/11/19	11:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2025/11/19	11:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2025/11/19	11:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/11/19	11:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/19	11:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/11/19	11:47:56.0	XRT_QT_PROG_SET_428_OG [0x1ac]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 03
2025/11/19	11:47:58.0	XRT_FL_PROG_SET_405_OG [0x195]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2025/11/19	11:48:00.0	XRT_Custom_430_OG [0x1ae]		
2025/11/19	11:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/19	13:44:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	13:44:56.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	13:44:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2025/11/19	13:45:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 02 03 74 01 db
2025/11/19	13:45:18.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2025/11/19	13:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2025/11/19	13:45:22.0	XRT_AEC_RESET_448_OG [0x1c0]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2025/11/19	13:45:24.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2025/11/19	13:45:26.0	XRT_FLD_RESET_434_OG [0x1b2]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/11/19	13:47:56.0	XRT_QT_PROG_SET_431_OG [0x1af]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 08
2025/11/19	13:47:58.0	XRT_FL_PROG_SET_405_OG [0x195]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2025/11/19	13:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/11/19	15:29:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	15:29:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/11/19	15:29:04.0	XRT_FLD_RESET_415_OG [0x19f]		

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2025/11/19	15:29:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/19	15:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/19	15:38:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/19	15:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	17:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	17:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	17:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/19	17:02:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/19	17:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/19	17:26:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	17:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	17:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	17:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/19	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2025/11/19	18:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/19	18:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/19	18:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/19	18:02:58.0	XRT_QT_PROG_SET_410_OG [0x19a]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/11/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	18:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	18:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/19	18:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	02 03 74 01 db
2025/11/19	18:10:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/19	18:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/19	18:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/19	18:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/19	18:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/19	18:12:56.0	XRT_QT_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/11/19	18:12:58.0	XRT_FL_PROG_SET_405_OG [0x195]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	18:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	18:39:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	18:39:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/19	18:39:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/19	18:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/19	19:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	19:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	20:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	20:16:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	20:16:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/19	20:16:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/19	20:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/19	20:39:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/19	20:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	21:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/19	21:53:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2025/11/19	21:53:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/19	21:53:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/19	21:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/19	22:16:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/19	22:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/19	23:30:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/19	23:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/19	23:30:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/19	23:30:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/19	23:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/19	23:51:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/19	23:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/20	01:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	01:07:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	01:07:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/20	01:07:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/20	01:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/20	01:15:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/20	01:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/20	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	01:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2025/11/20	02:00:00.0	AOCs_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2025/11/20	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2025/11/20	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2025/11/20	02:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2025/11/20	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2025/11/20	02:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/20	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2025/11/20	02:02:58.0	XRT_FL_PROG_SET_405_OG [0x195]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13				
2025/11/20	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/20	02:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	02:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	02:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/20	02:31:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/20	02:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/20	02:49:30.0	XRT_Custom_430_OG [0x1ae]							
2025/11/20	02:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/20	04:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	04:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	04:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/20	04:05:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2025/11/20	04:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2025/11/20	04:26:00.0	XRT_Custom_430_OG [0x1ae]							
2025/11/20	04:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2025/11/20	05:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	05:35:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2025/11/20	05:35:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2025/11/20	05:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2025/11/20	05:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/20	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	06:02:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	06:02:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/11/20	06:03:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/11/20	06:03:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/11/20	06:03:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/20	06:05:58.0	XRT_QT_PROG_SET_410_OG [0x19a]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2025/11/20	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/20	06:22:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	06:22:26.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	06:22:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/11/20	06:22:30.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 03 74 01 db
2025/11/20	06:22:48.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/11/20	06:22:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/11/20	06:22:52.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/11/20	06:22:54.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/11/20	06:22:56.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/20	06:25:26.0	XRT_QT_PROG_SET_431_OG [0x1af]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/11/20	06:25:28.0	XRT_FL_PROG_SET_405_OG [0x195]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2025/11/20	06:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/20	07:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	07:15:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	07:15:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/20	07:15:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/20	07:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/20	07:40:30.0	XRT_Custom_430_OG [0x1ae]			
2025/11/20	07:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/20	08:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	08:55:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	08:55:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/20	08:55:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/20	08:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/20	09:17:00.0	XRT_Custom_430_OG [0x1ae]			
2025/11/20	09:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/11/20	10:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	10:34:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	10:34:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/11/20	10:34:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/11/20	10:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/11/20	10:48:05.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/11/20	11:09:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00