

Period: 2025/12/16 11:13:00 - 2025/12/20 10:38:00

XOB #1CED: Synoptic for HOP448 w/ Al-mesh(5/128/723), Al-poly(12/181/1443), Thin-Be(33/512/4096), Thick-Be(32768), Med-Al(256/8192/32768), Med-Be(12/181/1443)														
Term		Pointing (x, y)			Comment									
12/16 17:59:00 - 12/16 18:05:54		Fixed (0.0, 0.0)			synoptic, shifted -4.0 min									
12/17 06:03:00 - 12/17 06:09:54		Fixed (0.0, 0.0)			HOP349 + synoptic									
12/17 18:12:00 - 12/17 18:18:54		Fixed (0.0, 0.0)			synoptic, shifted 9.0 min									
12/18 06:28:00 - 12/18 06:34:54		Fixed (0.0, 0.0)			HOP349 + synoptic, shifted 25.0 min									
PROG= 08 1-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 5 1-time(s) 2.0sec														
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
Seqn= 26 1-time(s) 2.0sec														
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 15 1-time(s) 2.0sec														
Al-poly/Open		Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Al-poly/Open		Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 83 1-time(s) 2.0sec														
thin-Be/Open		thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
thin-Be/Open		thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 23 1-time(s) 4.0sec														
Open/G-band		Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
Open/G-band		Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 1-time(s) 2.0sec														
Seqn= 41 1-time(s) 2.0sec														
Open/thick-Be		Open/thick-Be	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
Seqn= 17 1-time(s) 2.0sec														
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Al/Open		med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Al/Open		med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Seqn= 33 1-time(s) 2.0sec														
med-Be/Open		Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Be/Open		med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
med-Be/Open		med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min		
Term	Pointing (x, y)	Comment

12/17 04:07:30 - 12/17 05:59:54 Fixed (0.0, 0.0) HOP349 + synoptic
12/18 04:03:00 - 12/18 06:24:54 Fixed (0.0, 0.0) HOP349 + synoptic, shifted 25.0 min

PROG= 02 Inf-time(s)												
Subr= 1		1-time(s)		300.0sec								
Seqn=100		1-time(s)		2.0sec								
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 84		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/Open		close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 57		1-time(s)		2.0sec								
thin-Be/Open		thin-Be/Open		close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
thin-Be/Open		thin-Be/Open		close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
Seqn= 81		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0 0 2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0 0 2.0sec
Subr= 2		6-time(s)		1200.0sec								
Seqn= 8		1-time(s)		2.0sec								
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
Seqn= 6		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2 0 2.0sec
Seqn= 29		1-time(s)		2.0sec								
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3 0 2.0sec
Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + G

Term	Pointing (x, y)	Comment
12/16 11:35:46 - 12/16 17:55:54	Track (731.7, -271.4) @ 12/16 11:23:00	# OP start + 10min + AR14307
12/16 18:09:00 - 12/17 03:46:00	Track (766.9, -272.0) @ 12/16 18:06:00	AR obs.
12/17 04:07:30 - 12/17 05:59:54	Fixed (0.0, 0.0)	HOP349 + synoptic
12/17 06:13:00 - 12/17 18:08:54	Track (821.8, -273.3) @ 12/17 06:10:00	AR obs.
12/17 18:22:00 - 12/18 03:59:54	Track (865.6, -274.9) @ 12/17 18:19:00	AR obs.
12/18 04:03:00 - 12/18 06:24:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 25.0 min
12/18 06:38:00 - 12/18 09:07:30	Fixed (935.6, -227.6)	AR obs. (off limb)

PROG= 14 30-time(s)												
Subr= 1		1-time(s)		2.0sec								
Seqn= 68		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0 0 2.0sec
Subr= 2		20-time(s)		2.0sec								
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn= 73		1-time(s)		10.0sec								
thin-Be/Open		med-Be/Open		close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
med-Be/Open		Open/thick-Al		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Subr= 3		1-time(s)		2.0sec								
Seqn= 10		1-time(s)		2.0sec								
med-Al/Open		med-Al/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn= 87		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

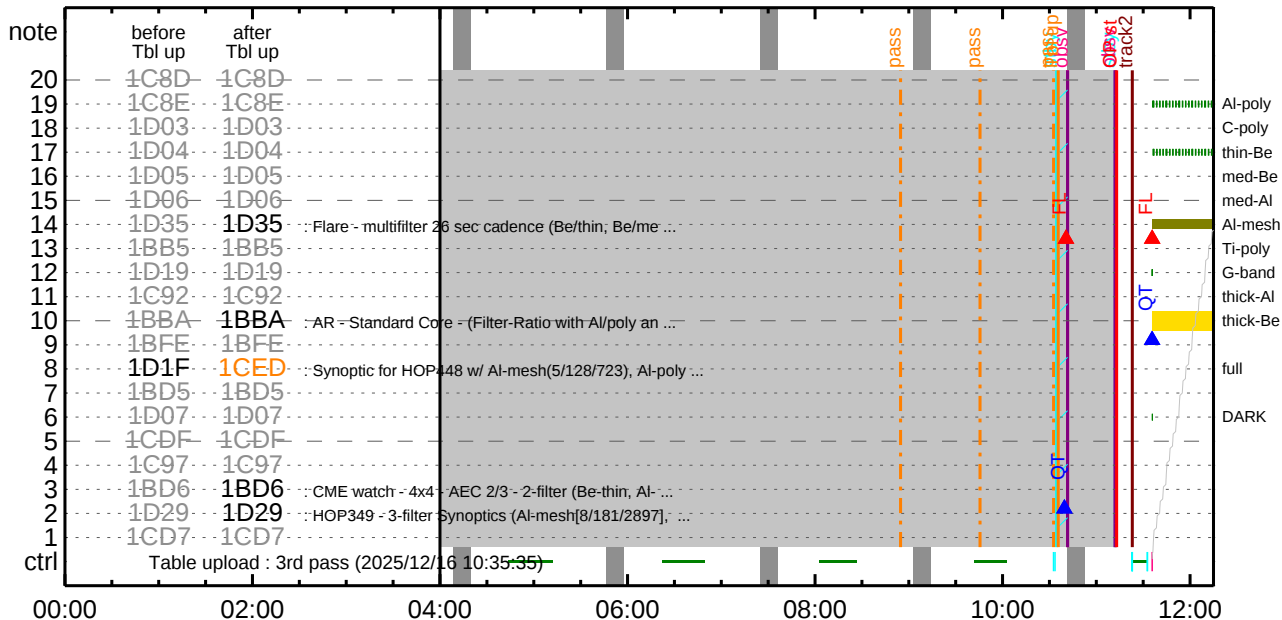
Flare Detection

* * * * *

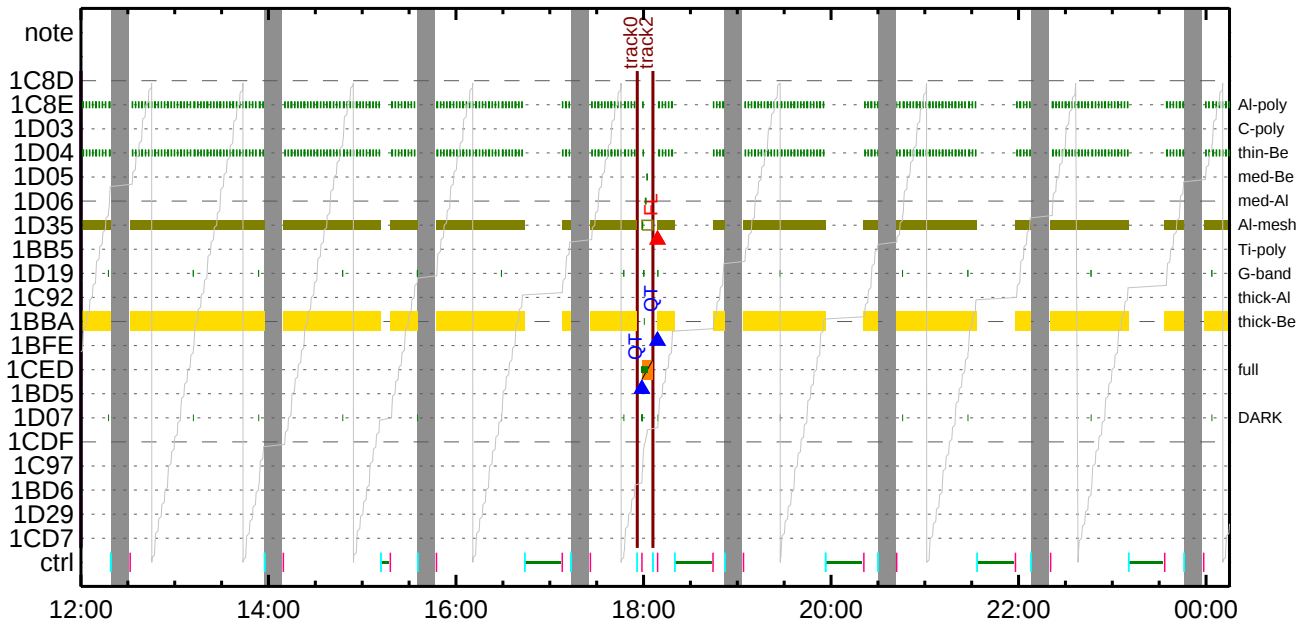
FLD Patrol		
Term	Pointing (x, y)	Comment
12/16 10:36:35 - 12/16 17:56:18	cannot be identified	
12/16 18:06:18 - 12/17 06:00:18	Track (766.9, -272.0) @ 12/16 18:06:00	AR obs.
12/17 06:10:18 - 12/17 18:09:18	Track (821.8, -273.3) @ 12/17 06:10:00	AR obs.
12/17 18:19:18 - 12/18 06:25:18	Track (865.6, -274.9) @ 12/17 18:19:00	AR obs.

12/18 06:35:18 - 12/20 10:38:00			Fixed (935.6, -227.6)			AR obs. (off limb)					
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

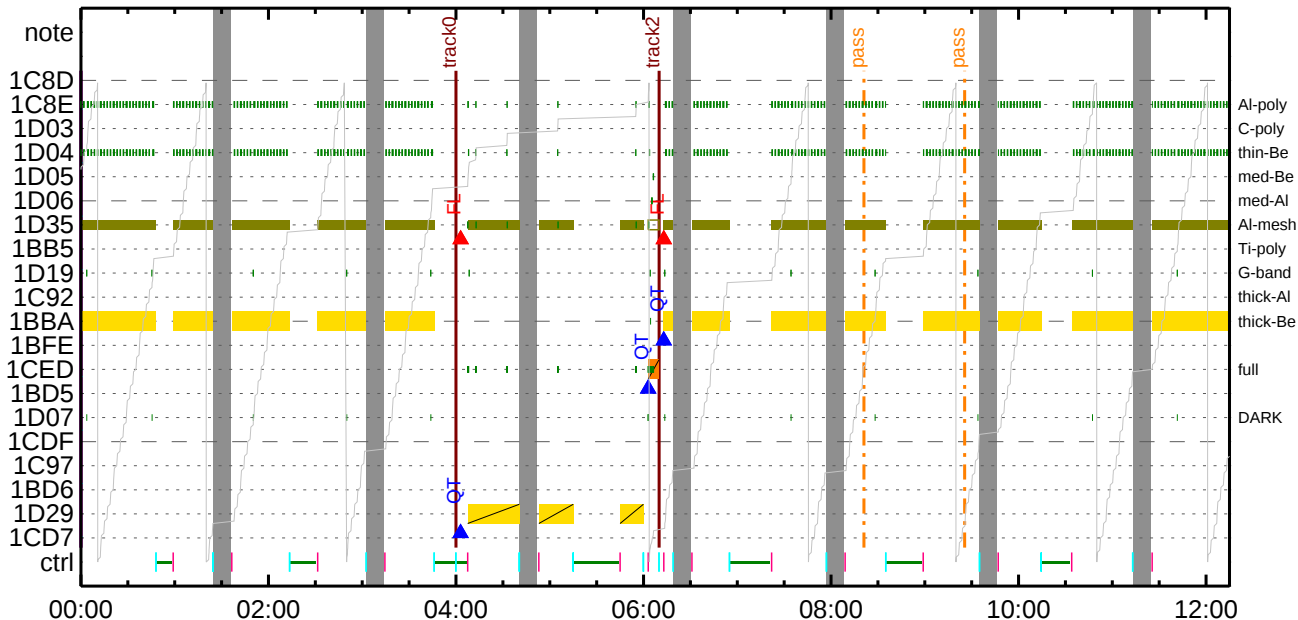
CMDI #0021 2025/12/16



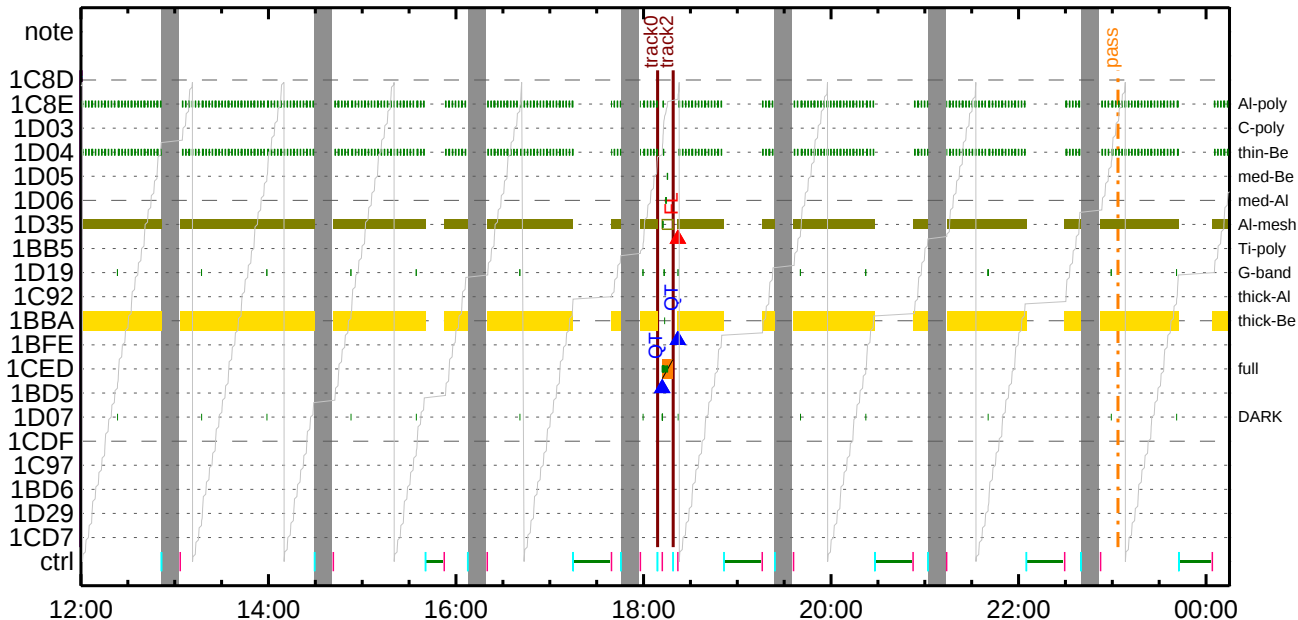
CMDI #0021 2025/12/16



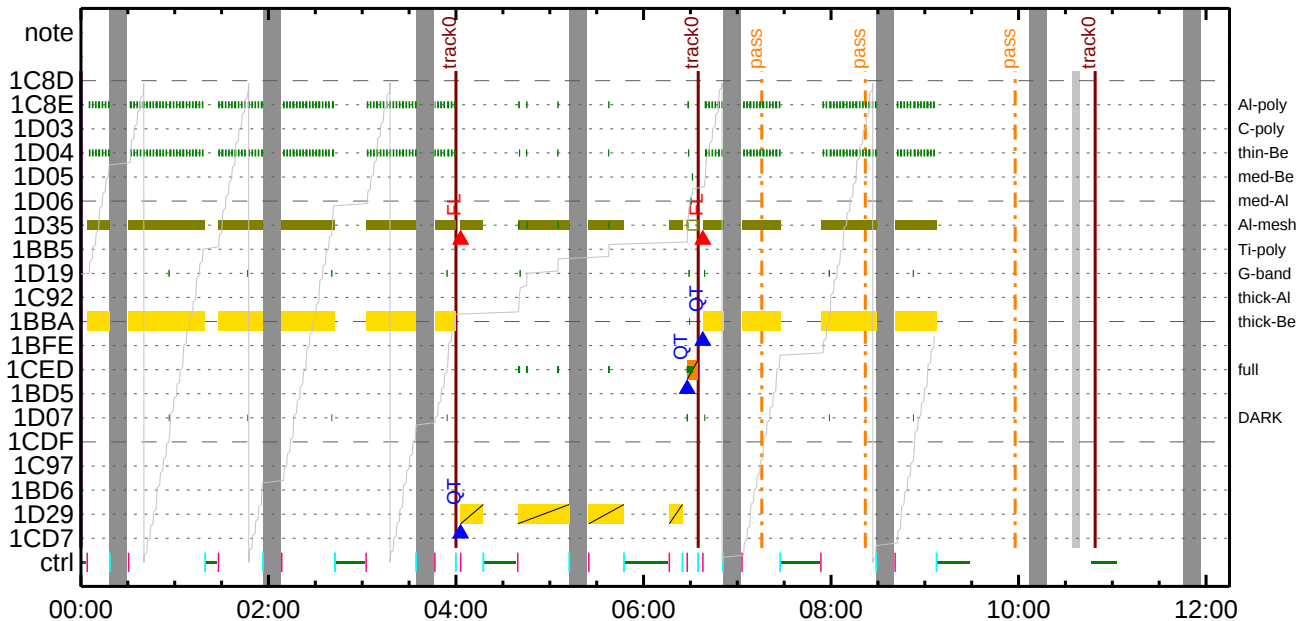
CMDI #0021 2025/12/17



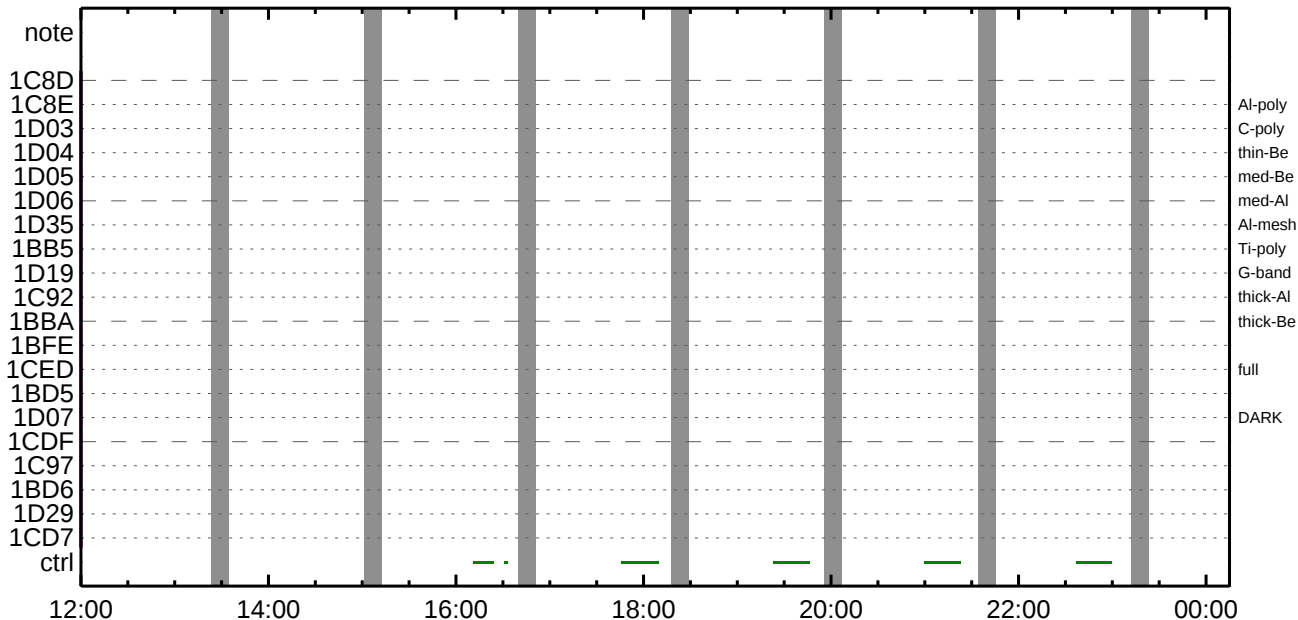
CMDI #0021 2025/12/17



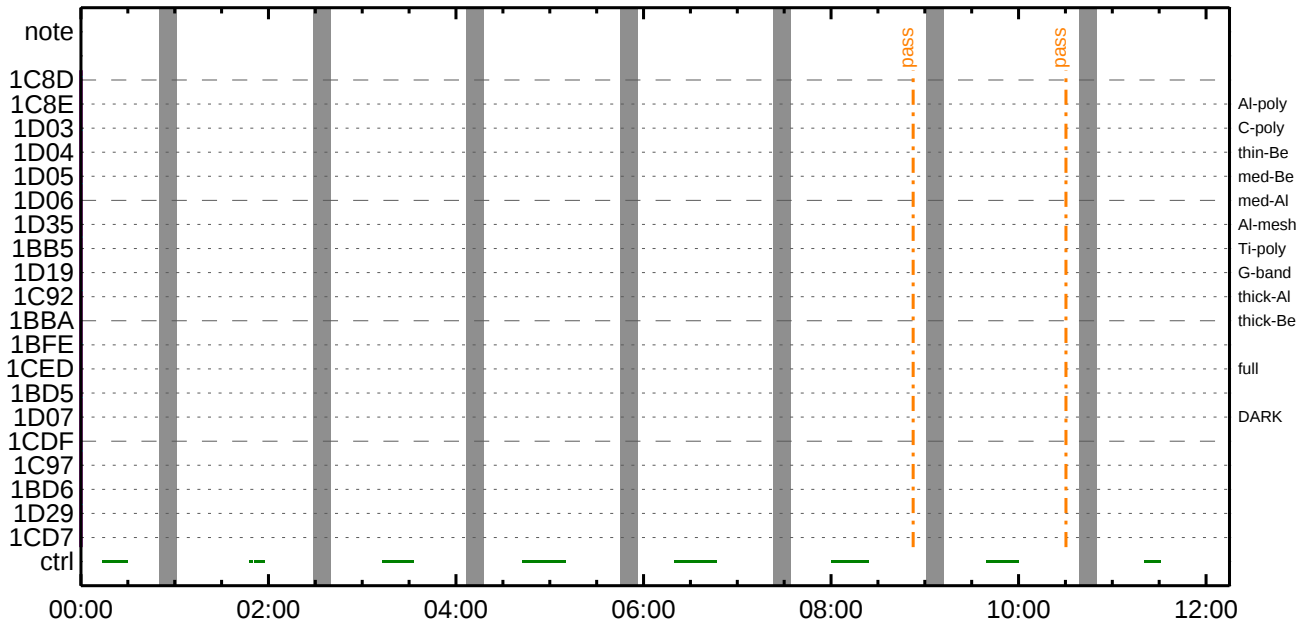
CMDI #0021 2025/12/18



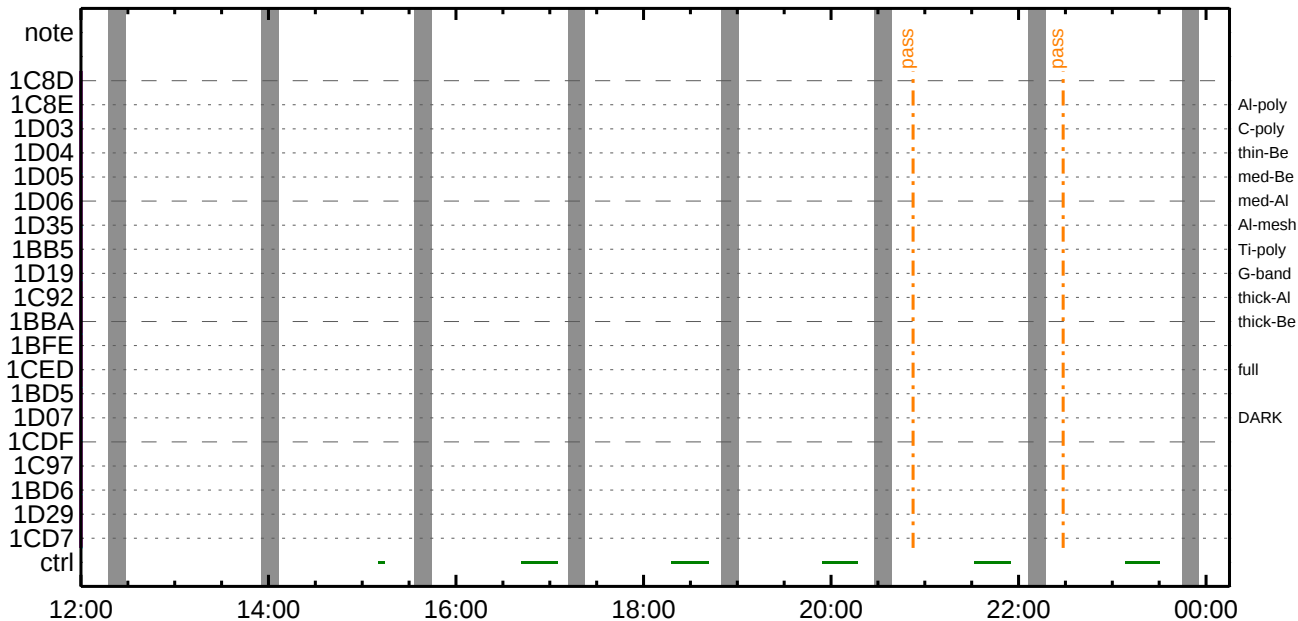
CMDI #0021 2025/12/18



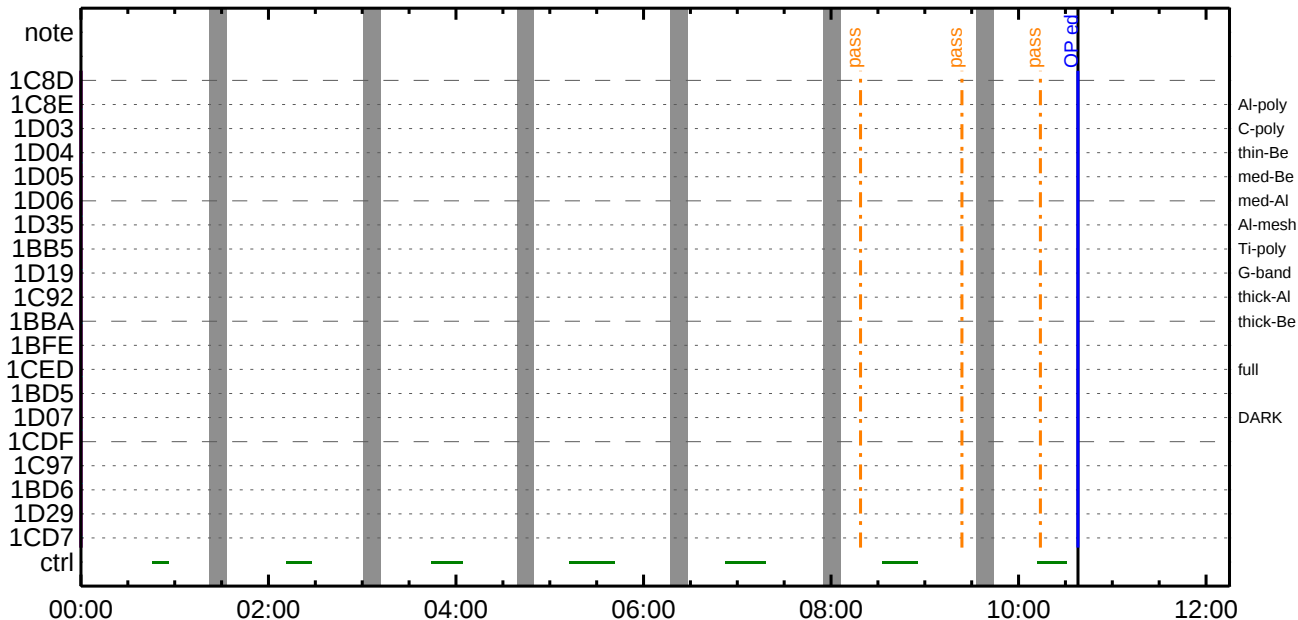
CMDI #0021 2025/12/19



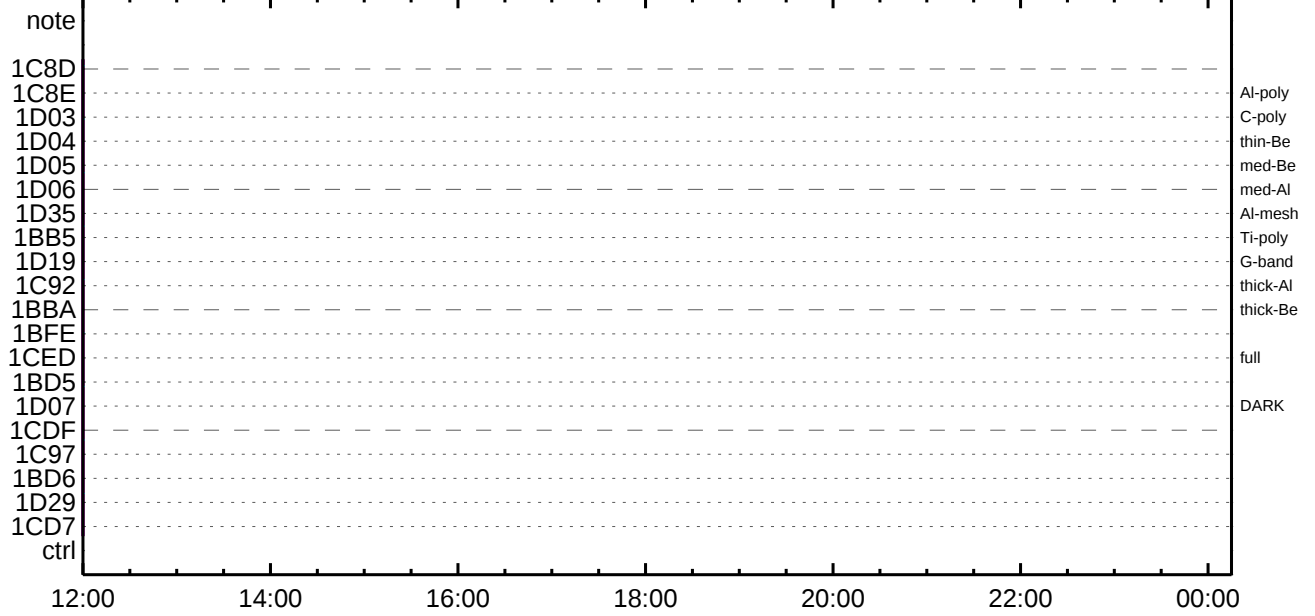
CMDI #0021 2025/12/19



CMDI #0021 2025/12/20



CMDI #0021 2025/12/20



```
main-687 2025-12-16 14:16:18 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÔñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Òòò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲ~¾ÔÄÖñ¬°ÄÄêñ•ñççé;ç°Ê²¼ñî°EÄ,¼ê½Çñò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²òÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ä;ç°Ê²¼ñò¼Ä¹Ôñ¹ñè;£
0055 C. ŲçŲóŲEŲEŲÄÚÂØñäÄ•Ä°²òÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEŲÄÚÂØ;ÊÄ•Ä°²òÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```

```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ä
0103 S. OP op-687:OP
0104 ( )
0105 S. OG og-687:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPÎÎ°èYA6Yx;ä
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YA6Yx½°Î»ð³ÎÇ$
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGÎ½È¹Ç•ë²ÏOKð³ÎÇ$
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YA6Yx½°Î»ð³ÎÇ$
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGÎ½È¹Ç•ë²ÏOKð³ÎÇ$
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YA6Yx½°Î»ð³ÎÇ$
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPÎ½È¹Ç•ë²ÏOKð³ÎÇ$
0165 C.
0166 C. ***** °È²¼ðÎ½Ä´¶Á°ðÈÉ¬°Á÷÷@ (¾âµ-YA6Yx¼È½ÇððÄÓÃæðÇ½°ð¬°ë¾ì¹ÇðÇâ) *****
0167 C. DHUYâ;4YE;È½ý½,¥ì;4YE;ÈððÎáµ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;$ OPOG UPLOAD¬-Á÷÷@NG½Î¾ì¹Ç;¢°È²¼ðÎTI-CMDÁ÷÷@ðÎ½Ä¹Ôð•ðÈððð³ðÈ;£
0180 C. µðµç;¢SETµÈDUMPµÎ£±°ì½Ñ¥¹µÇ¹Ôð;ð³ðÈ;£
0181 C.
0182 C. TI¥³¥P¥Ó¥ÉµððÄBÎç (UT)
0183 +. TI 2025-12-16 11:08:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2025-12-16 11:08:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2025-12-16 11:08:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2025-12-16 11:12:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê¼¤İÄê¼İİñİŸÄŸ$ŸÄŸ~¹aiÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]         EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]      EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]      EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]      EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¼È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHUŸâ;¼ŸÉ;Ê¼Ÿ½,Ÿİ;¼ŸÈ;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2025-12-16 11:12:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2025-12-16 11:12:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2025-12-16 11:12:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÄİ¤İ»ö¼Ÿ¤ÈÄ¤¤¹¤èDCBC•×²è *****
0265 C. (¼ª°İŸÖŸÄŸÈŸPŸÈŸâŸÇŸë¤È½¼¤¤¼Ä»Ü¤¹¤è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŸĐŸ¹•İ Daily±;İİñ¤È´Ø¤¹¤èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOSŸÄŸ$ŸÄŸ~¼Ä»Ü;ä
0276 C.
0277 C. ***** LOS *****
0278 C.
```



```
main-689 2025-12-16 14:18:18 186 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲÇŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÔñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EA, Í×ÈÝñäLOSñÞñÇñÎ»Þ'Òðò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÔñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÄŲ~¾ÔÄÖñ¬°ÄÄêñ•ñççé;ç°Ê²¼ñî°EA,¼ê½Çñðò¼Ä¹Ôñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EA,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôññ°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EA,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEÛÄÚÂØ;ÊÄ•Ä°²òÈò;Ë,ãñî°EA,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EA,ñ¬¼«E°Ää»ßñ•ñçç,ã;ç°Ê²¼ñðò¼Ä¹Ôñ¹ñè;£
0055 C. ŲçŲóŲEŲEÛÄÚÂØñäÄ•Ä°²òÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÔñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EA,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Ôññ°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EA,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEÛÄÚÂØ;ÊÄ•Ä°²òÈò;Ë,ãñî°EA,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ô,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EA,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EA,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
```

```
0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 85 83 06 06)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 80 80 20 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 80 80 08 20)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 85 83 08 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_AEC_RESET
0151 BC (d0)
0152 + DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0155 BC (c4 03)
0156 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0157 BC (c5 0e)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C.
0161 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0162 C.
0163 +. DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 +. TI 2025-12-16 11:12:02.0
0166 DC 07-F0 MDP_XRT_MODE_OBSV
0167 BC (c2)
0168 . C. ----- Success Verify ? OK / NG ____
0169 C.
0170 C. ***** XRT END *****
0171 C.
0172 . C. ***** MDP `ûÃîñÎ»ö¼ÝñÊÃÐñ¹ñÐDCBC•×²è *****
0173 C. (¾å°î¥Ó¥Ã¥Ê¥Ð¥Ê¥â¥ç¥Ëñ½¾¾ñ¼Ã»Ûñ¹ñË)
0174 . S. DC-BC dcbc-402:DCBC
0175 (MDP_known_event)
0176 C.
0177 C.
0178 . C. ***** ¥Ð¥¹•Ï Daily±¿îññÊ´Øñ¹ñÐDCBC•×²è *****
0179 . S. DC-BC dcbc-153:DCBC
0180 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0181 C.
0182 C.
0183 . C. ¿ãLOS¥Ã¥$¥Ã¥¹¼Ã»Û¿ã
0184 C.
0185 . C. ***** LOS *****
0186 C.
```

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 1/11

*** OP Sequence for XRT ***

2025/12/16	11:22:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	11:22:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	11:23:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 03 74 01 db
2025/12/16	11:23:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	11:23:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	11:23:04.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	11:23:06.5	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	11:26:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	11:32:40.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	11:32:42.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	11:32:44.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/12/16	11:33:04.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/12/16	11:33:06.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/12/16	11:33:08.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/12/16	11:33:10.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/16	11:33:12.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	11:35:42.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/12/16	11:35:44.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/12/16	11:35:46.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	12:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	12:19:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	12:19:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	12:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	12:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	12:30:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	12:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	13:57:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	13:57:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	13:57:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	13:57:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	14:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	14:08:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	14:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	15:12:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	15:12:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	15:12:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	15:12:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	15:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	15:17:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	15:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	15:35:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	15:35:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	15:35:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	15:35:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	15:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	15:46:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	15:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	16:44:00.0	XRT_CTRL_MANU_400_OG [0x190]			

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 2/11

2025/12/16	16:44:02.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	16:44:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	16:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	16:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	17:07:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	17:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	17:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	17:13:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	17:13:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	17:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	17:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	17:25:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	17:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	17:55:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	17:55:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/12/16	17:56:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/12/16	17:56:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/12/16	17:56:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/12/16	17:56:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/16	17:58:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/12/16	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	18:05:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/12/16	18:06:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 03 74 01 db
2025/12/16	18:06:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/12/16	18:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/12/16	18:06:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/12/16	18:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/16	18:06:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	18:08:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/12/16	18:08:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/12/16	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	18:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	18:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	18:20:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	18:20:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	18:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	18:43:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	18:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	18:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	18:52:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	18:52:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	18:52:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	18:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	19:03:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	19:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 3/11

2025/12/16	19:56:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	19:56:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	19:56:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	19:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	19:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	20:20:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	20:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	20:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	20:30:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	20:30:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	20:30:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	20:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	20:41:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	20:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	21:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	21:33:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	21:33:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	21:33:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	21:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	21:57:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	21:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	22:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	22:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	22:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	22:08:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	22:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	22:19:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	22:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	23:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	23:10:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	23:10:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	23:10:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	23:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	23:32:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	23:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/16	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	23:46:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/16	23:46:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/16	23:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/16	23:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/16	23:57:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/16	23:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	00:48:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	00:48:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	00:48:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	00:48:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	00:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	00:58:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	00:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	01:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 4/11

2025/12/17	01:24:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	01:24:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	01:24:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/17	01:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/17	01:35:30.0	XRT_Custom_430_OG [0x1ae]					
2025/12/17	01:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/17	02:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	02:13:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	02:13:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	02:13:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/17	02:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/17	02:30:30.0	XRT_Custom_430_OG [0x1ae]					
2025/12/17	02:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/17	03:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	03:02:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	03:02:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	03:02:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/17	03:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/17	03:13:30.0	XRT_Custom_430_OG [0x1ae]					
2025/12/17	03:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/17	03:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	03:46:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	03:46:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	03:46:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/17	03:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/17	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/12/17	04:00:00.0	AOCs_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/12/17	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/12/17	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/12/17	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/12/17	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/12/17	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2025/12/17	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/12/17	04:06:30.5	XRT_Custom_430_OG [0x1ae]					
2025/12/17	04:07:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/17	04:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	04:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	04:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	04:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/17	04:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/17	04:52:00.5	XRT_Custom_430_OG [0x1ae]					
2025/12/17	04:53:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/17	05:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	05:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/17	05:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/17	05:15:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 5/11

2025/12/17	05:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	05:44:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	05:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	05:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/12/17	06:00:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/12/17	06:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/12/17	06:00:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/17	06:02:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/12/17	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	06:09:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/12/17	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 03 74 01 db
2025/12/17	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/12/17	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/12/17	06:10:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/12/17	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/17	06:10:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	06:12:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/12/17	06:12:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/12/17	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	06:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	06:19:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	06:19:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	06:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	06:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	06:30:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	06:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	06:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	06:55:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	06:55:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	06:55:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	06:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	07:21:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	07:57:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	07:57:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	07:57:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	07:57:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	08:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	08:08:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	08:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	08:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	08:35:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	08:35:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	08:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	08:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 6/11

2025/12/17	08:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	08:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	09:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	09:35:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	09:35:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	09:35:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	09:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	09:46:00.0	XRT_Custom_430_OG [0x1ae]				
2025/12/17	09:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	10:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	10:14:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	10:14:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	10:14:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	10:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	10:33:00.0	XRT_Custom_430_OG [0x1ae]				
2025/12/17	10:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	11:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	11:13:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	11:13:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	11:13:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	11:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	11:24:30.0	XRT_Custom_430_OG [0x1ae]				
2025/12/17	11:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	12:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	12:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	12:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	12:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	12:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	13:02:30.0	XRT_Custom_430_OG [0x1ae]				
2025/12/17	13:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	14:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	14:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	14:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	14:29:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	14:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	14:40:30.0	XRT_Custom_430_OG [0x1ae]				
2025/12/17	14:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	15:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	15:40:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	15:40:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	15:40:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	15:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	15:51:30.0	XRT_Custom_430_OG [0x1ae]				
2025/12/17	15:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2025/12/17	16:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	16:07:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	16:07:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	16:07:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	16:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	16:19:00.0	XRT_Custom_430_OG [0x1ae]				

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 7/11

2025/12/17	16:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	17:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	17:15:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	17:15:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	17:15:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	17:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	17:38:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	17:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	17:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	17:45:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	17:45:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	17:45:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	17:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	17:57:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	17:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	18:08:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	18:08:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	18:08:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/12/17	18:09:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2025/12/17	18:09:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/12/17	18:09:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/12/17	18:09:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/17	18:11:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/12/17	18:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	18:18:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	18:18:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	18:18:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/12/17	18:19:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 03 74 01 db
2025/12/17	18:19:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/12/17	18:19:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/12/17	18:19:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/12/17	18:19:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/17	18:19:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	18:21:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/12/17	18:21:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/12/17	18:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	18:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	18:51:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	18:51:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	18:51:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	18:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/17	19:15:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/17	19:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/17	19:24:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	19:24:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/17	19:24:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/17	19:24:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/17	19:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 8/11

2025/12/17	19:35:00.0	XRT_Custom_430_OG [0x1ae]		
2025/12/17	19:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/17	20:28:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	20:28:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	20:28:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/17	20:28:06.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/17	20:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/17	20:51:30.0	XRT_Custom_430_OG [0x1ae]		
2025/12/17	20:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/17	21:02:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	21:02:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	21:02:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/17	21:02:06.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/17	21:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/17	21:13:00.0	XRT_Custom_430_OG [0x1ae]		
2025/12/17	21:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/17	22:05:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	22:05:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	22:05:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/17	22:05:06.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/17	22:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/17	22:28:30.0	XRT_Custom_430_OG [0x1ae]		
2025/12/17	22:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/17	22:40:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	22:40:02.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	22:40:04.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/17	22:40:06.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/17	22:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/17	22:51:30.0	XRT_Custom_430_OG [0x1ae]		
2025/12/17	22:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/17	23:42:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	23:42:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/17	23:42:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/17	23:42:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/17	23:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/18	00:03:00.0	XRT_Custom_430_OG [0x1ae]		
2025/12/18	00:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/18	00:18:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/18	00:18:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/18	00:18:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/18	00:18:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/18	00:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/18	00:29:30.0	XRT_Custom_430_OG [0x1ae]		
2025/12/18	00:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2025/12/18	01:19:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/18	01:19:32.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2025/12/18	01:19:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/18	01:19:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/18	01:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/18	01:27:00.0	XRT_Custom_430_OG [0x1ae]		
2025/12/18	01:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 9/11

2025/12/18	01:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	01:56:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	01:56:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	01:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/18	01:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/18	02:07:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/18	02:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/12/18	02:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	02:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	02:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	02:42:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/18	02:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/18	03:01:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/18	03:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/12/18	03:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	03:34:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	03:34:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	03:34:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/18	03:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/18	03:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/18	03:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/12/18	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	04:00:00.0	AOCs_Or-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2025/12/18	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2025/12/18	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2025/12/18	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2025/12/18	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2025/12/18	04:00:26.0	XRT_FLD_RESET_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2025/12/18	04:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/18	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2025/12/18	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2025/12/18	04:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	04:17:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	04:17:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	04:17:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/18	04:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/18	04:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/18	04:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/12/18	05:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	05:12:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	05:12:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2025/12/18	05:12:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2025/12/18	05:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2025/12/18	05:24:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2025/12/18	05:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2025/12/18	05:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2025/12/18	05:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 10/11

2025/12/18	05:47:34.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/18	05:47:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/18	05:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/18	06:15:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/18	06:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/18	06:24:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	06:24:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	06:24:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2025/12/18	06:25:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2025/12/18	06:25:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2025/12/18	06:25:22.0	XRT_ARS_DIS_435_OG [0x1b3]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/18	06:27:58.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2025/12/18	06:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/18	06:34:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	06:34:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	06:34:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2025/12/18	06:35:00.0	AOCs_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 14 39 ac d5
2025/12/18	06:35:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2025/12/18	06:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2025/12/18	06:35:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2025/12/18	06:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2025/12/18	06:35:26.0	XRT_FLD_RESET_434_OG [0x1b2]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/18	06:37:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2025/12/18	06:37:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2025/12/18	06:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/18	06:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	06:51:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	06:51:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/18	06:51:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/18	06:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/18	07:02:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/18	07:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/18	07:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	07:27:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	07:27:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/18	07:27:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/18	07:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/18	07:52:30.0	XRT_Custom_430_OG [0x1ae]			
2025/12/18	07:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/18	08:29:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	08:29:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	08:29:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2025/12/18	08:29:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2025/12/18	08:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2025/12/18	08:40:00.0	XRT_Custom_430_OG [0x1ae]			
2025/12/18	08:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2025/12/18	09:07:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2025/12/18	09:07:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

Dec 16, 25 14:36

XRT_OGLIST_0021.chk

Page 11/11

2025/12/18	09:07:34.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2025/12/18	09:07:36.0	XRT_PREFLR_STRT_404_OG [0x194]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2025/12/18	09:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2025/12/18	10:49:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00