XRT Timeline to be uploaded on 2026/03/05

Period: 2026/03/05 11:13:00 - 2026/03/10 10:54:00

* * * * * **Normal mode** * * * * *

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
03/05 11:26:00 - 03/05 18:59:54	Fixed (-22.0, -970.0)	# OP start + 10min + HOP 81
03/06 10:03:00 - 03/06 15:29:54	Fixed (-22.0, -959.0)	HOP 206

PROG= 17 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 16 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 90 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 3 60-time(s) 60.0sec

Seqn= 24 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #1D2F: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(12/256/2048), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/5

Term	Pointing (x, y)	Comment
03/05 19:03:00 - 03/05 19:09:54	Fixed (0.0, 0.0)	synoptic, shifted 1 hour
03/06 06:00:00 - 03/06 06:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min + HOP 349 & 448
03/06 17:58:30 - 03/06 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
03/07 06:03:00 - 03/07 06:09:54	Fixed (0.0, 0.0)	synoptic + HOP 349 & 448

PROG= 11 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 66 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 22 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 65 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 17 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 33 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 89 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1BA4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, Al/Poly context, with G-band (1ms/1ms

Term	Pointing (x, y)	Comment
03/05 19:13:00 - 03/06 03:59:54	Track (-460.9, 220.2) @ 03/05 19:10:00	AR 14384 obs.
03/06 06:08:00 - 03/06 09:41:00	Track (-368.8, 225.4) @ 03/06 06:05:00	AR 14384 obs.

PROG= 19 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Seqn= 78 1-time(s) 2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 40-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D29: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
03/06 04:03:00 - 03/06 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min + HOP 349 & 448
03/07 04:07:00 - 03/07 05:59:54	Fixed (0.0, 0.0)	synoptic + HOP 349 & 448

PROG= 02 Inf.-time(s)												
Subr= 1 1-time(s) 300.0sec												
Seqn=100 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 57 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2 6-time(s) 1200.0sec												
Seqn= 8 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D1F: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
03/06 15:33:00 - 03/06 17:55:24	Track (-286.2, 228.9) @ 03/06 15:30:00	AR 14384 obs.
03/06 18:08:30 - 03/07 03:45:30	Track (-263.0, 229.7) @ 03/06 18:05:30	AR 14383 obs.
03/07 06:13:00 - 03/07 10:48:00	Track (-153.4, 232.5) @ 03/07 06:10:00	AR 14383 obs.

PROG= 13 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 4-time(s) 2.0sec												
Seqn= 47 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 77 4-time(s) 600.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
03/05 11:26:00 - 03/05 18:59:54	Fixed (-22.0, -970.0)	# OP start + 10min + HOP 81
03/05 19:13:00 - 03/06 03:59:54	Track (-460.9, 220.2) @ 03/05 19:10:00	AR 14384 obs.

03/06 04:03:00 - 03/06 05:56:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min + HOP 349 & 448
03/06 06:08:00 - 03/06 09:41:00	Track (-368.8, 225.4) @ 03/06 06:05:00	AR 14384 obs.
03/06 10:03:00 - 03/06 15:29:54	Fixed (-22.0, -959.0)	HOP 206
03/06 15:33:00 - 03/06 17:55:24	Track (-286.2, 228.9) @ 03/06 15:30:00	AR 14384 obs.
03/06 18:08:30 - 03/07 03:45:30	Track (-263.0, 229.7) @ 03/06 18:05:30	AR 14383 obs.
03/07 04:07:00 - 03/07 05:59:54	Fixed (0.0, 0.0)	synoptic + HOP 349 & 448
03/07 06:13:00 - 03/07 10:48:00	Track (-153.4, 232.5) @ 03/07 06:10:00	AR 14383 obs.

PROG= 14 30-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 68 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 3 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

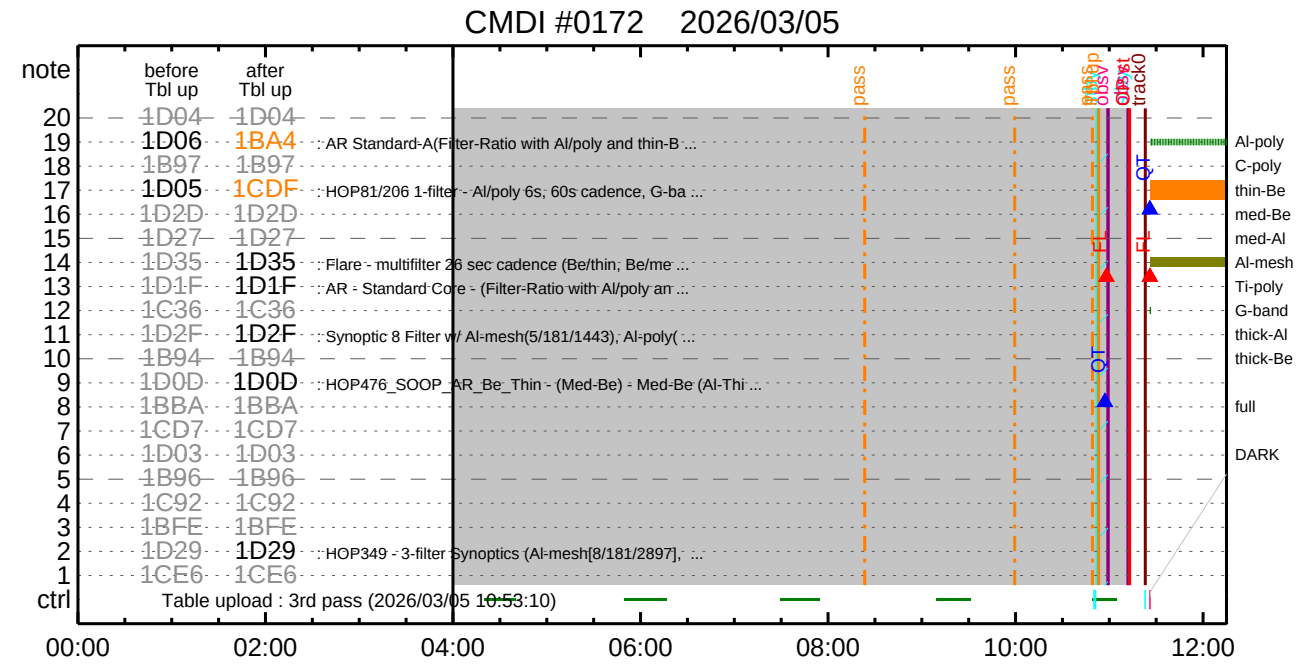
NOT USED

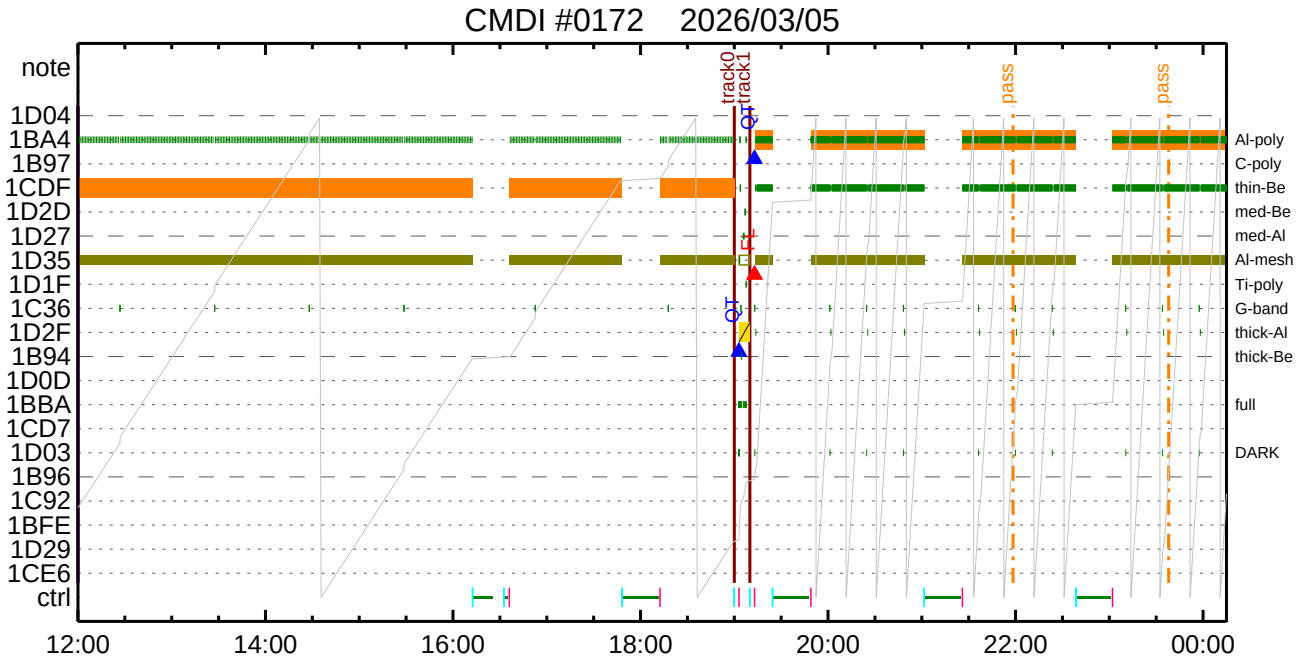
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

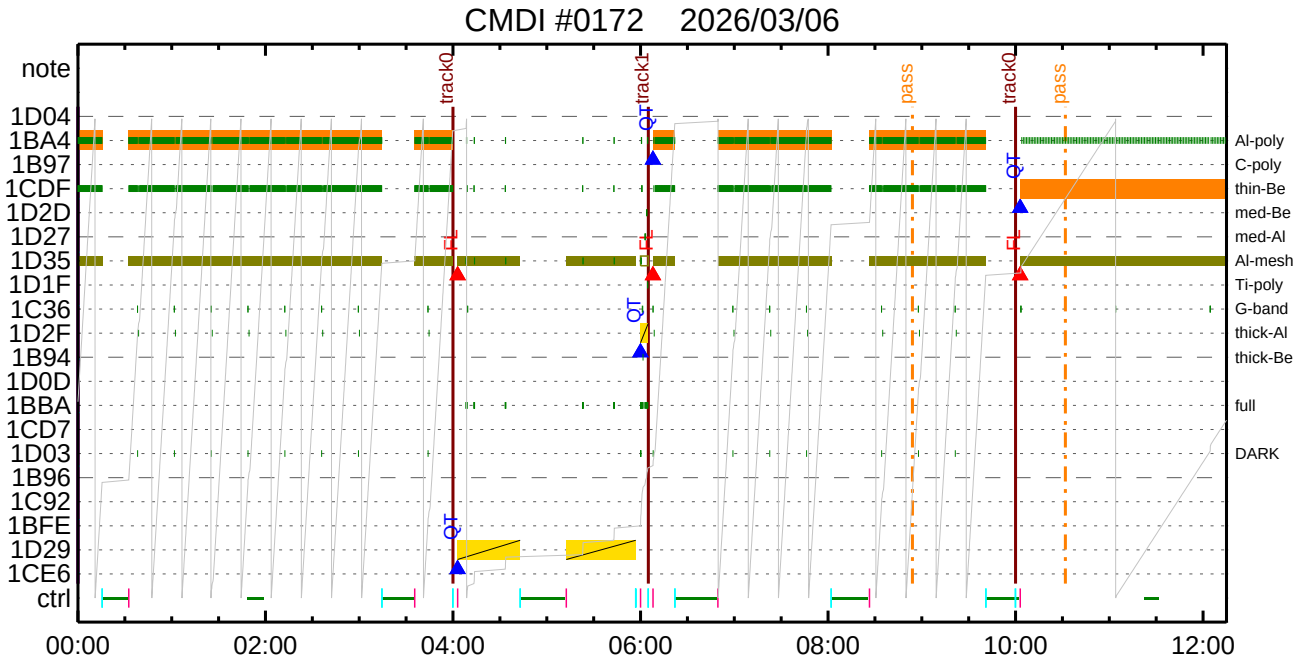
FLD Patrol

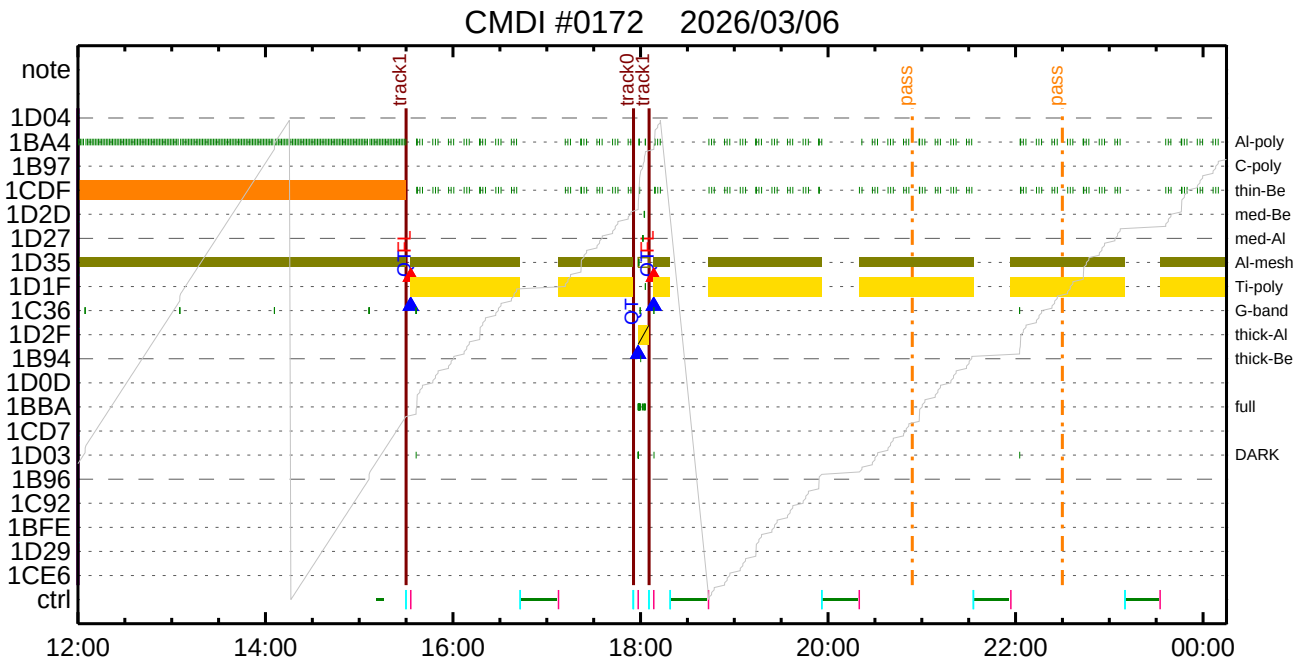
Term	Pointing (x, y)	Comment
03/05 10:54:10 - 03/05 19:00:18	cannot be identified	
03/05 19:10:18 - 03/06 05:57:18	Track (-460.9, 220.2) @ 03/05 19:10:00	AR 14384 obs.
03/06 06:05:18 - 03/06 17:55:48	Track (-368.8, 225.4) @ 03/06 06:05:00	AR 14384 obs.
03/06 18:05:48 - 03/07 06:00:18	Track (-263.0, 229.7) @ 03/06 18:05:30	AR 14383 obs.
03/07 06:10:18 - 03/10 10:54:00	Track (-153.4, 232.5) @ 03/07 06:10:00	AR 14383 obs.

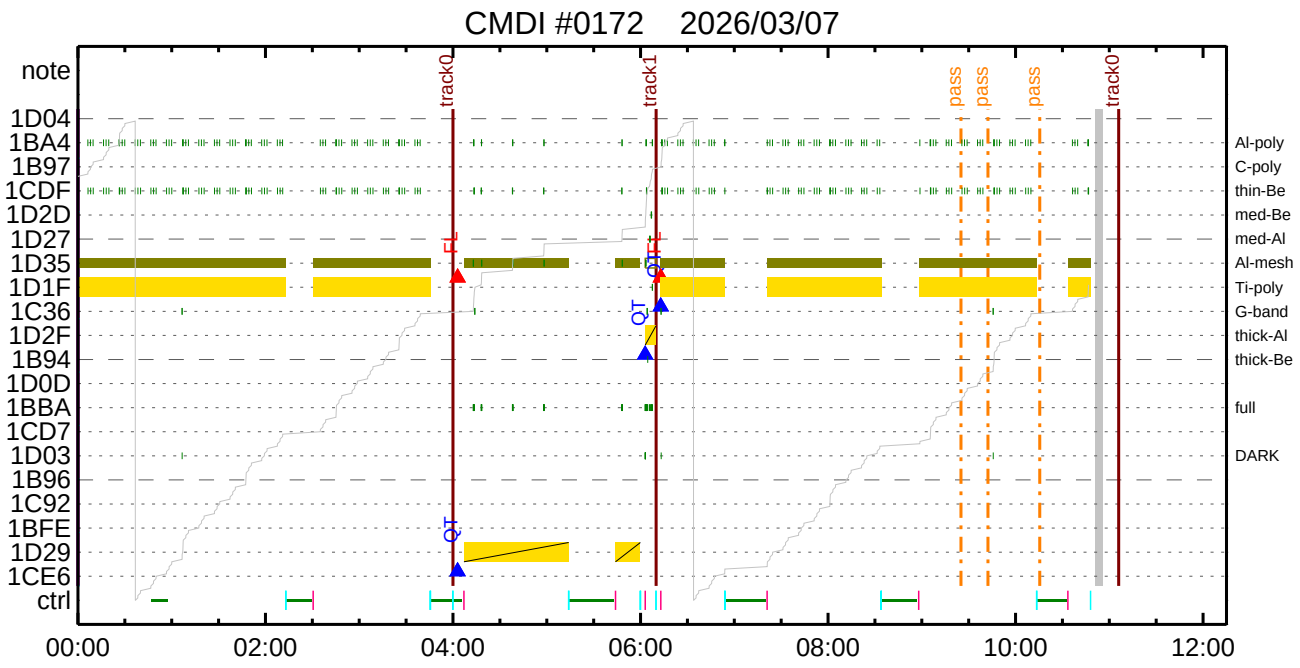
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec

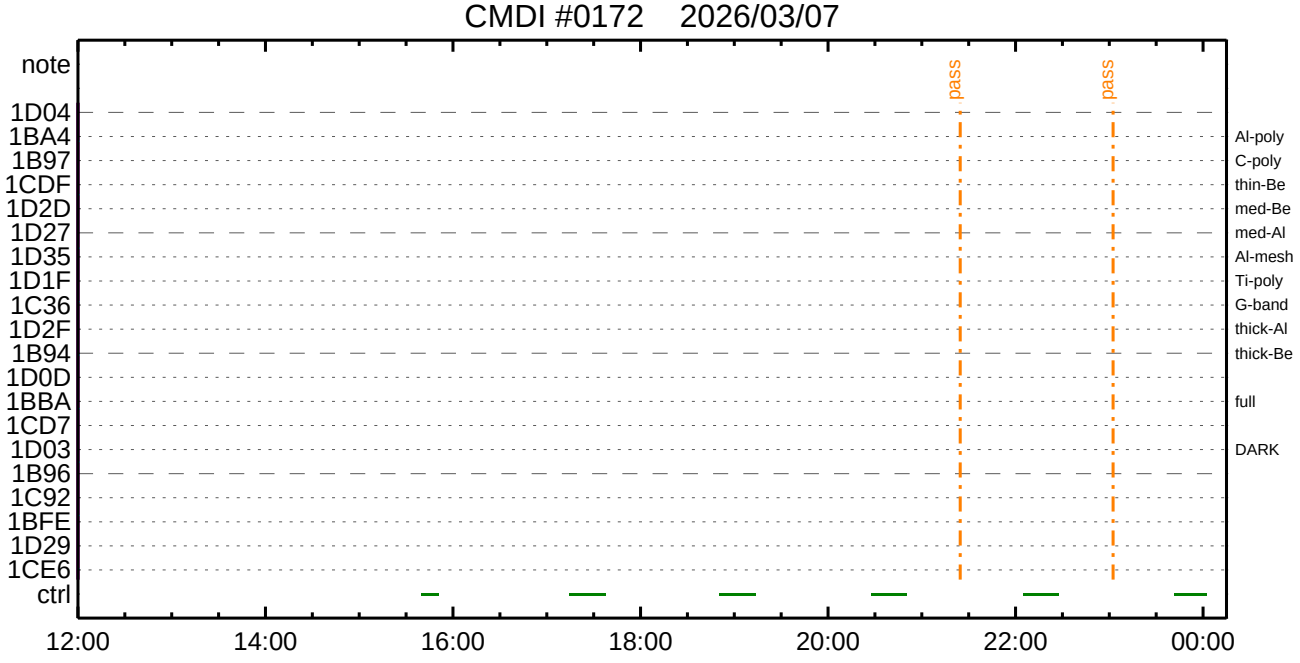

CMDI #0172 2026/03/05
note
before Tbl up
after Tbl up
20 1D04 1D04
19 1D06 1BA4 : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
18 1B97 1B97
17 1D05 1CDF : HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-ba ...
16 1D2D 1D2D
15 1D27 1D27
14 1D35 1D35 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
13 1D1F 1D1F : AR - Standard Core - (Filter-Ratio with Al/poly an ...
12 1C36 1C36
11 1D2F 1D2F : Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(...
10 1B94 1B94
9 1D0D 1D0D : HOP476_SOOP_AR_Be_Thin - (Med-Be) - Med-Be (Al-Thi ...
8 1BBA 1BBA
7 1CD7 1CD7
6 1D03 1D03
5 1B96 1B96
4 1C92 1C92
3 1BFE 1BFE
2 1D29 1D29 : HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], ...
1 1CE6 1CE6
ctrl Table upload : 3rd pass (2026/03/05 10:53:10)
pass
pass
track0
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

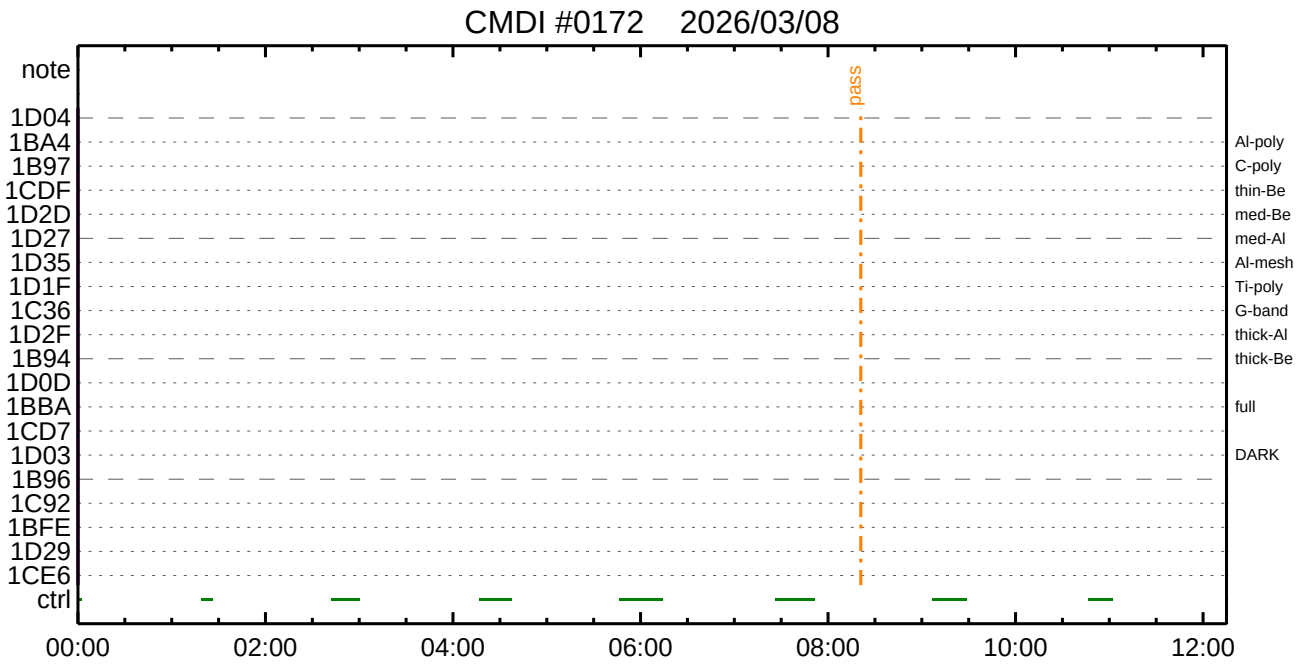

CMDI #0172 2026/03/05
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
track0
track1
QT
FL
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

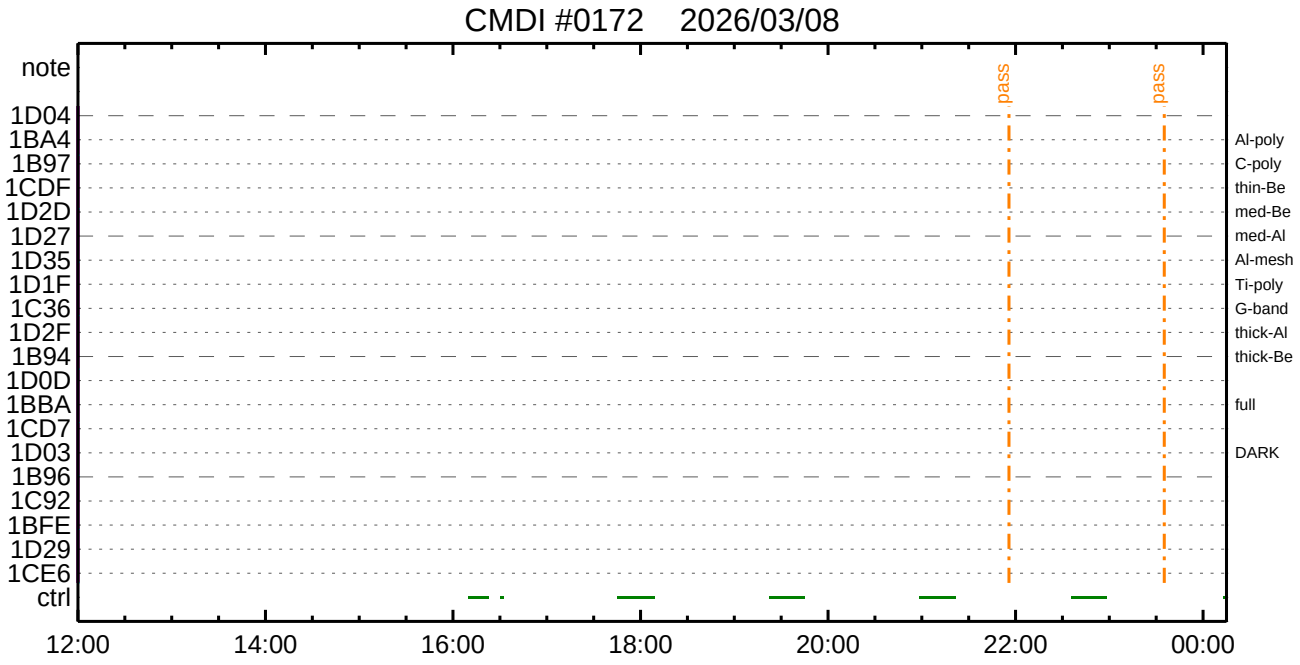

CMDI #0172 2026/03/06
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
track0
track1
track0
QT
FL
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

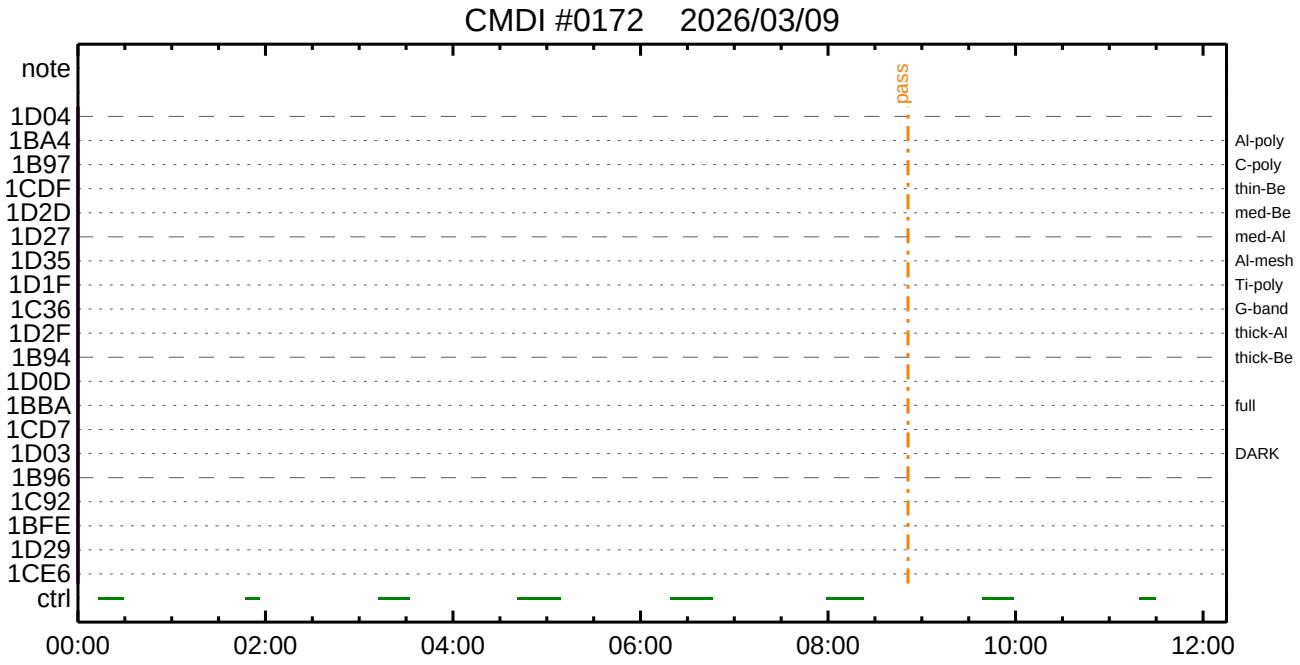

CMDI #0172 2026/03/06
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
track1
track0
track1
pass
pass
FL
FL
FL
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

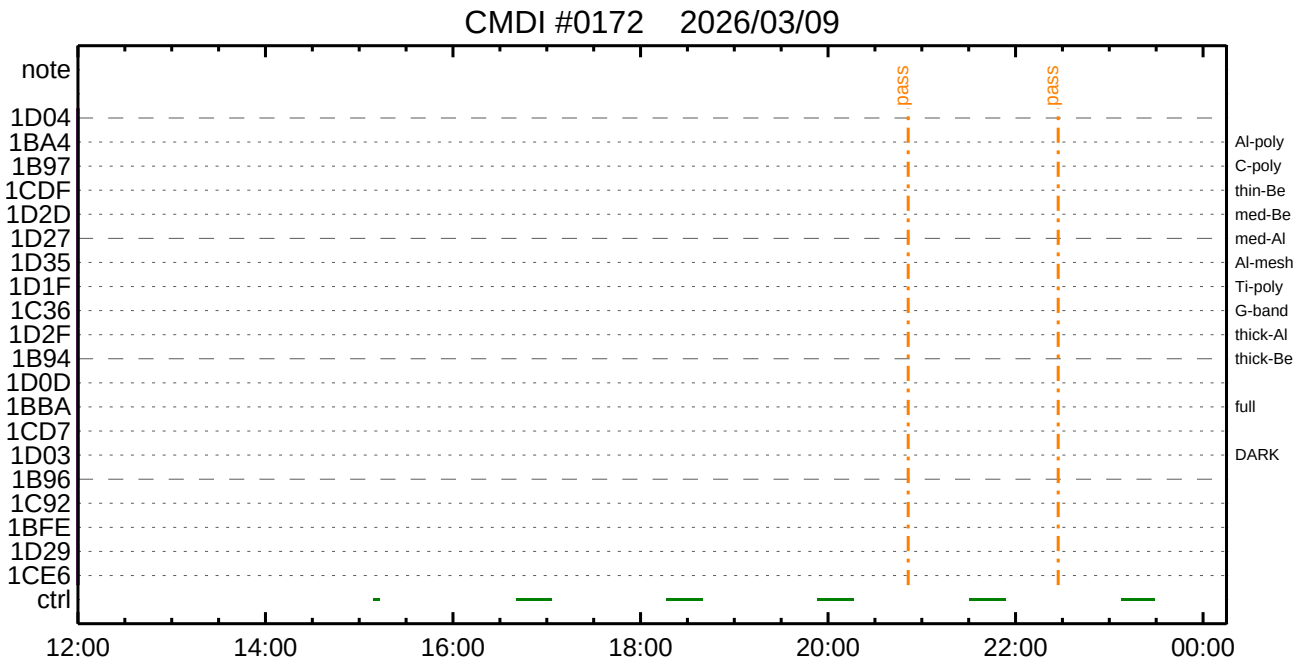

CMDI #0172 2026/03/07
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
track0
track1
pass
pass
pass
track0
FL
FL
FL
QT
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00

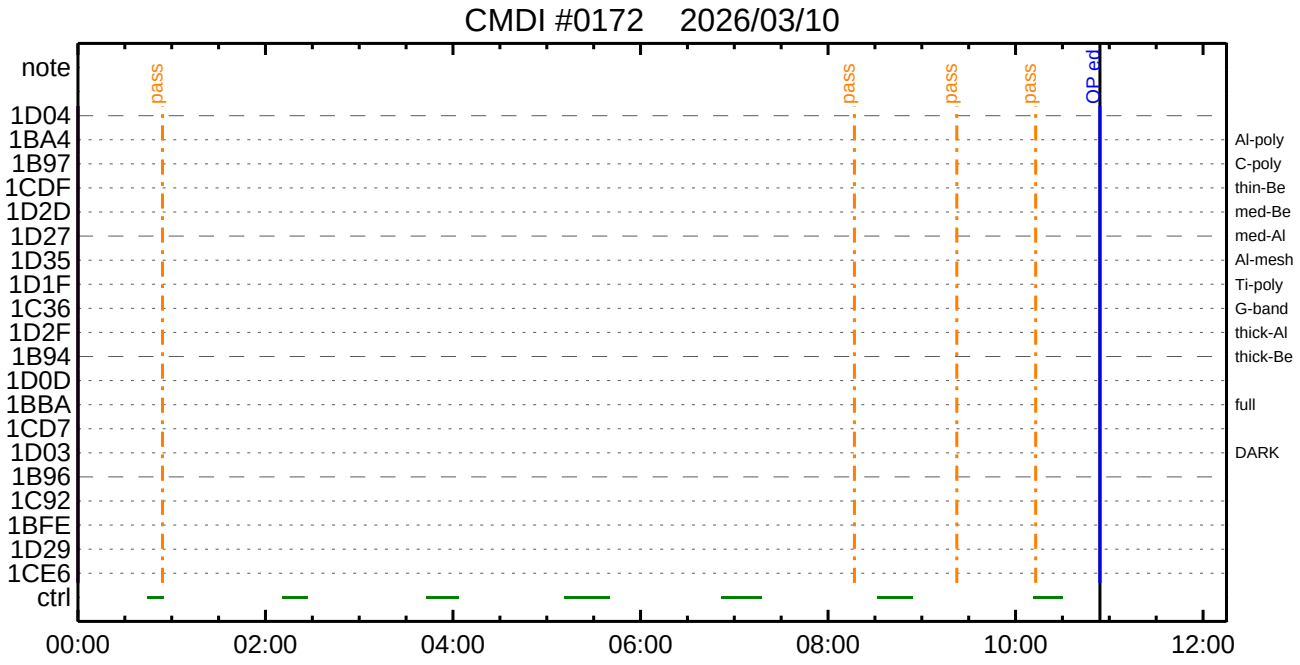

CMDI #0172 2026/03/07
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

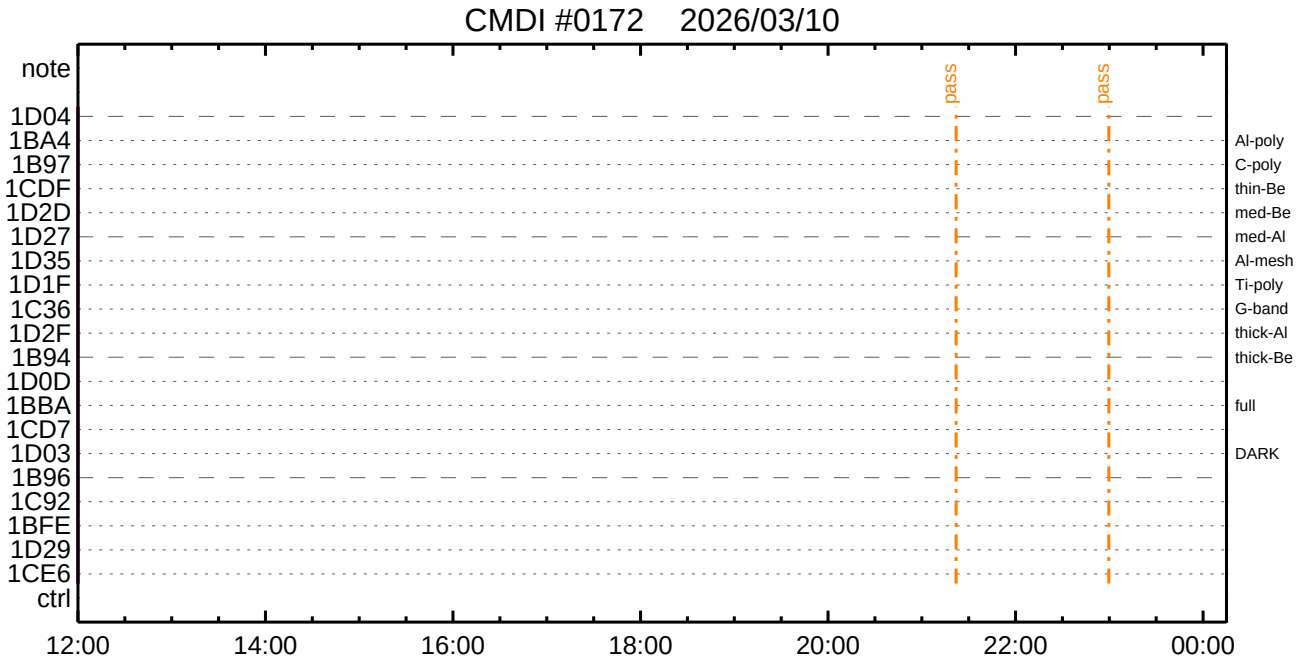

CMDI #0172 2026/03/08
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0172 2026/03/08
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0172 2026/03/09
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0172 2026/03/09
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0172 2026/03/10
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
pass
pass
pass
OP end
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0172 2026/03/10
note
1D04
1BA4
1B97
1CDF
1D2D
1D27
1D35
1D1F
1C36
1D2F
1B94
1D0D
1BBA
1CD7
1D03
1B96
1C92
1BFE
1D29
1CE6
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-838 2026-03-05 12:00:07 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×èÝ¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«Æ°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
     C.
     C.
   . C. ****************************
     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
     C. ****************************
     C.
   . C. ¡ãOP/OG¥í¡¼¥É¡ä
   . S. OP    op-838:OP
     ()
   . S. OG    og-838:OG
     ()
     C.
   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC        (20 00 7f 01 02)
     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC        (07 0b f8)
     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC        (20 80 7f 01 02)
     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC        (07 0b f8)
     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
  +. DC 01-23 DHU_DMA_DMP_PRM_SET
     BC        (21 00 41 01 02)
     C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
     C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
     C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
     C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
     C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
  +. DC 01-22 DHU_MODE_CHNG
     BC        (07 0b f8)
     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
     C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
     C.
   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤°Á÷¿® (¾åµ¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
  +. DC 01-22 DHU_MODE_CHNG
     BC        (02 0a f8)
     C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
     C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
     C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
     C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
     C.
   . C. *********************************
     C. TI-CMD SET (OPOG STOP/COPY/START)
     C. *********************************
     C.
   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
     C.
   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
  +. TI 2026-03-05 11:08:00.0
     DC 01-B3 DHU_OP_STOP
     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2026-03-05 11:08:01.0
     DC 01-B4 DHU_OP_COPY
     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
     C.
  +. TI 2026-03-05 11:08:01.0
     DC 01-B5 DHU_OPOG_COPY
     C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195  +. TI 2026-03-05 11:12:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]           EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]            EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]             EQ     0xB3
0204     C.
0205   . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]           EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]           EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]            EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]           EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     EXEC
0224     C.
0225   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     NON
0227     C.
0228   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0237     C.
0238     C. ********************************
0239     C.  SOT TI command set
0240     C. ********************************
0241     C. Execute, after the success of OP upload.
0242  +. TI 2026-03-05 11:12:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245   . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ************
0249   . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255  +. TI 2026-03-05 11:12:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258  +. TI 2026-03-05 11:12:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261   . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. ************ End EIS operation (TI set) ************
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ************
0267     C. Execute, after the success of OP upload.
0268  +. TI 2026-03-05 11:12:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271   . C.                        [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ************
0274     C.
0275   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277   . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282   . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288   . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-839 2026-03-05 12:00:07 169 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0099     C. Upload the Orbit Element and the Target Attitude
0100     C. RAM-ID:TARGET_ATT
0101   . S. RAM   ram-150:TARGET_ATT
0102     ()
0103     C.
0104     C.
0105     C. Set the dump memory area of TARGET_ATT
0106   +. DC 02-48 AOCU_DUMP_SET
0107     BC       (07 00 00 00 18 00)
0108     C.
0109     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110     C.
0111     C.
0112     C. Change the TLMFormatNo for the AOCS Dump Format
0113   +. DC 01-22 DHU_MODE_CHNG
0114     BC       (04 0b f8)
0115     C.
0116     C. Wait for AOCSDUMP to end
0117     C.
0118   . C. Check the dump memory
0119     C.
0120     C.                                      Result = OK [ ]
0121     C.
0122   +. DC 01-22 DHU_MODE_CHNG
0123     BC       (02 0a f8)
0124     C.
0125     C.                         <A_***>[TLM STS] FMT = 2 [ ]
0126     C.
0127   +. DC 02-8E AOCU_ORB_UPD
0128    . C.
0129    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130   +. DC 07-FC EIS_MODE_CHG_ENA
0131     BC       (20)
0132    . C. Verify EIS_MODE_CHG_FLG is ENA
0133   +. DC 07-FC EIS_MODE_MANU
0134     BC       (21 02)
0135    . C. Verify EIS in MANUAL mode
0136    . C. Estimated OBSTBL upload time is 11s
0137     C. *********************************
0138     C. EIS START OBSTBL LOAD
0139     C. *********************************
0140   . S. RAM   ram-820:EIS_OBSTBL
0141     ()
0142   +. DC 07-FC EIS_DUMP_OBSTBL
0143     BC       (07 07 07 00 00 70 00)
0144     C.
0145     C. Execute, after the success of OBSTBL upload.
0146     C. Set EIS TI-commands
0147   +. TI 2026-03-05 11:12:50.0
0148     DC 07-FC EIS_MODE_CHG_ENA
0149     BC       (20)
0150    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0151     C. *********************************
0152     C. EIS END OBSTBL LOAD
0153     C. *********************************
0154     C.
0155   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0156     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0157   . S. DC-BC dcbc-402:DCBC
0158     (MDP_known_event)
0159     C.
0160     C.
0161   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0162   . S. DC-BC dcbc-153:DCBC
0163     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164     C.
0165     C.
0166   . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
0167     C.
0168   . C. *********** LOS ***********
0169     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-840 2026-03-05 12:00:07 134 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ********************************
0015     C.  SOT table upload
0016     C. ********************************
0017   . C. < Stop SP table >
0018  +. DC 07-F0 MDP_SP_CTRL_MANU
0019     BC       (61)
0020     C. ------------------------------------
0021     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0022     C. ------------------------------------
0023     C.
0024   . C. <Upload SP Observation Table>
0025   . S. RAM   ram-281:MDP_OBS_S
0026     ()
0027     C.
0028   . C. < Dump RAMID=MDP_OBS_S >
0029  +. DC 07-F0 MDP_DUMP_SPTBL
0030     BC       (83 07 00 00 00 38 b8)
0031     C. ------------------------------------
0032     C.  MDP_OBS_S verify = OK/NG        [ ]
0033     C. ------------------------------------
0034     C.
0035     C. ********************************
0036     C.  SOT TI command set
0037     C. ********************************
0038     C. Execute, after the success of TBL upload.
0039  +. TI 2026-03-05 11:12:18.0
0040     DC 07-F0 MDP_SOT_MODE_OBSV
0041     BC       (40)
0042   . C. ------------------------------------
0043     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0044     C. ------------------------------------
0045     C.
0046     C.
0047     C. ************ XRT START ************
0048     C.
0049  +. DC 07-F0 MDP_XRT_CTRL_MANU
0050     BC       (c1)
0051  +. DC 07-F0 MDP_XRT_CTRL_MANU
0052     BC       (c1)
0053  +  DC 07-F0 MDP_XRT_MODE_STBY
0054     BC       (c3)
0055   . C. ------------ Success Verify ?              OK / NG____
0056     C.
0057     C. XRT Obs. Table Upload
0058   . S. RAM   ram-291:MDP_OBS_X
0059     ()
0060     C.
0061  +. DC 07-F0 MDP_DUMP_XRTTBL
0062     BC       (84 07 00 00 00 3a d4)
0063   . C. ------------ Comparison Check ?                        OK / ERR ____
0064     C.
0065     C.
0066  +. DC 07-F0 MDP_XRT_ROI_SET
0067     BC       (cd 01 b1 b1 04 04)
0068  +  DC 07-F0 MDP_XRT_ROI_SET
0069     BC       (cd 02 b1 b1 08 08)
0070  +  DC 07-F0 MDP_XRT_ROI_SET
0071     BC       (cd 03 b1 b1 08 08)
0072  +  DC 07-F0 MDP_XRT_ROI_SET
0073     BC       (cd 04 b1 b1 06 06)
0074  +  DC 07-F0 MDP_XRT_ROI_SET
0075     BC       (cd 05 85 83 06 06)
0076  +  DC 07-F0 MDP_XRT_ROI_SET
0077     BC       (cd 06 85 83 06 06)
0078  +  DC 07-F0 MDP_XRT_ROI_SET
0079     BC       (cd 07 80 80 06 06)
0080  +  DC 07-F0 MDP_XRT_ROI_SET
0081     BC       (cd 08 80 80 20 20)
0082  +  DC 07-F0 MDP_XRT_ROI_SET
0083     BC       (cd 09 80 80 20 08)
0084  +  DC 07-F0 MDP_XRT_ROI_SET
0085     BC       (cd 0a 80 80 08 20)
0086  +  DC 07-F0 MDP_XRT_ROI_SET
0087     BC       (cd 0b 85 83 08 08)
0088  +  DC 07-F0 MDP_XRT_ROI_SET
0089     BC       (cd 0f 80 80 06 06)
0090  +  DC 07-F0 MDP_XRT_ROI_SET
0091     BC       (cd 10 80 80 08 08)
0092  +  DC 07-F0 MDP_XRT_FLD_ENA
0093     BC       (d8)
0094  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0095     BC       (c8)
```

```
0096  +  DC 07-F0 MDP_XRT_ARS_DIS
0097     BC       (d5)
0098  +  DC 07-F0 MDP_XRT_AEC_RESET
0099     BC       (d0)
0100  +  DC 07-F0 MDP_XRT_FLD_RESET
0101     BC       (da)
0102  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0103     BC       (c4 09)
0104  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0105     BC       (c5 0e)
0106   . C. ---------- Success Verify ?            OK / NG ____
0107     C.
0108     C.
0109   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0110     C.
0111  +. DC 07-F0 MDP_XRT_MODE_OBSV
0112     BC       (c2)
0113  +. TI 2026-03-05 11:12:02.0
0114     DC 07-F0 MDP_XRT_MODE_OBSV
0115     BC       (c2)
0116   . C. ---------- Success Verify ?            OK / NG ____
0117     C.
0118     C. ************ XRT END ***********
0119     C.
0120   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0121     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0122   . S. DC-BC dcbc-402:DCBC
0123     (MDP_known_event)
0124     C.
0125     C.
0126   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0127   . S. DC-BC dcbc-153:DCBC
0128     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129     C.
0130     C.
0131   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0132     C.
0133   . C. *********** LOS ***********
0134     C.
```

```
*** OP Sequence for XRT ***

2026/03/05  11:22:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  11:22:56.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/03/05  11:23:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 00 56 35 01 f3
2026/03/05  11:23:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/05  11:23:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/05  11:23:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/05  11:23:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/05  11:23:24.0  XRT_FLD_RESET_425_OG [0x1a9]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  11:25:56.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2026/03/05  11:25:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/05  11:26:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  16:12:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  16:12:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  16:12:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  16:12:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/05  16:15:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/05  16:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  16:32:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  16:32:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  16:32:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/05  16:35:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/05  16:35:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/05  16:36:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  17:48:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  17:48:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  17:48:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  17:48:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/05  17:51:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/05  18:11:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/05  18:12:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  18:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  18:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  18:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/03/05  19:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/03/05  19:00:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/03/05  19:00:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/03/05  19:00:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/05  19:02:58.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2026/03/05  19:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  19:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  19:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  19:09:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/03/05  19:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 01 03 74 01 f3
2026/03/05  19:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/05  19:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/05  19:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/05  19:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
```

```
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/05  19:10:26.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  19:12:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2026/03/05  19:12:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/05  19:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  19:24:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  19:24:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  19:24:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  19:24:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/05  19:27:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/05  19:48:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/05  19:49:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  21:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  21:01:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  21:01:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  21:01:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/05  21:04:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/05  21:25:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/05  21:26:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/05  22:38:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  22:38:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/05  22:38:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/05  22:38:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/05  22:41:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/05  23:01:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/05  23:02:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  00:15:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  00:15:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  00:15:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  00:15:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  00:18:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  00:31:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  00:32:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  03:14:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  03:14:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  03:14:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  03:14:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  03:17:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  03:34:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  03:35:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/03/06  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2026/03/06  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/06  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/06  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/06  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  04:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  04:02:56.0  XRT_QT_PROG_SET_444_OG [0x1bc]
```

```
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2026/03/06  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/06  04:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  04:43:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  04:43:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  04:43:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  04:43:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  04:46:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  05:11:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  05:12:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  05:56:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  05:56:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  05:56:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/03/06  05:57:18.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2026/03/06  05:57:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/03/06  05:57:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  05:59:58.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2026/03/06  06:00:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  06:04:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  06:04:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  06:04:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/03/06  06:05:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 74 01 f3
2026/03/06  06:05:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/06  06:05:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/06  06:05:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/06  06:05:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  06:05:26.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  06:07:56.0  XRT_QT_PROG_SET_443_OG [0x1bb]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2026/03/06  06:07:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/06  06:08:00.5  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  06:22:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  06:22:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  06:22:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  06:22:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  06:25:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  06:48:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  06:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  08:02:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  08:02:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  08:02:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  08:02:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  08:05:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  08:25:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  08:26:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  09:41:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  09:41:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  09:41:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  09:41:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  09:44:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
```

```
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  09:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  09:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  09:59:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/03/06  10:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 00 55 3f 01 f3
2026/03/06  10:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/06  10:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/06  10:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/06  10:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  10:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  10:02:56.0  XRT_QT_PROG_SET_433_OG [0x1b1]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2026/03/06  10:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/06  10:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  15:29:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  15:29:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  15:29:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/03/06  15:30:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 74 01 f3
2026/03/06  15:30:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/06  15:30:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/06  15:30:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/06  15:30:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  15:30:26.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  15:32:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2026/03/06  15:32:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/06  15:33:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  16:43:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  16:43:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  16:43:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  16:43:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  16:46:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  17:06:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  17:07:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  17:55:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  17:55:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  17:55:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/03/06  17:55:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2026/03/06  17:55:48.0  XRT_FLD_DIS_409_OG [0x199]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2026/03/06  17:55:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/03/06  17:55:52.0  XRT_ARS_DIS_420_OG [0x1a4]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  17:58:28.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2026/03/06  17:58:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  18:05:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  18:05:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  18:05:28.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/03/06  18:05:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 01 03 74 01 f3
2026/03/06  18:05:48.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/03/06  18:05:50.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/03/06  18:05:52.0  XRT_AEC_RESET_448_OG [0x1c0]
```

```
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/03/06  18:05:54.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/03/06  18:05:56.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  18:08:26.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2026/03/06  18:08:28.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/03/06  18:08:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  18:19:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  18:19:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  18:19:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  18:19:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  18:22:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  18:42:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  18:43:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  19:56:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  19:56:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  19:56:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  19:56:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  19:59:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  20:19:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  20:20:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  21:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  21:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  21:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  21:33:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  21:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  21:56:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/06  21:57:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/06  23:10:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  23:10:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/06  23:10:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/06  23:10:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/06  23:13:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/06  23:31:30.5  XRT_Custom_430_OG [0x1ae]
2026/03/06  23:32:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/07  02:13:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  02:13:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  02:13:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/07  02:13:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/07  02:16:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/07  02:29:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/07  02:30:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/07  03:45:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  03:45:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  03:45:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/07  03:45:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/07  03:48:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/07  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/03/07  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
```

```
                                    AOCU_NM               5 02-76 00 00 00 00 00
2026/03/07  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA       1 07-F0 d8
2026/03/07  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2026/03/07  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET     1 07-F0 d0
2026/03/07  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2026/03/07  04:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2026/03/07  04:02:56.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 02
2026/03/07  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_FL_PROG_SET   2 07-F0 c5 0e
2026/03/07  04:06:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/07  04:07:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2026/03/07  05:14:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  05:14:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  05:14:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2026/03/07  05:14:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT   1 07-F0 e8
2026/03/07  05:17:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP   1 07-F0 e9
2026/03/07  05:43:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/07  05:44:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2026/03/07  05:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  05:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  05:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                    XRT_FOCUS_POSITION    4 07-F8 22 ff aa 00
2026/03/07  06:00:18.0  XRT_FLD_DIS_409_OG [0x199]
                                    MDP_XRT_FLD_DIS       1 07-F0 d9
2026/03/07  06:00:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_DIS   1 07-F0 c9
2026/03/07  06:00:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2026/03/07  06:02:58.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 0b
2026/03/07  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2026/03/07  06:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  06:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  06:09:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                    XRT_FOCUS_POSITION    4 07-F8 22 fe 97 00
2026/03/07  06:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                    AOCU_NM               5 02-76 01 03 74 01 f3
2026/03/07  06:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA       1 07-F0 d8
2026/03/07  06:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA   1 07-F0 c8
2026/03/07  06:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET     1 07-F0 d0
2026/03/07  06:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS       1 07-F0 d5
2026/03/07  06:10:26.0  XRT_FLD_RESET_401_OG [0x191]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2026/03/07  06:12:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_QT_PROG_SET   2 07-F0 c4 0d
2026/03/07  06:12:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_FL_PROG_SET   2 07-F0 c5 0e
2026/03/07  06:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2026/03/07  06:54:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  06:54:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  06:54:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2026/03/07  06:54:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT   1 07-F0 e8
2026/03/07  06:57:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP   1 07-F0 e9
2026/03/07  07:20:00.5  XRT_Custom_430_OG [0x1ae]
2026/03/07  07:21:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO     1 07-F0 c0
2026/03/07  08:34:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  08:34:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU     1 07-F0 c1
2026/03/07  08:34:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET     1 07-F0 da
2026/03/07  08:34:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT   1 07-F0 e8
2026/03/07  08:37:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
```

```
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/07  08:57:00.0  XRT_Custom_430_OG [0x1ae]
2026/03/07  08:58:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/07  10:13:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  10:13:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  10:13:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/03/07  10:13:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/03/07  10:16:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/03/07  10:32:30.0  XRT_Custom_430_OG [0x1ae]
2026/03/07  10:33:30.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/03/07  10:48:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/03/07  11:06:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
```