XRT Timeline to be uploaded on 2026/05/16

Period: 2026/05/16 11:09:00 - 2026/05/26 10:59:00

* * * * * **Normal mode** * * * * *

XOB #1D4A: HOP525_AR_ThinBe_MedBe_512x512_120sec_with_PFB2_3

Term	Pointing (x, y)	Comment
05/16 11:31:00 - 05/16 12:59:54	Track (203.9, 281.2) @ 05/16 11:19:00	# OP start + 10min, HOP 525

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 60-time(s) 120.0sec

Seqn= 62 1-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	DPCM	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	DPCM	3	0	2.0sec

Seqn= 44 1-time(s) 40.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	2	2.0sec

Seqn= 43 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	3	2.0sec

XOB #1BDE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
05/16 13:03:00 - 05/16 16:14:54	Track (217.9, 280.9) @ 05/16 13:00:00	AR obs
05/16 21:03:00 - 05/17 03:42:30	Track (283.5, 279.4) @ 05/16 21:00:00	AR obs

PROG= 13 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 5-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 96 4-time(s) 72.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1D2F: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(12/256/2048), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/5

Term	Pointing (x, y)	Comment
05/16 16:18:00 - 05/16 16:26:30	Fixed (0.0, 0.0)	synoptic, shifted manually
05/17 06:07:30 - 05/17 06:12:27	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 4.5 min

PROG= 11 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 5 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 66 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 22 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 19 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 23 1-time(s) 4.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 65 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 89 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D11: HOP480 - Filter-Ratio with thin-Be and Med-Be (short) with PFB, 384x384 at 1064 1048, with G-band (1ms/1ms VLS=CLS), 60 cad

Term	Pointing (x, y)	Comment
05/16 17:05:00 - 05/16 20:59:54	Track (250.8, 273.4) @ 05/16 17:00:00	DKIST coordination

PROG= 18 Inf.-time(s)													
Subr= 1 1-time(s) 10.0sec													
Seqn= 1 2-time(s) 5.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 240-time(s) 60.0sec													
Seqn= 61 1-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 28 1-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Subr= 3 240-time(s) 60.0sec													
Seqn= 61 1-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 28 1-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1C9B: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
05/17 04:23:00 - 05/17 06:04:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 4.5 min

PROG= 08 Inf.-time(s)													
Subr= 1 1-time(s) 300.0sec													
Seqn=100 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 57 1-time(s) 2.0sec													
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81 1-time(s) 2.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2 9-time(s) 360.0sec													
Seqn= 8 1-time(s) 2.0sec													
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec													
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Flare mode** * * * * *

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment
05/16 11:31:00 - 05/16 12:59:54	Track (203.9, 281.2) @ 05/16 11:19:00	# OP start + 10min, HOP 525

05/16 13:03:00 - 05/16 16:14:54	Track (217.9, 280.9) @ 05/16 13:00:00	AR obs														
05/16 21:03:00 - 05/17 03:42:30	Track (283.5, 279.4) @ 05/16 21:00:00	AR obs														
05/17 04:23:00 - 05/17 06:04:24	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted 4.5 min														
PROG= 14 30-time(s)																
Subr= 1 1-time(s) 2.0sec																
Seqn= 68 1-time(s) 2.0sec																
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec				
Subr= 2 20-time(s) 2.0sec																
Seqn= 11 1-time(s) 2.0sec																
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec				
Seqn= 73 1-time(s) 10.0sec																
thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
Subr= 3 1-time(s) 2.0sec																
Seqn= 10 1-time(s) 2.0sec																
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
Seqn= 11 1-time(s) 2.0sec																
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec				
Seqn= 87 1-time(s) 2.0sec																
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec				
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec				
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec				
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval				

XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384

Term	Pointing (x, y)	Comment
05/16 17:05:00 - 05/16 20:59:54	Track (250.8, 273.4) @ 05/16 17:00:00	DKIST coordination

PROG= 10 1-time(s)																
Subr= 1 1-time(s) 2.0sec																
Seqn= 67 1-time(s) 2.0sec																
thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec				
Subr= 2 1-time(s) 2.0sec																
Seqn= 59 255-time(s) 5.0sec																
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval				

* * * * * Active Region Search * * * * *

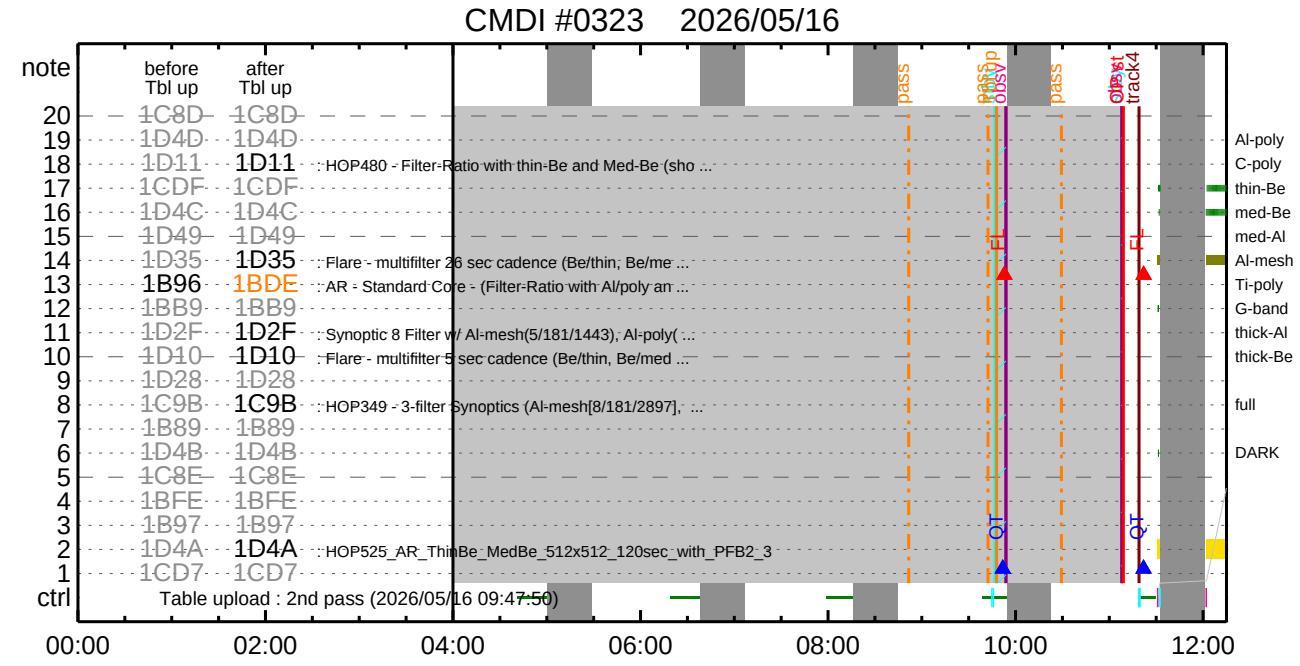
NOT USED

* * * * * Flare Detection * * * * *

FLD Patrol

Term	Pointing (x, y)	Comment
05/16 09:48:50 - 05/16 16:15:18	cannot be identified	
05/16 17:00:18 - 05/17 06:04:48	Track (250.8, 273.4) @ 05/16 17:00:00	DKIST coordination

Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval				


CMDI #0323 2026/05/16
note
before Tbl up
after Tbl up
20 1C8D 1C8D
19 1D4D 1D4D
18 1D11 1D11 : HOP480 - Filter-Ratio with thin-Be and Med-Be (sho ...
17 1CDF 1CDF
16 1D4C 1D4C
15 1D49 1D49
14 1D35 1D35 : Flare - multifilter 26 sec cadence (Be/thin, Be/me ...
13 1B96 1BDE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
12 1BB9 1BB9
11 1D2F 1D2F : Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(...
10 1D10 1D10 : Flare - multifilter 5 sec cadence (Be/thin, Be/med ...
9 1D28 1D28
8 1C9B 1C9B : HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], ...
7 1B89 1B89
6 1D4B 1D4B
5 1C8E 1C8E
4 1BFE 1BFE
3 1B97 1B97
2 1D4A 1D4A : HOP525_AR_ThinBe_MedBe_512x512_120sec_with_PFB2_3
1 1CD7 1CD7
ctrl Table upload : 2nd pass (2026/05/16 09:47:50)
pass
obsv
pass
track4
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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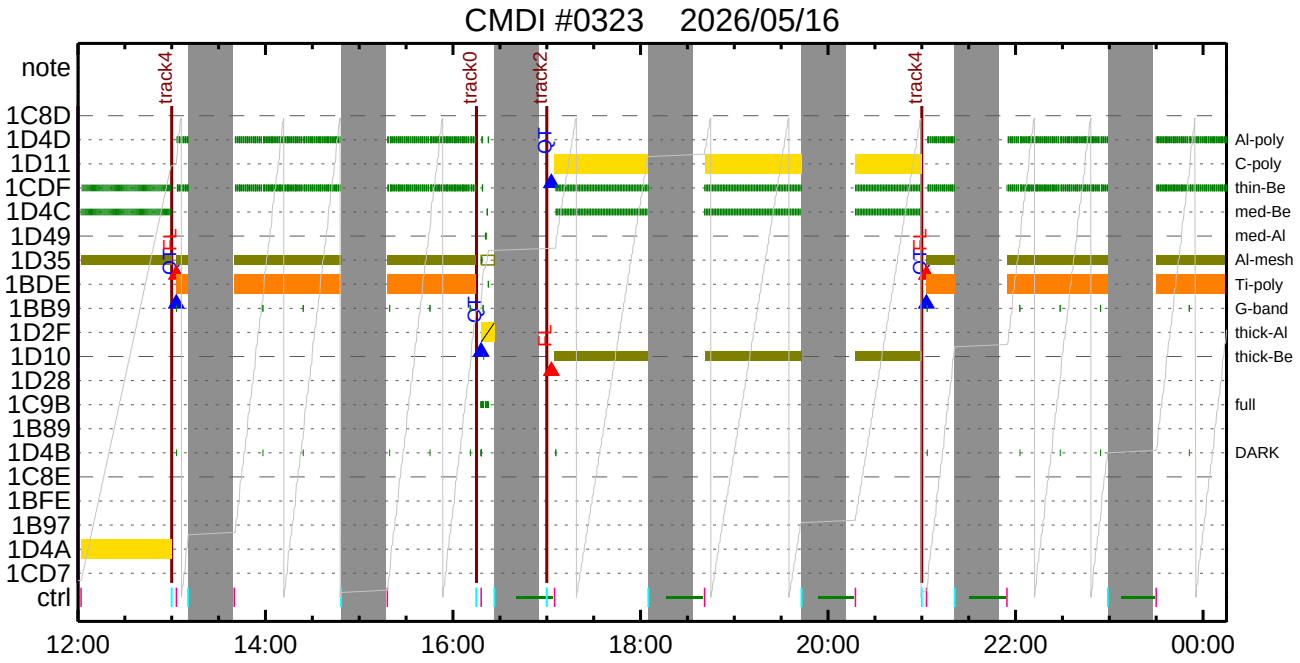
CMDI #0323 2026/05/16
note
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1D4D
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1CDF
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1D49
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1BDE
1BB9
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ctrl
track4
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FL
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Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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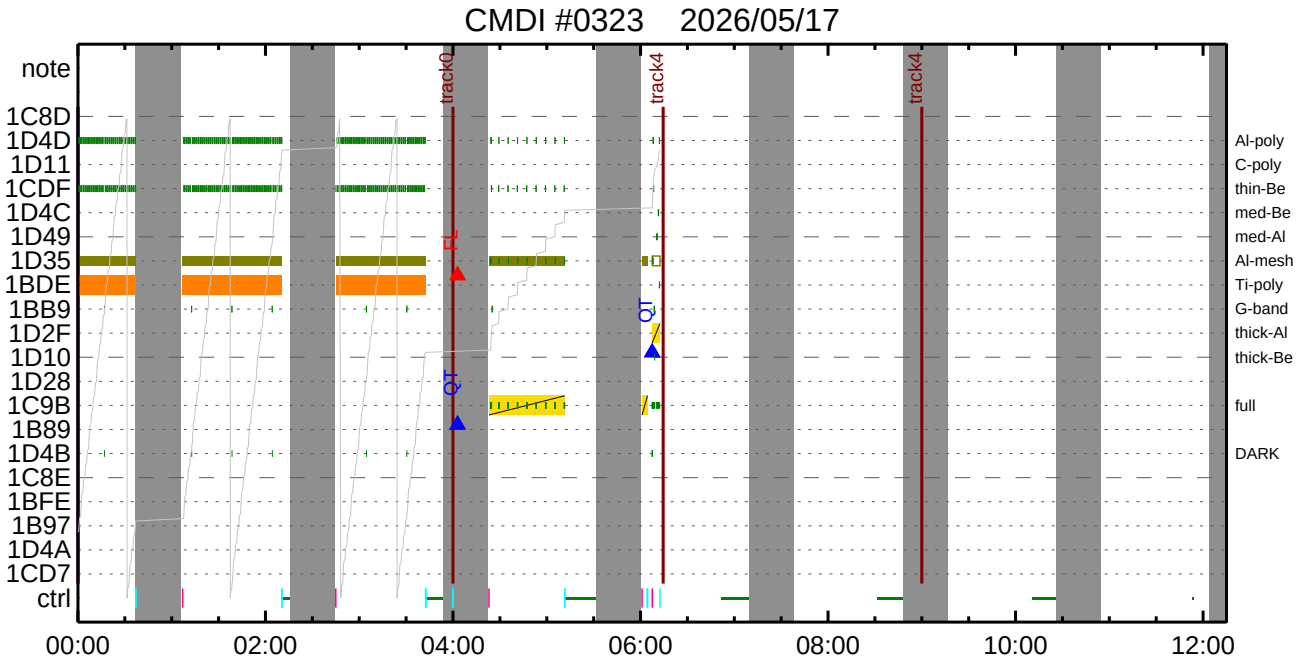
CMDI #0323 2026/05/17
note
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1CD7
ctrl
track0
track4
track4
FL
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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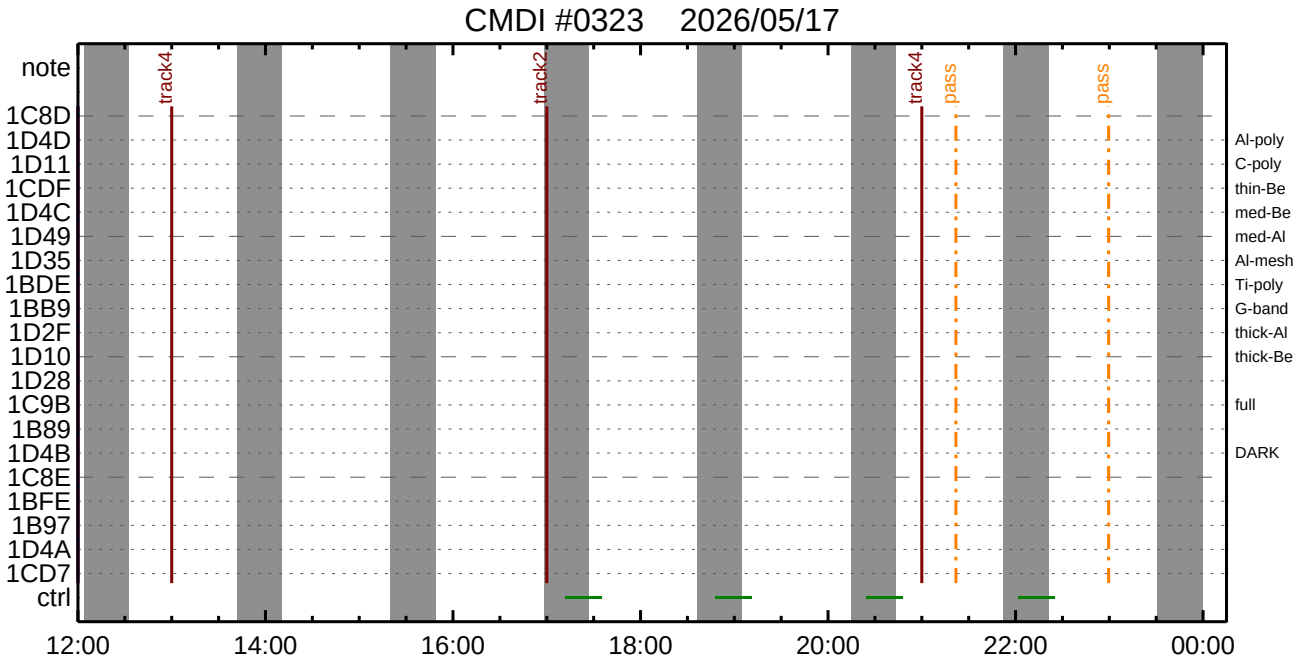
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note
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ctrl
track4
track2
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pass
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C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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thick-Be
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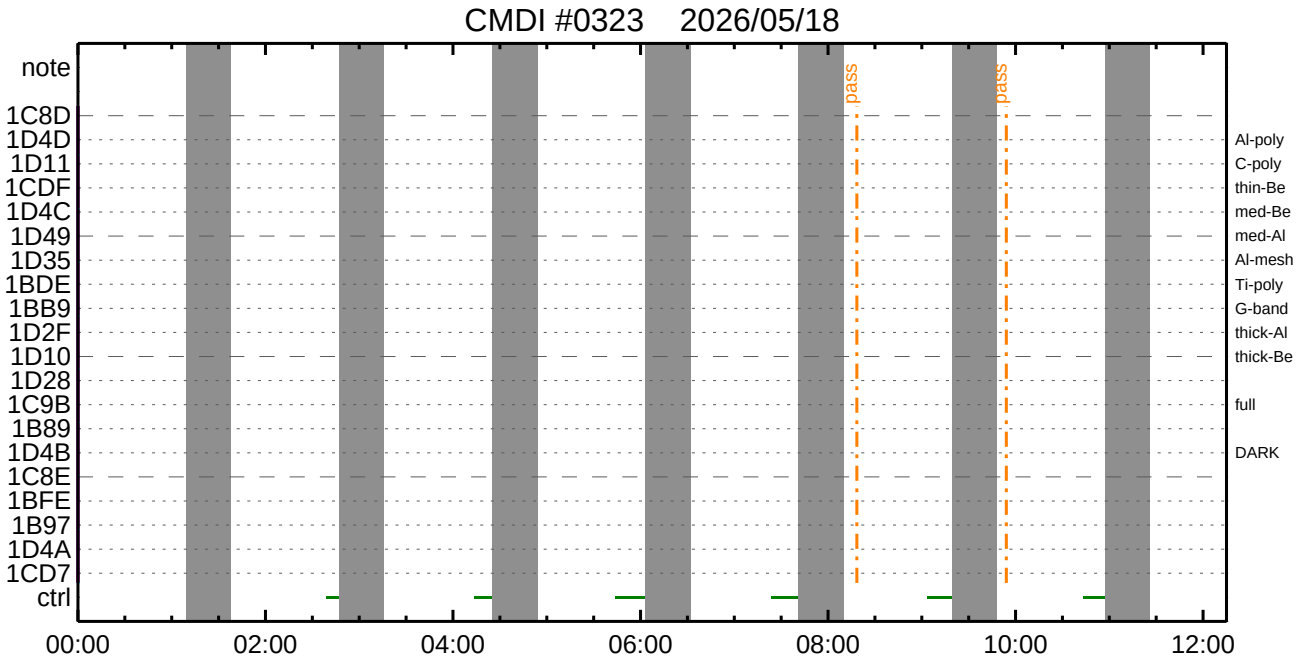
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note
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1CDF
1D4C
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1BDE
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1D2F
1D10
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1C9B
1B89
1D4B
1C8E
1BFE
1B97
1D4A
1CD7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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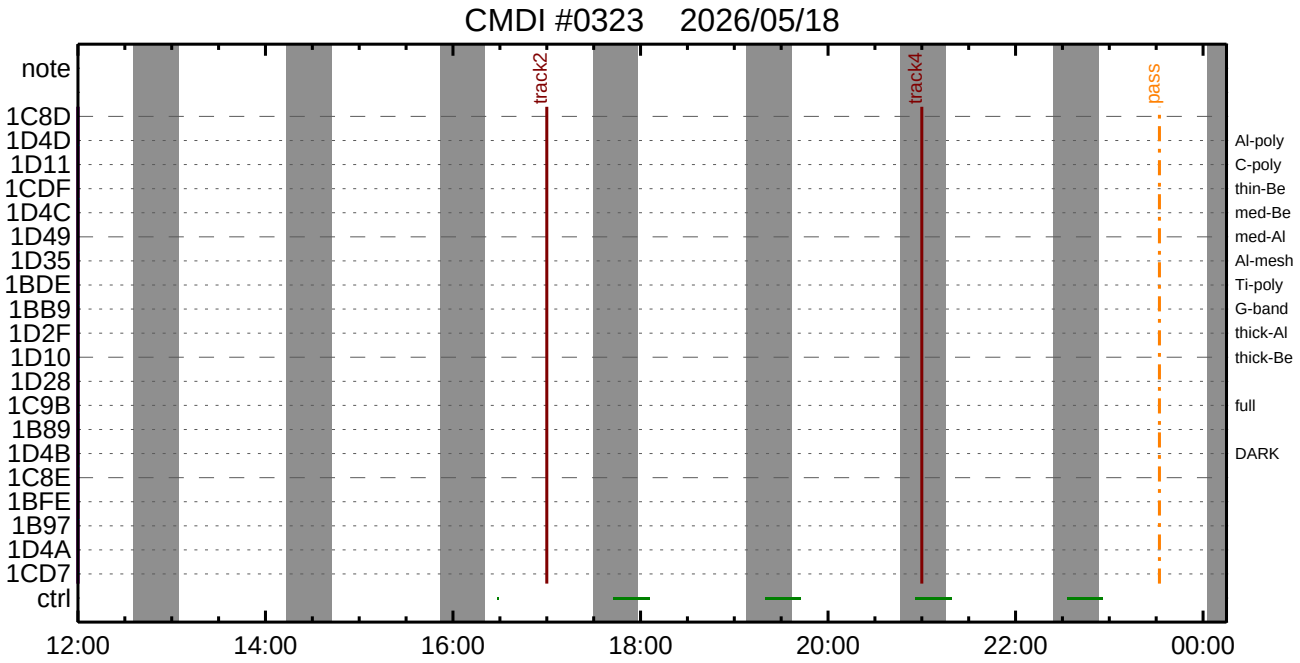
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note
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1CDF
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1D2F
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ctrl
track2
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pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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thick-Be
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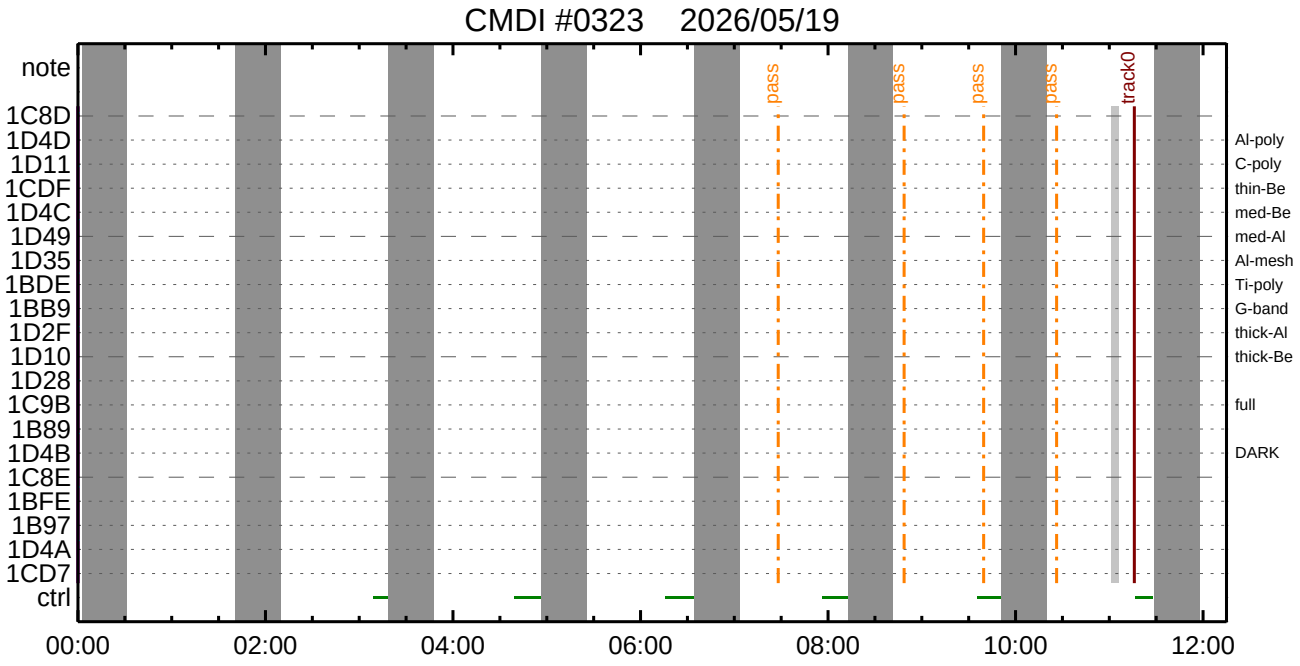
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note
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1BDE
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1C8E
1BFE
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1D4A
1CD7
ctrl
pass
pass
pass
pass
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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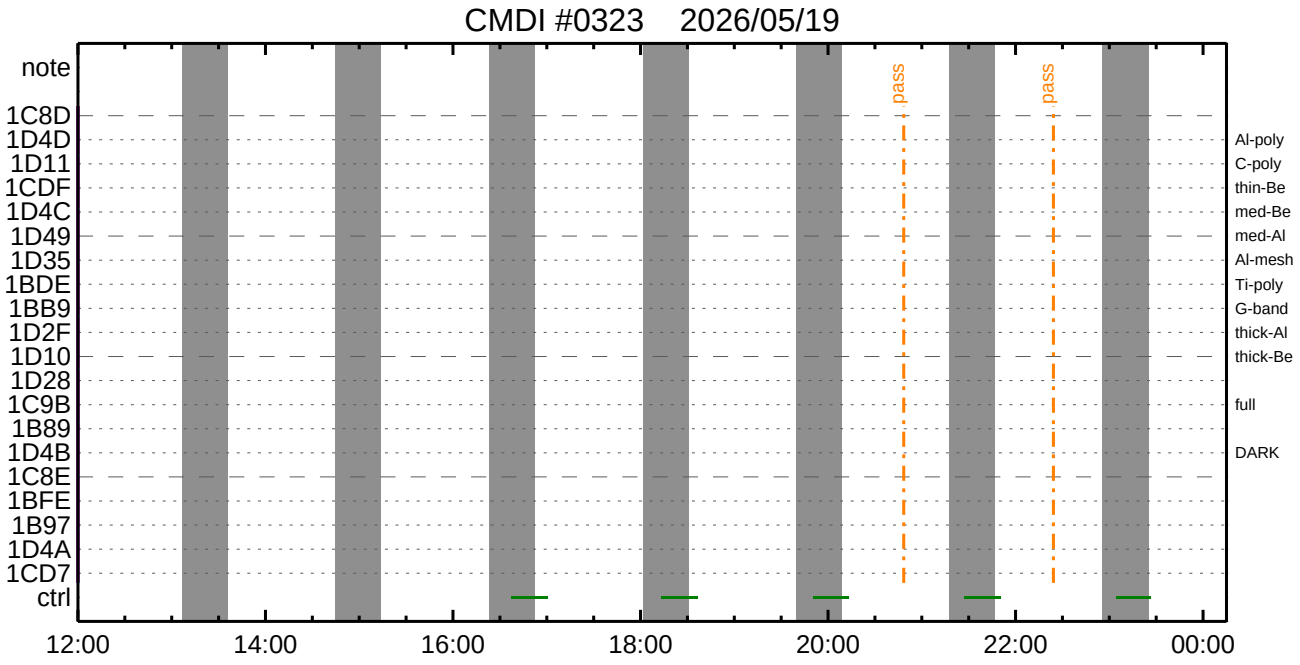
CMDI #0323 2026/05/19
note
1C8D
1D4D
1D11
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1D2F
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1D4A
1CD7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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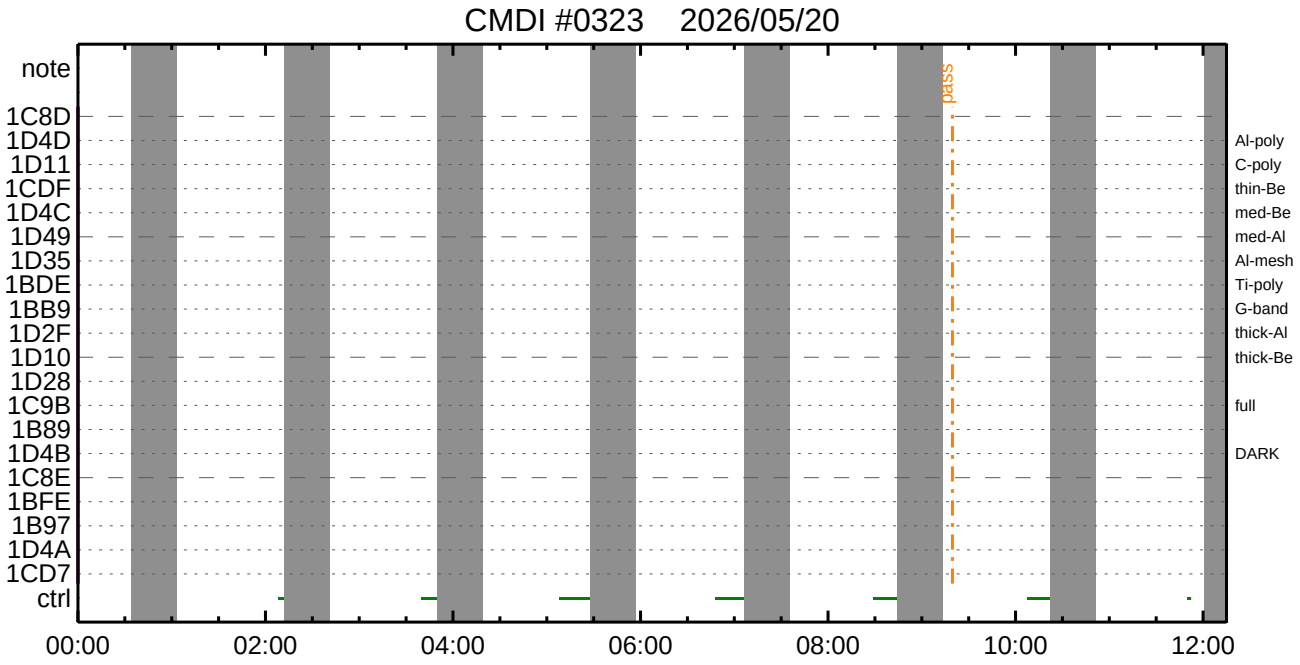
CMDI #0323 2026/05/20
note
1C8D
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ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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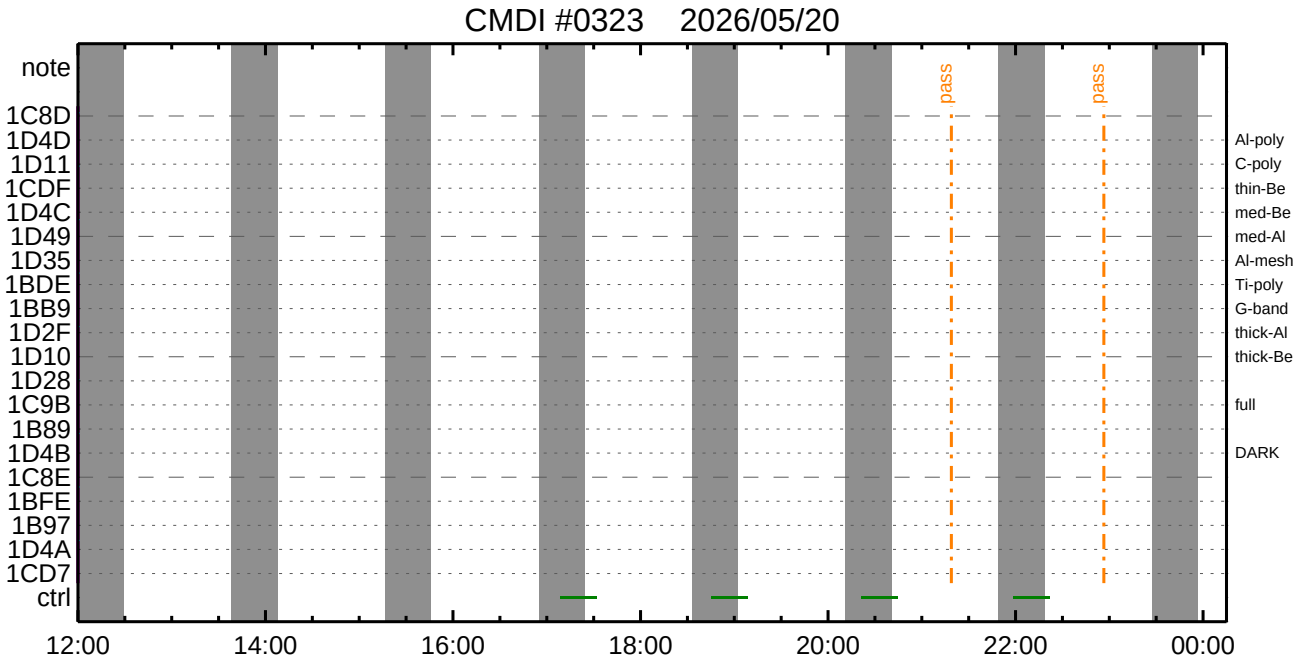
CMDI #0323 2026/05/20
note
1C8D
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ctrl
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med-Be
med-Al
Al-mesh
Ti-poly
G-band
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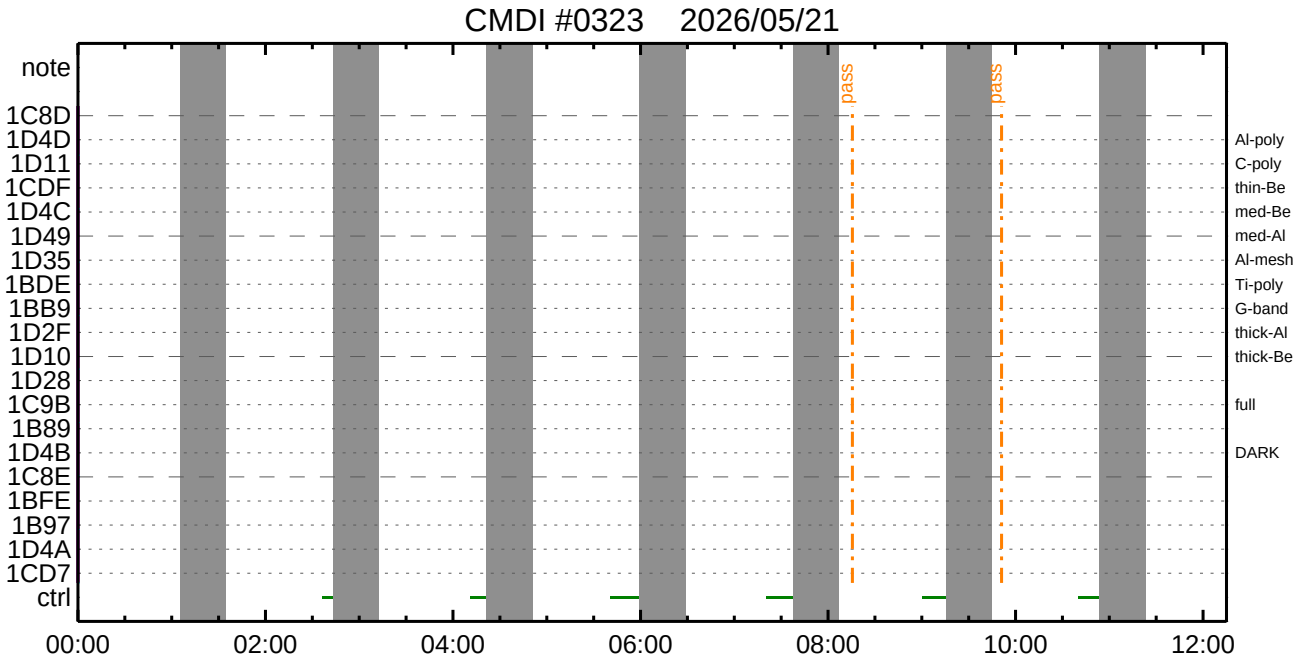
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1CD7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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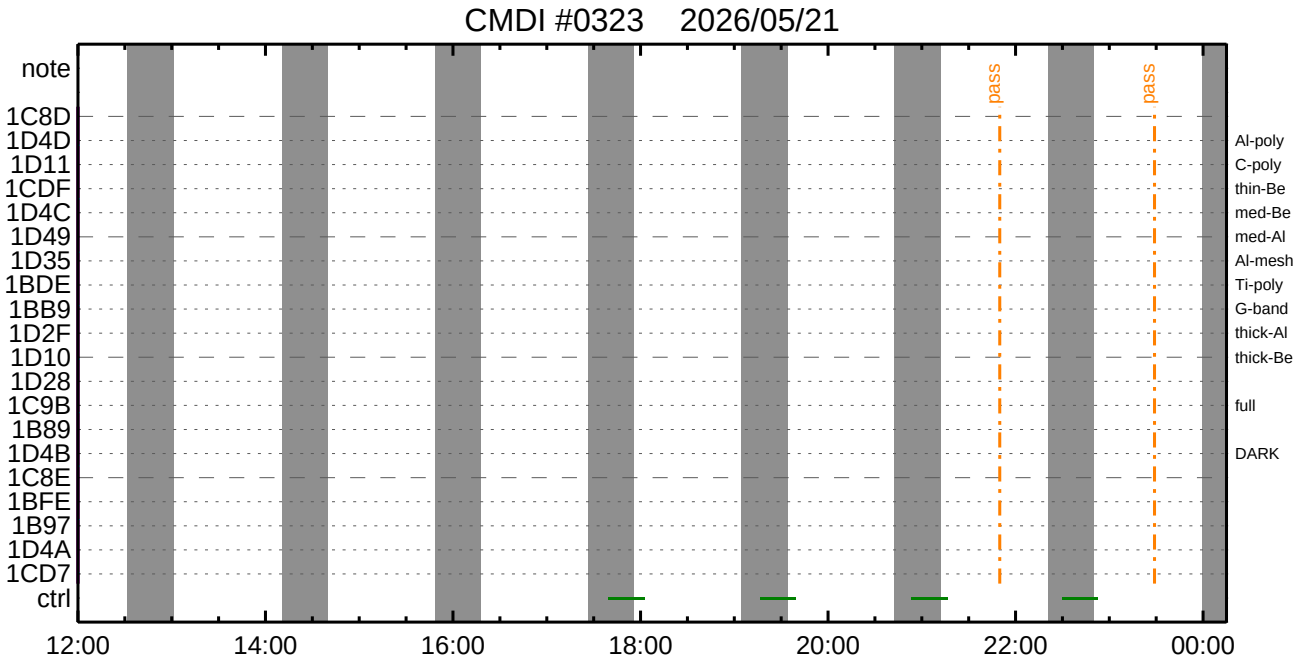
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note
1C8D
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med-Al
Al-mesh
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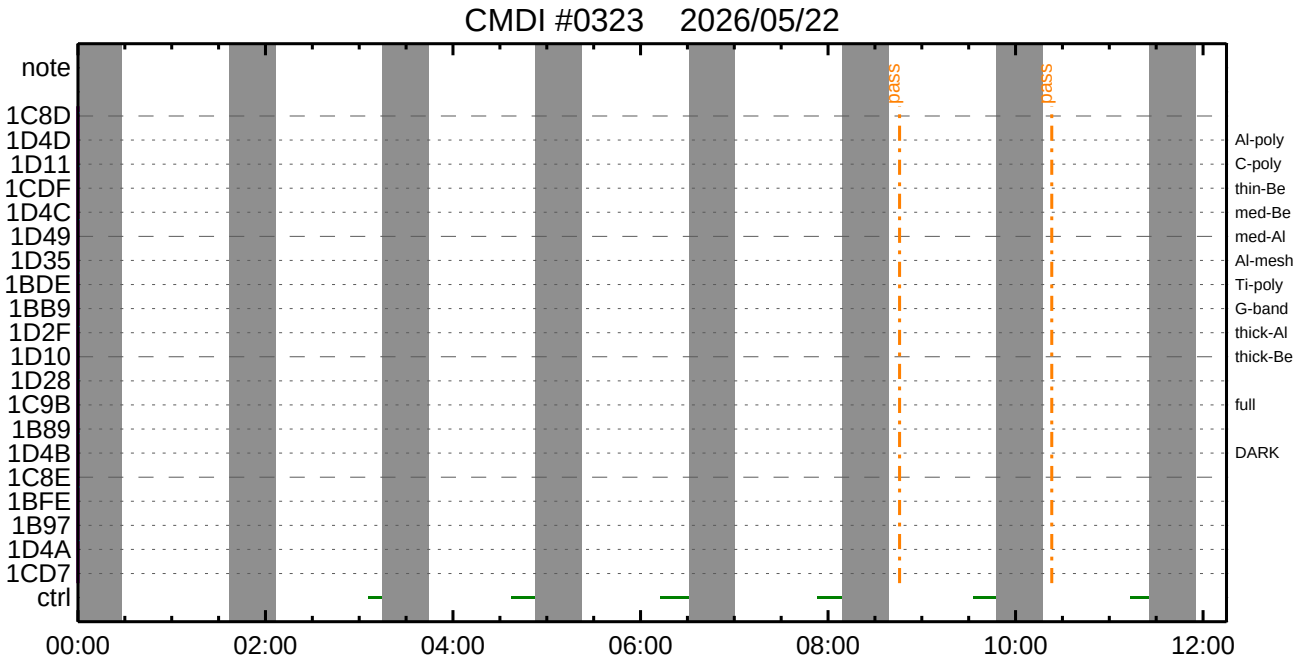
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note
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ctrl
pass
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thin-Be
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med-Al
Al-mesh
Ti-poly
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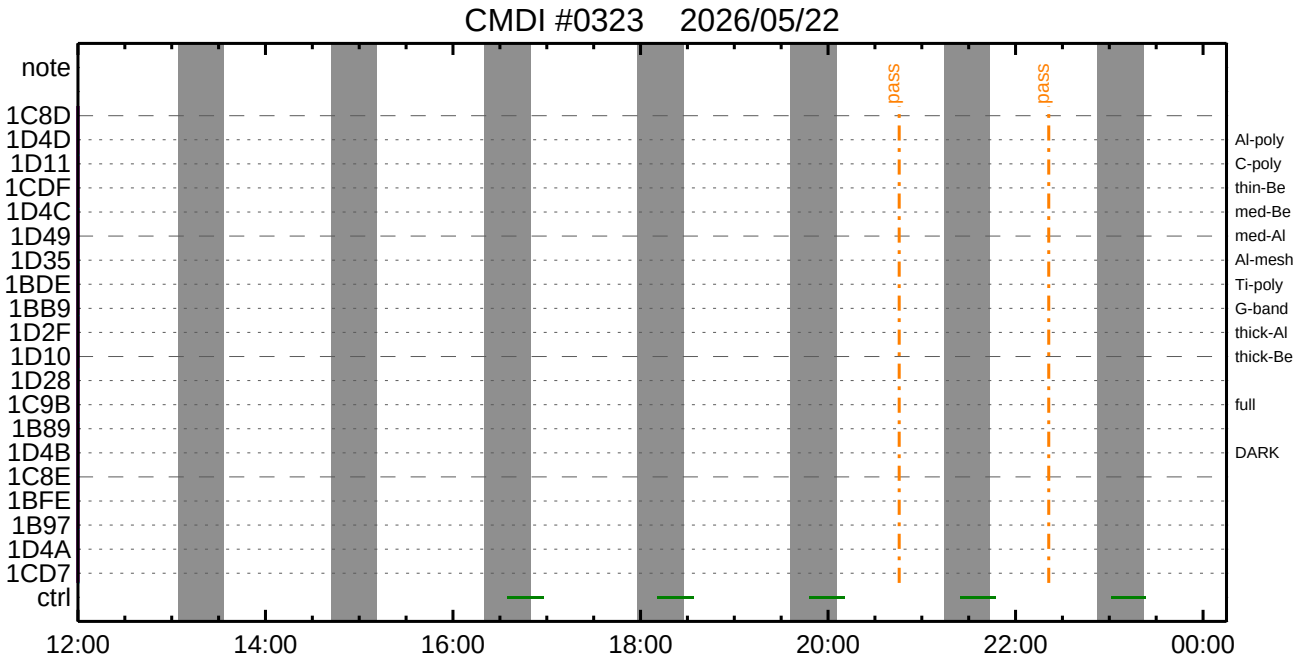
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note
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Al-mesh
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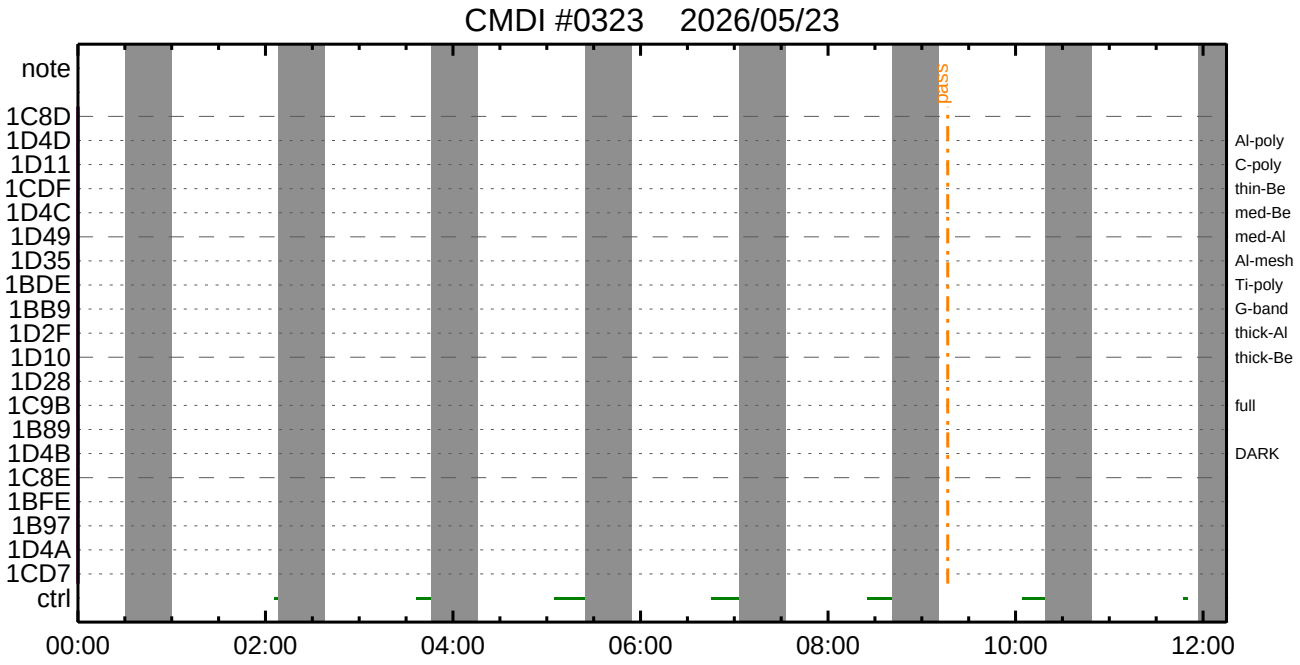
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note
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ctrl
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C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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thick-Be
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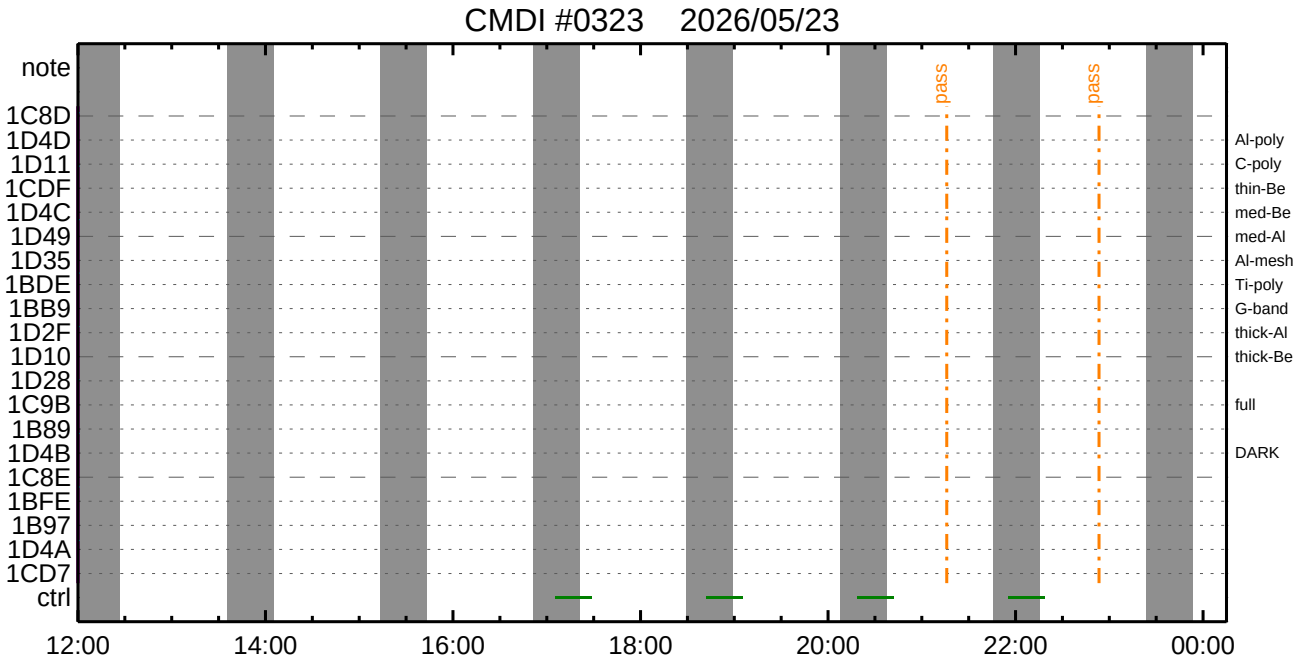
CMDI #0323 2026/05/23
note
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pass
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thin-Be
med-Be
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Al-mesh
Ti-poly
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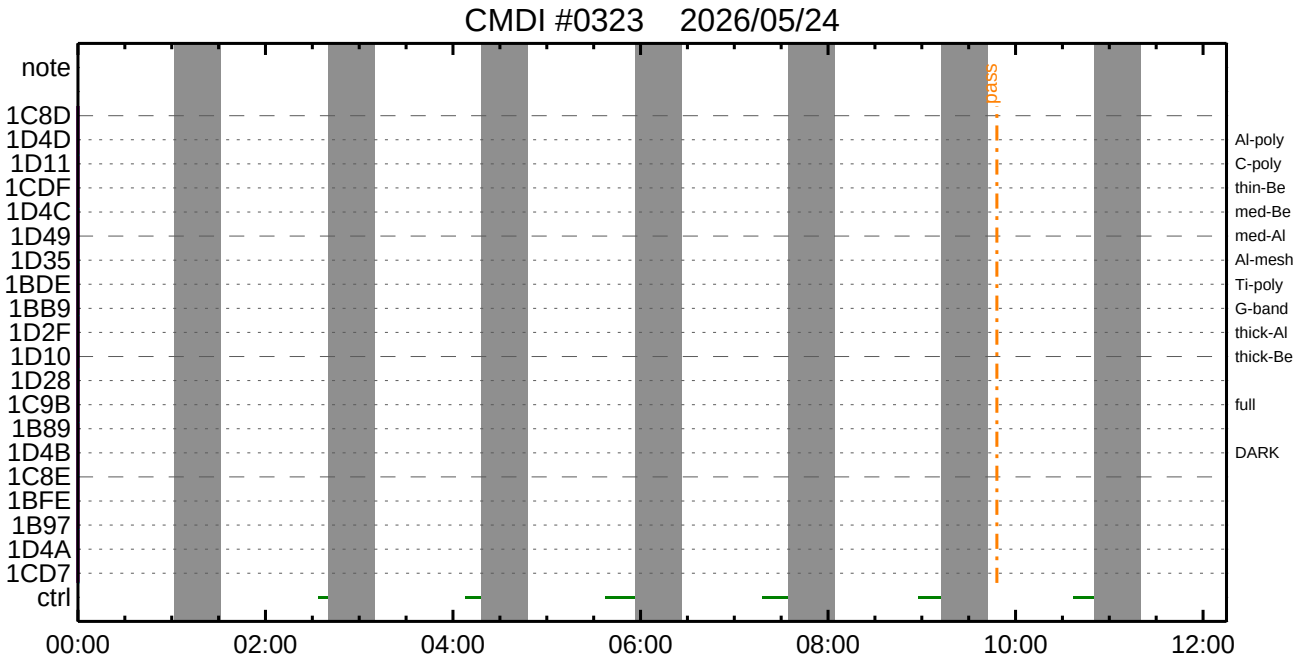
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1C8E
1BFE
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1D4A
1CD7
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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thick-Be
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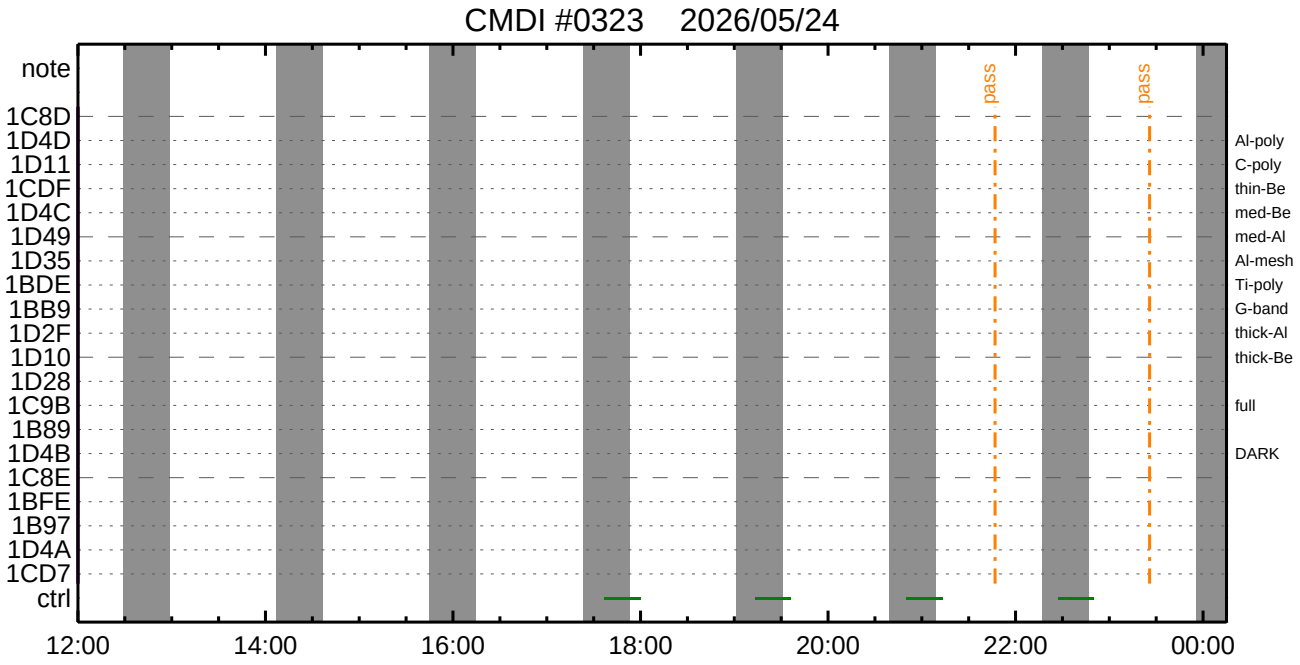
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note
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1CD7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
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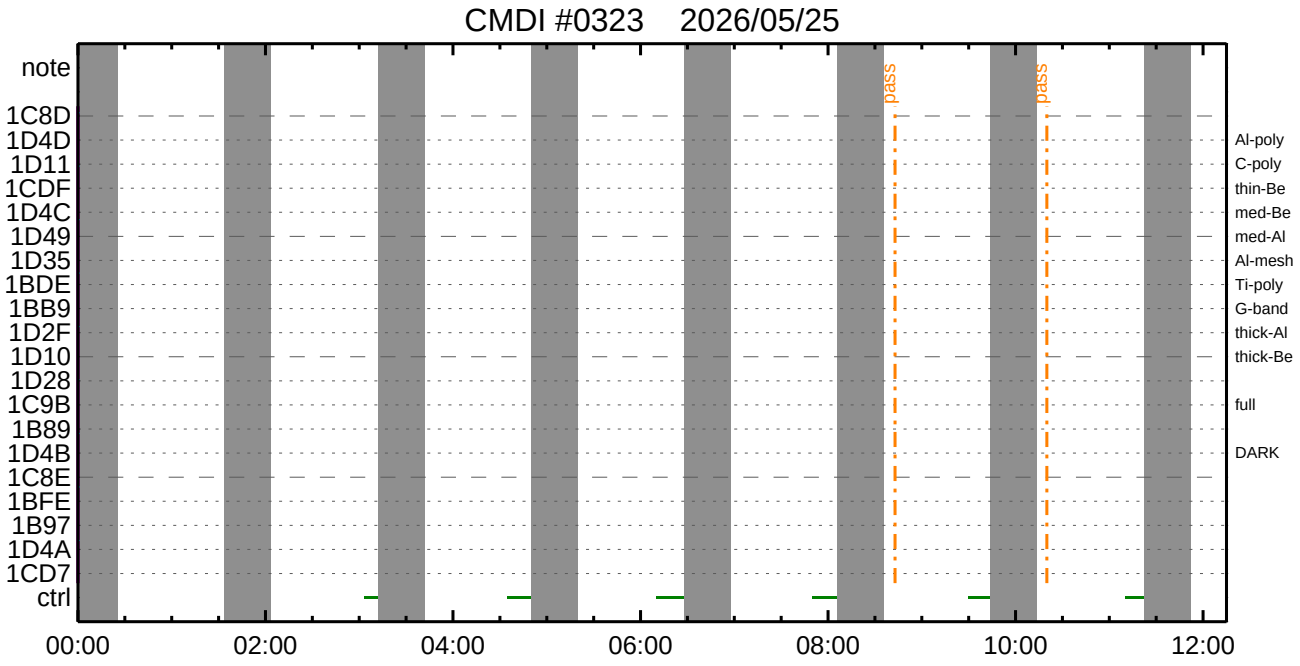
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1C8D
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1C8E
1BFE
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1CD7
ctrl
pass
pass
Al-poly
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thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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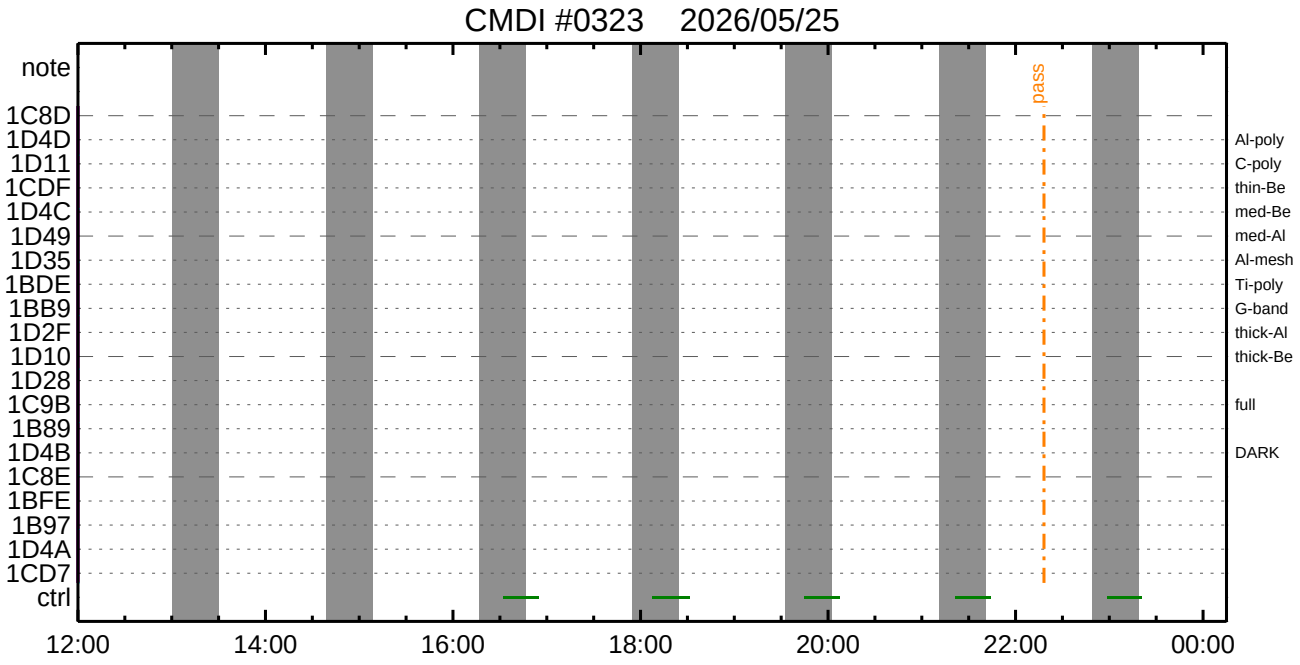
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1C8E
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1CD7
ctrl
pass
Al-poly
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thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
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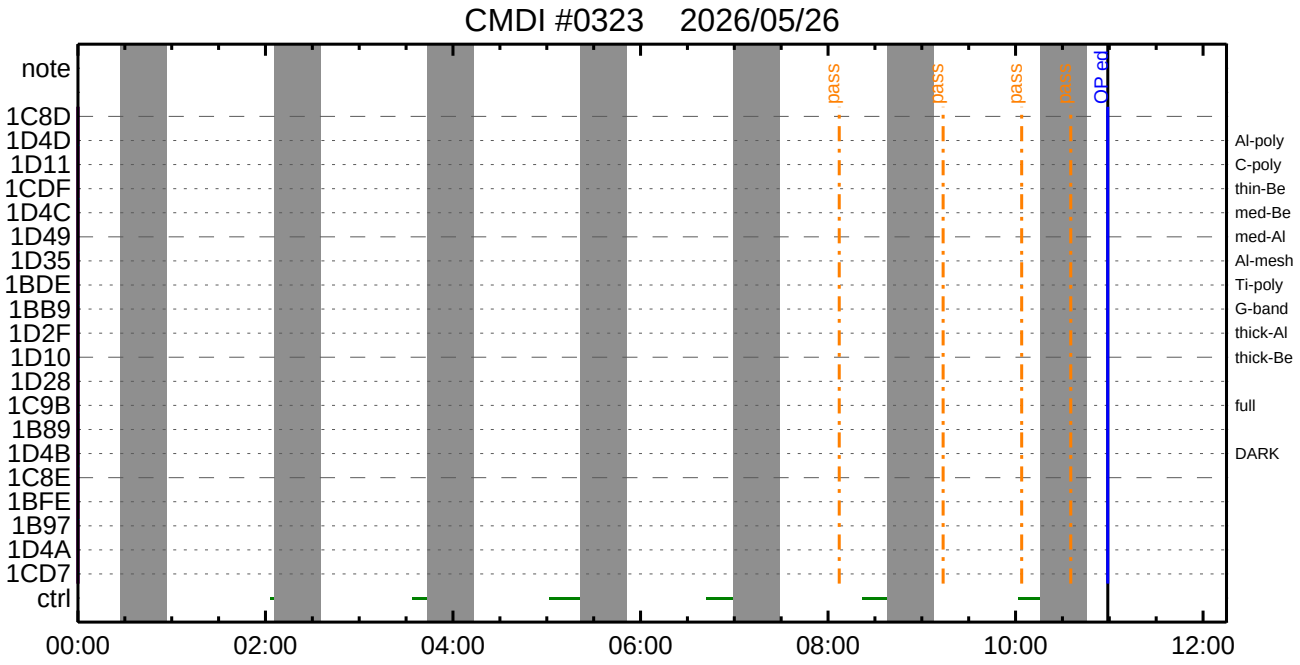
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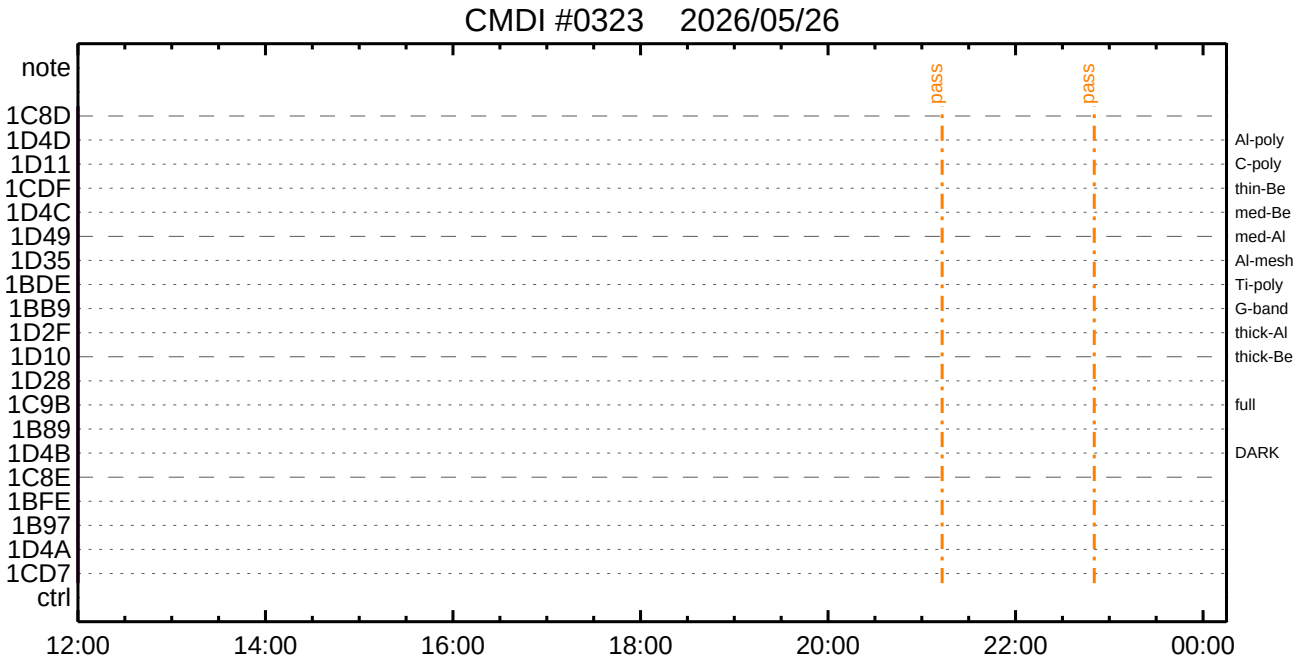
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note
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ctrl
pass
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Al-poly
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med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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```
(a) Spacecraft Operation Procedure (real-commands)

main-980 2026-05-16 13:11:32 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
    C.
    C.
  . C. ****************************
    C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
    C. ****************************
    C.
  . C. ¡ãOP/OG¥í¡¼¥É¡ä
  . S. OP    op-980:OP
    ()
  . S. OG    og-980:OG
    ()
    C.
  . C. ¡ãNMOG&OPÎÎºè¥À¥ó¥×¡ä
    C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (20 00 7f 01 02)
    C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
    C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (20 80 7f 01 02)
    C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
    C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
    C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
    C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
 +. DC 01-23 DHU_DMA_DMP_PRM_SET
    BC        (21 00 41 01 02)
    C.                      ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
    C.                      ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
    C.                      ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
    C.                      ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
    C.                      ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
 +. DC 01-22 DHU_MODE_CHNG
    BC        (07 0b f8)
    C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     7
    C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
    C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
    C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
  . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
    C.                      ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
  . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
    C.
  . C. ***** ºÊ²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Çâ) *****
    C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
 +. DC 01-22 DHU_MODE_CHNG
    BC        (02 0a f8)
    C.                      ¢¢[HK1_PKT_FORM_NO]             EQ     2
    C.                      ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
    C.                      ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
    C.                      ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
    C.
  . C. *********************************
    C. TI-CMD SET (OPOG STOP/COPY/START)
    C. *********************************
    C.
  . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢ºÊ²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
    C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
    C.
  . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
 +. TI 2026-05-16 11:04:00.0
    DC 01-B3 DHU_OP_STOP
    C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2026-05-16 11:04:01.0
    DC 01-B4 DHU_OP_COPY
    C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
    C.
 +. TI 2026-05-16 11:04:01.0
    DC 01-B5 DHU_OPOG_COPY
    C.                      ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195   +. TI 2026-05-16 11:08:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]           EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]            EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]             EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]           EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]           EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]            EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]           EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ª¤Î»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0237     C.
0238     C.
0239     C. ************ XRT START ************
0240     C. Execute, after the success of OP upload.
0241   +. TI 2026-05-16 11:08:00.0
0242     DC 07-F0 MDP_XRT_MODE_STBY
0243     BC       (c3)
0244    . C.                         [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0245     C.
0246     C. ************ XRT END ************
0247     C. ********************************
0248     C.  SOT TI command set
0249     C. ********************************
0250     C. Execute, after the success of OP upload.
0251   +. TI 2026-05-16 11:08:16.0
0252     DC 07-F0 MDP_SOT_MODE_STBY
0253     BC       (41)
0254    . C. -------------------------------------
0255     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0256     C. -------------------------------------
0257     C. ************ SOT END ************
0258    . C. Stop EIS observation and temporarily disable EIS mode changes
0259     C.
0260     C.
0261     C. ********** Start EIS operation (TI set) **********
0262     C. Execute, after the success of OP upload.
0263     C. Set EIS TI-commands
0264   +. TI 2026-05-16 11:08:30.0
0265     DC 07-FC EIS_MODE_MANU
0266     BC       (21 02)
0267   +. TI 2026-05-16 11:08:40.0
0268     DC 07-FC EIS_MODE_CHG_DIS
0269     BC       (22)
0270    . C.                         [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0271     C. ************ End EIS operation (TI set) ************
0272     C.
0273     C.
0274     C.
0275    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277    . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282    . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288    . C. ********** LOS **********
0289     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-982 2026-05-16 13:11:32 216 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ********************************
0099     C.  SOT table upload
0100     C. ********************************
0101   . C. < Stop SP table >
0102   +. DC 07-F0 MDP_SP_CTRL_MANU
0103     BC       (61)
0104     C. ------------------------------------
0105     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108   . C. <Upload SP Observation Table>
0109   . S. RAM   ram-284:MDP_OBS_S
0110     ()
0111     C.
0112   . C. < Dump RAMID=MDP_OBS_S >
0113   +. DC 07-F0 MDP_DUMP_SPTBL
0114     BC       (83 07 00 00 00 38 b8)
0115     C. ------------------------------------
0116     C.  MDP_OBS_S verify = OK/NG        [ ]
0117     C. ------------------------------------
0118     C.
0119     C. ********************************
0120     C.  SOT TI command set
0121     C. ********************************
0122     C. Execute, after the success of TBL upload.
0123   +. TI 2026-05-16 11:08:18.0
0124     DC 07-F0 MDP_SOT_MODE_OBSV
0125     BC       (40)
0126    . C. ------------------------------------
0127     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0128     C. ------------------------------------
0129     C.
0130     C.
0131     C. ************ XRT START ************
0132     C.
0133   +. DC 07-F0 MDP_XRT_CTRL_MANU
0134     BC       (c1)
0135   +. DC 07-F0 MDP_XRT_CTRL_MANU
0136     BC       (c1)
0137   +  DC 07-F0 MDP_XRT_MODE_STBY
0138     BC       (c3)
0139    . C. ----------- Success Verify ?              OK / NG____
0140     C.
0141     C. XRT Obs. Table Upload
0142   . S. RAM   ram-291:MDP_OBS_X
0143     ()
0144     C.
0145   +. DC 07-F0 MDP_DUMP_XRTTBL
0146     BC       (84 07 00 00 00 3a d4)
0147    . C. ----------- Comparison Check ?                        OK / ERR ____
0148     C.
0149     C.
0150   +. DC 07-F0 MDP_XRT_ROI_SET
0151     BC       (cd 01 b1 b1 04 04)
0152   +  DC 07-F0 MDP_XRT_ROI_SET
0153     BC       (cd 02 b1 b1 08 08)
0154   +  DC 07-F0 MDP_XRT_ROI_SET
0155     BC       (cd 03 b1 b1 08 08)
0156   +  DC 07-F0 MDP_XRT_ROI_SET
0157     BC       (cd 04 b1 b1 06 06)
0158   +  DC 07-F0 MDP_XRT_ROI_SET
0159     BC       (cd 05 85 83 06 06)
0160   +  DC 07-F0 MDP_XRT_ROI_SET
0161     BC       (cd 06 85 83 06 06)
0162   +  DC 07-F0 MDP_XRT_ROI_SET
0163     BC       (cd 07 85 83 08 08)
0164   +  DC 07-F0 MDP_XRT_ROI_SET
0165     BC       (cd 08 80 80 20 20)
0166   +  DC 07-F0 MDP_XRT_ROI_SET
0167     BC       (cd 09 80 80 20 08)
0168   +  DC 07-F0 MDP_XRT_ROI_SET
0169     BC       (cd 0a 80 80 08 20)
0170   +  DC 07-F0 MDP_XRT_ROI_SET
0171     BC       (cd 0f 80 80 06 06)
0172   +  DC 07-F0 MDP_XRT_ROI_SET
0173     BC       (cd 10 80 80 08 08)
0174   +  DC 07-F0 MDP_XRT_FLD_ENA
0175     BC       (d8)
0176   +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0177     BC       (c8)
0178   +  DC 07-F0 MDP_XRT_ARS_DIS
0179     BC       (d5)
0180   +  DC 07-F0 MDP_XRT_AEC_RESET
0181     BC       (d0)
0182   +  DC 07-F0 MDP_XRT_FLD_RESET
0183     BC       (da)
0184   +. DC 07-F0 MDP_XRT_QT_PROG_SET
0185     BC       (c4 02)
0186   +. DC 07-F0 MDP_XRT_FL_PROG_SET
0187     BC       (c5 0e)
0188    . C. ---------- Success Verify ?            OK / NG ____
0189     C.
0190     C.
0191    . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0192     C.
0193   +. DC 07-F0 MDP_XRT_MODE_OBSV
```

```
0194     BC       (c2)
0195  +. TI 2026-05-16 11:08:02.0
0196     DC 07-F0 MDP_XRT_MODE_OBSV
0197     BC       (c2)
0198   . C. ---------- Success Verify ?            OK / NG ____
0199     C.
0200     C. ************ XRT END ***********
0201     C.
0202   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0203     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0204   . S. DC-BC dcbc-402:DCBC
0205     (MDP_known_event)
0206     C.
0207     C.
0208   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0209   . S. DC-BC dcbc-153:DCBC
0210     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0211     C.
0212     C.
0213   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0214     C.
0215   . C. *********** LOS ***********
0216     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-983 2026-05-16 13:11:32 85 33 SOLAR-B MAIN //
0001     C.
0002    . C. *********** AOS ***********
0003     C.
0004    . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009    . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014    . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017    . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034    . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045    . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046   +. DC 07-FC EIS_MODE_CHG_ENA
0047     BC       (20)
0048    . C. Verify EIS_MODE_CHG_FLG is ENA
0049   +. DC 07-FC EIS_MODE_MANU
0050     BC       (21 02)
0051    . C. Verify EIS in MANUAL mode
0052    . C. Estimated OBSTBL upload time is 46s
0053     C. ********************************
0054     C. EIS START OBSTBL LOAD
0055     C. ********************************
0056    . S. RAM   ram-820:EIS_OBSTBL
0057     ()
0058   +. DC 07-FC EIS_DUMP_OBSTBL
0059     BC       (07 07 07 00 00 70 00)
0060     C.
0061     C. Execute, after the success of OBSTBL upload.
0062     C. Set EIS TI-commands
0063   +. TI 2026-05-16 11:08:50.0
0064     DC 07-FC EIS_MODE_CHG_ENA
0065     BC       (20)
0066    . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0067     C. ********************************
0068     C. EIS END OBSTBL LOAD
0069     C. ********************************
0070     C.
0071    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0072     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥ç¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0073    . S. DC-BC dcbc-402:DCBC
0074     (MDP_known_event)
0075     C.
0076     C.
0077    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0078    . S. DC-BC dcbc-153:DCBC
0079     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080     C.
0081     C.
0082    . C. ¡ãLOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0083     C.
0084    . C. *********** LOS ***********
0085     C.
```

```
*** OP Sequence for XRT ***

2026/05/16  11:18:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  11:18:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  11:18:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/05/16  11:19:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/16  11:19:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/05/16  11:19:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/05/16  11:19:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/05/16  11:19:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/16  11:19:26.0  XRT_FLD_RESET_401_OG [0x191]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  11:19:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  11:19:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  11:19:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  11:19:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  11:21:56.0  XRT_QT_PROG_SET_444_OG [0x1bc]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 02
2026/05/16  11:21:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/05/16  11:22:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  11:30:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  11:31:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  11:32:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  11:32:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  11:32:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  11:32:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  11:35:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  12:01:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  12:02:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  12:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  12:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  12:59:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/05/16  13:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/16  13:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/05/16  13:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/05/16  13:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/05/16  13:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/16  13:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  13:02:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2026/05/16  13:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/05/16  13:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  13:10:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  13:10:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  13:10:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  13:10:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  13:13:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  13:39:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  13:40:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  14:48:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  14:48:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  14:48:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
```

```
2026/05/16  14:48:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  14:51:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  15:17:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  15:18:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  16:14:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  16:14:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  16:14:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/05/16  16:15:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/05/16  16:15:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/05/16  16:15:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/05/16  16:15:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/16  16:17:58.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2026/05/16  16:18:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  16:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  16:26:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  16:26:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  16:26:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  16:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  16:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  16:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  16:59:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/05/16  17:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 05 58 01 db
2026/05/16  17:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/05/16  17:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/05/16  17:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/05/16  17:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/16  17:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  17:02:56.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 12
2026/05/16  17:02:58.0  XRT_FL_PROG_SET_441_OG [0x1b9]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0a
2026/05/16  17:04:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  17:05:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  18:05:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  18:05:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  18:05:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  18:05:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  18:08:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  18:40:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  18:41:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  19:43:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  19:43:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  19:43:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  19:43:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  19:46:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  20:16:30.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  20:17:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  20:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  20:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  20:59:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/05/16  21:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
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                                    AOCU_NM              5 02-76 04 05 58 01 db
2026/05/16  21:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2026/05/16  21:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/05/16  21:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2026/05/16  21:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/16  21:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  21:02:56.0  XRT_QT_PROG_SET_440_OG [0x1b8]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 0d
2026/05/16  21:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/05/16  21:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  21:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  21:21:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  21:21:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  21:21:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  21:24:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  21:53:30.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  21:54:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/16  22:59:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  22:59:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/16  22:59:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/16  22:59:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/16  23:02:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/16  23:29:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/16  23:30:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/17  00:37:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  00:37:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  00:37:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/17  00:37:06.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/17  00:40:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/17  01:06:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/17  01:07:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/17  02:10:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  02:10:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  02:10:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/17  02:10:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/17  02:13:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/17  02:44:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/17  02:45:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/17  03:42:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  03:42:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  03:42:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/17  03:42:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/17  03:45:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/17  03:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  03:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  03:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/05/17  04:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2026/05/17  04:00:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2026/05/17  04:00:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/05/17  04:00:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
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2026/05/17  04:00:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/17  04:00:26.0  XRT_FLD_RESET_401_OG [0x191]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/17  04:02:56.0  XRT_QT_PROG_SET_405_OG [0x195]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2026/05/17  04:02:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/05/17  04:22:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/17  04:23:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/17  05:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  05:11:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  05:11:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/05/17  05:11:36.0  XRT_PREFLR_STRT_431_OG [0x1af]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/05/17  05:14:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/05/17  06:00:00.0  XRT_Custom_430_OG [0x1ae]
2026/05/17  06:01:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/17  06:04:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  06:04:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  06:04:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/05/17  06:04:48.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/05/17  06:04:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/05/17  06:04:52.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/05/17  06:07:28.0  XRT_QT_PROG_SET_447_OG [0x1bf]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2026/05/17  06:07:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/05/17  06:12:27.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/05/17  06:14:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/17  06:17:30.0  XRT_TCIB_XRT_S_HTR_A_ENA_410_OG [0x19a]
                                TCIB_XRT_S_HTR_A_ENA 0 04-BC
2026/05/17  09:00:00.5  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/17  13:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/17  17:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 05 58 01 db
2026/05/17  21:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/18  17:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 02 05 58 01 db
2026/05/18  21:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 db
2026/05/19  11:16:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 00 00
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