XRT Timeline to be uploaded on 2026/08/04

Period: 2026/08/04 10:50:00 - 2026/08/08 10:45:00

* * * * * **Normal mode** * * * * *

XOB #1D03: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 1st Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term	Pointing (x, y)	Comment
08/05 12:19:00 - 08/05 12:21:54	Fixed (-528.4, -528.4)	Post bakeout Q1

PROG= 04 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 51 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D04: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 2nd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC

Term	Pointing (x, y)	Comment
08/05 12:25:00 - 08/05 12:31:54	Fixed (528.4, -528.4)	Post bakeout Q2

PROG= 19 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 38 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D05: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 3rd Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term	Pointing (x, y)	Comment
08/05 12:35:00 - 08/05 12:41:54	Fixed (528.4, 528.4)	Post bakeout Q3

PROG= 06 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 21 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 3 2-time(s) 2.0sec

Seqn= 34 1-time(s) 60.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #1D06: CCD Monitor During Bakeout - G-band 1ms - 1kx1k - Q90 - 4th Quadrant -Al/mesh(2048ms) - 1x1, Al/Poly(1443ms) - 2x2 - w leak image-1msC0

Term	Pointing (x, y)	Comment
08/05 12:45:00 - 08/05 12:51:54	Fixed (-528.4, 528.4)	Post bakeout Q4

PROG= 05 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 14 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	1ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec

Subr= 2 1-time(s) 120.0sec

Seqn= 93 2-time(s) 2.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec													
Seqn= 34 1-time(s) 60.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D2F: Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(12/256/2048), Thin-Be(45/1024/8192), Thick-Be(65536), Med-Al(512/8192/32768), Med-Be(256/5

Term	Pointing (x, y)	Comment
08/05 12:55:00 - 08/05 13:01:54	Fixed (0.0, 0.0)	Post bakeout synoptics
08/05 18:03:00 - 08/05 18:09:54	Fixed (0.0, 0.0)	synoptic
08/06 08:36:30 - 08/06 08:43:30	Fixed (0.0, 0.0)	synoptic + HOP 349

PROG= 17 1-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 66 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 22 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 19 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 65 1-time(s) 2.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec												
med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 89 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CD7: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/05 13:05:06 - 08/05 17:59:54	Track (501.8, 105.6) @ 08/05 13:02:00	AR 14498
08/05 18:13:00 - 08/05 19:55:00	Track (538.2, 108.2) @ 08/05 18:10:00	# AR 14498
08/06 09:26:30 - 08/06 10:23:00	Track (633.8, 116.7) @ 08/06 08:45:00	# AR 14498

PROG= 01 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 92 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 5-time(s) 2.0sec												
Seqn= 47 1-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96 4-time(s) 180.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1CDF: HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-band - 384x384 1ms

Term	Pointing (x, y)	Comment
08/05 20:29:30 - 08/06 00:49:30	Fixed (-20.0, -980.0)	HOP 81 S pole

PROG= 11 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 16 2-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 90 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

Subr= 3 60-time(s) 60.0sec

Seqn= 24 1-time(s) 30.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec

XOB #1BFE: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/06 01:16:01 - 08/06 06:20:54	Track (585.7, 112.1) @ 08/06 01:11:00	AR 14498 + HOP 173

PROG= 07 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 92 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Subr= 2 4-time(s) 2.0sec

Seqn= 47 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Seqn= 77 4-time(s) 300.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	95.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec

XOB #1C9B: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 mir

Term	Pointing (x, y)	Comment
08/06 06:24:00 - 08/06 08:33:24	Fixed (0.0, 0.0)	synoptic + HOP 349

PROG= 08 Inf.-time(s)

Subr= 1 1-time(s) 300.0sec

Seqn=100 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 84 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 57 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 81 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	1024x1024 (1536, 1536)	Q=95	0	0	2.0sec

Subr= 2 9-time(s) 360.0sec

Seqn= 8 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec

Seqn= 29 1-time(s) 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

* * * * * **Flare mode** * * * * *

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gb

Term	Pointing (x, y)	Comment

08/05 13:05:06 - 08/05 17:59:54	Track (501.8, 105.6)	@ 08/05 13:02:00	AR 14498
08/05 18:13:00 - 08/05 19:55:00	Track (538.2, 108.2)	@ 08/05 18:10:00	# AR 14498
08/06 01:16:01 - 08/06 06:20:54	Track (585.7, 112.1)	@ 08/06 01:11:00	AR 14498 + HOP 173
08/06 06:24:00 - 08/06 08:33:24	Fixed (0.0, 0.0)		synoptic + HOP 349
08/06 09:26:30 - 08/06 10:23:00	Track (633.8, 116.7)	@ 08/06 08:45:00	# AR 14498

PROG= 14 30-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 68 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 20-time(s) 2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 73 1-time(s) 10.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 3 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 87 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

XOB #1D10: Flare - multifilter 5 sec cadence (Be/thin, Be/med), AEC 3, 384x384

Term	Pointing (x, y)	Comment
08/05 20:29:30 - 08/06 00:49:30	Fixed (-20.0, -980.0)	HOP 81 S pole

PROG= 16 1-time(s)

Subr= 1 1-time(s) 2.0sec

Seqn= 67 1-time(s) 2.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 59 255-time(s) 5.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

* * * * * **Active Region Search** * * * * *

NOT USED

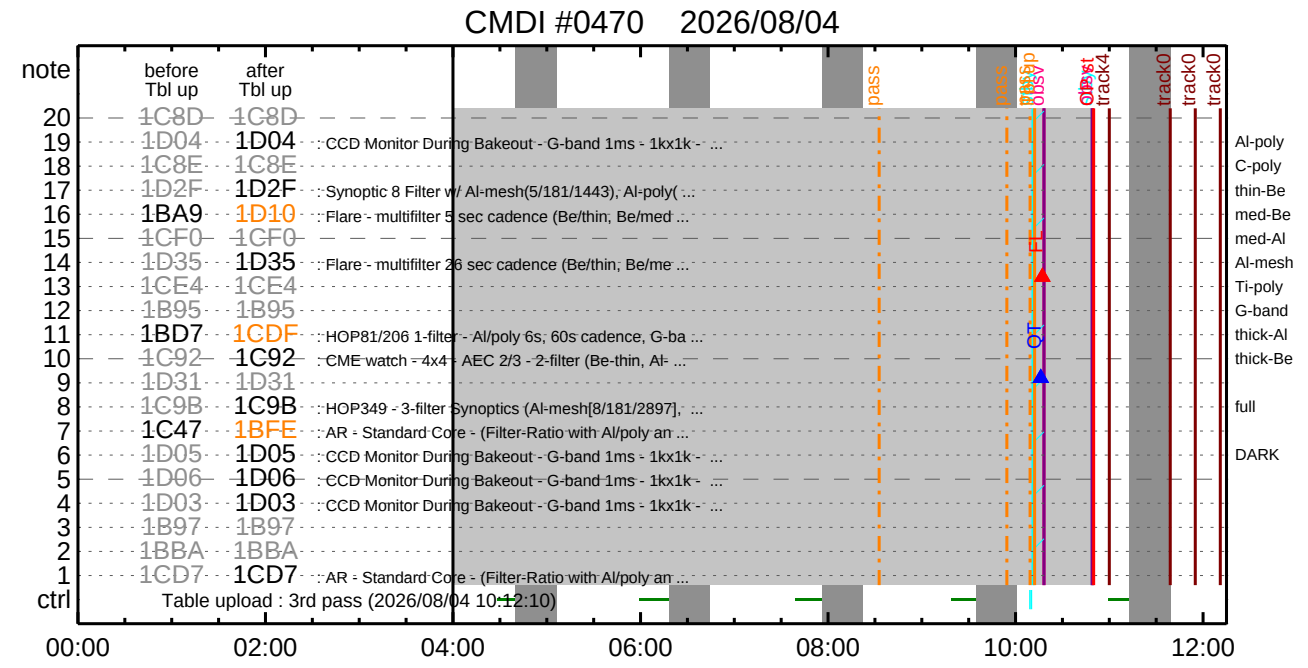
* * * * * **Flare Detection** * * * * *

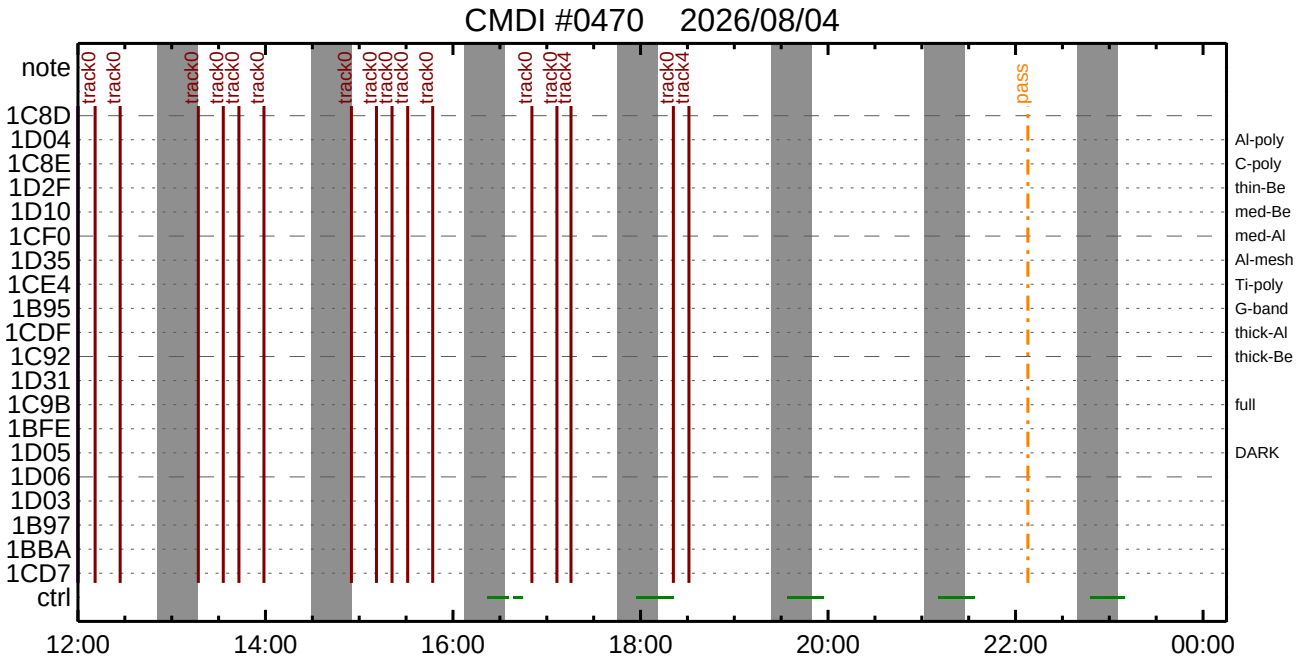
FLD Patrol

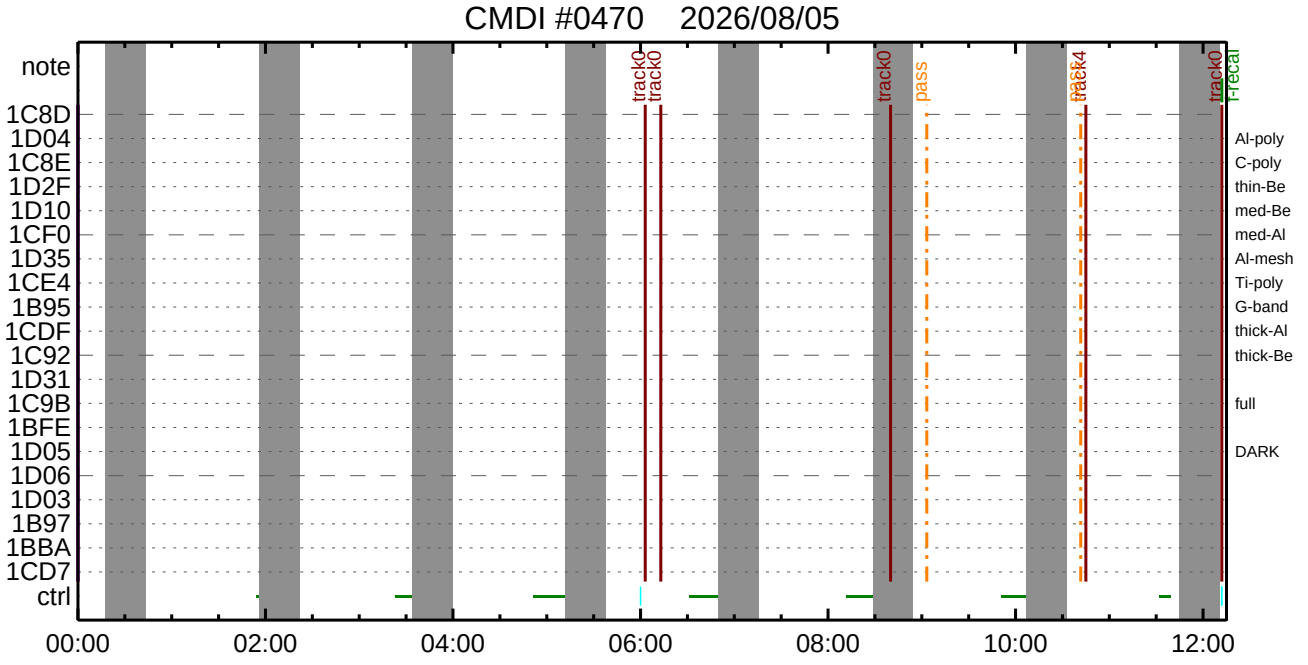
Term	Pointing (x, y)		Comment
08/04 10:13:10 - 08/05 12:18:56	cannot be identified		
08/05 13:02:23 - 08/05 18:00:18	Track (501.8, 105.6)	@ 08/05 13:02:00	AR 14498
08/05 18:10:18 - 08/06 08:33:48	Track (538.2, 108.2)	@ 08/05 18:10:00	# AR 14498
08/06 08:45:18 - 08/08 10:45:00	Track (633.8, 116.7)	@ 08/06 08:45:00	# AR 14498

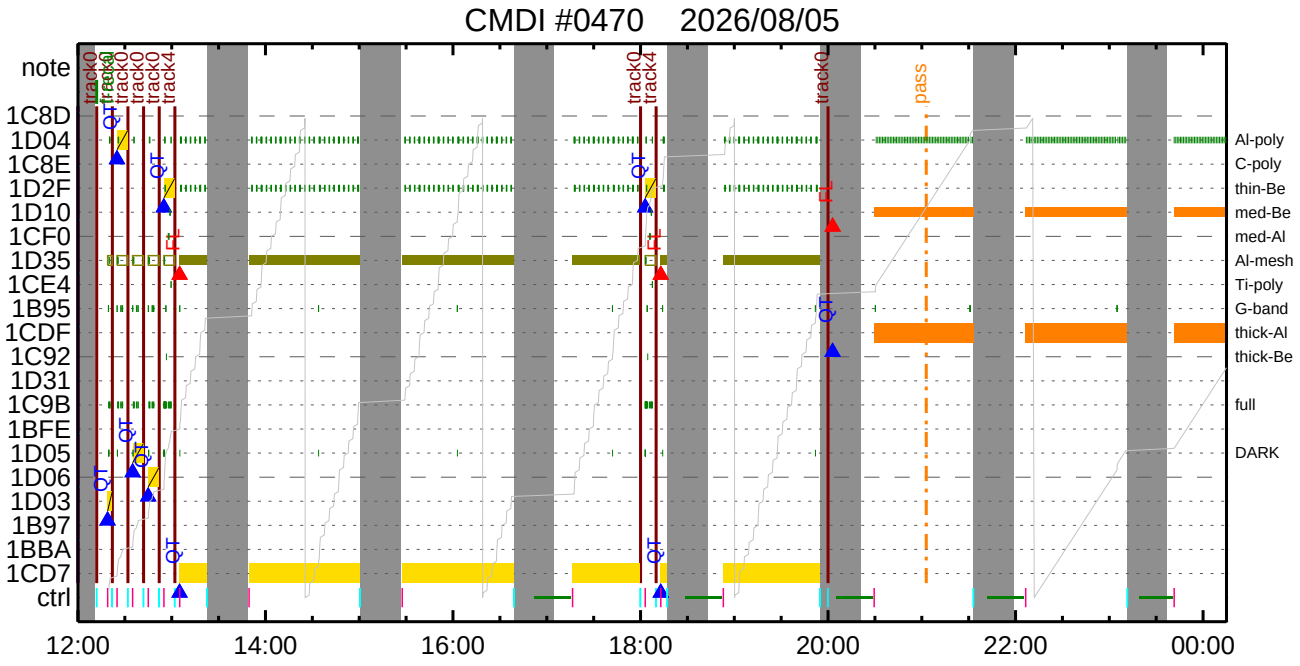
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50			30sec

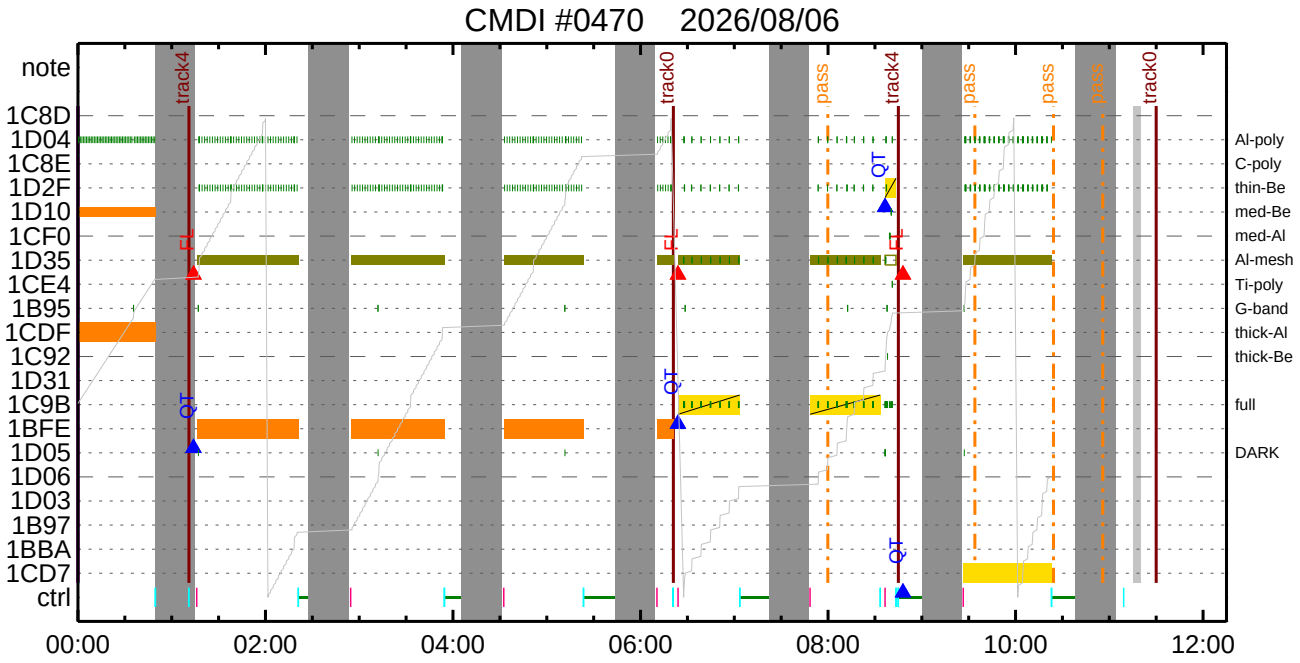
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0470 2026/08/04
note
before Tbl up
after Tbl up
20 1C8D 1C8D
19 1D04 1D04 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
18 1C8E 1C8E
17 1D2F 1D2F : Synoptic 8 Filter w/ Al-mesh(5/181/1443), Al-poly(...
16 1BA9 1D10 : Flare - multifilter 5 sec cadence (Be/thin; Be/med ...
15 1CF0 1CF0
14 1D35 1D35 : Flare - multifilter 26 sec cadence (Be/thin; Be/me ...
13 1CE4 1CE4
12 1B95 1B95
11 1BD7 1CDF : HOP81/206 1-filter - Al/poly 6s, 60s cadence, G-ba ...
10 1C92 1C92 : CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al- ...
9 1D31 1D31
8 1C9B 1C9B : HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], ...
7 1C47 1BFE : AR - Standard Core - (Filter-Ratio with Al/poly an ...
6 1D05 1D05 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
5 1D06 1D06 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
4 1D03 1D03 : CCD Monitor During Bakeout - G-band 1ms - 1kx1k - ...
3 1B97 1B97
2 1BBA 1BBA
1 1CD7 1CD7 : AR - Standard Core - (Filter-Ratio with Al/poly an ...
ctrl Table upload : 3rd pass (2026/08/04 10:12:10)
pass
obsp
obsv
FL
QT
track4
track0
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0470 2026/08/04
note
1C8D
1D04
1C8E
1D2F
1D10
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
track0
track4
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0470 2026/08/05
note
1C8D
1D04
1C8E
1D2F
1D10
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
track0
pass
track4
Frecal
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0470 2026/08/05
note
1C8D
1D04
1C8E
1D2F
1D10
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
track0
track4
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0470 2026/08/06
note
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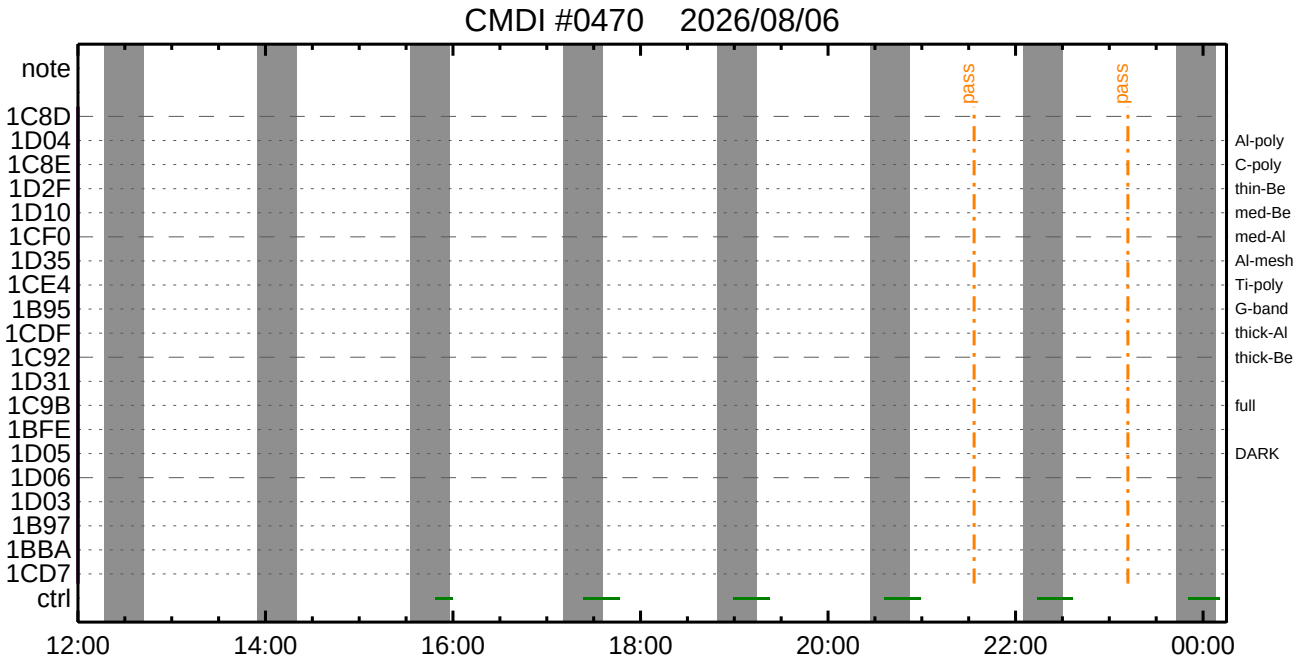
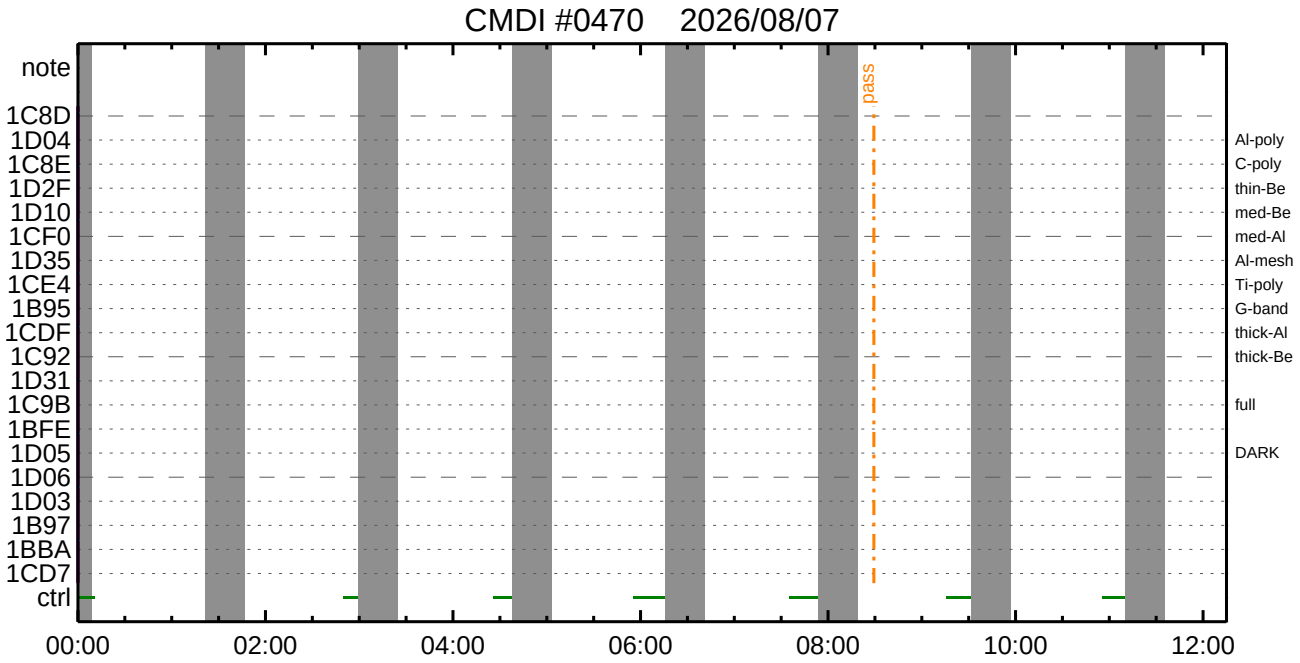
1D04
1C8E
1D2F
1D10
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
track4
track0
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0470 2026/08/06
note
1C8D
1D04
1C8E
1D2F
1D10
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0470 2026/08/07
note
1C8D
1D04
1C8E
1D2F
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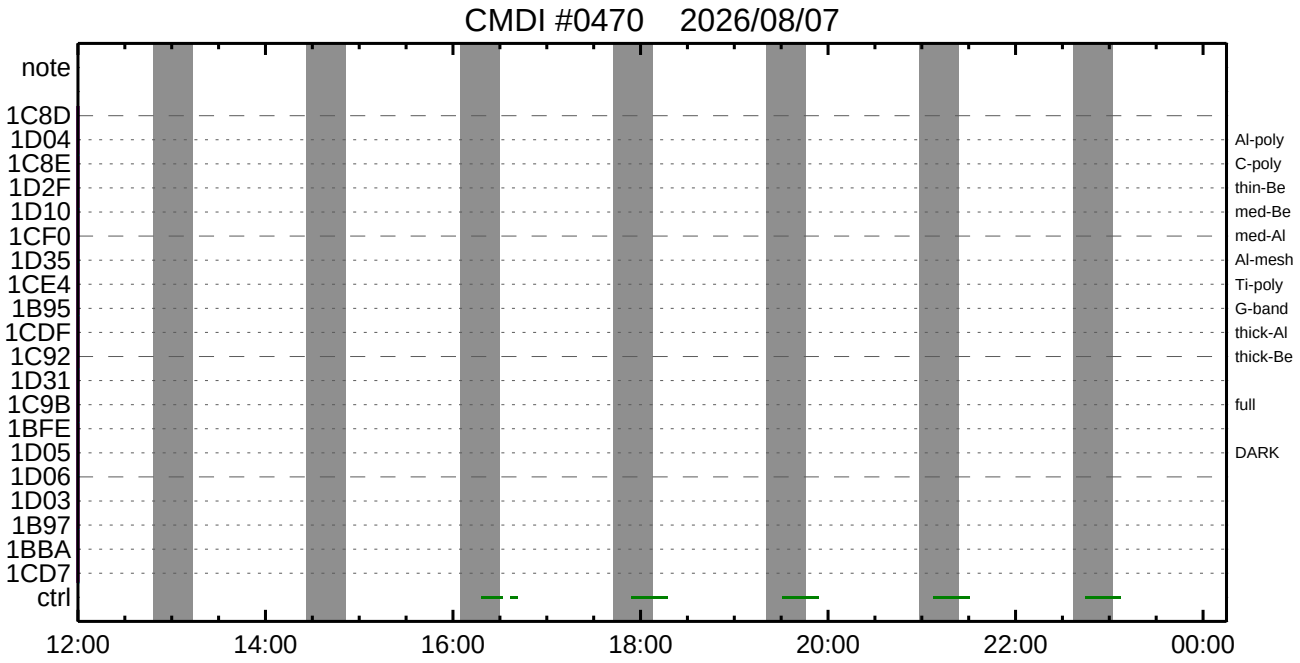
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0470 2026/08/07
note
1C8D
1D04
1C8E
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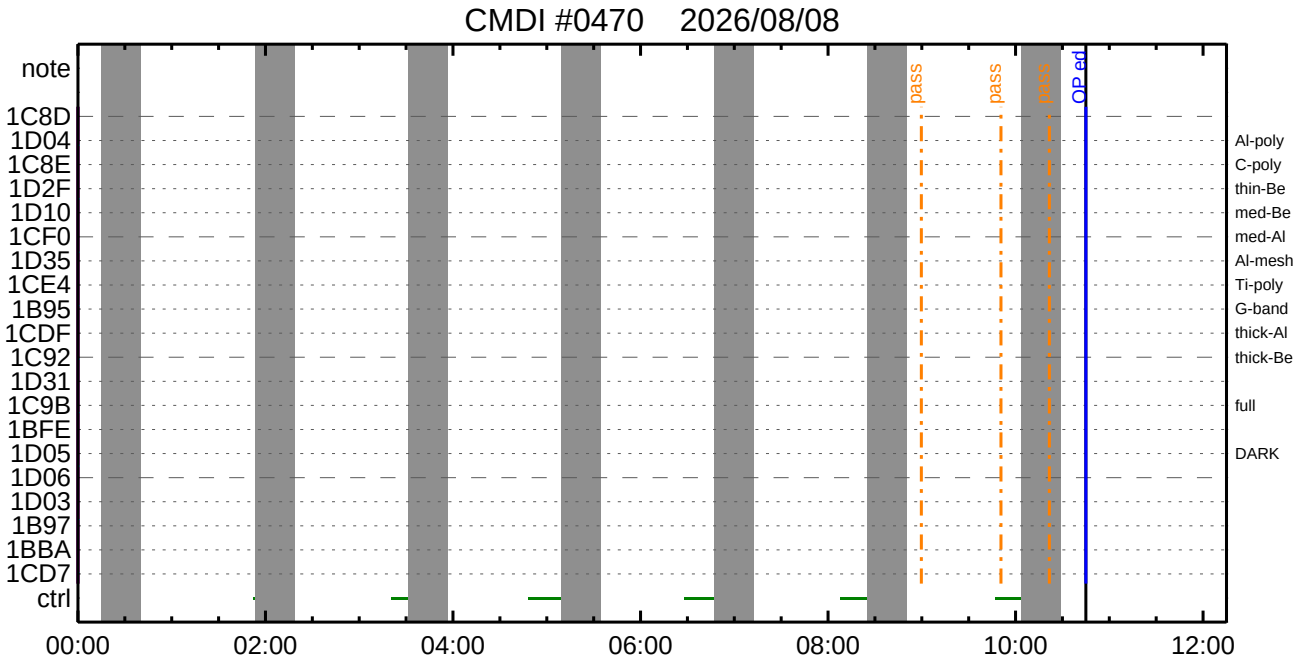
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0470 2026/08/08
note
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1D04
1C8E
1D2F
1D10
1CF0
1D35
1CE4
1B95
1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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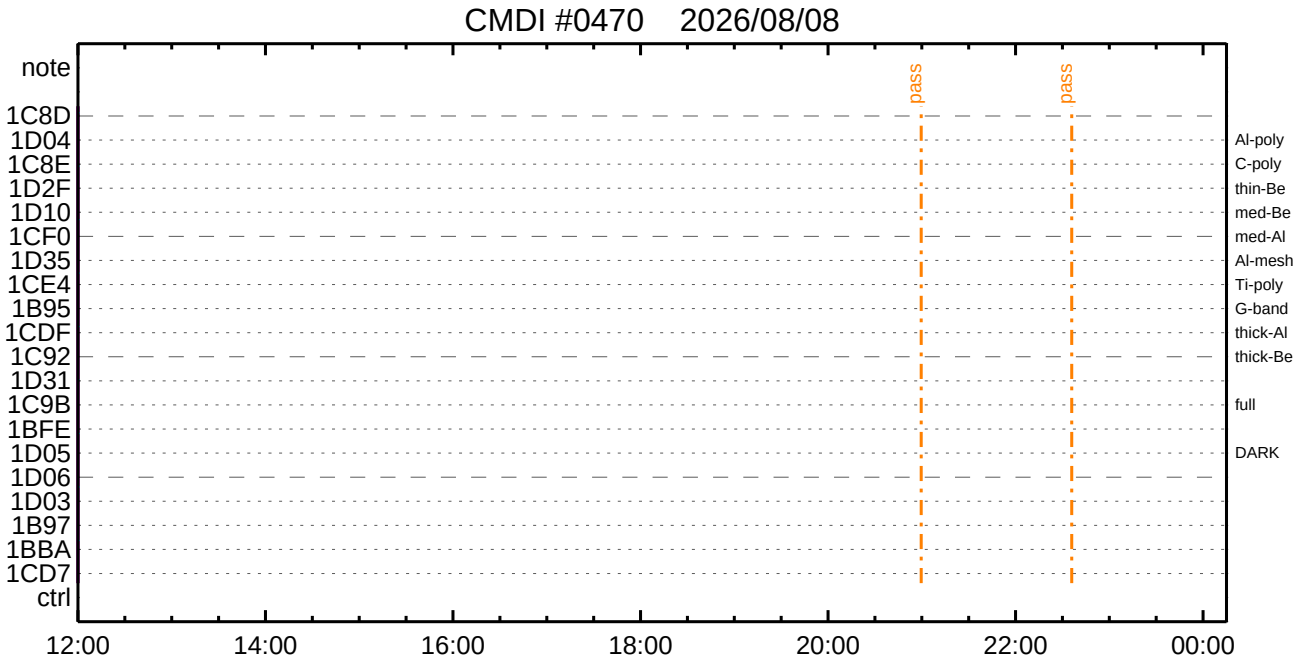
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0470 2026/08/08
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1D04
1C8E
1D2F
1D10
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1CE4
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1CDF
1C92
1D31
1C9B
1BFE
1D05
1D06
1D03
1B97
1BBA
1CD7
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

```
(a) Spacecraft Operation Procedure (real-commands)

main-141 2026-08-04 11:40:55 278 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-141:OP
0104     ()
0105   . S. OG    og-141:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117   +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136   +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                       ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                       ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                       ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                       ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                       ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155   +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                       ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý¸¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168   +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                       ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                       ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                       ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                       ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. ********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. ********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¥Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183   +. TI 2026-08-04 10:45:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187   +. TI 2026-08-04 10:45:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191   +. TI 2026-08-04 10:45:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                       ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

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0194     C.
0195   +. TI 2026-08-04 10:49:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                        ¢¢[HK1_TI_CMD_ENA/DIS]           EQ     ENA
0201     C.                        ¢¢[HK1_TI_CMD_NUM]               EQ     4
0202     C.                        ¢¢[HK1_NEXT_EXEC_PIM]            EQ     DHU
0203     C.                        ¢¢[HK1_NEXT_EXEC_DC]             EQ     0xB3
0204     C.
0205    . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210   +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                        ¢¢[HK1_DMP_TOP_ADRS_1]           EQ     07
0213     C.                        ¢¢[HK1_DMP_TOP_ADRS_0]           EQ     2B
0214     C.                        ¢¢[HK1_DMP_BLOCK_NUM]            EQ     3
0215     C.                        ¢¢[HK1_DMP_REPEAT_NUM]           EQ     0
0216     C.                        ¢¢[HK1_DMA_DMP_PIM]              EQ     DHU
0217   +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     7
0220     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.25 s
0221     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32k
0222     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0223     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     EXEC
0224     C.
0225    . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                        ¢¢[HK1_DMP_CHK_FLG]              EQ     NON
0227     C.
0228    . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230    . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231   +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                        ¢¢[HK1_PKT_FORM_NO]              EQ     2
0234     C.                        ¢¢[HK1_PKT_GEN_TIME]             EQ     0.5S
0235     C.                        ¢¢[HK1_S_TLM_BIT_RATE]           EQ     32K
0236     C.                        ¢¢[HK1_X_TLM_BIT_RATE]           EQ     4M
0237     C.
0238    . C. Stop EIS observation and temporarily disable EIS mode changes
0239     C.
0240     C.
0241     C. ********** Start EIS operation (TI set) **********
0242     C. Execute, after the success of OP upload.
0243     C. Set EIS TI-commands
0244   +. TI 2026-08-04 10:49:30.0
0245     DC 07-FC EIS_MODE_MANU
0246     BC       (21 02)
0247   +. TI 2026-08-04 10:49:40.0
0248     DC 07-FC EIS_MODE_CHG_DIS
0249     BC       (22)
0250    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0251     C. ************ End EIS operation (TI set) ************
0252     C.
0253     C.
0254     C.
0255     C. ************ XRT START ************
0256     C. Execute, after the success of OP upload.
0257   +. TI 2026-08-04 10:49:00.0
0258     DC 07-F0 MDP_XRT_MODE_STBY
0259     BC       (c3)
0260    . C.                       [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0261     C.
0262     C. ************ XRT END ************
0263     C.
0264    . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0265     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0266    . S. DC-BC dcbc-402:DCBC
0267     (MDP_known_event)
0268     C.
0269     C.
0270    . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0271    . S. DC-BC dcbc-153:DCBC
0272     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273     C.
0274     C.
0275    . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0276     C.
0277    . C. *********** LOS ***********
0278     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-142 2026-08-04 11:40:55 94 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022   +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029   +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038   +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                          <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043   +. DC 02-8E AOCU_ORB_UPD
0044    . C.
0045   . C. ***** AOCS Commands (Orbital Element Update) *****
0046     C. Update the orbital element
0047   +. DC 02-50 AOCU_ORB_PRPGT_START
0048     BC       (16)
0049   +  DC 02-8E AOCU_ORB_UPD
0050     C.
0051     C.       <A_ORB>[ORBIT] EPC = 5151461.7 +- 1.0 (s) [ ]
0052     C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055   +. DC 07-FC EIS_MODE_CHG_ENA
0056     BC       (20)
0057   . C. Verify EIS_MODE_CHG_FLG is ENA
0058   +. DC 07-FC EIS_MODE_MANU
0059     BC       (21 02)
0060   . C. Verify EIS in MANUAL mode
0061   . C. Estimated OBSTBL upload time is 59s
0062     C. *********************************
0063     C. EIS START OBSTBL LOAD
0064     C. *********************************
0065   . S. RAM   ram-820:EIS_OBSTBL
0066     ()
0067   +. DC 07-FC EIS_DUMP_OBSTBL
0068     BC       (07 07 07 00 00 70 00)
0069     C.
0070     C. Execute, after the success of OBSTBL upload.
0071     C. Set EIS TI-commands
0072   +. TI 2026-08-04 10:49:50.0
0073     DC 07-FC EIS_MODE_CHG_ENA
0074     BC       (20)
0075   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0076     C. *********************************
0077     C. EIS END OBSTBL LOAD
0078     C. *********************************
0079     C.
0080   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0081     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0082   . S. DC-BC dcbc-402:DCBC
0083     (MDP_known_event)
0084     C.
0085     C.
0086   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0087   . S. DC-BC dcbc-153:DCBC
0088     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089     C.
0090     C.
0091   . C. ¡ãLOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0092     C.
0093   . C. *********** LOS ***********
0094     C.
```

```
(a) Spacecraft Operation Procedure (real-commands)

main-143 2026-08-04 11:40:55 192 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥$¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007   +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼°ÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011   +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ °ÆÀ¸Í×È¥¤äLOS¤Þ¤Ç¤Î»Þ´Ö¤ò¹ÍÎ¸¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019   +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021   +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023   +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                      ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                      ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê•¤¿¤é¡¢°Ê²¼¤Î°ÆÀ¸¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ï°ÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1°ÆÀ¸³«»Ï¡ä
0037   +. DC 01-29 DHU_S/X_VC4_OFF
0038   +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040   +  DC 06-B3 DR_REP_START
0041   +  DC 01-32 DHU_X_VC4_ON
0042     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0043     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0044     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0047   +. DC 06-B3 DR_REP_START
0048   +  DC 01-32 DHU_X_VC4_ON
0049     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸¡¾Ú)
0050     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0051     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1°ÆÀ¸¤¬¼«°Ää»ß¤•¤¿¸å¡¢¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Ä°²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ï°ÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢¢°Ê²¼¤Ï¼Â¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2°ÆÀ¸³«»Ï¡ä
0063   +. DC 01-29 DHU_S/X_VC4_OFF
0064   +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066   +  DC 06-B3 DR_REP_START
0067   +  DC 01-32 DHU_X_VC4_ON
0068     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0069     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0070     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Ä°²óÈò¡Ë¸å¤Î°ÆÀ¸°Æ³«¡ä
0073   +. DC 06-B3 DR_REP_START
0074   +  DC 01-32 DHU_X_VC4_ON
0075     C.                      ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸¡¾Ú)
0076     C.                      ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸¡¾Ú)
0077     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DR°ÆÀ¸Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDR°ÆÀ¸Ää»ß¡ä
0084   +. DC 06-B4 DR_REP_STOP
0085   +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                      ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                      ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                      ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091   +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093   +  DC 03-B5 TCIA_XPA_OFF
0094     C.                      ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                      ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098     C.
0099     C. ************ XRT START ************
0100     C.
0101  +. DC 07-F0 MDP_XRT_CTRL_MANU
0102     BC       (c1)
0103  +. DC 07-F0 MDP_XRT_CTRL_MANU
0104     BC       (c1)
0105  +  DC 07-F0 MDP_XRT_MODE_STBY
0106     BC       (c3)
0107   . C. ------------ Success Verify ?              OK / NG____
0108     C.
0109     C. XRT Obs. Table Upload
0110   . S. RAM   ram-291:MDP_OBS_X
0111     ()
0112     C.
0113  +. DC 07-F0 MDP_DUMP_XRTTBL
0114     BC       (84 07 00 00 00 3a d4)
0115   . C. ------------ Comparison Check ?                        OK / ERR ____
0116     C.
0117     C.
0118  +. DC 07-F0 MDP_XRT_ROI_SET
0119     BC       (cd 01 b1 b1 04 04)
0120  +  DC 07-F0 MDP_XRT_ROI_SET
0121     BC       (cd 02 b1 b1 08 08)
0122  +  DC 07-F0 MDP_XRT_ROI_SET
0123     BC       (cd 03 b1 b1 08 08)
0124  +  DC 07-F0 MDP_XRT_ROI_SET
0125     BC       (cd 04 b1 b1 06 06)
0126  +  DC 07-F0 MDP_XRT_ROI_SET
0127     BC       (cd 05 85 83 06 06)
0128  +  DC 07-F0 MDP_XRT_ROI_SET
0129     BC       (cd 06 80 80 08 08)
0130  +  DC 07-F0 MDP_XRT_ROI_SET
0131     BC       (cd 07 80 80 20 20)
0132  +  DC 07-F0 MDP_XRT_ROI_SET
0133     BC       (cd 08 c0 c0 10 10)
0134  +  DC 07-F0 MDP_XRT_ROI_SET
0135     BC       (cd 09 40 c0 10 10)
0136  +  DC 07-F0 MDP_XRT_ROI_SET
0137     BC       (cd 0a 40 40 10 10)
0138  +  DC 07-F0 MDP_XRT_ROI_SET
0139     BC       (cd 0b c0 40 10 10)
0140  +  DC 07-F0 MDP_XRT_ROI_SET
0141     BC       (cd 0c 80 80 20 08)
0142  +  DC 07-F0 MDP_XRT_ROI_SET
0143     BC       (cd 0d 80 80 08 20)
0144  +  DC 07-F0 MDP_XRT_ROI_SET
0145     BC       (cd 0e 85 83 06 06)
0146  +  DC 07-F0 MDP_XRT_ROI_SET
0147     BC       (cd 0f 80 80 06 06)
0148  +  DC 07-F0 MDP_XRT_ROI_SET
0149     BC       (cd 10 80 80 08 08)
0150  +  DC 07-F0 MDP_XRT_FLD_ENA
0151     BC       (d8)
0152  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0153     BC       (c8)
0154  +  DC 07-F0 MDP_XRT_ARS_DIS
0155     BC       (d5)
0156  +  DC 07-F0 MDP_XRT_AEC_RESET
0157     BC       (d0)
0158  +  DC 07-F0 MDP_XRT_FLD_RESET
0159     BC       (da)
0160  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0161     BC       (c4 0a)
0162  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0163     BC       (c5 0e)
0164   . C. ---------- Success Verify ?            OK / NG ____
0165     C.
0166     C.
0167   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0168     C.
0169  +. DC 07-F0 MDP_XRT_MODE_OBSV
0170     BC       (c2)
0171  +. TI 2026-08-04 10:49:02.0
0172     DC 07-F0 MDP_XRT_MODE_OBSV
0173     BC       (c2)
0174   . C. ---------- Success Verify ?            OK / NG ____
0175     C.
0176     C. ************ XRT END ***********
0177     C.
0178   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0179     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0180   . S. DC-BC dcbc-402:DCBC
0181     (MDP_known_event)
0182     C.
0183     C.
0184   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0185   . S. DC-BC dcbc-153:DCBC
0186     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0187     C.
0188     C.
0189   . C. ¡ãLOS¥Á¥Ş¥Ã¥¯¼Â»Ü¡ä
0190     C.
0191   . C. ************ LOS ************
0192     C.
```

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*** OP Sequence for XRT ***

2026/08/04  11:00:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/04  11:39:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                AOCU_NM              5 02-76 00 00 00 ac cd
2026/08/04  11:55:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                AOCU_NM              5 02-76 00 00 00 d6 67
2026/08/04  12:11:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/08/04  12:27:00.0  AOCS_ORe-point_Start_5_OG [0x09b]
                                AOCU_NM              5 02-76 00 00 00 29 99
2026/08/04  13:17:00.0  AOCS_ORe-point_Start_6_OG [0x09c]
                                AOCU_NM              5 02-76 00 00 00 53 33
2026/08/04  13:33:00.0  AOCS_ORe-point_Start_7_OG [0x09d]
                                AOCU_NM              5 02-76 00 d6 36 b7 8e
2026/08/04  13:43:00.0  AOCS_ORe-point_Start_8_OG [0x09e]
                                AOCU_NM              5 02-76 00 b4 b5 db 75
2026/08/04  13:59:00.0  AOCS_ORe-point_Start_9_OG [0x09f]
                                AOCU_NM              5 02-76 00 ac 5b 00 00
2026/08/04  14:55:00.0  AOCS_ORe-point_Start_10_OG [0x0a0]
                                AOCU_NM              5 02-76 00 b4 b5 24 8b
2026/08/04  15:11:00.0  AOCS_ORe-point_Start_11_OG [0x0a1]
                                AOCU_NM              5 02-76 00 d6 36 48 72
2026/08/04  15:21:00.0  AOCS_ORe-point_Start_12_OG [0x0a2]
                                AOCU_NM              5 02-76 00 29 ca b7 8e
2026/08/04  15:31:00.0  AOCS_ORe-point_Start_13_OG [0x0a3]
                                AOCU_NM              5 02-76 00 4b 4b db 75
2026/08/04  15:47:00.0  AOCS_ORe-point_Start_14_OG [0x0a4]
                                AOCU_NM              5 02-76 00 53 a5 00 00
2026/08/04  16:50:30.0  AOCS_ORe-point_Start_15_OG [0x0a5]
                                AOCU_NM              5 02-76 00 4b 4b 24 8b
2026/08/04  17:06:30.0  AOCS_ORe-point_Start_16_OG [0x0a6]
                                AOCU_NM              5 02-76 00 29 db 48 72
2026/08/04  17:15:30.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/04  18:21:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/08/04  18:31:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/05  06:00:00.5  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  06:00:02.5  XRT_TCIB_XRT_S_HTR_A_DIS_433_OG [0x1b1]
                                TCIB_XRT_S_HTR_A_DIS 0 04-C0
2026/08/05  06:03:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/08/05  06:13:00.0  AOCS_ORe-point_Start_17_OG [0x0a7]
                                AOCU_NM              5 02-76 00 ad 59 00 00
2026/08/05  08:40:00.0  AOCS_ORe-point_Start_18_OG [0x0a8]
                                AOCU_NM              5 02-76 00 00 00 56 35
2026/08/05  10:45:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/05  12:11:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:11:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:11:58.0  XRT_FOCUS_RECALIBRATE_407_OG [0x197]
                                XRT_FOCUS_RECAL      2 07-F8 78 00
2026/08/05  12:12:00.0  AOCS_ORe-point_Start_19_OG [0x0a9]
                                AOCU_NM              5 02-76 00 2e f9 2e f9
2026/08/05  12:15:58.0  XRT_FOCUS_POSITION_431_OG [0x1af]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/05  12:18:52.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  12:18:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/05  12:18:56.0  XRT_FLD_DIS_443_OG [0x1bb]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/05  12:18:58.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2026/08/05  12:19:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  12:21:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:21:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:21:58.0  XRT_FOCUS_POSITION_431_OG [0x1af]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/05  12:22:00.0  AOCS_ORe-point_Start_20_OG [0x0aa]
                                AOCU_NM              5 02-76 00 2e f9 d1 07
2026/08/05  12:24:52.0  XRT_ARS_DIS_432_OG [0x1b0]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  12:24:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/05  12:24:56.0  XRT_FLD_DIS_443_OG [0x1bb]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/05  12:24:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 13
2026/08/05  12:25:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  12:31:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:31:56.0  XRT_CTRL_MANU_402_OG [0x192]
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                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:31:58.0  XRT_FOCUS_POSITION_431_OG [0x1af]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/05  12:32:00.0  AOCS_ORe-point_Start_21_OG [0x0ab]
                                    AOCU_NM              5 02-76 00 d1 07 d1 07
2026/08/05  12:34:52.0  XRT_ARS_DIS_432_OG [0x1b0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  12:34:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/05  12:34:56.0  XRT_FLD_DIS_443_OG [0x1bb]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/05  12:34:58.0  XRT_QT_PROG_SET_410_OG [0x19a]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 06
2026/08/05  12:35:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  12:41:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:41:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:41:58.0  XRT_FOCUS_POSITION_431_OG [0x1af]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/05  12:42:00.0  AOCS_ORe-point_Start_22_OG [0x0ac]
                                    AOCU_NM              5 02-76 00 d1 07 2e f9
2026/08/05  12:44:52.0  XRT_ARS_DIS_432_OG [0x1b0]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  12:44:54.0  XRT_FLRCTRL_DIS_449_OG [0x1c1]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/05  12:44:56.0  XRT_FLD_DIS_443_OG [0x1bb]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/05  12:44:58.0  XRT_QT_PROG_SET_437_OG [0x1b5]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 05
2026/08/05  12:45:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  12:51:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:51:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  12:51:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                    XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/05  12:52:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 00 00 00 00 00
2026/08/05  12:52:18.0  XRT_FLD_DIS_409_OG [0x199]
                                    MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/05  12:52:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                    MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/05  12:52:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  12:54:58.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2026/08/05  12:55:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  13:01:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  13:01:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  13:01:58.0  XRT_ROI_A_440_OG [0x1b8]
                                    MDP_XRT_ROI_SET      6 07-F0 cd 05 85 83 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 06 80 80 08 08
                                    MDP_XRT_ROI_SET      6 07-F0 cd 07 80 80 20 20
                                    MDP_XRT_ROI_SET      6 07-F0 cd 08 85 83 08 08
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0c 80 80 20 08
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0d 80 80 08 20
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0e 85 83 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
2026/08/05  13:01:58.5  XRT_ROI_B_442_OG [0x1ba]
                                    MDP_XRT_ROI_SET      6 07-F0 cd 0f 80 80 06 06
                                    MDP_XRT_ROI_SET      6 07-F0 cd 10 80 80 08 08
2026/08/05  13:02:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/05  13:02:03.5  XRT_FOCUS_POSITION_414_OG [0x19e]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/08/05  13:02:23.5  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2026/08/05  13:02:25.5  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/08/05  13:02:27.5  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2026/08/05  13:02:29.5  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  13:02:31.5  XRT_FLD_RESET_401_OG [0x191]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  13:05:01.5  XRT_QT_PROG_SET_445_OG [0x1bd]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2026/08/05  13:05:04.5  XRT_FL_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/08/05  13:05:06.5  XRT_CTRL_AUTO_408_OG [0x198]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  13:22:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  13:22:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  13:22:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
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2026/08/05  13:22:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  13:25:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  13:48:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  13:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  15:00:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  15:00:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  15:00:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  15:00:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  15:03:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  15:26:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  15:27:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  16:39:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  16:39:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  16:39:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  16:39:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  16:42:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  17:15:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  17:16:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  17:59:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  17:59:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/05  18:00:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/08/05  18:00:18.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/05  18:00:20.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/05  18:00:22.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  18:02:58.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2026/08/05  18:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  18:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  18:09:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  18:09:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/08/05  18:10:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/05  18:10:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/08/05  18:10:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/08/05  18:10:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/08/05  18:10:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  18:10:26.0  XRT_FLD_RESET_401_OG [0x191]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  18:12:56.0  XRT_QT_PROG_SET_445_OG [0x1bd]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2026/08/05  18:12:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/08/05  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  18:17:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  18:17:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  18:17:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  18:17:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  18:20:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  18:52:00.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  18:53:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  19:55:00.5  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  19:55:02.5  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  19:55:04.5  XRT_FLD_RESET_415_OG [0x19f]
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                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  19:55:06.5  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  19:58:14.5  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  19:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  19:59:56.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/08/05  20:00:00.0  AOCS_ORe-point_Start_23_OG [0x0ad]
                                        AOCU_NM              5 02-76 00 57 1a 01 ca
2026/08/05  20:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/08/05  20:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/08/05  20:00:20.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/08/05  20:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/05  20:00:24.0  XRT_FLD_RESET_422_OG [0x1a6]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  20:02:56.0  XRT_QT_PROG_SET_427_OG [0x1ab]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0b
2026/08/05  20:02:58.0  XRT_FL_PROG_SET_429_OG [0x1ad]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 10
2026/08/05  20:28:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  20:29:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  21:33:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  21:33:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  21:33:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  21:33:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  21:36:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  22:05:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  22:06:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/05  23:11:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  23:11:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/05  23:11:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/05  23:11:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/05  23:14:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/05  23:40:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/05  23:41:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  00:49:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  00:49:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  00:49:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  00:49:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  00:52:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  01:10:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  01:10:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  01:10:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/08/06  01:11:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/06  01:11:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2026/08/06  01:11:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/08/06  01:11:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2026/08/06  01:11:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/06  01:11:26.0  XRT_FLD_RESET_401_OG [0x191]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  01:13:56.0  XRT_QT_PROG_SET_434_OG [0x1b2]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 07
2026/08/06  01:13:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/08/06  01:15:01.0  XRT_Custom_430_OG [0x1ae]
2026/08/06  01:16:01.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  02:21:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  02:21:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
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2026/08/06  02:21:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  02:21:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  02:24:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  02:53:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/06  02:54:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  03:54:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  03:54:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  03:54:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  03:54:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  03:57:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  04:31:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/06  04:32:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  05:23:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  05:23:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  05:23:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  05:23:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  05:26:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  06:09:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/06  06:10:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  06:20:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  06:20:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  06:20:58.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/06  06:21:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                AOCU_NM              5 02-76 00 00 00 00 00
2026/08/06  06:21:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                MDP_XRT_FLD_ENA      1 07-F0 d8
2026/08/06  06:21:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/08/06  06:21:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                MDP_XRT_AEC_RESET    1 07-F0 d0
2026/08/06  06:21:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/06  06:21:26.0  XRT_FLD_RESET_401_OG [0x191]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  06:23:56.0  XRT_QT_PROG_SET_405_OG [0x195]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 08
2026/08/06  06:23:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/08/06  06:24:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  07:03:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  07:03:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  07:03:34.0  XRT_FLD_RESET_415_OG [0x19f]
                                MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  07:03:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  07:06:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  07:47:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/06  07:48:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  08:33:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  08:33:26.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  08:33:28.0  XRT_FOCUS_POSITION_406_OG [0x196]
                                XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2026/08/06  08:33:48.0  XRT_FLD_DIS_409_OG [0x199]
                                MDP_XRT_FLD_DIS      1 07-F0 d9
2026/08/06  08:33:50.0  XRT_FLRCTRL_DIS_413_OG [0x19d]
                                MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2026/08/06  08:33:52.0  XRT_ARS_DIS_420_OG [0x1a4]
                                MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/06  08:36:28.0  XRT_QT_PROG_SET_428_OG [0x1ac]
                                MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2026/08/06  08:36:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  08:43:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  08:43:32.0  XRT_CTRL_MANU_402_OG [0x192]
                                MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  08:43:34.0  XRT_FLD_RESET_415_OG [0x19f]
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                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  08:43:36.0  XRT_PREFLR_STRT_404_OG [0x194]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  08:44:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  08:44:56.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  08:44:58.0  XRT_FOCUS_POSITION_414_OG [0x19e]
                                    XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2026/08/06  08:45:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                    AOCU_NM              5 02-76 04 05 58 01 ca
2026/08/06  08:45:18.0  XRT_FLD_ENA_411_OG [0x19b]
                                    MDP_XRT_FLD_ENA      1 07-F0 d8
2026/08/06  08:45:20.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                    MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2026/08/06  08:45:22.0  XRT_AEC_RESET_448_OG [0x1c0]
                                    MDP_XRT_AEC_RESET    1 07-F0 d0
2026/08/06  08:45:24.0  XRT_ARS_DIS_423_OG [0x1a7]
                                    MDP_XRT_ARS_DIS      1 07-F0 d5
2026/08/06  08:45:26.0  XRT_FLD_RESET_401_OG [0x191]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  08:46:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  08:47:56.0  XRT_QT_PROG_SET_445_OG [0x1bd]
                                    MDP_XRT_QT_PROG_SET  2 07-F0 c4 01
2026/08/06  08:47:58.0  XRT_FL_PROG_SET_439_OG [0x1b7]
                                    MDP_XRT_FL_PROG_SET  2 07-F0 c5 0e
2026/08/06  09:25:30.0  XRT_Custom_430_OG [0x1ae]
2026/08/06  09:26:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                    MDP_XRT_CTRL_AUTO    1 07-F0 c0
2026/08/06  10:23:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  10:23:02.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  10:23:04.0  XRT_FLD_RESET_415_OG [0x19f]
                                    MDP_XRT_FLD_RESET    1 07-F0 da
2026/08/06  10:23:06.0  XRT_PREFLR_STRT_404_OG [0x194]
                                    MDP_XRT_PREFLR_STRT  1 07-F0 e8
2026/08/06  10:26:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                    MDP_XRT_PREFLR_STOP  1 07-F0 e9
2026/08/06  11:09:10.0  XRT_CTRL_MANU_402_OG [0x192]
                                    MDP_XRT_CTRL_MANU    1 07-F0 c1
2026/08/06  11:30:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                    AOCU_NM              5 02-76 00 00 00 00 00
```