

Period: 2026/08/11 10:55:00 - 2026/08/15 12:15:00

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Term	Pointing (x, y)	Comment
08/11 16:23:00 - 08/11 16:29:54	Fixed (0.0, 0.0)	synoptic, shifter -1.5 hr
08/12 06:05:06 - 08/12 06:12:24	Fixed (0.0, 0.0)	synoptic, shifted -2hr, HOP 349, 448
08/12 16:03:00 - 08/12 16:16:14	Fixed (0.0, 0.0)	synoptic, shifted -2 hr
08/13 06:38:00 - 08/13 06:43:54	Fixed (0.0, 0.0)	synoptic, shifted -2 hr, HOP 349, 448

PROG= 17 1-time(s)

└ **Subr= 1 1-time(s) 2.0sec**

└ **Seqn= 5 1-time(s) 2.0sec**

- └ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
- └ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
- └ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
- └ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
- └ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec

└ **Seqn= 66 1-time(s) 2.0sec**

- └ Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- └ Open/Al-mesh Open/Al-mesh close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- └ Open/Al-mesh Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

└ **Seqn= 22 1-time(s) 2.0sec**

	Al-poly/Open	Al-poly/Open	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 19 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	44ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23 1-time(s) 4.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 17 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 33 1-time(s) 2.0sec													
	med-Be/Open	Open/thick-Al	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 89 1-time(s) 2.0sec													
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1BD5: CME watch - 4x4 - AEC 2/3 - 2-filter (Be-thin, Al-poly) - G-band (1x1,512x512,1ms) - Leak (1x1,512x512,1ms) - 180s cad (G-band/Leak last)

Term			Pointing (x, y)					Comment									
08/11 17:10:00 - 08/11 23:06:00			Track (-729.0, -55.9) @ 08/11 16:30:00					HOP 538									
08/12 19:33:00 - 08/12 23:29:54			Track (-542.3, -74.9) @ 08/12 19:30:00					HOP538									
PROG= 18 Inf.-time(s)																	
Subr= 1 20-time(s) 180.0sec																	
Seqn= 8 1-time(s) 2.0sec																	
thin-Be/Open			med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec			
thin-Be/Open			med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2 0 2.0sec			
Seqn= 6 1-time(s) 2.0sec																	
Al-poly/Open			Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec			
Al-poly/Open			Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2 0 2.0sec			
Subr= 2 1-time(s) 2.0sec																	
Seqn= 30 1-time(s) 2.0sec																	
Open/G-band			Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=90	0 0 2.0sec			
Open/G-band			Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1024, 1024)		Q=95	0 0 2.0sec			
Default Filter			Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval			

XOB #1C9B: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term			Pointing (x, y)				Comment						
08/12 04:25:30 - 08/12 05:16:30			Fixed (		0.0, 0.0)		synoptic, shifted -2hr, HOP 349, 448						
08/13 04:03:00 - 08/13 05:49:00			Fixed (		0.0, 0.0)		synoptic, shifted -2 hr, HOP 349, 448						
PROG= 08 Inf.-time(s)													
Subr= 1		1-time(s)		300.0sec									
Seqn=100		1-time(s)		2.0sec									
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
Seqn= 84		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
Seqn= 57		1-time(s)		2.0sec									
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
└ thin-Be/Open		thin-Be/Open		close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec
Seqn= 81		1-time(s)		2.0sec									
└ Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=90	0 0 2.0sec
└ Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)		Q=95	0 0 2.0sec
Subr= 2		9-time(s)		360.0sec									
Seqn= 8		1-time(s)		2.0sec									
└ thin-Be/Open		med-Be/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ thin-Be/Open		med-Be/Open		close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2 0 2.0sec
Seqn= 6		1-time(s)		2.0sec									
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ Al-poly/Open		Al-poly/Open		close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)		DPCM	2 0 2.0sec
Seqn= 29		1-time(s)		2.0sec									
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	3 0 2.0sec
└ Open/Al-mesh		Open/Al-mesh		close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

└─┐	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─┐	Seqn= 73 1-time(s) 10.0sec												
└─┐	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Subr= 3 1-time(s) 2.0sec												
└─┐	Seqn= 10 1-time(s) 2.0sec												
└─┐	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─┐	Seqn= 11 1-time(s) 2.0sec												
└─┐	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─┐	Seqn= 87 1-time(s) 2.0sec												
└─┐	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─┐	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─┐	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
└─┐	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

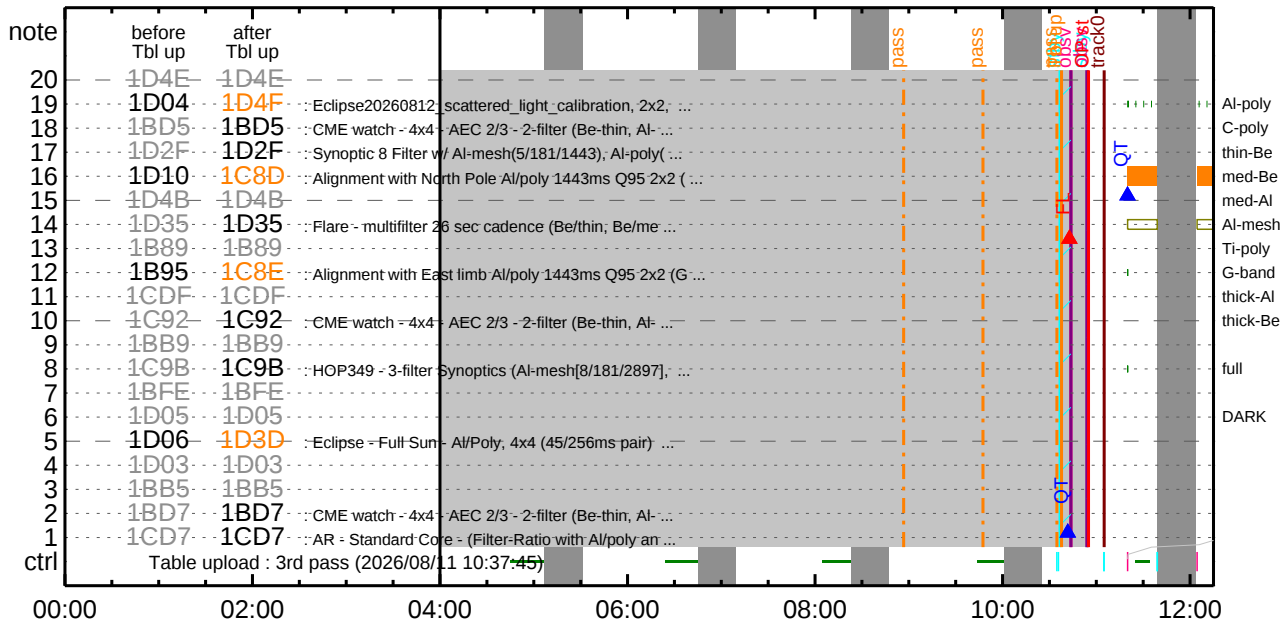
* * * * *

Flare Detection

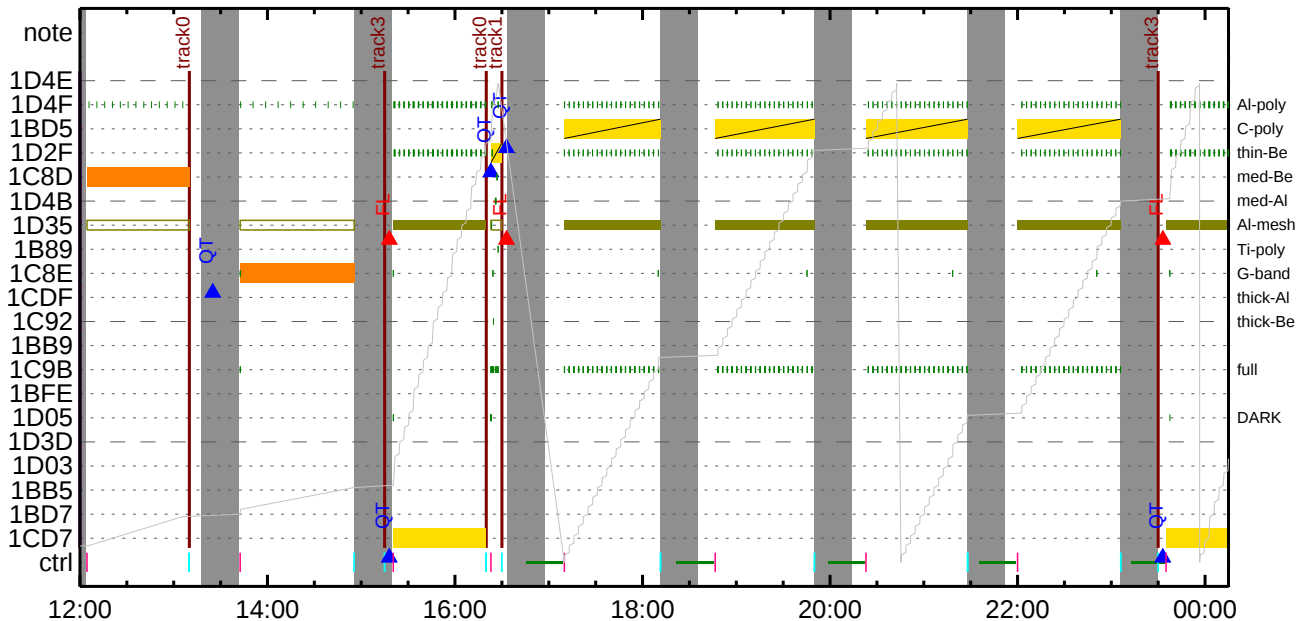
* * * * *

FLD Patrol													
Term				Pointing (x, y)				Comment					
08/11 10:38:45 - 08/11 11:05:18				cannot be identified									
08/11 15:15:18 - 08/11 16:20:18				Track (296.2, -421.4) @ 08/11 15:15:00				Track AR14504					
08/11 16:30:18 - 08/12 06:02:24				Track (-729.0, -55.9) @ 08/11 16:30:00				HOP 538					
08/12 06:12:48 - 08/12 16:00:18				Track (414.2, -415.7) @ 08/12 06:12:30				Track AR14503					
08/12 16:30:18 - 08/12 17:31:18				Fixed (-970.0, 0.0)				HOP516					
08/12 18:05:24 - 08/12 19:10:24				Fixed (-970.0, 0.0)				HOP516					
08/12 19:30:18 - 08/13 06:35:18				Track (-542.3, -74.9) @ 08/12 19:30:00				HOP538					
08/13 06:44:18 - 08/15 12:15:00				Track (587.0, -402.3) @ 08/13 06:44:00				Track AR14503					
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4ms	Obs	8x8	Q=50			30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

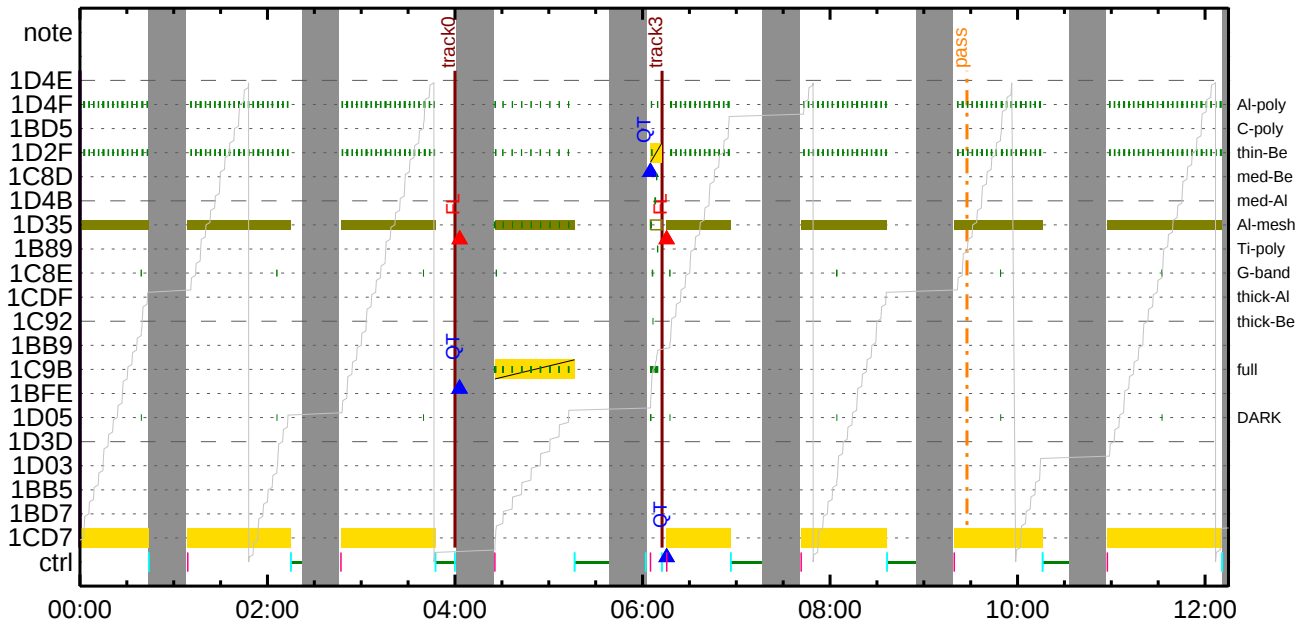
CMDI #0486 2026/08/11



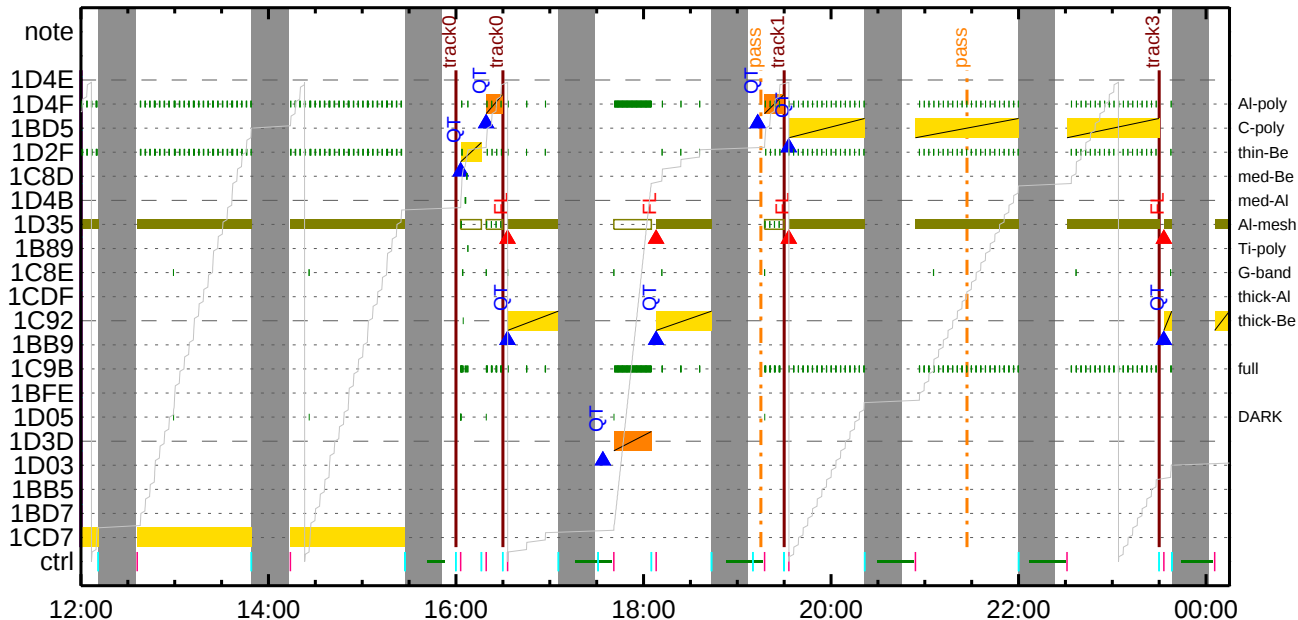
CMDI #0486 2026/08/11



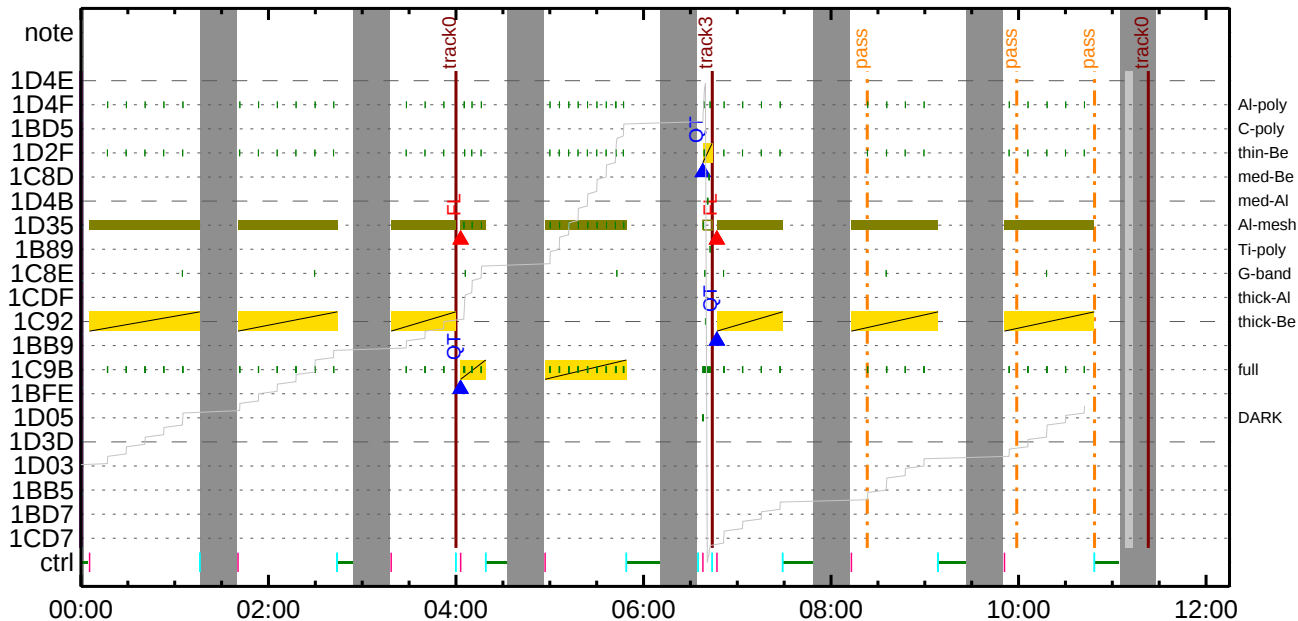
CMDI #0486 2026/08/12



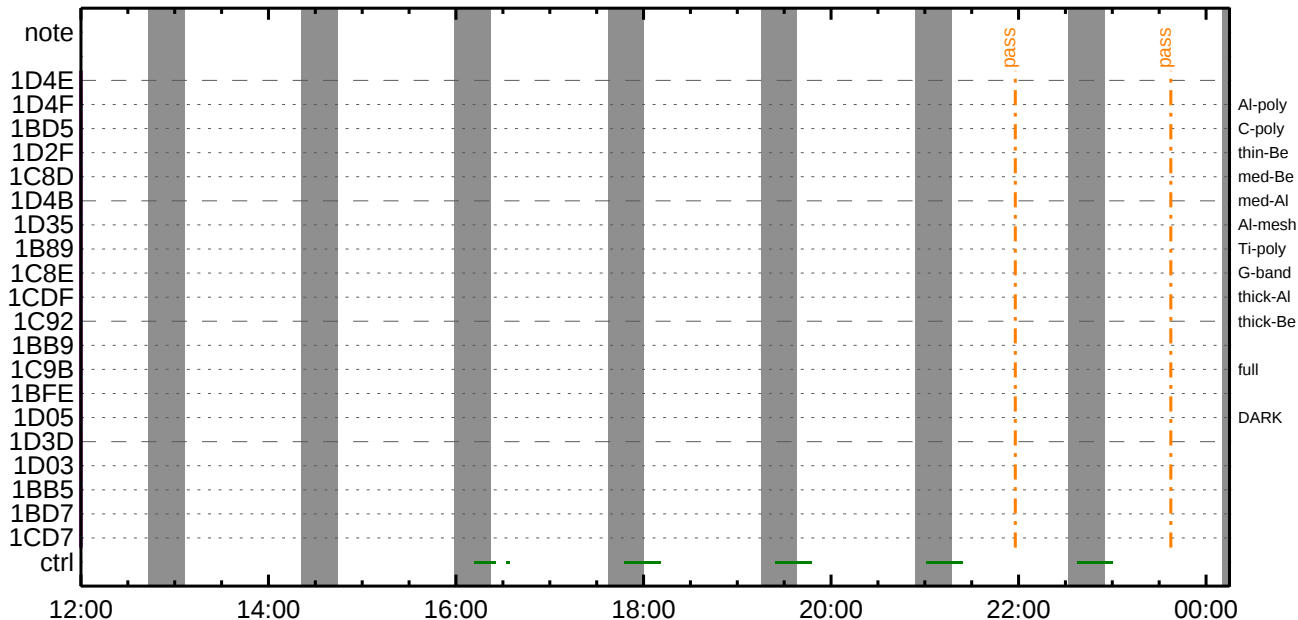
CMDI #0486 2026/08/12



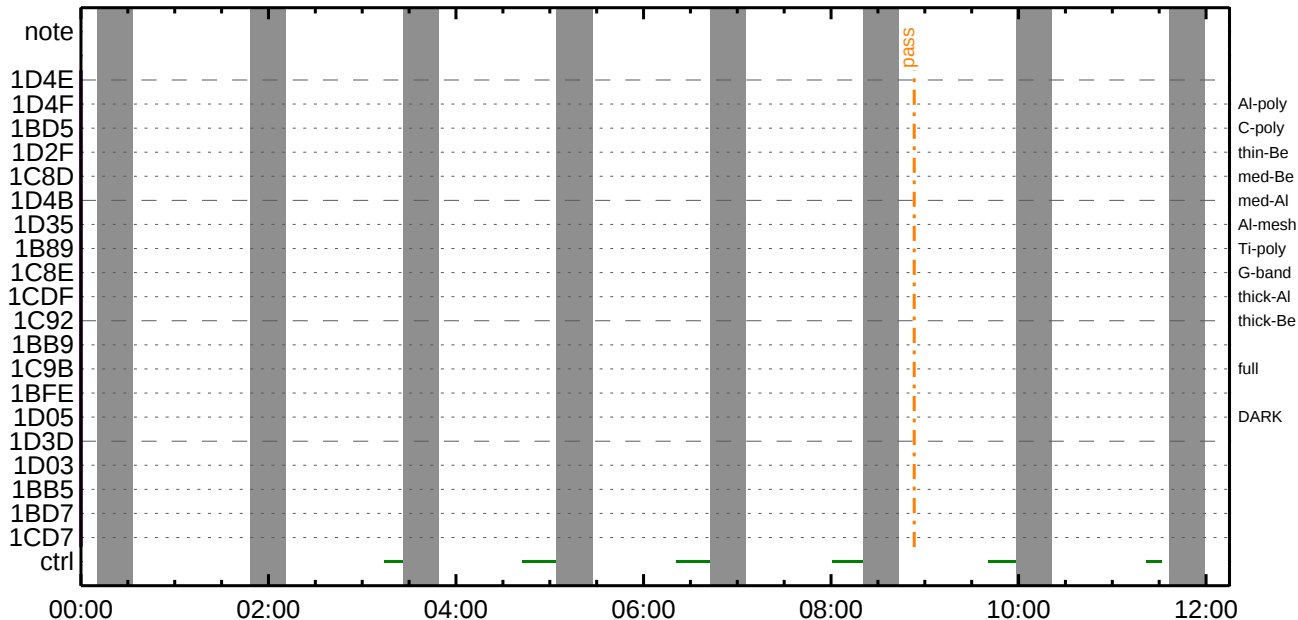
CMDI #0486 2026/08/13



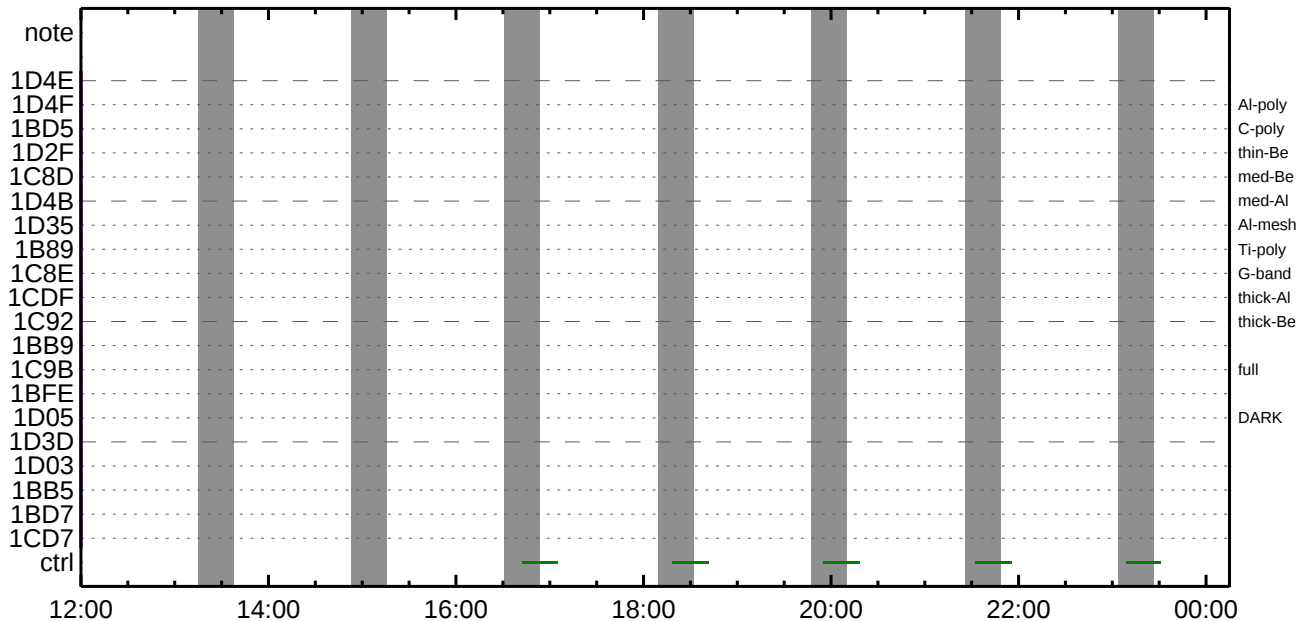
CMDI #0486 2026/08/13



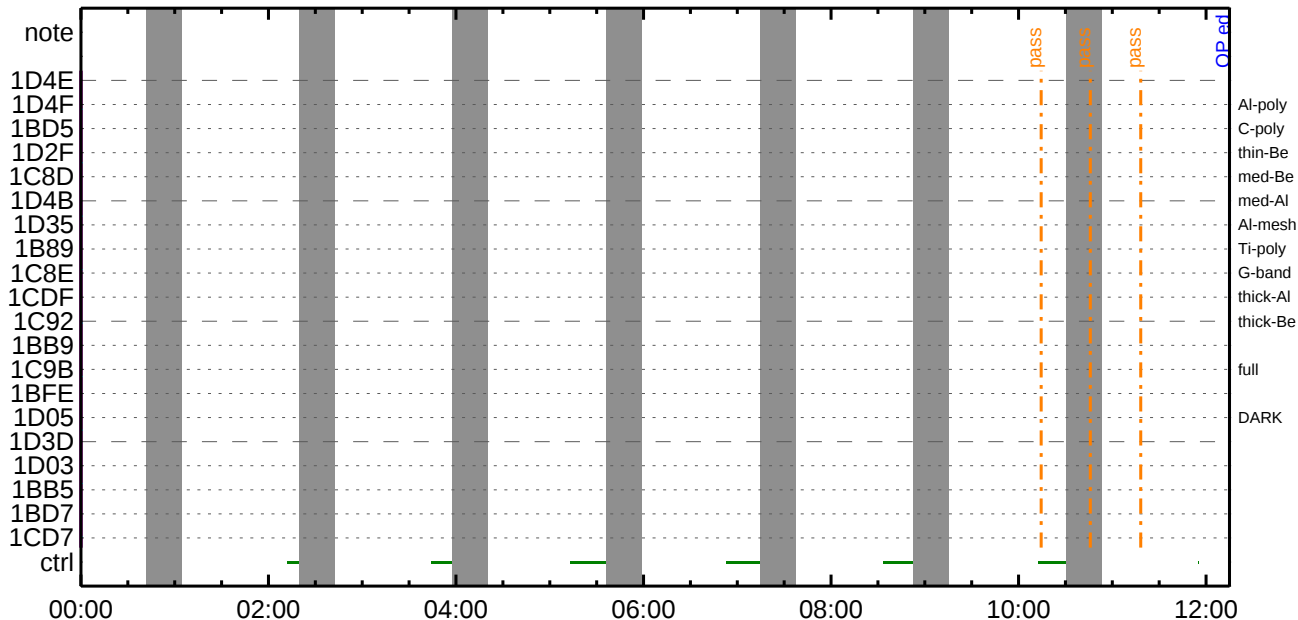
CMDI #0486 2026/08/14

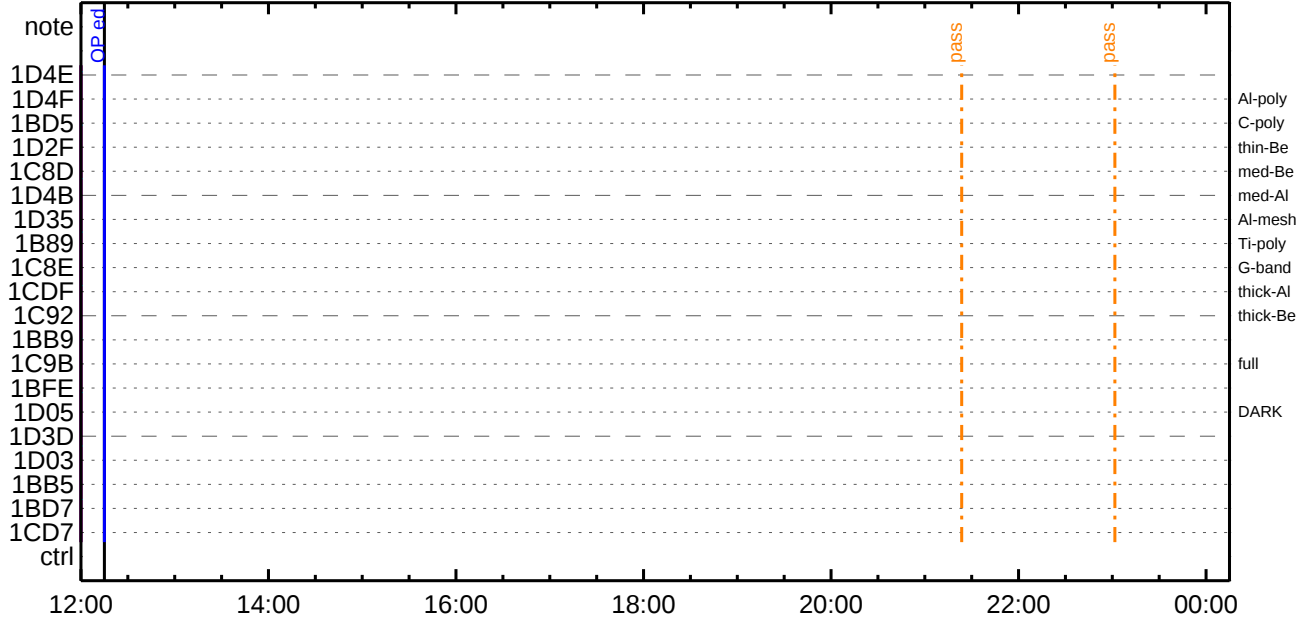


CMDI #0486 2026/08/14



CMDI #0486 2026/08/15






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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGŵı;¼ŴÉ;¼ŴŴóŴ×
0100 C. *****
0101 C.
0102 C. ;ãOP/OGŵı;¼ŴÉ;ã
0103 S. OP op-154:OP
0104 ()
0105 S. OG og-154:OG
0106 ()
0107 C.
0108 C. ;ãNMOG&OPîî°èŴŴŴóŴ×;ã
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ŴŴŴóŴ×½ªî»¤ð³îÇ$
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ$
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ŴŴŴóŴ×½ªî»¤ð³îÇ$
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ$
0146 C.
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ŴŴŴóŴ×½ªî»¤ð³îÇ$
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ$
0165 C.
0166 C. ***** °Ē²¼¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ$ *****
0167 C. DHUŴã;¼ŴÉ;Ē¼Ŵ½,Ŵı;¼ŴÉ;Ē¤ðİã¤¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;$ OPOG UPLOAD¤¬Á÷Ē@NG¤İ¼ı¹Ç;Ē°Ē²¼¤î¼Ē¹Ç•Ē²İOK¤ð³îÇ$ *****
0180 C. ¢¤¤Ē;ĒÇSET¤ĒDUMP¤İĒ±°ıŴŴŴı¹Ç¹Ē¤ı¤³¤Ē;Ē
0181 C.
0182 C. TIŴ³ŴŴŴóŴŴĒ¤ðİãĒİĒ (UT)
0183 +. TI 2026-08-11 10:50:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2026-08-11 10:50:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2026-08-11 10:50:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2026-08-11 10:54:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê¼¤İÄê¼İİñ¤İ¥Á¥$¥Ä¥~¹aiÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİ°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]      EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]      EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]      EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¼È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHU¥â;¼¥É;Ê¼ý½,¥İ;¼¥È;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2026-08-11 10:54:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2026-08-11 10:54:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2026-08-11 10:54:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÄİ¤İ»ö¼¥¤ÈÄ¤¹¤èDCBC•×²è *****
0265 C. (¼å°İ¥Ö¥Ä¥È¥Ð¥È¥â¥Ç¥È¤È¼¤¤¼Ä»Ü¤¹¤è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ¥Ð¥¹•İ Daily±;İİñ¤È´Ø¤¹¤èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOS¥Ä¥$¥Ä¥~¼Ä»Ü;ä
0276 C.
0277 C. ***** LOS *****
0278 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 +. DC 07-F0 MDP_XRT_CTRL_MANU
0104 BC (c1)
0105 + DC 07-F0 MDP_XRT_MODE_STBY
0106 BC (c3)
0107 . C. ----- Success Verify ? OK / NG____
0108 C.
0109 C. XRT Obs. Table Upload
0110 . S. RAM ram-291:MDP_OBS_X
0111 ()
0112 C.
0113 +. DC 07-F0 MDP_DUMP_XRTTBL
0114 BC (84 07 00 00 00 3a d4)
0115 . C. ----- Comparison Check ? OK / ERR ____
0116 C.
0117 C.
0118 +. DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 01 b1 b1 04 04)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 02 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 03 b1 b1 08 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 04 b1 b1 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 05 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 06 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 07 80 80 08 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 08 80 60 20 18)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 09 a0 80 18 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0a 85 83 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0b 80 80 20 08)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0c 80 80 08 20)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 0d 85 83 08 08)
0144 + DC 07-F0 MDP_XRT_ROI_SET
0145 BC (cd 0f 80 80 06 06)
0146 + DC 07-F0 MDP_XRT_ROI_SET
0147 BC (cd 10 80 80 08 08)
0148 + DC 07-F0 MDP_XRT_FLD_ENA
0149 BC (d8)
0150 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0151 BC (c8)
0152 + DC 07-F0 MDP_XRT_ARS_DIS
0153 BC (d5)
0154 + DC 07-F0 MDP_XRT_AEC_RESET
0155 BC (d0)
0156 + DC 07-F0 MDP_XRT_FLD_RESET
0157 BC (da)
0158 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0159 BC (c4 02)
0160 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0161 BC (c5 0e)
0162 . C. ----- Success Verify ? OK / NG ____
0163 C.
0164 C.
0165 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0166 C.
0167 +. DC 07-F0 MDP_XRT_MODE_OBSV
0168 BC (c2)
0169 +. TI 2026-08-11 10:54:02.0
0170 DC 07-F0 MDP_XRT_MODE_OBSV
0171 BC (c2)
0172 . C. ----- Success Verify ? OK / NG ____
0173 C.
0174 C. ***** XRT END *****
0175 C.
0176 . C. ***** MDP 'ûÃîï»ö¼ÝñÊÄðñ¹ëDCBC•x²è *****
0177 C. (%â°î¥Ö¥Ã¥Ê¥Ð¥Ë¥Ä¥Ç¥ÈñÊ¼ñ¼Ã»Ûñ¹ë)
0178 . S. DC-BC dcbc-402:DCBC
0179 (MDP_known_event)
0180 C.
0181 C.
0182 . C. ***** ¥Ð¥¹•ï Daily±¿íññÊ'Øñ¹ëDCBC•x²è *****
0183 . S. DC-BC dcbc-153:DCBC
0184 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0185 C.
0186 C.
0187 . C. ;ãLOS¥Á¥S¥Ã¥¹¼Ã»Û;ä
0188 C.
0189 . C. ***** LOS *****
0190 C.
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*** OP Sequence for XRT ***

2026/08/11	11:04:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	11:04:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	11:04:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2026/08/11	11:05:00.0	AOCs_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 ad 59 00 00
2026/08/11	11:05:18.0	XRT_FLD_DIS_436_OG [0x1b4]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2026/08/11	11:19:54.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/11	11:19:56.0	XRT_ARS_DIS_432_OG [0x1b0]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/11	11:19:58.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2026/08/11	11:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/11	11:39:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	11:39:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	11:39:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/11	11:39:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/11	11:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/11	12:03:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/11	12:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/11	13:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	13:09:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	13:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2026/08/11	13:10:00.0	AOCs_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 56 35
2026/08/11	13:10:18.0	XRT_FLD_DIS_436_OG [0x1b4]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2026/08/11	13:24:54.0	XRT_ARS_DIS_432_OG [0x1b0]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/11	13:24:56.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/11	13:24:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2026/08/11	13:41:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/11	13:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/11	14:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	14:55:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	14:55:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/11	14:55:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/11	14:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/11	15:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	15:14:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	15:14:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2026/08/11	15:15:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 04 8b 02 0c
2026/08/11	15:15:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/11	15:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/11	15:15:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/11	15:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/11	15:15:26.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/11	15:17:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2026/08/11	15:17:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/11	15:19:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/11	15:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/11	16:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	16:19:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/11	16:19:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2026/08/11	16:20:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2026/08/11	16:20:18.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2026/08/11	16:20:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2026/08/11	16:20:22.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2026/08/11	16:22:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2026/08/11	16:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/11	16:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	16:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	16:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2026/08/11	16:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	01	04	8b	02	0c
2026/08/11	16:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2026/08/11	16:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2026/08/11	16:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2026/08/11	16:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2026/08/11	16:30:26.0	XRT_FLD_RESET_401_OG [0x191]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/11	16:32:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2026/08/11	16:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2026/08/11	17:09:00.0	XRT_Custom_430_OG [0x1ae]							
2026/08/11	17:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/11	18:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	18:11:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	18:11:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/11	18:11:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/11	18:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/11	18:45:30.0	XRT_Custom_430_OG [0x1ae]							
2026/08/11	18:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/11	19:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	19:50:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	19:50:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/11	19:50:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/11	19:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/11	20:22:00.0	XRT_Custom_430_OG [0x1ae]							
2026/08/11	20:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/11	21:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	21:28:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	21:28:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/11	21:28:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/11	21:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/11	21:59:00.0	XRT_Custom_430_OG [0x1ae]							
2026/08/11	22:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/11	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	23:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	23:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/11	23:06:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/11	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/11	23:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	23:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/11	23:29:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2026/08/11	23:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							

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2026/08/11	23:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	5	02-76	03	04	8b	02	0c
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2026/08/11	23:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	d0				
2026/08/11	23:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	d0				
		MDP_XRT_AEC_RESET	1	07-F0	d5				
2026/08/11	23:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	da				
		MDP_XRT_ARS_DIS	2	07-F0	c4	01			
2026/08/11	23:30:26.0	XRT_FLD_RESET_401_OG [0x191]	2	07-F0	c5	0e			
		MDP_XRT_FLD_RESET	2	07-F0	c5	0e			
2026/08/11	23:32:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	1	07-F0	c0				
		MDP_XRT_QT_PROG_SET	1	07-F0	c1				
2026/08/11	23:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	1	07-F0	c1				
		MDP_XRT_FL_PROG_SET	1	07-F0	da				
2026/08/11	23:34:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	e8				
2026/08/11	23:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	e9				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	00:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	00:44:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	da				
		MDP_XRT_CTRL_MANU	1	07-F0	e8				
2026/08/12	00:44:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	e9				
		MDP_XRT_FLD_RESET	1	07-F0	c0				
2026/08/12	00:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	c1				
		MDP_XRT_PREFLR_STRT	1	07-F0	c1				
2026/08/12	00:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	da				
		MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2026/08/12	01:08:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	e9				
2026/08/12	01:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2026/08/12	02:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	02:15:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	da				
		MDP_XRT_CTRL_MANU	1	07-F0	e8				
2026/08/12	02:15:04.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	e9				
		MDP_XRT_FLD_RESET	1	07-F0	c0				
2026/08/12	02:15:06.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	c1				
		MDP_XRT_PREFLR_STRT	1	07-F0	c1				
2026/08/12	02:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	da				
		MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2026/08/12	02:46:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	e9				
2026/08/12	02:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0				
		MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2026/08/12	03:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	03:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	da				
		MDP_XRT_CTRL_MANU	1	07-F0	e8				
2026/08/12	03:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	e9				
		MDP_XRT_FLD_RESET	1	07-F0	c0				
2026/08/12	03:47:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	c1				
		MDP_XRT_PREFLR_STRT	1	07-F0	c1				
2026/08/12	03:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	da				
		MDP_XRT_PREFLR_STOP	1	07-F0	e8				
2026/08/12	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	e9				
		MDP_XRT_CTRL_MANU	1	07-F0	c0				
2026/08/12	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION	5	02-76	00	00	00	00	00
2026/08/12	04:00:00.0	AOCs_ORe-point_Start_4_OG [0x09a]	5	02-76	00	00	00	00	00
		AOCU_NM	1	07-F0	d8				
2026/08/12	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	c8				
		MDP_XRT_FLD_ENA	1	07-F0	d0				
2026/08/12	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	d5				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	da				
2026/08/12	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	1	07-F0	e8				
		MDP_XRT_AEC_RESET	1	07-F0	e9				
2026/08/12	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	c0				
		MDP_XRT_ARS_DIS	1	07-F0	c1				
2026/08/12	04:00:26.0	XRT_FLD_RESET_401_OG [0x191]	1	07-F0	da				
		MDP_XRT_FLD_RESET	2	07-F0	c4	08			
2026/08/12	04:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]	2	07-F0	c5	0e			
		MDP_XRT_QT_PROG_SET	2	07-F0	c5	0e			
2026/08/12	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	2	07-F0	c5	0e			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2026/08/12	04:24:30.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0				
2026/08/12	04:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c1				
		MDP_XRT_CTRL_AUTO	1	07-F0	c1				
2026/08/12	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	da				
		MDP_XRT_CTRL_MANU	1	07-F0	e8				
2026/08/12	05:16:32.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	e9				
		MDP_XRT_CTRL_MANU	1	07-F0	c0				
2026/08/12	05:16:34.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	c1				
		MDP_XRT_FLD_RESET	1	07-F0	c1				
2026/08/12	05:16:36.0	XRT_PREFLR_STRT_404_OG [0x194]	1	07-F0	da				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/12	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9				
		MDP_XRT_PREFLR_STOP	1	07-F0	c0				
2026/08/12	06:02:00.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	06:02:02.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1				
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2026/08/12	06:02:04.0	XRT_FOCUS_POSITION_406_OG [0x196]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2026/08/12	06:02:24.0	XRT_FLD_DIS_409_OG [0x199]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2026/08/12	06:02:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2026/08/12	06:02:28.0	XRT_ARS_DIS_420_OG [0x1a4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2026/08/12	06:05:04.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2026/08/12	06:05:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	06:12:26.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	06:12:28.0	XRT_FOCUS_POSITION_414_OG [0x19e]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2026/08/12	06:12:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03	04	8b	02	0c
2026/08/12	06:12:48.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2026/08/12	06:12:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2026/08/12	06:12:52.0	XRT_AEC_RESET_448_OG [0x1c0]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2026/08/12	06:12:54.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2026/08/12	06:12:56.0	XRT_FLD_RESET_401_OG [0x191]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/12	06:15:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2026/08/12	06:15:28.0	XRT_FL_PROG_SET_439_OG [0x1b7]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0e			
2026/08/12	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	06:56:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	06:56:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	06:56:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/12	06:56:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/12	06:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/12	07:40:30.0	XRT_Custom_430_OG [0x1ae]							
2026/08/12	07:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	08:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	08:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	08:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/12	08:36:36.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/12	08:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/12	09:18:30.0	XRT_Custom_430_OG [0x1ae]							
2026/08/12	09:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	10:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	10:16:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	10:16:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/12	10:16:06.0	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/12	10:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/12	10:56:30.0	XRT_Custom_430_OG [0x1ae]							
2026/08/12	10:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	12:11:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	12:11:02.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	12:11:04.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2026/08/12	12:11:06.5	XRT_PREFLR_STRT_404_OG [0x194]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2026/08/12	12:14:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2026/08/12	12:35:00.0	XRT_Custom_430_OG [0x1ae]							
2026/08/12	12:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2026/08/12	13:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	13:49:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2026/08/12	13:49:04.0	XRT_FLD_RESET_415_OG [0x19f]							

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2026/08/12	13:49:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	13:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	14:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/12	14:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	15:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	15:27:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	15:27:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	15:27:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	15:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/12	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	15:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	15:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	16:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2026/08/12	16:00:18.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2026/08/12	16:00:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/12	16:00:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	16:02:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2026/08/12	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	16:16:14.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	16:16:16.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	16:16:18.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	16:16:38.0	XRT_FLD_DIS_409_OG [0x199]	MDP_XRT_FLD_DIS	1	07-F0	d9
2026/08/12	16:16:40.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/12	16:16:42.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	16:19:18.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2026/08/12	16:19:20.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	16:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	16:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	16:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	16:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 56 35
2026/08/12	16:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/12	16:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/12	16:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/12	16:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	16:30:26.0	XRT_FLD_RESET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	16:32:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2026/08/12	16:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/12	16:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	17:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	17:05:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	17:05:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	17:05:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	17:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/12	17:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	17:30:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	17:30:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	17:31:18.0	XRT_FLD_DIS_409_OG [0x199]				

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2026/08/12	17:31:20.0	MDP_XRT_FLD_DIS	1	07-F0	d9
		XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/12	17:31:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	17:33:58.0	XRT_QT_PROG_SET_437_OG [0x1b5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2026/08/12	17:40:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/12	17:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	18:05:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	18:05:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	18:05:04.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	18:05:24.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/12	18:05:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/12	18:05:28.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/12	18:05:30.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	18:05:32.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	18:08:02.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2026/08/12	18:08:04.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/12	18:08:06.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	18:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	18:43:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	18:43:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	18:43:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	18:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/12	19:10:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	19:10:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	19:10:04.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	19:10:24.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2026/08/12	19:10:26.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/12	19:10:28.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	19:13:04.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2026/08/12	19:16:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/12	19:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	19:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	19:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	19:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	19:30:00.0	AOCs_ORe-point_Start_5_OG [0x09b]			
		AOCU_NM	5	02-76	01 04 8b 02 0c
2026/08/12	19:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/12	19:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/12	19:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/12	19:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	19:30:26.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	19:32:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2026/08/12	19:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/12	19:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	20:21:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	20:21:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	20:21:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	20:21:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	20:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2026/08/12	20:53:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/12	20:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	22:00:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	22:00:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	22:00:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	22:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/12	22:30:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/12	22:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	23:29:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	23:29:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	23:29:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/12	23:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 04 8b 02 0c
2026/08/12	23:30:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/12	23:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/12	23:30:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/12	23:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/12	23:30:26.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	23:32:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2026/08/12	23:32:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/12	23:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/12	23:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	23:38:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/12	23:38:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/12	23:38:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/12	23:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	00:04:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/13	00:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	01:16:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	01:16:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	01:16:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	01:16:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	01:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	01:39:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/13	01:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	02:44:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	02:44:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	02:44:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	02:44:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	02:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	03:17:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/13	03:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/13	04:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2026/08/13	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/13	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/13	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0

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2026/08/13	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/13	04:00:26.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	04:02:56.0	XRT_QT_PROG_SET_405_OG [0x195]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2026/08/13	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/13	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	04:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	04:19:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	04:19:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	04:19:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	04:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	04:56:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/13	04:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	05:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	05:49:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	05:49:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	05:49:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	05:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	06:34:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	06:34:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	06:34:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/13	06:35:18.0	XRT_FLD_DIS_409_OG [0x199]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2026/08/13	06:35:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2026/08/13	06:35:22.0	XRT_ARS_DIS_420_OG [0x1a4]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/13	06:37:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2026/08/13	06:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	06:43:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	06:43:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	06:43:58.0	XRT_FOCUS_POSITION_406_OG [0x196]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2026/08/13	06:44:00.0	AOCs_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	03 04 8b 02 0c
2026/08/13	06:44:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/13	06:44:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/13	06:44:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/13	06:44:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/13	06:44:26.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	06:46:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2026/08/13	06:46:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e
2026/08/13	06:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	07:29:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	07:29:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	07:29:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	07:29:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	07:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	08:12:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/13	08:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	09:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	09:08:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	09:08:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	09:08:36.0	XRT_PREFLR_STRT_404_OG [0x194]			

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2026/08/13	09:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	09:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	09:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/13	10:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	10:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/13	10:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/13	10:48:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/13	10:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/13	11:23:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00