

XRT Timeline to be uploaded on 2026/08/22

Period: 2026/08/22 11:03:00 - 2026/08/27 11:29:00

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Normal mode

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XOB #1BBA: AR - Standard Core - (Filter-Ratio with Al/poly and thin-Be long/short pairs) with PFB, 384x384 at 1064 1048, thin-Be, and Al/poly context, with

Term	Pointing (x, y)	Comment
08/22 11:18:30 - 08/22 22:25:00	Track (-732.1, -55.0) @ 08/22 11:13:00	# OP start + 10min + AR14513 obs. include HOP534 and HOP542
08/22 23:04:00 - 08/23 03:04:00	Track (-655.8, -64.5) @ 08/22 23:01:00	AR14513 obs.

PROG= 16 Inf-time(s)

Subr= 1		1-time(s)	2.0sec										
Seqn= 92		1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2		5-time(s)	2.0sec										
Seqn= 47		1-time(s)	2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 96		4-time(s)	120.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	1	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1D50: Synoptic 8 Filter w/ Al-mesh(8/256/2048), Al-poly(24/362/4096), Thin-Be(64/2048/11571), Thick-Be(65536), Med-Al(512/11571/46284), Med-Be(25

Term	Pointing (x, y)	Comment
08/22 22:54:00 - 08/22 23:00:54	Fixed (0.0, 0.0)	synoptic shifted 4h 51min
08/23 05:47:00 - 08/23 05:54:10	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -16.0 min

PROG= 04 1-time(s)

Subr= 1		1-time(s)	2.0sec										
Seqn= 5		1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 79		1-time(s)	2.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 2		1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 64		1-time(s)	2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 23		1-time(s)	4.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec										
Seqn= 65		1-time(s)	2.0sec										
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 98		1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Al/Open	med-Al/Open	close	Safe	Norm	45.2s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 25		1-time(s)	2.0sec										
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 55		1-time(s)	2.0sec										
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1C47: HOP349 - 3-filter Synoptics (Al-mesh[8/181/2897], Al-poly[16/181/4096], thin-Be[64/1024/11571] with 512x512 G-band+Leak(1064,1048) - 45 min

Term	Pointing (x, y)	Comment
08/23 04:03:00 - 08/23 05:43:54	Fixed (0.0, 0.0)	HOP349 + synoptic, shifted -16.0 min

PROG= 12 Inf-time(s)

Subr= 1 1-time(s) 300.0sec													
Seqn=100 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 57 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 81 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Subr= 2 15-time(s) 180.0sec													
Seqn= 8 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 6 1-time(s) 2.0sec													
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	4x4	2048x2048 (1024, 1024)	DPCM	2	0	2.0sec
Seqn= 29 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	3	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	250ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

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Flare mode

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XOB #1D27: HOP500_SOOP_FLARE_MODE Be Thick 2s Fixed Exposure													
Term			Pointing (x, y)					Comment					
08/22 11:18:30 - 08/22 22:25:00			Track (-732.1, -55.0) @ 08/22 11:13:00					# OP start + 10min + AR14513 obs. include HOP534 and HOP542					
08/22 23:04:00 - 08/23 03:04:00			Track (-655.8, -64.5) @ 08/22 23:01:00					AR14513 obs.					
PROG= 18 30-time(s)													
Subr= 1		1-time(s)		2.0sec									
Seqn= 87		1-time(s)		2.0sec									
└ Open/G-band		Open/G-band		open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0 0 2.0sec
└ Open/G-band		Open/G-band		close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)		Q=98	0 0 2.0sec
└ Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)		Q=98	0 0 2.0sec
└ Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)		Q=98	0 0 2.0sec
Subr= 2		120-time(s)		2.0sec									
Seqn= 31		1-time(s)		2.0sec									
└ med-Be/Open		Open/thick-Al		close	Safe	Norm	16ms	Obs	1x1	384x384 (1024, 1024)		DPCM	3 0 2.0sec
Seqn= 20		10-time(s)		2.0sec									
└ Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)		DPCM	0 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #1D35: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Be/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + GL														
Term			Pointing (x, y)					Comment						
08/23 04:03:00 - 08/23 05:43:54			Fixed (0.0, 0.0)					HOP349 + synoptic, shifted -16.0 min						
PROG= 14 30-time(s)														
Subr= 1 1-time(s) 2.0sec														
Seqn= 68 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=95	0	0	2.0sec	
Subr= 2 20-time(s) 2.0sec														
Seqn= 11 1-time(s) 2.0sec														
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 73 1-time(s) 10.0sec														
	thin-Be/Open	med-Be/Open	close	Safe	Norm	125ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
	med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Subr= 3 1-time(s) 2.0sec														
Seqn= 10 1-time(s) 2.0sec														
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec	
Seqn= 11 1-time(s) 2.0sec														
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec	
Seqn= 87 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	1ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval	

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Active Region Search

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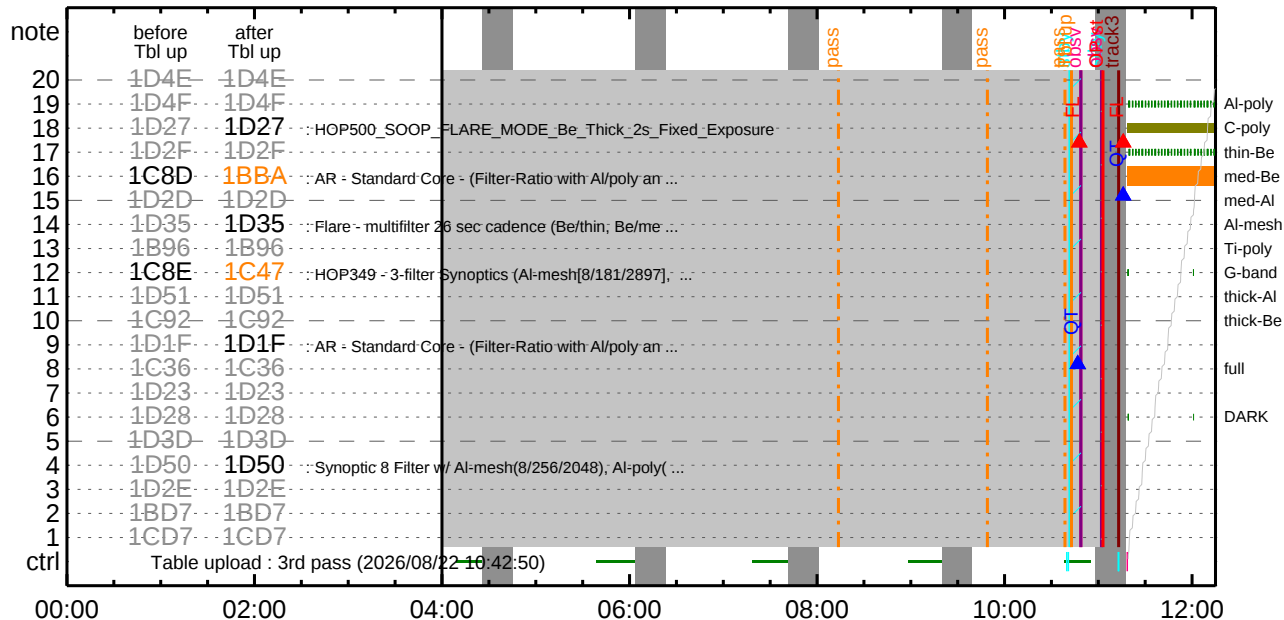
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Flare Detection

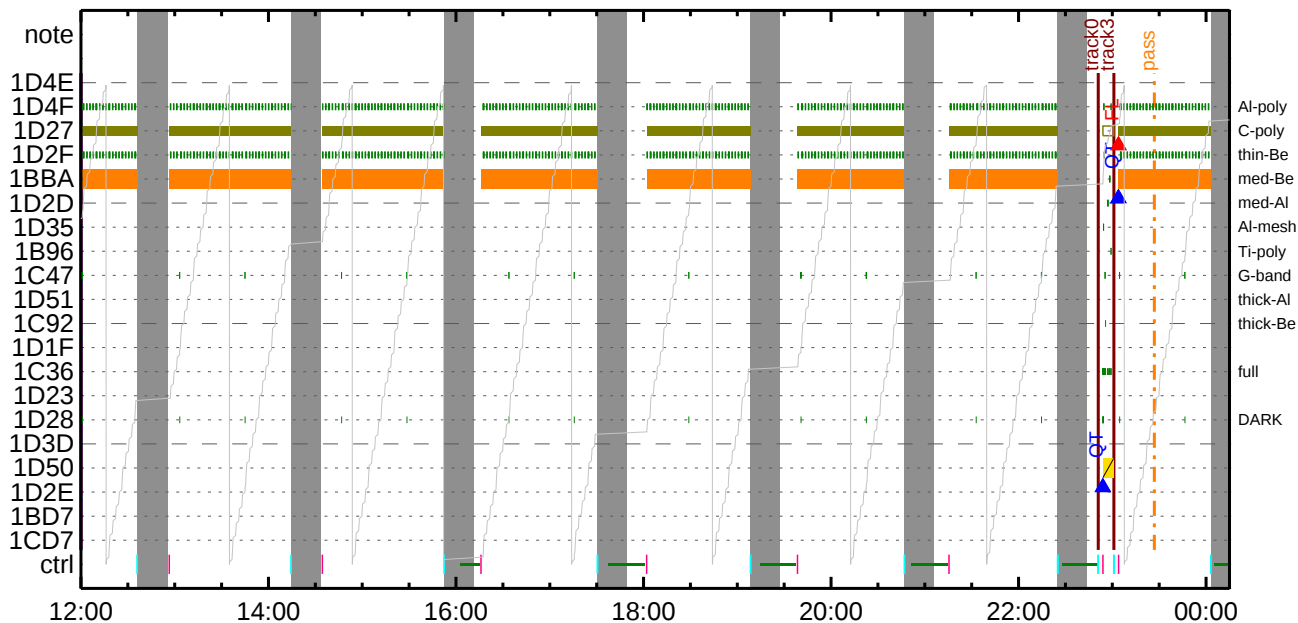
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FLD Patrol												
Term			Pointing (x, y)						Comment			
08/22 10:43:50 - 08/22 22:51:18			cannot be identified									
08/22 23:01:18 - 08/23 05:44:18			Track (-655.8, -64.5) @ 08/22 23:01:00 AR14513 obs.									
AI-poly/Open	AI-poly/Open	close	Safe	Norm	4ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

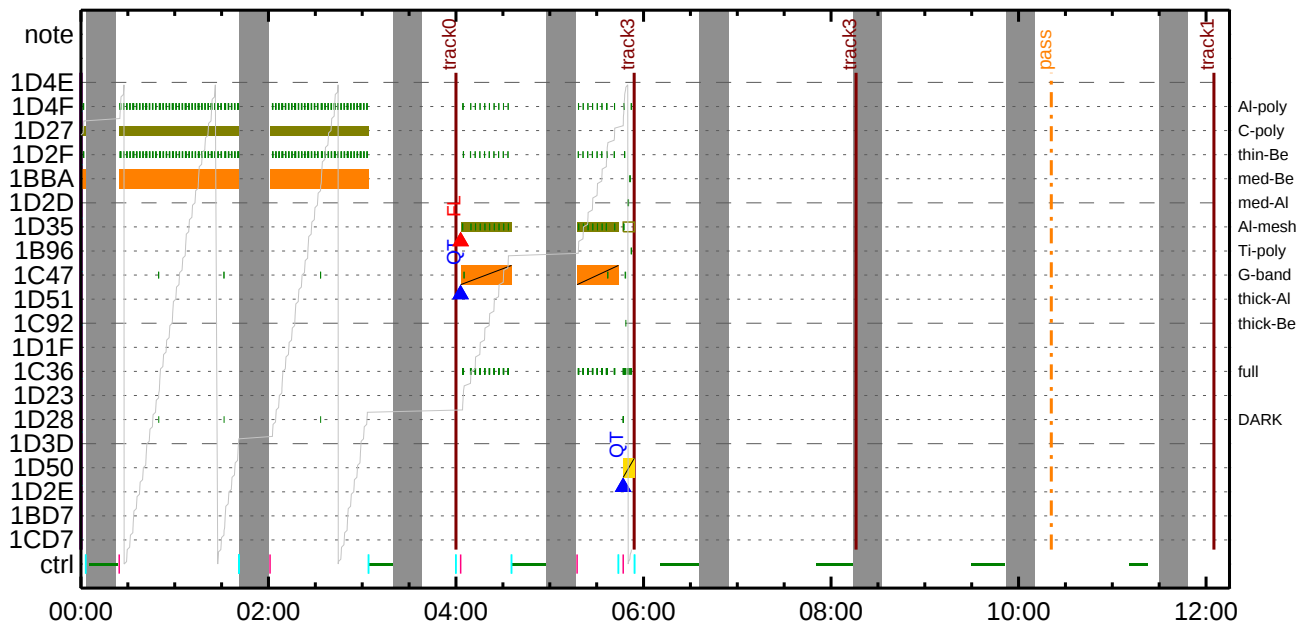
CMDI #0516 2026/08/22



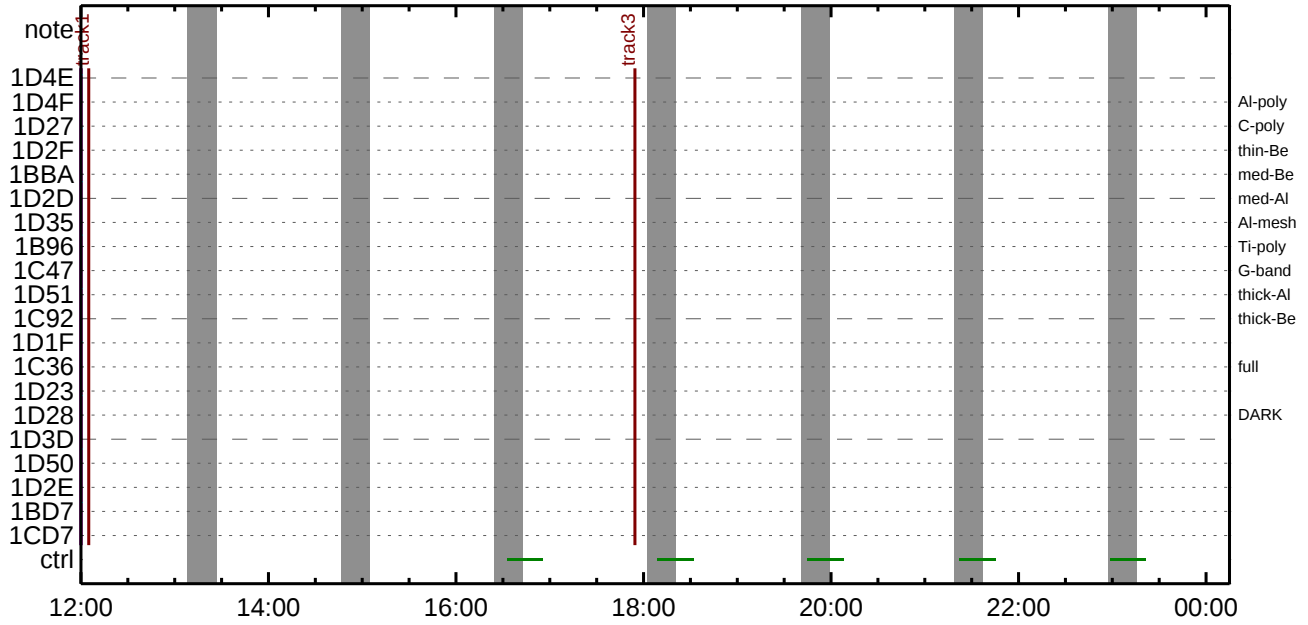
CMDI #0516 2026/08/22



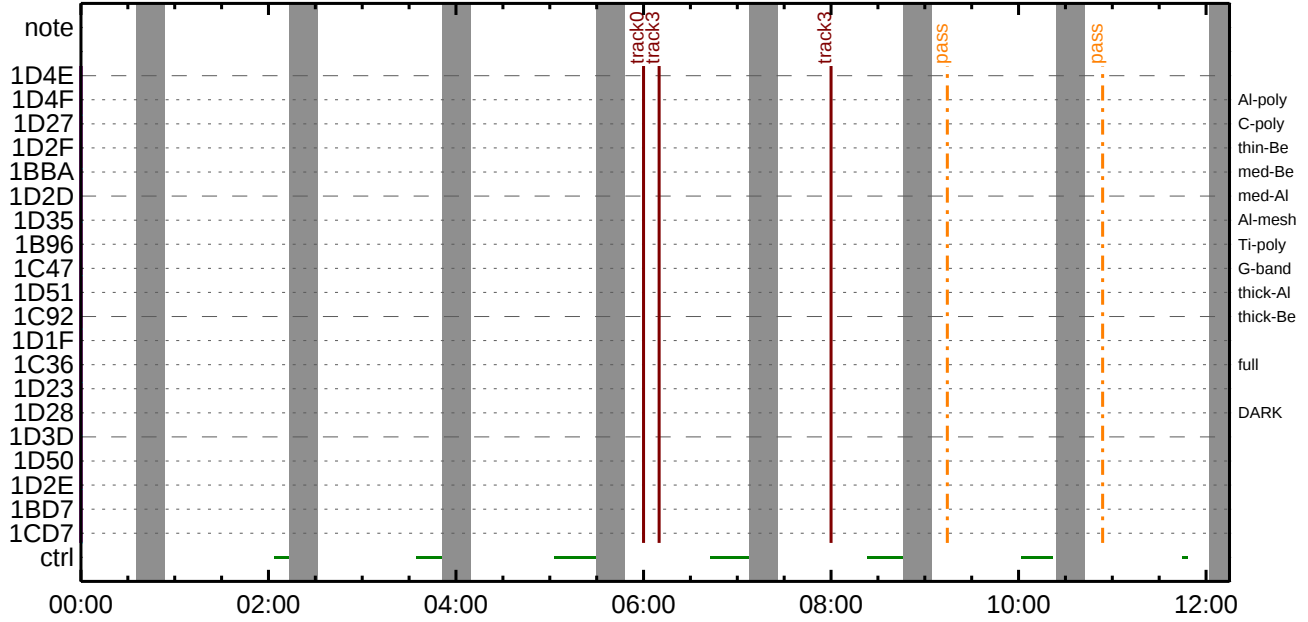
CMDI #0516 2026/08/23



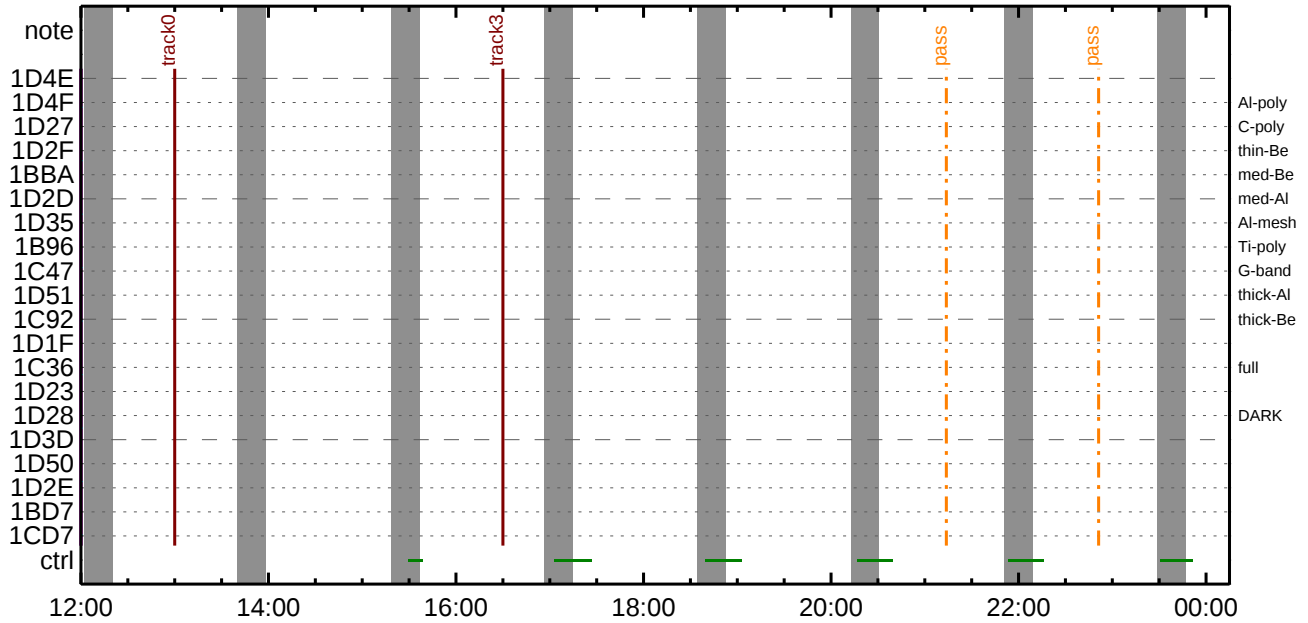
CMDI #0516 2026/08/23



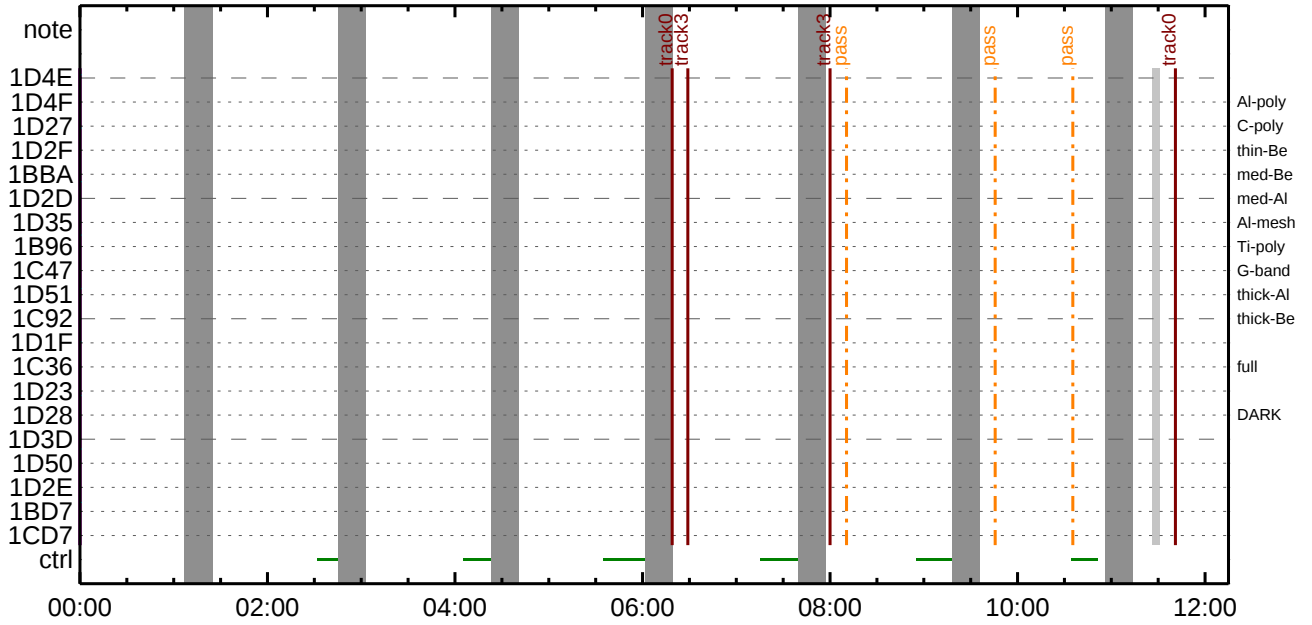
CMDI #0516 2026/08/24



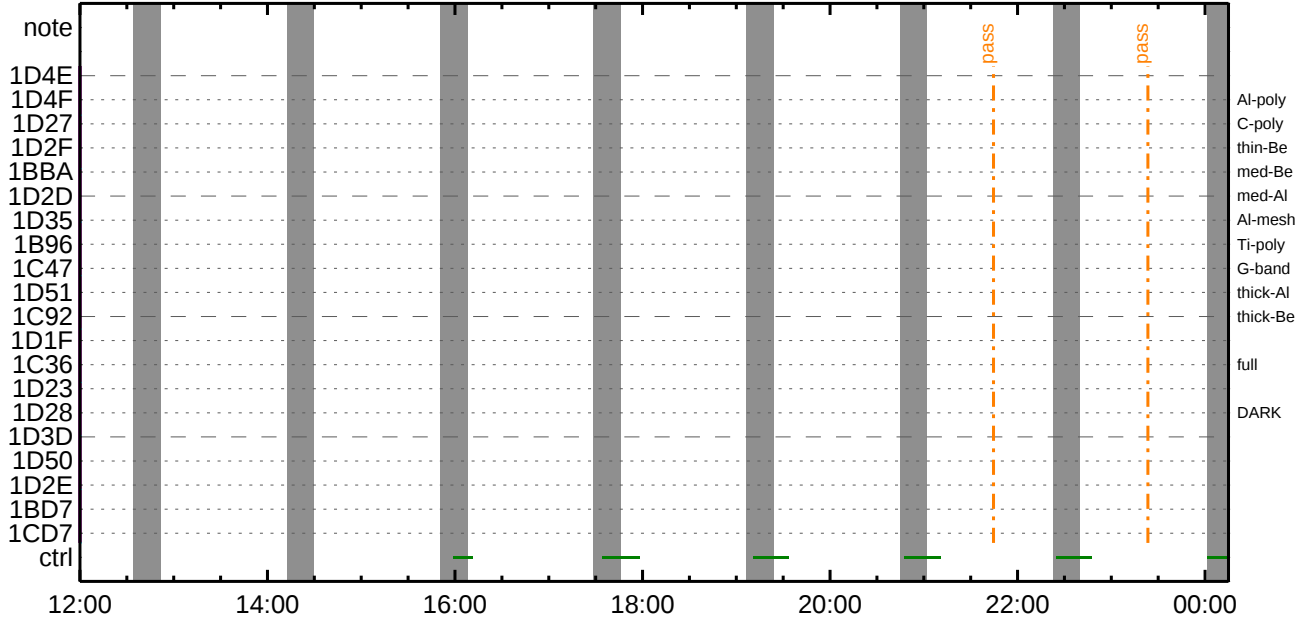
CMDI #0516 2026/08/24



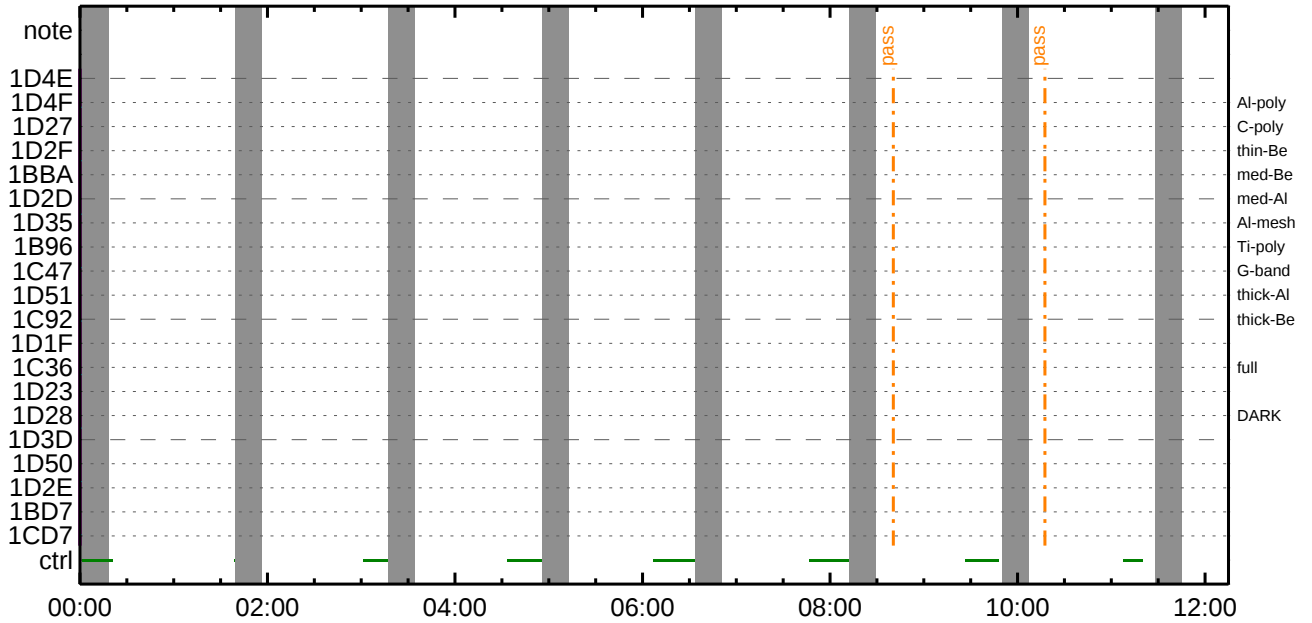
CMDI #0516 2026/08/25



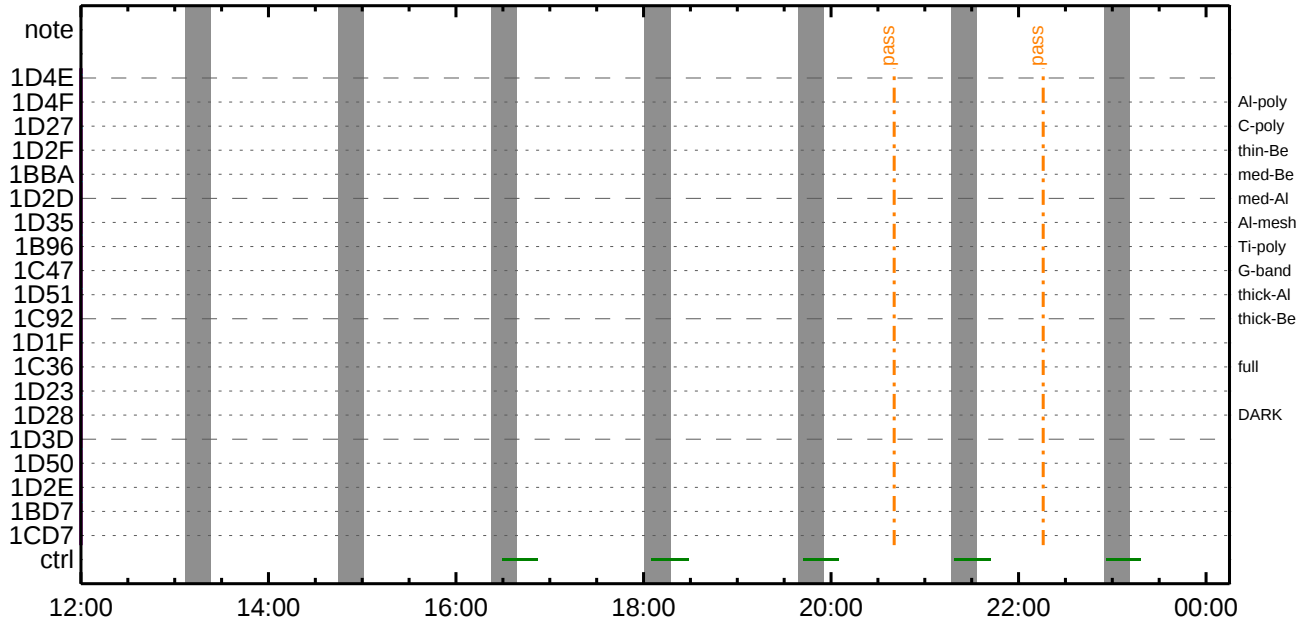
CMDI #0516 2026/08/25



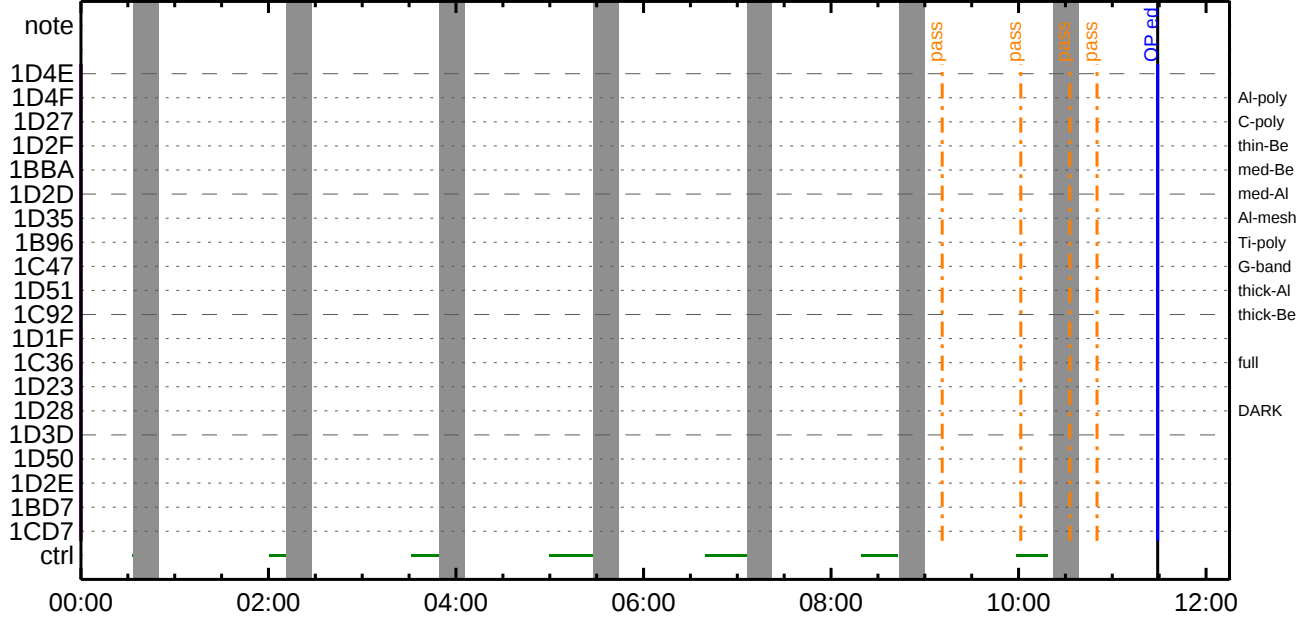
CMDI #0516 2026/08/26



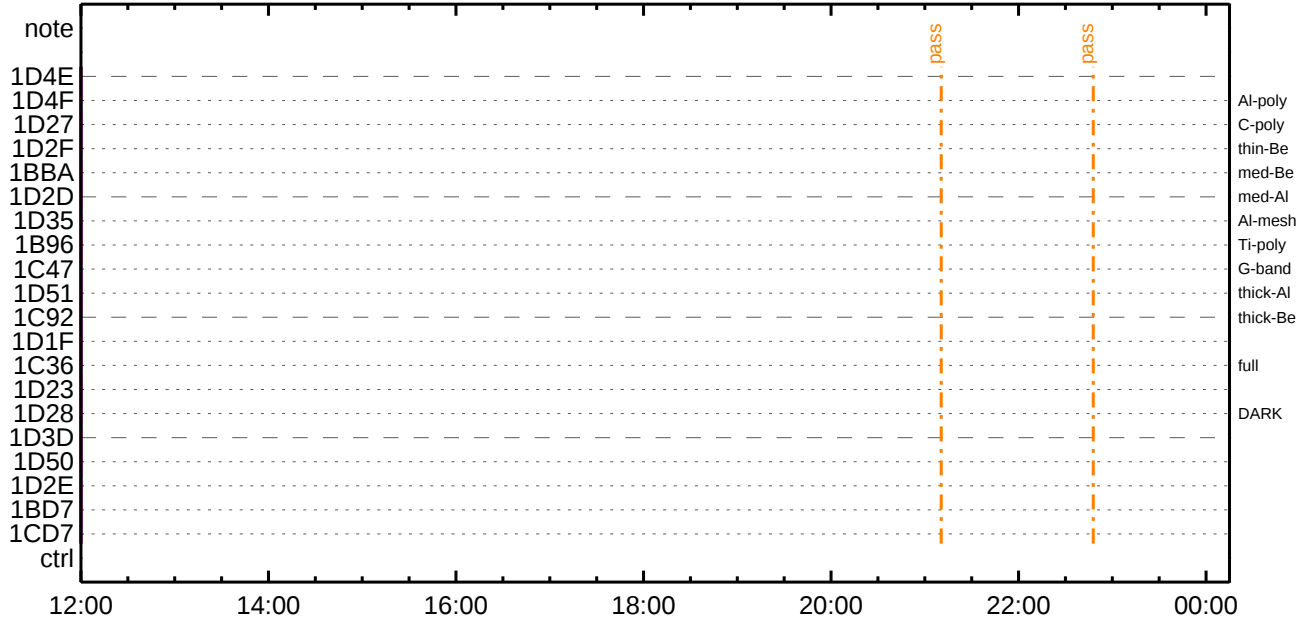
CMDI #0516 2026/08/26



CMDI #0516 2026/08/27



CMDI #0516 2026/08/27



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main-180 2026-08-22 13:24:49 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Öòò¹íî,ñ•;çÉÖÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÔÄÖñ¬°ÄÄèñ•ñççé;ç°Ê²¼ñî°EÄ,¼ê½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ»°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲEÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲEÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ»°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲEÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
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0096 . C.
0097 . C.
0098 . C. *****
0099 . C. OP/OGYí;¼YÉ;|YÀYóY×
0100 . C. *****
0101 . C.
0102 . C. ;ãOP/OGYí;¼YÉ;ä
0103 . S. OP      op-180:OP
0104 . ()
0105 . S. OG      og-180:OG
0106 . ()
0107 . C.
0108 . C. ;ãNMOG&OPîî°èYÀYóY×;ä
0109 . C. NMOG(0x200000-0x207FFF;$ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC      (20 00 7f 01 02)
0112 . C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ      40
0113 . C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ      0
0114 . C.          ¢¢[HK1_DMP_BLOCK_NUM]          EQ     127
0115 . C.          ¢¢[HK1_DMP_REPEAT_NUM]         EQ      0
0116 . C.          ¢¢[HK1_DMA_DMP_PIM]            EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC      (07 0b f8)
0119 . C.          ¢¢[HK1_PKT_FORM_NO]             EQ      7
0120 . C.          ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121 . C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ     32k
0122 . C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ      4M
0123 . C.          ¢¢[HK1_DMP_CHK_FLG]            EQ     EXEC
0124 . C. YÀYóY×½ªî»ð³îÇ$
0125 . C.          ¢¢[HK1_DMP_CHK_FLG]            EQ     NON
0126 . C. RAM ID=NMOGîî¾È¹Ç•ë²ïOKðð³îÇ$
0127 . C.
0128 . C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC      (20 80 7f 01 02)
0131 . C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132 . C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ      0
0133 . C.          ¢¢[HK1_DMP_BLOCK_NUM]          EQ     127
0134 . C.          ¢¢[HK1_DMP_REPEAT_NUM]         EQ      0
0135 . C.          ¢¢[HK1_DMA_DMP_PIM]            EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC      (07 0b f8)
0138 . C.          ¢¢[HK1_PKT_FORM_NO]             EQ      7
0139 . C.          ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140 . C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ     32k
0141 . C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ      4M
0142 . C.          ¢¢[HK1_DMP_CHK_FLG]            EQ     EXEC
0143 . C. YÀYóY×½ªî»ð³îÇ$
0144 . C.          ¢¢[HK1_DMP_CHK_FLG]            EQ     NON
0145 . C. RAM ID=NMOGîî¾È¹Ç•ë²ïOKðð³îÇ$
0146 . C.
0147 . C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC      (21 00 41 01 02)
0150 . C.          ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151 . C.          ¢¢[HK1_DMP_TOP_ADRS_0]          EQ      0
0152 . C.          ¢¢[HK1_DMP_BLOCK_NUM]          EQ     65
0153 . C.          ¢¢[HK1_DMP_REPEAT_NUM]         EQ      0
0154 . C.          ¢¢[HK1_DMA_DMP_PIM]            EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC      (07 0b f8)
0157 . C.          ¢¢[HK1_PKT_FORM_NO]             EQ      7
0158 . C.          ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159 . C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ     32k
0160 . C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ      4M
0161 . C.          ¢¢[HK1_DMP_CHK_FLG]            EQ     EXEC
0162 . C. YÀYóY×½ªî»ð³îÇ$
0163 . C.          ¢¢[HK1_DMP_CHK_FLG]            EQ     NON
0164 . C. RAM ID=NMOG, RAM ID=OPîî¾È¹Ç•ë²ïOKðð³îÇ$
0165 . C.
0166 . C. ***** °Ê²¼ðî¼Á´¶Á°ðÊÊ¬ð°Á÷÷@ (¾âµ-YÀYóY×¼Ê½ÇððÁÓÃæðÇ½ªª¬ðë¾ì¹ÇðÇðâ) *****
0167 . C. DHUYâ;¼YÉ;Ê¼ý½,Yì;¼YÉ;ÊððîÁª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC      (02 0a f8)
0170 . C.          ¢¢[HK1_PKT_FORM_NO]             EQ      2
0171 . C.          ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172 . C.          ¢¢[HK1_S_TLM_BIT_RATE]         EQ     32K
0173 . C.          ¢¢[HK1_X_TLM_BIT_RATE]         EQ      4M
0174 . C.
0175 . C. *****
0176 . C. TI-CMD SET (OPOG STOP/COPY/START)
0177 . C. *****
0178 . C.
0179 . C. NOTICE ;$ OPOG UPLOADª¬Á÷÷@NGªî¾ì¹Ç;ç°Ê²¼ðî¼TI-CMDÁ÷÷@ªî¼Á¹Ôª•ðÊªªª³ªÊ;£
0180 . C.          ¢ªªç;çSETªÊDUMPªîÊ±°ìYÑY¹ªÇ¹Ôª;ª³ªÊ;£
0181 . C.
0182 . C. TIY³YÐYóYÉððÄîÇ(UT)
0183 +. TI 2026-08-22 10:58:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 . C.          ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186 . C.
0187 +. TI 2026-08-22 10:58:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 . C.          ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190 . C.
0191 +. TI 2026-08-22 10:58:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 . C.          ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP

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0194 C.
0195 +. TI 2026-08-22 11:02:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê¼¼¤İÄê¼İİŃ¤İŸÄŸ$ŸÄŸ~¹aiÜ
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]          EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]        EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIİİİ°èŸÄŸÖŸ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]      EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]      EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]        EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]      EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]          EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      EXEC
0224 C.
0225 C. ŸÄŸÖŸ×½ªİ»¤ò³İÇ$
0226 C.          ¢¢[HK1_DMP_CHK_FLG]          EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL¤İ¼È¹Ç•ë²İOK¤ò³İÇ$
0229 C.
0230 C. DHUŸâ;¼ŸÉ;Ê¼Ÿ½,Ÿİ;¼ŸÈ;Ë¤òİá¤¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]          EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]          EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]      EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]      EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2026-08-22 11:02:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2026-08-22 11:02:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]          EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2026-08-22 11:02:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÄİ¤İ»ö¼Ÿ¤ÈÄ¤¤¹¤èDCBC•×²è *****
0265 C. (¼å°İŸÖŸÄŸÈŸPŸÈŸâŸÇŸë¤È½¼¤¼Ä»Ü¤¹¤è)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŸÐŸ¹•İ Daily±;İŃ¤È´Ø¤¹¤èDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOSŸÄŸ$ŸÄŸ~¼Ä»Ü;ä
0276 C.
0277 C. ***** LOS *****
0278 C.
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main-181 2026-08-22 13:24:49 169 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSŲÁŲ$ŲÃŲ~¼Ä»Ü;ä
0005 C.
0006 C. ŲÄŲß;¼Ų³ŲÐŲóŲÉÄ÷ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äí;ËñçðÀñ•µ°E»Í×ÅÇñÎŲçŲÃŲ×Ųí;¼ŲÉ;ËËè²µ•íîÊ;ËÈ¼°ÇÖñ•ñç¼¹¹Çñİ;çÀ®,ùñ¹ñèñÞñÇÄ÷ç®ñ•ñÈñññ³ñÈ;£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷ç®µ;ON
0016 C. *****
0017 C. °EÄ,Í×ÈÝñäLOSñÞñÇñÎ»Þ'Öòò¹íî,ñ•;çÉÔÍ×ñÈXÄÓONñİ¹ÖñÈñíñÈñññ³ñÈ;£
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XŲÐŲóŲÉŲíŲÃŲ~¾ÒÄÖñ¬°ÄÄèñ•ñççé;ç°Ê²¼ñî°EÄ,¼è½Çñò¼Ä¹Öñ¹ñè;£
0030 C.
0031 . C. *****
0032 C. DR PT1 Äî¼í°EÄ,
0033 C. *****
0034 C. ° RESTART;ÊPT1;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ»°;çDCBC-150ñØçÊñà;£
0035 C.
0036 . C. ;ãPT1°EÄ,³«»î;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0045 C.
0046 . C. ;ãŲçŲóŲEŲËÈÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö,;¼Ü)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0052 C.
0053 C.
0054 . C. PT1°EÄ,ñ¬¼«E°Ää»ßñ•ñçç,ä;ç°Ê²¼ñò¼Ä¹Öñ¹ñè;£
0055 C. ŲçŲóŲEŲËÈÄÚÂØñäÄ•Ä°²öÈòñ¬¼áñç¼¹¹Çñİ'°î»ñ¹ñèñÞñÇÄÖñÄ;£
0056 C.
0057 . C. *****
0058 C. DR PT2 Äî¼í°EÄ,
0059 C. *****
0060 C. ° RESTART;ÊPT2;Ëñ•ñçç¼¹¹Çñİ;ç°Ê²¼ñî¼Ä¹Öññ»°;çDCBC-151ñØçÊñà;£
0061 C.
0062 . C. ;ãPT2°EÄ,³«»î;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0071 C.
0072 . C. ;ãŲçŲóŲEŲËÈÄÚÂØ;ÊÄ•Ä°²öÈò;Ë,ãñî°EÄ,°E³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö,;¼Ü)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö,;¼Ü)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö,;¼Ü)
0078 C.
0079 . C. *****
0080 C. DR°EÄ,Ää»ß;çXÄ÷ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°EÄ,Ää»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÄ÷ç®µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1 XPA ON/OFF] EO OFF
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0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 35s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ()
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2026-08-22 11:02:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 . C. ***** MDP 'ûÃîî»ö¼ÝñÊÄñ¹ñëDCBC•x²è *****
0156 C. (¼å°îÿÖ¥Ã¥Ê¥Ð¥Ê¥â¥ç¥ëñ¼ñ¼Ã»Û¹ñë)
0157 . S. DC-BC dcbc-402:DCBC
0158 (MDP_known_event)
0159 C.
0160 C.
0161 . C. ***** ¥Ð¥¹•ï Daily±¿ínñÊ'Øñ¹ñëDCBC•x²è *****
0162 . S. DC-BC dcbc-153:DCBC
0163 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0164 C.
0165 C.
0166 . C. ;ãLOS¥Ã¥$¥Ã¥~¼Ã»Û;ä
0167 C.
0168 . C. ***** LOS *****
0169 C.

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main-182 2026-08-22 13:24:29 102 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSŴÁ¥$ŷÃŶ~¼Ä»Ü;ä  
0005 C.  
0006 C. ŸÀŸß;¼Ÿ³ŸÐŸóŸÉÁ÷¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010 C. Āī;ĒꞤ¿ꞡȦ•µ°E»İ×ÅÇꝢİŲçŷĂŵ×ŷı;¼ŷÊ;ĔÊè½µ•îÎĖ;ĔꝢÊ¼°ČÔꝢ•ꝛ¿¼ı¹ÇꝢİ;ćÀ®,̀ùṁ¹ꝑēꝣꝢꝢĆÁ÷¿®Ꝣ•ꝑĔꝢꝢꝢ³ꝑĔ;Ǽ;  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 C.  
0015 C. ***** XRT START *****  
0016 C.  
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU  
0018 BC (c1)  
0019 +. DC 07-F0 MDP_XRT_CTRL_MANU  
0020 BC (c1)  
0021 +. DC 07-F0 MDP_XRT_MODE_STBY  
0022 BC (c3)  
0023 . C. ----- Success Verify ? OK / NG_____  
0024 C.  
0025 C. XRT Obs. Table Upload  
0026 . S. RAM ram-291:MDP_OBS_X  
0027 ()  
0028 C.  
0029 +. DC 07-F0 MDP_DUMP_XRTTBL  
0030 BC (84 07 00 00 00 3a d4)  
0031 . C. ----- Comparison Check ? OK / ERR ____  
0032 C.  
0033 C.  
0034 +. DC 07-F0 MDP_XRT_ROI_SET  
0035 BC (cd 01 b1 b1 04 04)  
0036 +. DC 07-F0 MDP_XRT_ROI_SET  
0037 BC (cd 02 b1 b1 08 08)  
0038 +. DC 07-F0 MDP_XRT_ROI_SET  
0039 BC (cd 03 b1 b1 08 08)  
0040 +. DC 07-F0 MDP_XRT_ROI_SET  
0041 BC (cd 04 b1 b1 06 06)  
0042 +. DC 07-F0 MDP_XRT_ROI_SET  
0043 BC (cd 05 85 83 06 06)  
0044 +. DC 07-F0 MDP_XRT_ROI_SET  
0045 BC (cd 06 85 83 06 06)  
0046 +. DC 07-F0 MDP_XRT_ROI_SET  
0047 BC (cd 07 80 80 06 06)  
0048 +. DC 07-F0 MDP_XRT_ROI_SET  
0049 BC (cd 08 80 80 20 20)  
0050 +. DC 07-F0 MDP_XRT_ROI_SET  
0051 BC (cd 09 80 80 20 08)  
0052 +. DC 07-F0 MDP_XRT_ROI_SET  
0053 BC (cd 0a 80 80 08 20)  
0054 +. DC 07-F0 MDP_XRT_ROI_SET  
0055 BC (cd 0b 85 83 08 08)  
0056 +. DC 07-F0 MDP_XRT_ROI_SET  
0057 BC (cd 0f 80 80 06 06)  
0058 +. DC 07-F0 MDP_XRT_ROI_SET  
0059 BC (cd 10 80 80 08 08)  
0060 +. DC 07-F0 MDP_XRT_FLD_ENA  
0061 BC (d8)  
0062 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA  
0063 BC (c8)  
0064 +. DC 07-F0 MDP_XRT_ARS_DIS  
0065 BC (d5)  
0066 +. DC 07-F0 MDP_XRT_AEC_RESET  
0067 BC (d0)  
0068 +. DC 07-F0 MDP_XRT_FLD_RESET  
0069 BC (da)  
0070 +. DC 07-F0 MDP_XRT_QT_PROG_SET  
0071 BC (c4 09)  
0072 +. DC 07-F0 MDP_XRT_FL_PROG_SET  
0073 BC (c5 12)  
0074 . C. ----- Success Verify ? OK / NG ____  
0075 C.  
0076 C.  
0077 . C. All OK? Yes--> Please Proceed. / No --> Stop here.  
0078 C.  
0079 +. DC 07-F0 MDP_XRT_MODE_OBSV  
0080 BC (c2)  
0081 +. TI 2026-08-22 11:02:02.0  
0082 DC 07-F0 MDP_XRT_MODE_OBSV  
0083 BC (c2)  
0084 . C. ----- Success Verify ? OK / NG ____  
0085 C.  
0086 C. ***** XRT END *****  
0087 C.  
0088 . C. ***** MDP ‘ũĀĩñ=î»ô¾ŷꝢĕÂĐꝢ¹ꝓēDCBC•x²ê *****  
0089 C. (%ā°ìŷōŷĀŷËŸŰŷÏŷĚŷĬŷŲŷčŷēꝢĕ½ꝢꝢ¼Ā»ÜꝢ¹ꝓē)  
0090 . S. DC-BC dcbc-402:DCBC  
0091 (MDP_known_event)  
0092 C.  
0093 C.  
0094 . C. ***** ŸÐŸ¹•İ Daily+¿İÑꝢĔ‘ØꝢ¹ꝓēDCBC•x²ê *****  
0095 . S. DC-BC dcdbc-153:DCBC
```

0096 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0097 C.
0098 C.
0099 . C. ;äLOSŸÁŸSŸÃŸ-¼Â»Ü;ä
0100 C.
0101 . C. ***** LOS *****
0102 C.

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*** OP Sequence for XRT ***

2026/08/22	11:12:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	11:12:56.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	11:12:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2026/08/22	11:13:00.0	AOCs_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 03 e7 02 0c
2026/08/22	11:13:18.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2026/08/22	11:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2026/08/22	11:13:22.0	XRT_AEC_RESET_448_OG [0x1c0]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2026/08/22	11:13:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2026/08/22	11:13:26.0	XRT_FLD_RESET_401_OG [0x191]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/22	11:15:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2026/08/22	11:15:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 12
2026/08/22	11:17:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/22	11:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/22	12:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	12:36:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	12:36:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/22	12:36:06.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/22	12:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/22	12:55:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/22	12:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/22	14:14:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	14:14:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	14:14:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/22	14:14:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/22	14:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/22	14:33:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/22	14:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/22	15:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	15:52:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	15:52:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/22	15:52:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/22	15:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/22	16:15:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/22	16:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/22	17:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	17:30:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	17:30:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/22	17:30:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/22	17:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/22	18:01:00.0	XRT_Custom_430_OG [0x1ae]			
2026/08/22	18:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/22	19:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	19:08:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2026/08/22	19:08:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2026/08/22	19:08:36.0	XRT_PREFLR_STRT_404_OG [0x194]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2026/08/22	19:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2026/08/22	19:37:30.0	XRT_Custom_430_OG [0x1ae]			
2026/08/22	19:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2026/08/22	20:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			

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2026/08/22	20:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	20:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	20:47:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/22	20:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2026/08/22	21:14:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2026/08/22	21:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/22	22:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	22:25:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	22:25:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	22:25:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/22	22:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2026/08/22	22:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2026/08/22	22:50:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	22:50:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	22:51:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2026/08/22	22:51:18.0	XRT_FLD_DIS_409_OG [0x199]	AOCU_NM	5	02-76	00 00 00 00 00	
2026/08/22	22:51:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2026/08/22	22:51:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2026/08/22	22:53:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2026/08/22	22:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2026/08/22	23:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/22	23:00:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	23:00:58.0	XRT_FOCUS_POSITION_414_OG [0x19e]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/22	23:01:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2026/08/22	23:01:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/22	23:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2026/08/22	23:01:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2026/08/22	23:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2026/08/22	23:01:26.0	XRT_FLD_RESET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2026/08/22	23:03:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/22	23:03:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2026/08/22	23:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 12	
2026/08/23	00:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/23	00:03:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	00:03:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	00:03:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/23	00:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2026/08/23	00:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2026/08/23	00:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/23	01:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	01:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	01:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	01:41:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/23	01:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2026/08/23	02:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2026/08/23	02:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/23	03:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	03:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2026/08/23	03:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	03:04:06.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/23	03:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2026/08/23	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2026/08/23	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	03:59:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2026/08/23	04:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2026/08/23	04:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2026/08/23	04:00:22.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2026/08/23	04:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2026/08/23	04:00:26.0	XRT_FLD_RESET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2026/08/23	04:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/23	04:02:58.0	XRT_FL_PROG_SET_439_OG [0x1b7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c	
2026/08/23	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0e	
2026/08/23	04:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/23	04:35:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	04:35:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	04:35:36.0	XRT_PREFLR_STRT_404_OG [0x194]	MDP_XRT_FLD_RESET	1	07-F0	da	
2026/08/23	04:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2026/08/23	05:16:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2026/08/23	05:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2026/08/23	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/23	05:43:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	05:43:58.0	XRT_FOCUS_POSITION_406_OG [0x196]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	05:44:18.0	XRT_FLD_DIS_409_OG [0x199]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2026/08/23	05:44:20.0	XRT_FLRCTRL_DIS_413_OG [0x19d]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2026/08/23	05:44:22.0	XRT_ARS_DIS_420_OG [0x1a4]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2026/08/23	05:46:58.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2026/08/23	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2026/08/23	05:54:00.0	AOCS_ORe-point_Start_1_OG [0x097]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2026/08/23	05:54:10.0	XRT_CTRL_MANU_402_OG [0x192]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/23	05:54:20.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	05:54:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2026/08/23	08:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2026/08/23	12:05:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/23	17:54:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 03 e7 02 0c	
2026/08/24	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/24	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2026/08/24	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/24	13:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/24	16:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 b3 e8 02 0c	
2026/08/25	06:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/25	06:29:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00	
2026/08/25	08:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 03 e7 02 0c	
2026/08/25	11:41:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 03 e7 02 0c	
			AOCU_NM	5	02-76	00 00 00 00 00	